Modelling and design of FTJs as high reading-impedance synaptic devices

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1 Abstract
We present an in-house modelling framework for Ferroelectric Tunnelling Junctions (FTJ), and an insightful study of the design of FTJs as synaptic devices. Results show that a moderately low-κ tunnelling dielectric (e.g. SiO$_2$) can increase the read current and the current dynamic range.

Keywords - Ferroelectric Tunnelling Junctions, Neuromorphic Computing

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2 Introduction
There exists a growing need for electron devices capable of non volatile, multi-level adjustments of the resistance [1]. In fact, such synaptic devices can improve by orders of magnitude the energy efficiency of neural network hardware [2], for which purpose low programming energy and high reading-impedance are crucial [3]. Ferroelectric Tunneling Junction (FTJ) is a promising candidate as energy efficient synaptic device, and a four level operation has been experimentally reported in a Metal-Ferroelectric-Insulator-Metal (MFIM) architecture (Fig.1(a)) [4]. The design of a MFIM FTJ has a delicate trade-off between the read operation, demanding a large enough dielectric voltage drop $V_D$ (i.e. $qV_D>[\Phi_{MD}-\chi_F]$), see Fig2(a)) and thus a small dielectric capacitance $C_D=\varepsilon_D/t_D$, and the retention condition requiring instead a large $C_D/C_F$ ratio to minimize the depolarization field $E_{DEP}=P_r[\varepsilon_F(C_D/C_F+1)]^{-1}$ (Fig.2(b)). Furthermore the dielectric thickness $t_D$ and its electron affinity $\chi_D$ have a large impact on the read tunnelling current.

Due to the many material and device options, there is an urgent need for a simulation driven optimization of FTJs. However the modelling of FTJs is challenging, as it entails the ferroelectric dynamics for a three dimensional (3D) electrostatics and the tunnelling through the dielectric stack.

In this work we first present an in-house developed modelling framework for FTJs, including the 3D treatment of the ferroelectric dynamics and electrostatics, and the tunnelling transport through the MFIM stack. The model is then validated against experiments [4,5], and used for an insightful design study of FTJs, to optimize the read current dynamic range.
3 Modelling approach

Our starting point is the multi-domain Landau, Ginzburg, Devonshire (LGD) model (Eq.1, \( i = 1, 2 \cdots n_D \), with \( n_D \) being the number of domains) for a MFIM capacitor presented in [6–8]. In Eq.1 \( \alpha_i, \beta_i, \gamma_i \) are the domain dependent ferroelectric anisotropy constants, \( C_0=(C_D+C_F), d \) is the side of the square domain, \( k \) and \( w \) are the coupling constant and the inter-domain region width for the domain wall energy (Fig.1(b)), while the capacitances \( C_{i,j} \) provide a three dimensional description of the depolarization energy and obey the sum rules \( \sum_{j=1}^{n_D}(1/C_{i,j}) \simeq \sum_{i=1}^{n_D}(1/C_{i,j}) \simeq 1/C_0 \) [8]. At each time \( t \) and bias \( V_T(t) \), Eq.1 provides all the domain polarizations \( P_i(t) \), so that the dielectric, \( V_{D,i} \), and ferroelectric, \( V_{F,i} \), voltage drops are uniquely given by Eqs.2 [8]. As it can be seen, the resistivity \( \rho \) sets a time scale \( t_\rho = \rho/(2|\langle \alpha \rangle|) \) of the ferroelectric dynamics (where \( \langle \alpha \rangle \) is the \( \alpha \) value averaged over domains), so that a slow bias \( V_T(t) \) compared to \( t_\rho \) results in a quasi static behavior.

The tunnelling read current, \( I_{R,i} \), is estimated as the sum of the \( I_{R,i} \) in each domain, in turn given by Eq.3 resulting from a Landauer model [9]. In Eq.3 the Fermi functions are \( f_{0,MD(F)}(E)=[1+\exp(E-E_{f,MD(F)})]^{-1} \), with \( E_{f,MD} \), \( E_{f,FM} \) being the Fermi levels of the electrodes as defined in Fig.1(a). Eq.3 assumes an effective mass approximation and an energy separability \( E=E_{\perp}+\varepsilon(k) \), with the transverse energy \( \varepsilon(k) \) being conserved in the tunnelling process [10]. For a tunnelling transmission, \( T_i(E_{\perp}) \), independent of \((k_x,k_y)\), the sum over \((k_x,k_y)\) can be evaluated analytically as in Eq.4 [11], thus obtaining the \( I_{R,i} \) expression in Eq.5 where, for a read voltage \( V_R \) applied to the MF electrode, we have \( E_{f,FM}=E_{f,MD}-qV_R \). Finally the transmission \( T_i(E_{\perp}) \) is calculated accordingly with a WKB approximation and an effective oxide mass \( m_{ox} \). For WKB calculations the conduction band profiles \( E_{CD,i}(z) \), \( E_{CF,i}(z) \) in the oxide and ferroelectric are assumed to be linear and set by the \( V_{D,i} \) and \( V_{F,i} \) (Eqs.2), which simplifies the determination of the tunneling extrema \( z_{in}, z_{out} \) (Fig.2(b)). Depending on the band diagram, two tunnelling paths may be involved in the WKB calculation at a given \( E_{\perp} \) (Fig.2(b)), in which case \( T_i(E_{\perp}) \) is obtained by the product of the two tunnelling transmissions. This approach neglects the influence on \( T_i(E_{\perp}) \) of interference effects, which is a reasonable approximation also in virtue of the empirical calibration of some modelling parameters discussed below.

4 Model validation against experiments

We validated our model by comparison with measured data for both the ferroelectric \( Q-V_F \) curve (with \( Q=P+\varepsilon q\varepsilon_F E_F \) [3], and the \( I_R \) in corresponding FTJs [4]. Fig.3 reports the measured \( Q-V_F \) data for \( \text{Hf}_0.5\text{Zr}_0.5\text{O}_2 \) (HZO) reported in [5], and the simulations from the LGD multi-domain model in Eq.1.

The agreement between simulations and experiments is good, and the match in the switching region improves by accounting for domain-to-domain variations of \( \alpha_i, \beta_i \) and \( \gamma_i \). Throughout this work we will use the nominal values for \( \alpha, \beta \) and \( \gamma \) reported in the caption of Fig.3.

We simulated a MFIM structure featuring a 12nm HZO ferroelectric, a 2nm Al2O3 dielectric and TiN metal electrodes [3] (see Tab.1 for material parameters). Figure 4(top) shows examples of the setting and reading waveforms employed for the simulations of the FTJs, that were shaped to emulate the triangular waveforms used in the experiments [4]. As it can be seen the \( V_T \) waveforms are very slow compared to \( t_\rho \), hence simulations correspond to a quasi static operation.

In Fig.4(bottom), different \( V_{\text{SET}} \) values clearly result in different fractions, \( f_{UP} \), of domains with a positive polarization, stemming from the minor loops in the \( Q \) vs. \( V_T \) curve shown in the inset. By inspecting the \( f_{UP} \) in the set and read operation we also see that the MFIM device suffers from a quite strong depolarization effect. In fact, for a given \( V_{\text{SET}} \), the \( f_{UP} \) during retention (i.e. for \( V_T=0 \text{ V} \)) and read (i.e. for \( V_T=2 \text{ V} \)) is significantly smaller than the corresponding \( f_{UP} \) reached in the set operation. This occurs because the fairly large \( t_D=2 \text{ nm} \) results in a strong depolarization
field (Fig.2(b)), producing a back-switching to $P_1<0$ of some of the domains having $P_1>0$ during the setting phase.

Figure 5 finally compares simulations and experiments for the $I_R$ of FTJs at a read voltage $V_R=2$ V. Here we notice that the $m_\parallel$ in Eq.5 corresponds to an effective mass for the density of states of the metal electrodes. In the lack of a better $m_\parallel$ determination, we used the popular assumption $m_\parallel \approx m_0$ [12]. Our simulations in Fig.5 can track experiments well with reasonable values of the oxide mass $m_\text{ox}$ (Tab.1) [13].

5 Simulation based design of FTJs

The minimum $I_R$ value required by applications is set by the transistors leakage current and by the noise of the sense amplifier, and for recent designs of neuromorphic processors a reasonable target is approximately 100 pA [14,15]. The results in Fig.5 show that for $V_{SET}=2.5$ V a device area larger than $10^4 \mu$m$^2$ is needed to reach the $I_R=100$ pA. Moreover the current ratio $R_I=|I_{R,max}/I_{R,min}|$ is only about ten, that seems too small for the desired 4–bit resolution of the synaptic weights [16]. Hence the primary goals of our FTJ design exploration are the increase of $I_R$ and $R_I$.

The most obvious route to increase $I_R$ is the scaling of the dielectric thickness $t_D$, whose effects are illustrated in Fig.6 for the HZO/Al$_2$O$_3$ stack. As it can be seen $I_R$ increases by thinning the Al$_2$O$_3$ layer, but $R_I$ degrades (see inset). A marked $R_I$ reduction with decreasing $t_D$ has been observed also in experiments [6]. Fig.6 also shows that, by thinning Al$_2$O$_3$ and thus increasing $C_D=\varepsilon_d/t_D$, the average $V_{D,up}$ for the positive polarization domains decreases (right $y$-axis), and eventually $qV_{D,up}$ cannot overcome $[\Phi_{MD}\!-\!\chi_F]\!\approx\!2.45$ eV (see Fig.2(a)).

In order to reduce $C_D$ for a given $t_D$ we explored SiO$_2$, having a dielectric constant about 2.5 times smaller than Al$_2$O$_3$. Figure 7 reports $I_R$ and $R_I$ versus $V_{SET}$ for two variants of a HZO/SiO$_2$ based FTJ, and compared to the HZO/Al$_2$O$_3$ baseline case of Fig.5 (black). By using a 1 nm SiO$_2$ layer and maintaining TiN electrodes with $V_R=2.0$ V (red) the $I_R$ largely increases, but $R_I$ does not improve. In the second option of the HZO/SiO$_2$ based FTJ we consider a low workfunction Al electrode ($\Phi_M\!\approx\!4.08$ eV), so as to reduce the SiO$_2$ tunnelling barrier $[\Phi_{MD}\!-\!\chi_D]$. This leads to a large $I_R$ increase at fixed $V_R$, that we exploited to decrease $V_R$ to 1.5 V and the minimum $V_{SET}$ to 2 V. The corresponding results in Fig.7 (green) show a large improvement for both $I_R$ and $R_I$ compared to the HZO/Al$_2$O$_3$ case. Indeed, Fig.7(b) reveals that the 1 nm SiO$_2$ design leads to $V_{D,up}$ values comparable to the 2 nm Al$_2$O$_3$ for $V_R=2$ V; moreover for Al electrodes the $qV_{D,up}$ can overcome $[\Phi_{MD}\!-\!\chi_F]$ even for $V_R=1.5$ V.

6 Conclusions

By using an in-house developed simulator for FTJs calibrated against experiments, we studied the delicate tradeoffs between the reading efficiency and the depolarization effects for FTJs based synaptic devices. Our results show that a SiO$_2$ tunnelling dielectric and low workfunction metal electrodes can greatly increase the read current dynamic range and thus enable a multi-bit synaptic weight resolution.

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\begin{align*}
t_F \rho dP_i \frac{dP_i}{dt} &= -\left(2\alpha_i P_i + 4\beta_i P_i^3 + 6\gamma_i P_i^5\right) t_F + t_F k \frac{d}{dw} \sum_{j=1}^{n_D} P_j - \frac{1}{2} \sum_{j=1}^{n_D} \left( \frac{1}{C_{i,j}} + \frac{1}{C_{j,i}} \right) P_j + \frac{C_D}{C_0} V_T \\
V_{D,i} &= \frac{1}{d^2} \int D_i V_D(r) dr - \sum_{j=1}^{n_D} \frac{C_F}{C_0} V_T, \quad V_{F,i} = V_T - V_{D,i} = -\frac{1}{d^2} \sum_{j=1}^{n_D} \left( \frac{1}{C_{i,j}} + \frac{1}{C_{j,i}} \right) P_j + \frac{C_D}{C_0} V_T, \\
I_{R,i} &= \frac{q}{\pi h} \int_{k_x, k_y} T_i (E_{\perp}) [f_{0,M_D} (E_{\perp} + \varepsilon(k)) - f_{0,M_F} (E_{\perp} + \varepsilon(k))] dE_{\perp} - \ln(1 + \exp(\eta_{f,M})) \\
&= \frac{A(K_B T) m_{\parallel}}{2\pi h^2} \ln[1 + \exp(\eta_{f,M})] \\
&= \frac{(E_{f,M} - E_{\perp})}{K_B T} \\
I_{R,i} &= \frac{A(K_B T) m_{\parallel} q}{2\pi^2 h^3} \int_{-\infty}^{+\infty} T_i (E_{\perp}) \{\ln[1 + \exp(\eta_{f,M})] - \ln[1 + \exp(\eta_{f,M})]\} dE_{\perp}
\end{align*}
\]

Figure 1: (a) Sketch of a MFIM based FTJ, where MF, MD are the electrodes contacting respectively the ferroelectric and the dielectric, while \( t_F \) and \( t_D \) are the ferroelectric and dielectric thickness. A positive ferroelectric polarization points towards the dielectric (red arrow). (b) Zoom on ferroelectric domains where \( d \) is the side of the square domain and \( w \) is the width of the domain-wall region used for the domain wall energy in LGD (Eq.1) [8]. Throughout the work we used \( d=5 \text{ nm}, w/d = 0.1 \) and the domain wall coupling factor in Eq.1 was set to \( k/w = 2 \times 10^{-3}[m^2/F] \). \( V_T \) is the external bias.

Figure 2: Band diagram across the MFIM stack. \( E_0, \Phi_{MF}, \Phi_{MD} \) are respectively the vacuum level, and the work function of the MF and MD electrodes. \( \chi_F, \chi_D \) are the electron affinity of the ferroelectric and dielectric, \( E_{F,MD}, E_{F,MF} \) are the Fermi levels of the MD and MF electrode. (a) Read condition with a read voltage \( V_T = V_R \): \( qV_D \) should be larger than the ferroelectric tunnelling barrier \( \Phi_{MD} - \chi_F \), so that the ferroelectric conduction band profile can drop below \( E_{F,MD} \); (b) Retention condition for \( V_T = 0 \): the depolarization field \( E_{DEP} \approx P_r \left[ \varepsilon_F (C_D/C_F + 1) \right]^{-1} \) should be minimized (\( P_r \) is the remnant polarization).
|                  | HZO | Al₂O₃ | SiO₂ |
|------------------|-----|-------|------|
| $\chi_D, \chi_F$ [eV] | 2.1 | 1.4   | 0.95 |
| $m_{ox}$ [$m_0$]     | 0.4 | 0.3   | 0.5  |
| $\epsilon_D, \epsilon_F$ [$\epsilon_0$] | 30  | 10    | 3.9  |

Table 1: Material parameters used in simulations. The work function $\Phi_M$ of Al and TiN were taken as 4.08eV and 4.55eV, respectively.

Figure 3: Experimental [5] (symbols) and simulated (lines) polarization versus ferroelectric voltage characteristic of a Hf₀.₅Zr₀.₅O₂ layer with $t_F=12$ nm. The nominal values of the anisotropy constants in the LGD equations are $\alpha=-5.8\cdot10^8 \text{m/F}$, $\beta=2.9\cdot10^9 \text{m}^5/\text{F/C}^2$, $\gamma=6.5\cdot10^{10} \text{m}^9/\text{F/C}^4$, and domain to domain variations are introduced according to a normal distribution of $\alpha_i$, $\beta_i$, $\gamma_i$, where $\sigma_{\alpha,\beta,\gamma}$ denote the standard deviations normalized to the mean values. Both experiments and simulations correspond to a quasi static condition.

Figure 4: (Top) Examples of setting and reading waveforms used in the simulations of the MFIM based FTJs of [4] (see parameters in Tab.1); read voltage is $V_R=2$ V. (Bottom) Corresponding fractions $f_{UP}$, defined as the percentage of the domains having a positive polarization during the set and read operation and for different $V_{SET}$. The inset shows minor loops in the $Q$ versus $V_T$ plots corresponding to different $V_{SET}$ values.
Figure 5: Experiments from [4] (boxes, device area ≈ 3.14 · 10⁻⁴ cm²), and simulations (black solid line) for the read current at \( V_R = 2 \) V of an HZO/\( \text{Al}_2\text{O}_3 \) FTJ (12nm / 2nm) and versus the set voltage \( V_{SET} \). The box plots for experiments were inferred from the cycle to cycle variations reported in Fig.3(d) of [4].

Figure 6: Read current at \( V_R = 2 \) V (left y axis) versus the \( \text{Al}_2\text{O}_3 \) thickness for an HZO/\( \text{Al}_2\text{O}_3 \) FTJ (\( t_F = 12 \)nm) and for \( V_{SET} = 2.5 \) V or 6.5 V. The average voltage drop, \( V_{D,up} \), for the positive polarization domains is also reported (right y axis) in read condition, and the current ratio \( R_I = [I_{R,max}/I_{R,min}] \) is shown in the inset.
Figure 7: (a) Read current (left y-axis) and $R_I = |I_{R_{\text{max}}}/I_{R_{\text{min}}}|$ (right y-axis) versus the set voltage for the TiN/HZO/Al$_2$O$_3$/TiN structure ($t_D=2$nm, $V_R=2$ V) in Fig.5 and for two variants of an HZO/SiO$_2$ based FTJ, namely for TiN/HZO/SiO$_2$/TiN ($t_D=1$nm, $V_R=2$ V), and for Al/HZO/SiO$_2$/Al ($t_D=1$nm, $V_R=1.5$ V). The HZO thickness is $t_F=12$nm in all cases. (b) The average voltage drop, $V_{D_{\text{up}}}$, for the positive polarization domains (as in Fig.6), for the different design options. The ferroelectric tunnelling barrier $[\Phi_{MD}-\chi_F]$ is substantially reduced for the Al electrode.