Phase change memory: Operation, current challenges and future prospects

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Abstract

Phase change memory has become known and most advanced resistive memory technology to meet the consumer’s applications. It is a non-volatile random access memory. It plays a vital role in order to overcome the increasing demand of powerful data storage devices. In the present paper, advantages over other memories have been discussed in detail. This review focuses on the device operation of PCM. The current challenges and future prospects have also been outlined.

Keywords: Phase change memory, phase change materials, set and reset, PCM lance cell, Non-volatile memory, Ovonic Threshold Switching.

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1. Introduction

In the present era, the electronic appliances are widely used. Semiconductors emerge as the heart of the modern electronic world. It is widely used from digital watches to super computers. The modern information society is marked by fast increasing demand for effective data storage devices, which results into the innovative storage concepts. The capability of humankind to conserve knowledge has been a salient feature of man development. Books can’t store more information while huge amount of data can stored in USB Flash Drives, Floppy disk, DVD and semiconductor chips[1].Non -volatile storage technologies play a basic involvement in microelectronic industry. Till now, more than thirty known Non-volatile memory technologies have been participating to enter in steady increasing NVM market[2].The steadily increasing use of digital watches, android phones, MP3 players asserts in enhancement of memory strength and features. However, CD can store 700 MB of data, DVDs can store upto 8.4GB of data, Blu-ray disc can store 50GB of data[3]. HVDs are expected to store data in TBs..

Phase Change Memory is such a memory which can store more data than that of CDs, DVDs, Blu-ray etc within the same area[4]. PCM is a combination of Flash and DRAM. As it has higher latency, higher density, higher endurance, low power consumption, fast operation speed, it has been participating to enter in the steady growing Non-volatile market and emerging as storage class memory (SCM)[5].In this memory, data can be read and write even when power goes off. Phase transition phenomenon occurs to store information. Recently IBM researchers have presented a PCM chip that can store three bits per
The first part of review has been focussed on comparison of Phase change memory with various memories which clarifies that PCM is an emerging technology. In the second part of review, PCM lance cell and Phase Change materials have been discussed. Recent developments on PCM, current challenges and future prospects are discussed in last section.

2. Comparison of PCM with other memories

The recent memory technologies have a great extent of circumstances. DRAMs, Flash memory, SRAM, EPROMs, etc have many limitations such as SRAMs are much expensive, EPROMs require high power and have poor flexibility [6-7]. There is difficult to integrate DRAM[8]. Comparison of PCM with other memory as shown in table 1. So to maintain the forthcoming market of wireless and portable gadgets, a higher performance at lower price is required. Ovonic Threshold Switching is an optimistic device to meet this need. Phase change memory operates in a picosecond timescale, that’s why they are faster than flash memory[9]. The high density of OUM results into the storage of multiple bits of information within a small area. Fast operation speed, better endurance, low power consumption make it a powerful data storage device. PCM is a non-volatile RAM that employs the chalcogenide materials for storage of binary data[10]. In this paper, we focused on one of the most prominent memory technology – Chalcogenide RAM, with a focus on their device operation.

| Attributes     | PCM   | EPROM | NOR   | NAND  | DRAM |
|----------------|-------|-------|-------|-------|------|
| Software       | Easy  | Easy  | Moderate | Hard | Easy |
| Read latency   | 50-100 ns | 200 ns | 70-100 ns | 15-50 µs | 20-80 ns |
| Write bandwidth| 1-15+MB/s | 13-30 kB/s | 0.5-2MB/s | 10+MB/s | 100+MB/s |
| Erase          | No    | No    | Yes   | Yes   | No   |
| Non-volatile   | Yes   | Es    | Yes   | Yes   | No   |
| Endurance      | $10^{19}$ | $10^{7}$-$10^{9}$ | $10^{7}$ | $10^{17}$ | Unlimited |
| Granularity    | Small/Byte | Small/Byte | Large | Large | Small/Byte |

3. Modelling and device function

PCM contains PCM lance cell that includes mainly phase change materials, metal electrode, resistive plug, transistor. As manufactured, the material is in high conductance state. It is because the action temperature of the metal linked layers is abundant to crystallize. At beginning, programming region is melted then a large electrical current pulse applied for a short duration to quench it into amorphous state. Then, a region of amorphous stain material is formed in PCM cell. This amorphous region is in sequence with low resistance state of PCM and successfully governs the resistance of memory cell between the top electrode and lower electrode.

An electrical pulse or laser pulse is applied to temper the active region at a temperature between the crystallization temperature and the melting temperature for a long duration to get back crystalline state. To read the state of programming region, cell resistance is determined by applying an electrical current [11]. The mobile phase change material is intermediate between heater and top electrode. Fig 1 shows PCM Lance Cell and SET and RESET Pulse. When an electrical pulse or laser pulse is applied to the PCM lance cell, the joule heating of mushroom shaped volume of materials occurs. When there is a change of phase from amorphous to crystalline, then SET Pulse is achieved which corresponds to logic ‘1’. RESET pulse is achieved when state changes from crystalline to amorphous and that corresponds to logic ‘0’ [12] given to cell not by energy imparted to cell.

RESET resistance is affected by pulse current amplitude. The important characteristic of phase change memory is Ovonic Threshold Switching. The electrical resistance of two phases differs in magnitude. On applying electrical pulse, as the bias reaches a certain voltage Vth, there is a drastic and sudden change from low conductance to high conductance which results into ovonic threshold switching phenomenon[11-12]. The set process is dependent on ovonic threshold switching. On applying electric field across the amorphous region, approaching the threshold value, conductance of the amorphous region increases. This ovonic threshold switching phenomenon, is prime to successful set action of the memory. The ovonic threshold switching effect raise the conductance of these materials to the dynamic resistance and authorize set programming. The thermal conductivity, the device structure and resistivity governs the reset current. Basically, the functioning speed of PCM is governed by set action time as it takes limited time to change amorphous region into crystalline region.
Phase change materials

To store the binary data, OUM devices uses chalcogenide materials. The semiconducting glasses made using VI group elements of periodic table are chalcogenides[13]. In optical recording techniques, the chalcogenide glasses which have characteristic property of phase transition perform a keyrole[14]. PCM employs the orders of magnitude large difference between the conductivity of amorphous and crystalline phase in these materials[15]. The two different solid phases of materials are amorphous and crystalline. Due to differences in their structure, they have distinct electrical and optical properties[16]. These distinct properties can be considered to store information in technological practice. To prepare amorphous stain, a pulsed sharp laser beam is applied for a short time. Thereafter, the temperature of phase change layer becomes higher than melting point. On removing pulse, the liquefied region is quenched to the amorphous state. The written mark can be read out by studying the difference in the reflectance.

Binary compound Ge-Te is such a phase change material that has good thermal stability than Ge-Sb-Tl based PCM material[15] and also reduces energy consumption[17].GeTe dopped with carbon is a novel phase change material as it provides both good data retention and also good programming characteristics[18]. Se$_{82-x}$Te$_{18}$Sb$_x$ (x=0,4,8,12) based phase change material allows low thermal conductivity, low melting point[19].On increasing Sb to Se-Te system ,activation energy and glass transition temperature increases. This is favourable for making memory with good characteristics. Ternary compound such as Ge$_2$Sb$_2$Te$_5$ is widely used in DVDs and Ge$_8$Sb$_2$Te$_{11}$ in Blu-ray discs. Now-a-days, the structure of CDs, DVDs, Blu-ray disc remains same as the earlier ones, only the variation occurs in thickness and composition of phase change material. Carbon nanotubes based material gives boost to latent energy storage[20]. Recently neural network based phase change material are fabricated to enhance memory performances[21]. Quarternary compounds based materials are becoming new trends for the storage of data in memory.

4. Current challenges and future prospects

The challenges include the reduction of heating in the PCM lance cell. More heating may cause damage to PCM lance cell. Suitable phase change materials are required so as to increase the rate of crystallization and amorphization. Reduction in SET and RESET current is needed. Synthesis of these materials is still a challenge. These materials should have economical manufacturing process. Many more companies are looking forward for MLC(Multilevel cell) for PCM. However, even though vastly optimistic, C-RAM needs to enhance its cost-effectiveness as compared to DRAM, NAND, SRAM and Flash. Furthermore, the feasibility of expanding new compulsive application PCM-based has not been seriously displayed yet.

Researchers suggest that phase change materials can be scaled to nanometer sizes that exhibit same phase change behaviour[22]. Manufactures focus on composite electrode and advanced phase change material to enhance the thermal efficiency of devices[22].Challenge is to reduce the device size so as to minimize power consumption. However, there is need to innovate fast switching materials so as to increase data rate to achieve a memory device, capable of non-volatile operations beyond gigahertz data-transfer rates.
5. Conclusion

In the present paper, the recent developments of phase change memory have been outlined. Phase change memory offer several important properties for high endurance, fast operation speed, data retention characteristics, high density. Because of the features of PCM, many industries are looking forward to Ovonic Unified Memory that might replace flash memory. It is potentially competitive with volatile memory technologies. Current challenges and future prospects of PCM have also been discussed. The principal aim of today PCM developers is to increase the density of memory and decrease energy and power utilization. Another prime feature is the enhancement of data retention property to label embedded memory application. A prime feature for OUM success, is as apparent the evolution of definitive, good-controlled fabricating process. To achieve such ambitious goals, latest PCM materials is expected to be developed in coming days.

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