Description of self-synchronization effects in distributed Josephson junction arrays using harmonic analysis and power balance

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Power generation and synchronization in Josephson junction arrays have attracted attention for a long time. This stems from fundamental interest in nonlinear coupled systems as well as from potential in practical applications. In this paper we study the case of an array of junctions coupled to a distributed transmission line either driven by an external microwave or in a self-oscillating mode. We simplify the theoretical treatment in terms of harmonic analysis and power balance. We apply the model to explain the large operation margins of SNS- and SINIS-junction arrays. We show the validity of the approach by comparing with experiments and simulations with self-oscillating es-SIS junction arrays.

Josephson junctions (JJs) are well known to be able to generate microwave power. Their dynamics can also phase-lock either to an external microwave bias or to the self-generated signal from an array of junctions 1. A common configuration is to couple a number of JJs through a distributed transmission line resonator 2. Both overdamped 2 and underdamped 3 junctions in this configuration are known to be able to mutually synchronize. Long arrays of JJs coupled to a transmission line driven by external microwave bias are known to exhibit properties, which can only be explained by the collective dynamics of the JJ-transmission line system 4, 5.

Coupled Josephson junction arrays have been modelled by semiclassical dynamical simulations 7, 8, 9, 10, or in terms of lengthy perturbation analysis 11. The dynamics of coupled arrays is complex making engineering of such devices challenging, often based on trial and error. However, in most practical situations it suffices to study the situation, where all the junctions are phase-locked to a driving signal (either external or self-generated). Recognizing this, we develop a simplified modelling technique. We have previously considered the case of external microwave bias, for which we calculated the gain in the limit of very small power self-generation 5. Here we extend the analysis to the case of arbitrarily large gain, and apply it to self-stabilized overdamped JJ arrays. We analyze also the self-oscillating array by means of a power balance equation. Finally, we compare the results of the analytic model with simulations and experiments.

We assume that the tunnel element is effectively voltage biased. This is ensured by a small impedance $|Z(f)| \ll \Phi_0 f / I_c$, connected in parallel with the tunnel element (see Fig. 1(a)). Here $I_c$ is the critical current, $\Phi_0$ is the flux quantum and $f = \omega / 2\pi$ is the drive frequency. We further assume that the dynamics is phase-locked to the driving signal. Below we drop the explicit $f$-dependence from the function notation, since all the quantities are evaluated at that frequency. The condition for phase-locking (to the first Shapiro step) is

$$|\delta I_0| < I_c \left| J_1 \left( \frac{\delta I_0}{I_c} \right) \right|,$$

(1)

where $\delta I_1 = |Z| I_1 / \Phi_0 f$, $\delta I_0 = (I_b - \Phi_0 f / R)$ and $J_1$ is the Bessel function of the first kind. Here $I_b$ is the DC bias current, $R$ is the DC resistance in parallel to the junction and $I_1$ is the amplitude of the driving signal.

Using the assumptions above, we get the fundamental frequency component of the Josephson current $I_{f1}$ (see Fig. 1(a)) 3. Within harmonic approximation the voltage across the tunnel element is $V_{f1} (I_1) = Z (I_1 - I_{f1} I_1)$, where the phase is referred to the driving signal $I_1$. We define dynamical impedance $Z_d (I_1) = R_d + i X_d = V_{f1} (I_1) / I_1$, in series with $\omega L_d \gg X_d$, where $L_d$ is the transmission line inductance per junction length. We neglect $X_d$, assuming $X_d < \omega L_d$. The effects discussed here follow from the real part $R_d (I_1) = \text{Re} \left( Z_d (I_1) \right) = \text{Re} \left( Z \right) - \text{Re} \left( Z I_{f1} / I_1 \right)$. $\text{Re} \left( Z \right)$ is the real part of the linear impedance parallel to the junction, and $\text{Re} \left( Z I_{f1} / I_1 \right)$ is due to the Josephson effect.

Knowing $R_d$, various properties of arrays are readily obtained. We consider first the case of external microwave drive (Fig. 1(b)). With $|R_d| \ll \omega L_d$ the (nonlinear) attenuation per junction is

$$\eta (I_1 (x_k)) = - \frac{1}{2} R_d (I_1 (x_k)) / Z_0,$$

(2)

where $Z_0$ is the transmission line impedance. If $R_d > 0$, the junction attenuates and if $R_d < 0$, it amplifies the driving current. This is a generalization from the theory of low-loss transmission lines (e.g. 12). The current distribution $I_1 (x_k)$ along an array ($x_k$ being the position of the $k$:th junction) can be recursively obtained from $I_1 (x_{k+1}) = I_1 (x_k) \exp \left( \eta (I_1 (x_k)) \right)$. This also generalizes our previous results 5 to arbitrarily large gains.
junctions, if leakage resistance can typically be neglected. This is also a good approximation for externally shunted SIS (es-SIS)
due to intrinsic damping \[4, 13\]. With the above notation, self-compensation occurs if the amplitude of
the propagating signal is initially between \(I_{\text{min}}\) and \(I_{\text{max}}\) such that (see \[2, 11\])

\[
\eta(I_1) = 0, I_1 = I_1', \quad \eta(I_1) > 0, I_{1,\text{min}} < I_1 < I_1', \quad \eta(I_1) < 0, I_1' < I_1 < I_{1,\text{max}}.
\]
In addition, $I_{1,\min}$ and $I_{1,\max}$ need to satisfy $\mathbf{I}_1$. Then $I_1(x)$ approaches the stable point $I'_1$ towards the end of the transmission line. Fig. 2 shows the maximum step width $2\delta I_{0,\max}$ and pump current margin $I_{1,\max} - I_{1,\min}$, within which the array self-stabilizes the microwave current. Typical values of the design parameter $\Phi_0f/I_cR$ of overdamped arrays vary between 0.5 and 2. In this range, the maximum self-stabilized step width varies between 0.2$I_c$ and 0.6$I_c$. Using the parameters of Fig. 2 in $\mathbf{I}_2$ ($\Phi_0f/I_cR \approx 1.3$), our model predicts $2\delta I_{0,\max} \approx 400 \mu A$ and $20\log_{10}(I_{1,\max}/I_{1,\min}) \approx 18$ dB, which are in excellent agreement with the experimental data ($2\delta I_{0,\max} \approx 400 \mu A$ and $20\log_{10}(I_{1,\max}/I_{1,\min}) \gtrsim 10$ dB).

In SIS and es-SIS junctions, $\eta$ does not change sign as function of $I_1$ (see the second term in $\mathbf{I}_5$). Therefore the tendency for power self-stabilization is not present. The much smaller attenuation of the capacitive environment makes phase-locking of all junctions possible in this case for a reasonable number of junctions $\mathbf{I}_6$.

Finally, to study self-oscillating arrays, we prepared arrays embedded in open-ended $\lambda/2$ or $\lambda$ microstrip resonators. The circuits were fabricated using the Nb/Al/AlOx/Nb fabrication line of VTT \cite{15} and measured at 4.2 K. The resonators were 800 $\mu$m or 1600 $\mu$m long Nb strips having a resonant frequency $f$ nominally about 80 GHz. The resonators had 2-28 es-SIS JJ:s centered around the node(s) of the standing wave. A photograph of one of the structures is shown in Fig. 3(a). Measured IV curves are shown in Fig. 3(b).

The AC current distribution in the resonators is given as $I_1(x) = I_0 \sin(2\pi x/\lambda)$. The excess loss is given as $P_{ex} = \omega L_{0} I_0^2/2Q$, where $L_{0}$ is the total inductance of the resonator and $Q$ is the intrinsic Q-value. Using Eqs. $\mathbf{I}_4$,\(\mathbf{I}_5\) the power balance stands $\frac{1}{2} \sum_{k=1}^{N} (L_{eff}/L_s)^2 R \times (I_0 \sin(2\pi x_k/\lambda))^2 - 2\Phi_0f \times \delta I_0 = \pi f L_{0} I_0^2/2Q$. This is solved for $I_0$. For $\delta I_0 < 0$, no physical solution exist, since $R_{d,C}$ can be negative only for positive $\delta I_0$. We get

$$I_1(x) = \sqrt{\frac{N\Phi_0f \times \delta I_0}{2\pi f L_{0} I_0^2/2Q + (1/2) (L_{eff}/L_s)^2 R \sum_{k=1}^{N} \sin^2(2\pi x_k/\lambda)}} \sin \left(\frac{2\pi x}{\lambda}\right).$$

$$\delta I_0 < I_c \left| \frac{I_1(x_k)}{2\pi \Phi_0 f^2 C} \right|.$$  

Here we have rewritten $\mathbf{I}_1$ for convenience. For the resistive environment also the negative side of the step $\delta I_0 < 0$ appears.

The solutions for which all junctions are phase-locked to the driving signal appear in the IV curves of Fig. 3 as steps with voltage $\langle V \rangle = N\Phi_0f$ (for simplicity we omit the small dependence of $f$ on the bias point due to $X_d$). This happens for the solutions of $\mathbf{I}_7$ that satisfy $\mathbf{I}_8$ for all $x_k$. Solutions where no phase locking occurs are described by a resistive IV curve determined by the shunt resistors. We assume here that these are the only types of solutions.

The theoretical IV curves obtained in this way are shown as solid lines in Fig. 3(b). The fitting parameters are the Q-value and the resonant frequency $f$. The Q-value was fitted to the case with $N = 2$, for which the effect of the parameter spread is the smallest. The result $Q = 420$ was used for all the arrays. This corresponds to a transmission line loss of $\alpha = \pi/\lambda Q \approx 8.7$ dB $\approx 41$ dB/m, very close to that of an earlier measurement (50 dB/m) \cite{14}. The loss is dominated by the dielectric (PECVD SiO$_2$) loss. The resonance frequencies for all devices were very close to the nominal value 80 GHz. Other parameters were obtained independently either from the IV curves or from the known circuit and material properties.

The fact that steps appear only for $\delta I_0 \gtrsim 0$ as expected is clarified for $N = 2$ in Fig. 3(b). The step widths are close to the values predicted by the theory, though smaller. The difference is explained by parameter spread (the shunt resistances varied up to $\pm 5\%$ within a chip, changing the effective bias point $\delta I_0$ by tens of $\mu A$).
FIG. 3: (a) An experimental array with a $\lambda/2$-resonator having $N = 8$ JJ:s. Typical circuit parameters are $I_c = 280 \mu A$, $C = 59$ pF, $R = 0.26 \Omega$, $L_s = 3.4$ pH, $L_t = 0.2$ pH, $L_{00} = 5.7$ pH and $f = 80$ GHz. (b) Current-voltage curves measured from the resonators with $N$ JJ:s (small red circles). The device with $N = 2 \times 14$ JJ:s is based on a $\lambda$-resonator. Others are based on $\lambda/2$ resonators. The voltage is scaled to that of a single junction and curves are lifted by 40 $\mu V$ with respect to the one below for clarity. The analytic curves are shown by solid black lines and the simulated points are denoted by large blue diamonds.

To further study the validity of the model we performed time-domain simulations using Stewart-McCumber model for the junctions and a distributed model of a dissipative transmission line to describe the resonator sections. We found good agreement with the analytic model. The model step widths are well reproduced, though the small bending of the plateau due to finite $X_d$ is not accounted for by the single-frequency model. As assumed, in all cases the numerical solutions were such that either all the junctions were synchronized or nonsynchronized. However, for simulated devices having even larger number of JJ:s per resonant node than our experimental devices, we found also partially synchronized states. Also in these cases the bias range of the completely synchronized state was correctly predicted by the analytic model.

In summary, we have developed a simple analytic technique for quantitative modelling of distributed JJ systems with a strong preferred frequency set by either an external RF signal or a high-Q resonance. The results clarify differences between underdamped and overdamped systems, a source of some controversy recently. The model provides an intuitive way of understanding the dynamics of phase locking and power generation in JJ arrays. The model predicts results in agreement with simulations and experiments and it can be used to engineer practical devices, such as mm-wave local oscillators and JJ voltage standards. An interesting alternative voltage standards is a JJ array with no external microwave source, locking the self-generated signal directly to a frequency reference. The power balance equation is easily generalized to externally loaded systems to calculate the power output. Other types of physical systems can be analyzed similarly. For example, the exact dual for systems described here is a distributed parallel array of quantum phase slip junctions where the ”conductor gain” of JJ arrays is replaced by a ”dielectric gain”.

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