A Three Demultiplexer C-Band Using Angled Multimode Interference in GaN–SiO\(_2\) Slot Waveguide Structures

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Abstract: One of the most common techniques for increasing data bitrate using the telecommunication system is to use dense wavelength division multiplexing (DWDM). However, the implementation of DWDM with more channels requires additional waveguide coupler devices and greater energy consumption, which can limit the system performances. To solve these issues, we propose a new approach for designing the demultiplexer using angled multimode interference (AMMI) in gallium nitride (GaN)–silica (SiO\(_2\)) slot waveguide structures. SiO\(_2\) and GaN materials are selected for confining the infrared light inside the GaN areas under the transverse electric (TE) field mode. The results show that, after 3.56 mm light propagation, three infrared wavelengths in the C-band can be demultiplexed using a single AMMI coupler with a power loss of 1.31 to 2.44 dB, large bandwidth of 12 to 13.69 nm, very low power back reflection of 47.64 to 48.76 dB, and crosstalk of \(-12.67\) to \(-15.62\) dB. Thus, the proposed design has the potential for improving performances in the telecommunication system that works with DWDM technology.

Keywords: demultiplexer; AMMI; slot-waveguide; GaN; C-band

1. Introduction

Optical demultiplexing devices are key elements in the optical telecommunication system that works with dense wavelength division multiplexing (DWDM) and serve the purpose of increasing the data transfer capacity for the enlarged number of end-users [1]. DWDM apparatus being investigated constantly includes arrayed waveguide gratings (AWGs) [2], multimode interference (MMI) devices [3], photonic crystal fiber [4], and angled MMI (AMMI) [5].

Multimode interference device operation is based on the self-imaging principle, in which the outline of the input field is replicated along the propagation axis of the waveguide at periodical points [6]. MMI devices are incorporated into photonic integrated circuits (PIC) thanks to their low excess loss, large optical bandwidth, and plain structure [7,8]. AMMI is a form of MMI device in which the single-mode access waveguides are tilted in the desired angle with respect to the propagation axis of the multi-mode MMI core. The tilted inputs/outputs allow the design of a single AMMI demultiplexer with more channels (>2) compared with one MMI coupler. Power back reflection is propagating light that reflects back into the input source owing to the refractive index mismatch at the boundaries between different materials and a side effect of the self-imaging effect [9]. In terms of losses, MMI or AWG are inferior to the AMMI in the case of back reflection that is inherent in their architecture [10]. Slot waveguide structure as introduced by Almeida et al. [11] is a novel waveguide mechanism that is applied for splitters [12], AMMIs [13], and other applications and can be utilized using an already existing and sufficient manufacturing process in the silicon on insulator (SOI) method [14]. In a slot waveguide, a layer of a low refractive index material (slot area) is surrounded by two layers of high...
refractive index materials and the guidance of light is the result of the total internal reflection (TIR) effect [15]. In addition, because of the strong power confinement inside the slot area, there are no confinement losses in the slot waveguide structure [16].

Gallium nitride (GaN) is a transparent semiconductor material with notable features such as superior electrical properties, resistance to temperature [17], and a wide spectral range. Recent studies show the gain of using GaN-based conventional waveguides for optical splitters [18] or couplers [19]. Silica (SiO$_2$) is a clear glass-type low-absorption insulator that is not moisture-absorbing nor permeable to gasses [20] and has low reactivity with acids. MMI devices based on slot waveguides technology are sensitive to the contrast in the refractive indices, which can affect their performance [21]. Hence, it is better to use GaN–SiO$_2$ slot waveguide, which has a smaller difference in the refractive index compared with other materials combinations (e.g., silicon–SiO$_2$ [22]).

The horizontal slot waveguide manufacturing process applied by lithography patterning and a multilayered deposition or thermal oxidation is superior to the vertical slot waveguide in terms of lower interface roughness, which can lead to a decrease of scattering loss. The horizontal structure also has little to no constraints on the waveguide thickness with layers up to a nanometer precision [23,24]. SOI nanophotonic devices may be fabricated on a BOX layer on top of a Si wafer substrate [25] with GaN–SiO$_2$ stacking such as demultiplexers [15] or splitters [21]. The multilayering process of the waveguide and the protective cladding consists of an e-beam lithography patterning, thin film deposition of GaN and SiO$_2$ using plasma enhanced chemical vapor deposition, alongside cleaning and etching of layer surfaces [26–28].

In this paper, we introduce an innovative design of a three-channel AMMI based on the GaN–SiO$_2$ slot waveguide structure. The device functions as a 1×3 demultiplexer with operating wavelengths of 1.53, 1.5475, and 1.565 μm. Numerical investigations were carried out on the geometrical criteria of the device to find the optimal angle for the input/outputs waveguide taper. Moreover, the self-imaging effect in the MMI coupler and strong power confinement inside the GaN slot waveguide areas are achieved for a highly desired power at the outputs.

The simulations and the optimizations were carried out using the full vector beam propagation method (FV-BPM) [29] alongside MATLAB script codes.

The novelty of using GaN–SiO$_2$ slot waveguide technology is the ability to manipulate the light propagation in the MMI section for obtaining a lower beat length for small channel spacing and low back reflection (BR) at the input source.

The proposed device in this work can be used in a DWDM system for increasing the communication data bitrate.

2. Slot Waveguide AMMI Structure and Theoretical Aspect

The horizontal slot waveguide structure consists of a slot region that rests between two slab layers and its schematic sketch at the x–y plane is shown in Figure 1. The slot area is represented in gray color, which is SiO$_2$ material with refractive index $n_S$ and thickness layer of $H_S$. The slab layers are shown in green color for GaN material with refractive index $n_H$ and thickness layer of $H_H$. The surrounding cladding area is shown in gray color, which is SiO$_2$ material. The slot waveguide width is $W$ and the total thickness of the slot waveguide is $H_T$.

Figure 2 shows the 1×3 AMMI demultiplexer structure at the x–z plane. The 1×3 demultiplexer is based on one MMI coupler with 1/3 tilted angle input/output waveguide tapers accordingly. The MMI coupler width is $W_{MMI}$ and the length is $L_{MMI}$. Each input or output taper waveguide is titled with an angle $\theta_t$ and is tapered from $W_{t,1}$ (single mode width) to $W_{t,2}$ (multimode width) over a length of $L_t$.

The optimal geometrical parameters of the AMMI demultiplexer are $W_{MMI} = 20.5$ μm, $L_{MMI} = 3.56$ mm, length of the taper of 120 μm with width variation between 0.5 and 42.4 μm, and an optimal tilt angle of the tapers of 0.3 rad. The output channels are tagged as Ch$_i$ and their lengths along the propagation direction (z-axis) are given by $L_i$ ($i = A, B, C$), as summarized in Table 1. The gap
between channel A and channel B was set to 127 μm and the gap between channel B and channel C was set to 132 μm.

![Schematic sketch of slot waveguide structure at the x-y plane.](image1)

**Figure 1.** Schematic sketch of slot waveguide structure at the x-y plane.

**Figure 2.** Schematic sketch of the 1 × 3 angled multimode interference (AMMI) demultiplexer at the x-z plane.

![Schematic sketch of the 1 × 3 angled multimode interference (AMMI) demultiplexer at the x-z plane.](image2)

**Table 1.** Optimized design length values for the three-channel angled multimode interference (AMMI).

| Ch_i | λ_i (μm) | L_i (mm) |
|------|----------|----------|
| Ch_A | 1.53 | 3.44 |
| Ch_B | 1.5475 | 3.27 |
| Ch_C | 1.565 | 3.095 |

In a slot waveguide, the discontinuity of the electric field in the boundaries between the high and low refractive index materials is the reason for obtaining strong light confinement. The amplitudes ratio (AR) of the electric field inside/outside the boundary is a function of the refractive indices of
the materials inside/outside the boundaries, respectively. This relation is described in Equation (1), where \( a \) represents the value of the axis for the boundary [11].

\[
\text{AR} = \frac{E_{x|a=+}}{E_{x|a=-}} = \frac{n_H^2}{n_S^2}, \quad (1)
\]

In our design, the AR and the refractive indices (n\(_S\) and n\(_H\)) for the chosen slot waveguide materials as a function of the operating wavelengths are shown in Table 2.

| \( \lambda (\mu\text{m}) \) | n\(_H\) | n\(_S\) | AR   |
|-----------------|--------|--------|------|
| 1.53            | 2.3173 | 1.44   | 2.589|
| 1.5475          | 2.3169 | 1.44   | 2.588|
| 1.565           | 2.3165 | 1.44   | 2.587|

The access waveguides are placed along the propagation region to satisfy the periodic self-imaging and TIR effects. These mechanisms allow guiding the input signal to the desired output channel, which is located at different points along the z-axis (L\(_i\)) and can be estimated by Equation (2) [5].

\[
L_i \approx \frac{4n_{\text{eff}}W_{\text{eff}}^2}{\lambda_i}, \quad i = A, B, C \quad (2)
\]

where \( n_{\text{eff}} \) is the effective refractive index of the major mode; \( \lambda_i \) is the operating wavelength; and \( W_{\text{eff}} \) is the effective width of the MMI coupler, which incorporates the Imbert Fedorov effect, as stated in Equation (3) in the case of transverse electric (TE) mode [30].

\[
W_{\text{eff}} = W_{\text{MMI}} + \frac{\lambda_i}{\pi} (n_{\text{eff}}^2 - n_S^2)^{-1/2} \quad (3)
\]

The crosstalk is given by Equation (4).

\[
\text{Crosstalk (dB)} = 10 \log \left( \frac{P_m}{P_n} \right) \quad (4)
\]

where \( P_n \) is the desired power from the transmission channel and \( P_m \) is the interference power from the neighboring channels.

The insertion loss (IL) at the channels output contributes to the examination of the device performance and is given by Equation (5).

\[
\text{IL (dB)} = -10 \log \left( \frac{P_{\text{out}}}{P_{\text{in}}} \right) \quad (5)
\]

where \( P_{\text{in}} \) is the input taper power and \( P_{\text{out}} \) is the power at the output taper channel.

3. Simulation Results

The slot waveguide structure was solved using FV-BPM simulations combined with MATLAB script codes, which were used to analyze and set the optimal geometrical parameters. From these simulations, Figure 3a,b and Figure 4a,b, the optimal slot thickness parameters are \( H_H = 300 \text{ nm}, H_S = 75 \text{ nm}, H_T = 675 \text{ nm}, \) and \( W = W_{t,1} = 500 \text{ nm} \). These geometric values were chosen to support strong light confinement in the GaN–SiO\(_2\) slot waveguide during light propagation and are the result of optimizations performed using the FV-BPM solver.
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### Simulation Results

The slot waveguide structure was solved using FV-BPM simulations combined with MATLAB script codes, which were used to analyze and set the optimal geometrical parameters. From these simulations, Figures 3a, b and 4a, b, the optimal slot thickness parameters are $H_H = 300$ nm, $H_S = 75$ nm, $H_T = 675$ nm, and $W = W_t,1 = 500$ nm. These geometric values were chosen to support strong light confinement in the GaN–SiO$_2$ slot waveguide during light propagation and are the result of optimizations performed using the FV-BPM solver.

Figure 3a, b shows the normalized power as a function of the GaN ($H_H$) and SiO$_2$ ($H_S$) layer thickness at 1.53 μm wavelength. From Figure 3a, b, the tolerance ranges for the GaN and SiO$_2$ thickness layer that ensure strong power confinement (above 90% of the normalized power) inside the slot waveguide can be found and their values are 270–328 nm and 10–119 nm, respectively.

Figure 4a shows the fundamental quasi-TE mode solution at the x–y plane for a wavelength of 1.53 μm. It can be observed that the light is highly confined inside the GaN layers (red color). The red color in the slab (GaN) areas indicates a strong intensity near the boundaries between the slot and the slab areas, as expected from theory. Figure 4b shows the vertical cut at $x = 0$ nm and the normalized power level for the slot waveguide thickness over the y-axis. The wide tolerance range of the high-power level over the y-axis (around 700 nm) can be noticed in Figure 4b. This range can be very helpful for utilizing better adjustments between the transmission signal and the waveguide input taper. Figure 4c shows the existence of a quasi-TM polarized mode solution at the x–y plane of the suggested device for a wavelength of 1.53 μm. This solution shows strong light confinement inside the slot area (SiO$_2$) and further investigations and simulations can be carried out on the quasi-TM polarization with relevant geometry adjustments. However, in this study, the proposed device was set to the quasi-TE polarization for obtaining a single-mode transmission.
The location of a channel directly affects the MMI (Multi-Mode Interferometer) with losses of 1.4 dB for the same error. ±0.3 rad. In this figure, it can be seen that channels A and B are less sensitive to the fabrication tilt error compared to channel C. The maximum power at the output tapers and very low BR were achieved in our design. The optimal taper length was set to achieve maximum power at the output tapers and very low BR. The optimal value was chosen to satisfy the highest output power for all three channels, with losses of 0.08, 0.05, and 0.1 dB, respectively.

The proposed device was set to achieve maximum power at the output tapers and very low BR. In our design, the optimal taper length was set to achieve maximum power at the output tapers and very low BR. The chosen value for θt was set to achieve maximum power at the output tapers and very low BR. In our design, the optimal value was set to achieve maximum power at the output tapers and very low BR. In this figure, it can be seen that channels A and B are less sensitive to the fabrication tilt error of ±0.01 rad (±0.5 degree) with losses of 0.4 and 0.8 dB, respectively, and channel C is more sensitive, with losses of 1.4 dB for the same error.

Figure 5 shows the normalized power as a function of the tilt angle of the input and output tapered waveguides (θt) for the operating wavelengths. The chosen value for θt was set to achieve maximum power at the output tapers and very low BR. In our design, the optimal value was set to be 0.3 rad. In this figure, it can be seen that channels A and B are less sensitive to the fabrication tilt error of ±0.01 rad (±0.5 degree) with losses of 0.4 and 0.8 dB, respectively, and channel C is more sensitive, with losses of 1.4 dB for the same error.

Figure 5. Normalized output power as a function of θt for the operating wavelengths.
Figure 6 shows the optimization process for the MMI coupler width (W_{MMI}) and it is based on an estimation performed using Equation (3) with FV-BPM simulations. The W_{MMI} optimal value was selected to be 20.5 µm to ensure the highest output power for all channels and the best tolerance ranges for fabrication errors. In this figure, it can be seen that channels A, B, and C are robust to the fabrication error for the MMI coupler width of ±20 nm from the optimal value with losses of 0.47, 0.75, and 0.96 dB, respectively.

![Figure 6.](image)

**Figure 6.** Normalized output power as a function of W_{MMI} (blue color for Ch_{A}, black color for Ch_{B}, and red color for Ch_{C}).

Figure 7a–c shows the normalized power for each output taper for the operating wavelengths as a function of the locations along the z-axis of the output channels L_{A}, L_{B}, and L_{C}, accordingly, as stated in Table 2. The location of a channel directly affects the power distribution between the channels. The individual outputs are located to satisfy the beat length requirements of the chosen quasi-TE polarization for a chosen wavelength and polarization deviations caused by spatial shift may cause an abrupt power variation in the output, as seen in Figure 7b. Therefore, it is important to clarify that the selected optimal values were chosen to satisfy the highest output power for all three channels. In this figure, it can be seen that channels A, B, and C have a low sensitivity to fabrication error for the MMI coupler length of ±1 µm from the optimal value with losses of 0.08, 0.05, and 0.1 dB, respectively.

Figure 8a–c shows the light propagation of the operating wavelengths from the input taper to the desired output taper (channel) through the MMI coupler. Figure 8a shows the light transmission of 1.53 µm wavelength from the input tilt taper to the output taper Ch_{A} at 3.56 mm. Figure 8b shows the light transmission of 1.5475 µm wavelength from the input tilt taper to the output taper Ch_{B} at 3.39 mm. Figure 8c shows the light transmission of 1.565 µm wavelength from the input tilt taper to the output taper Ch_{C} at 3.215 mm. From these figures, it can be seen that low losses occur between the output tapered channels (blue color) and inside the MMI coupler (blue color).
Figure 7. Normalized output taper power as a function of $L_i$ (a), $L_A$ (Ch$_A$), (b) $L_B$ (Ch$_B$), and (c) $L_C$ (Ch$_C$).
the light transmission of 1.5475 μm wavelength from the input tilt taper to the output taper ChB at 3.39 mm. Figure 8c shows the light transmission of 1.565 μm wavelength from the input tilt taper to the output taper ChC at 3.215 mm. From these figures, it can be seen that low losses occur between the output tapered channels (blue color) and inside the MMI coupler (blue color).

Figure 8. Light intensity profile of the AMMI 1 × 3 demultiplexer at the x–z plane. (a) 1.53 μm, (b) 1.5475 μm, and (c) 1.565 μm.

Figure 9 shows the optical transmittance of the AMMI device for wavelengths in and around the C-band range (1522–1573 nm) as a result of FV-BPM simulations combined with MATLAB script codes. From Figure 9 combined with Equations (4) and (5), the values of the IL, crosstalk, and full width at half maximum (FWHM) for each output channel are obtained as shown in Table 3.

Figure 9. Optical spectrum around the C-band.
Table 3. Values of the insertion loss (IL), crosstalk, and FWHM for the operational wavelengths.

| λ (nm) | 1530  | 1547.5 | 1565  |
|--------|-------|--------|-------|
| Channel Notation | Ch_A | Ch_B | Ch_C |
| Crosstalk (dB) | −12.79 | −12.67 | −15.62 |
| FWHM (nm) | 13.69 | 12.01 | 12.2 |
| Insertion Losses (dB) | 2.44 | 1.31 | 1.36 |

Figure 10a,b show the normalized power as a function of GaN (H_H) and SiO₂ (H_S) layer thickness for the operating wavelengths. The chosen values were set to achieve maximum power at the output taper. In Figure 10a, it can be seen that channels A, B, and C have a sensitivity to fabrication error for the GaN thickness of ±10 nm from the optimal value with losses of 1.05, 1.17, and 2 dB, respectively. In Figure 10b, it can be seen that channels A, B, and C have a sensitivity to fabrication error for the SiO₂ thickness of ±5 nm from the optimal value with losses of 1.15, 1.38, and 2.14 dB, respectively.

![Normalized power as a function of waveguide thickness](image)

Another important property of the device is the BR, which can affect the telecommunication system, especially in the case of reflections that are propagated in the reverse light directly into the laser.
source. The MMI coupler can suffer from reflections because of the effective index mismatch and the self-imaging effect. In our design, reflections can happen at the interfaces between the MMI coupler (GaN–SiO₂) and the SiO₂ cladding. To solve this issue, we have integrated angled input and outputs waveguide tapers that are used to minimize reflections. Using a finite difference time domain (FDTD) algorithm, the power level of light reflections coming from the MMI coupler for each operating wavelength can be found. A monitor was located in the input waveguide taper to collect the light reflections coming from the MMI coupler, as shown in Figure 11a. Figure 11b shows the power BR as a function of cT (optical path length) for 1.53 µm wavelength. The optical path length was set to 700 µm to include all reflections coming from the MMI coupler owing to the self-imaging effect. Table 4 shows that reflection losses can be neglected because of the lowest average power BR (48.03 dB) for the operating wavelengths. This is a major advance compared with a standard MMI coupler that usually suffers from BR losses.

![Figure 11](image_url)

**Figure 11.** Power back reflection: (a) finite difference time domain (FDTD) simulation image and (b) power back reflection as a function of cT at 1.53 µm wavelength.

**Table 4.** Power back reflection loss values for the operating wavelengths.

| Wavelength (µm) | 1.53  | 1.5475 | 1.565 |
|----------------|-------|--------|-------|
| Power Back reflection loss (dB) | 48.76 | 47.85  | 47.64 |

To study the advantages of using AMMI-based GaN slot-waveguide technology, several C-band/O-band AMMI demultiplexers that were previously published and currently used were compared to this work, as shown in Table 5. The attributes compared incorporate waveguide technology (WT), number of channels (NOC), channels spacing (CS) in terms of spectral range, footprint (width × length), maximum insertion losses (IL), best crosstalk (XT), maximum BR, and year of publication.

The proposed design has two main benefits compared with the other AMMI demultiplexer devices: small CS and low BR. These advantages are because GaN–SiO₂ slot-waveguide technology can obtain a small effective index in the MMI section, which leads to a very low BR and low beat length. The BR property can be utilized especially for the communication system that works with a laser source that suffered from BR.
Table 5. Comparison between key characteristics of various AMMI demultiplexer types. SOI, silicon on insulator; WT, waveguide technology; NOC, number of channels; CS, channels spacing; IL, insertion losses; XT, best crosstalk; BR, back reflection.

| AMMI Demultiplexer Type [5,31] | WT          | NOC | CS [nm] | Footprint [mm²] | IL [dB] | XT [dB] | BR [dB] | Year       |
|-------------------------------|-------------|-----|---------|-----------------|---------|---------|---------|-----------|
| SOI Rib                       | 4           | 21  | 0.0145  | ~2              | ~25     | Not stated | 2011 |
| N-rich Silicon nitride Strip  | 4           | 20  | 0.034   | ~1.5            | ~20     | Not stated | 2018 |
| This device Slot-waveguide    | 3           | 17.5| 0.0758  | ~2.4            | ~15     | ~48     | In this paper |
4. GaN Fabrication Losses

GaN (stack) waveguide losses from the fabrication process consisting of scattering loss, free carriers loss, and crystallization level impact are discussed later in this paper and are described in detail by Chen, H [32], and Al-Zouhbi [33].

Propagation losses in the GaN layers are influenced directly from the practical fabrication of the layers and should be considered prior to manufacturing of the suggested device. Some of the contributors to the propagation losses are surface roughness due to surface roughness and free carrier loss [32] alongside the crystallization level of GaN [33]. The scattering loss is expected to be higher than that stated in [32] (~2.5 dB/cm) because of the large device dimensions, despite the large wavelength. The intrinsically n-type doped GaN may possess a high-density n-type free carrier (up to $10^{17}$ cm$^{-3}$), which will contribute to the free carrier loss of more than 1 dB/cm [32]. To lower these losses, surface roughness can be improved by better controlling the dry etching speeds at grain boundaries or by small power applications. Amorphous GaN in comparison with crystalline GaN has the advantages of lower manufacturing costs, better suitability for deposition on various substrates, while relieving the difficulty of lattice matching of the Ga-N bond; in contrast, however, it may additionally have significant material absorption of up to 13 dB/cm [33].

Recently, researchers show the ability to fabricate GaN rib waveguides on Ga$_2$O$_3$ substrate, using a wet-etch process for improving side-wall verticality and reduction of waveguide surface roughness. The propagation loss in the GaN waveguides has been experimentally determined to be 7.5 dB/cm at the C-band spectrum [34]. Using this fabrication technique, the propagation losses can be estimated to be around 0.75 dB/mm. Thus, the fabrication losses for the proposed device in an actual system for the three channels Ch$^A$, Ch$^B$, and Ch$^C$ are estimated to be 2.67, 2.54, and 2.41 dB, respectively.

5. Conclusions

This paper demonstrates a novel and new design of a 1 × 3 AMMI demultiplexer device based on GaN–SiO$_2$ slot waveguide structures operating in the C-band spectrum range. The study of the light coupling between the tilt angled input and output waveguide taper to the MMI coupler using the self-imaging effect shows the ability to obtain a higher power level in the output channels by setting the suitable tilt angle. We proved that it is possible to use a single AMMI coupler to divide three channels with a very low power back reflection (47.64 to 48.76 dB) in comparison with a single standard MMI coupler that can divide only two channels suffering from high power back reflection. These advantages can be utilized for reducing footprint size and total power back reflection for improving communication system performances.

The results show that three operated wavelengths of 1.53, 1.5475, and 1.565 μm can be demultiplexing with low IL of 1.31 to 2.44 dB, good crosstalk of −12.67 to −15.62 dB, and large bandwidth of 12 to 13.69 nm. The quasi-TE fundamental mode solution has a wide range of 700 nm with a high intensity level over the y-axis that can be utilized for reducing coupling losses due to the mismatch modes between the transmission signal to the tilt angle input taper waveguide.

In addition, numerical investigations and optimizations of the key geometrical parameters presented a robust device that has good tolerance ranges for fabrication errors for up to 1.4 dB for the access waveguides tilt angle, up to 0.96 dB for slot waveguide thickness, and up to 0.1 dB for channels’ location, while maintaining a footprint in the range of similar devices of approximately 0.07585 mm$^2$ (width × length). In conclusion, the design has great potential for improving performances in the telecommunication system that works with DWDM technology.

Although only the 1 × 3 wavelength GaN–SiO$_2$ AMMI demultiplexer configuration is considered in this paper, the demultiplexer can operate as a 3 × 1 multiplexer with a reversed direction of the guided light. It is also possible to proceed to manufacture considering the proposed device configuration or to test other combinations of materials.
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