Custom Power Device in Multilevel Inverter for Power Quality Improvement

Geevarghese Mathew Kurian, P. Aruna Jeyanthi, D. Devaraj

Abstract- A dynamic voltage restorer (DVR) is a FACTS gadget, which is utilized fundamentally in transmission lines to remunerate the voltage list and voltage swell that happens on hold. A DVR is a circuit, which made out of intensity electronic parts like diodes and thyristors. It is generally utilized because of its miniature size and proficient activity. This paper proposes a cascaded inverter type DVR to repay voltage hang in the utilities for power appropriation, which is used for country zone advancement. The DVR infuses a voltage arrangement to the framework voltage. The multi carrier PWM strategy is utilized to produce terminating voltage to inverter. The proposed framework decreases the voltage list and complete consonant bending of the conveyance framework. The proposed framework is simulated utilizing the MATLAB/Simulink.

Index terms: Power quality, Multilevel inverter, Total harmonic distortion (THD), Dynamic voltage restorer (DVR), Multi carrier pulse width modulation (MCPWM).

I. INTRODUCTION

A good electric power quality within the allowable standard limits is of great importance in an electric power distribution network [1-5]. Power quality is becoming an important concern nowadays due to many reasons. Quality of voltage taken from the utility or that supplied to the consumer is referred to as voltage quality. The fluctuation of voltage, current or frequency from its optimal value can lead to malfunctioning of the equipment and can be considered as a serious issue in the power quality. Overall, power quality can be represented as a mix of quality of the voltage and current.

Electric power is the cornerstone for the economic progress for a developing nation like India. After the invention of electric power, many experiments are tried to enhance the distribution production, and usage of electricity. Since requirement of electric power is being increasing day by day, an efficient management of energy is required. Power quality is an important factor. Series threat faced by power system today is harmonic pollution. Frequency of supply, voltage, shape of waveform etc. determines the quality of electric power. These problems will oppose the transfer of the electricity. In medium voltage and high power condition the introduction of multilevel inverter has become boons [1-2]. Although this method has some drawbacks [3]. However, these multilevel inverters also have some disadvantages like the output voltage is comparatively less than the input voltage. In addition, operation in high frequency is not possible due to switching loss. Due to switching loss, harmonics cannot be controlled. There are different methods have been used for reducing harmonic contents in the system [6-8]. Also for the transmission of power, output voltage needed to be amplified with the help of transformer. The power quality problem occurs due to the nonlinear load connected in the system. Voltage fluctuations and distortions happen when the power is transferred.

The power quality is being reduced as it transferred to a distance. In rural areas the power generating station and the load centre far apart from each other. The use of FLEXIBLE ac transmission systems (FACTS) in power system provides the advantage of enhancement of quality of power. The transfer capacity conjointly and the quality of power of ac system interconnections are improved [10]. Both Transmission and distribution (T&D) networks forms the most critical part of power grid. As we progress towards micro-grid, it is most important to modernize the T&D networks and convert it to “Micro-grid ready”. For the past three decades the concept of flexible ac transmission systems (FACTS) has been well-popularised. With the enormous advancements in the technology of power electronics in the past few decades there has been a trend towards a new generation of FACTS devices. This novel FACTS technology has led to the transition of transmission networks from the old concept to the “smart” concept[11]. The proposed new DVR control system can support the load voltages-related power quality issues unrelated to the load current profile [12]. According to Wu, Jinn-Chang, seven level inverter structure will improve the efficiency of power quality management[13]. The Dynamic Voltage Restorer (DVR) is an effective method for the enhancement of power quality This System describes the working of a 15 level multilevel inverter with DVR for reduced harmonic distortion.

II. PROPOSED SYSTEM

In this system contains two parts. A charging part and an inverter part. Which is connected to the AC bus. An H-bridge topology is used for this inverter. The charging part consist of a solar panel, which used to charge the inverter battery. Here we use three solar panels, which is located at different places. The solar panel is the source energy to the inverter.
The inverter will feed power to the load. The conventional multilevel inverters also have some frequency distortion problem. The total harmonic distortion of the inverter can be reduced by using this 15 level inverter. As per the IEEE 519 standard, the percentage of total harmonic distortion for high voltages must be less than 8%. We proposed this system for rural area development. The load centres are at a distance from the power generating stations. Hence, we have to transmit the power to some distance. Then the problem of voltage sag and swell arises. For this, we are implementing a DVR in the transmission line at the load side.

![Fig. 1 Proposed system diagram](image)

### III. INVERTER SYSTEM

A 15 level inverter is used here. It consists of seven switches, three diodes and three power supply units. MOSFET is used as the switch. 15 level output is obtained from this inverter which has seven operating modes. As the number of stepped levels increases, the total harmonic distortion is also decreases. The functioning of the inverter can be divided into positive and negative half cycle. Each half cycle has seven working modes. Each mode of this system will produce one step. The output of the entire system becomes more sinusoidal and hence improves the power quality.

| Switching combination | Output |
|-----------------------|--------|
| Ss1 Ss2 Ss3 S1 S2 S3 | V\text{OUT}<V1 |
| ON OF OF O OF O F   |        |
| OFF ON OF O OF O F  | V1<V\text{OUT}<V2 |
| OFF OFF OFF OFF F N  | V2<V\text{OUT}<V1+V2 |
| OFF OFF OFF ON F N   | V1+V2<V\text{OUT}<V3 |
| OFF OFF ON OFF F N   | V1>V2+V3<V\text{OUT} |
| OFF ON ON F N F     | V1+V2+V3<V\text{OUT} |

![Fig. 2. Circuit diagram of proposed system](image)

### IV. DYNAMIC VOLTAGE RESTORER

The dynamic voltage restorer is a high-speed custom power electronic device and it is a type of FACTS device used for series injection of voltage to the network. The block diagram of the DVR is depicted below.

![Fig. 3. Block diagram of DVR.](image)

### V. CONTROLLER UNIT

Various number of modulating techniques are available for manipulating the multilevel inverter. Here we are using the multi carrier pulse width modulation (MCPWM). It is used to produce the seven pulses for the generation of 15 level output of the inverter. Easy implementation and simplicity makes the use of this inverter more preferable. It is shown in the figure below. In this, different triangular carriers are placed by phase disposition mechanism. The advantage is that phase disposition mechanism are uncomplicated and less total harmonic distortion in the system. These triangular carriers compared with a reference sine signal to produce switching signal of the switches. The carrier frequency is the same switching frequency of inverter.
The system can be implemented for the development of rural areas. We can use solar panel as the source of energy to the inverter. This renewable energy can be implemented to face the energy scarcity. The hardware can be adapted to the rural areas, which is far away from the generating stations, and to the isolated areas. By using PI controller instead of the DVR, we can reduce the percentage of THD to an extreme reduced level.

REFERENCES
1. Geervarghes Mathew Kurian, Prof P. Aruna Jeyenth, Prof. D. Devaraj, P.G. Anilkumar, Dept. of EEE, Kalasalingam University, “RTC based solar power multi-level Inverter”, IEEE transactions on 2018.
2. Cheng-Han Hsieh, Tsong-Juu Liang, Fellow, IEEE, Shih-Ming Chen, and Shih-Wen Tsai, Design and implementation of a novel multilevel DC to AC inverter, IEEE Transactions on Industry Applications 2016.
3. Krishna Kumar Gupta ; Alekh Ranjan ; Pallavee Bhatnagar ; Lalit Kumar Sahu, Shailendra Jain, "Multilevel inverter topologies with reduced device count: a review" IEEE Transactions on Power Electronics, 2016, Page: 135 – 151.
4. Alian Chen, Xiangning He, “Research on the hybrid clamped multilevel inverter topologies”, IEEE Transactions on Industrial Electronics, 2006.
5. Sid-Ali Amamra, Kamal Meighiche, Abderrezzak Cherifi, Bruno Francois, Multilevel inverter for Renewable Energy Grid Integration, IEEE Transactions on Industrial Electronics, 2017.
6. Ehsan Najafi, Abdul Halim Mohamed Yatim, Design and Implementation of a New multilevel inverter topology”, IEEE Transactions on Industrial Electronics, 2012.
7. Vincent Roberge, Mohammed Tarbouchi, and Francis Okou, “Strategies to Accelerate Harmonic Minimization in Multilevel Inverters Using a Parallel Genetic Algorithm on Graphical Processing Unit”, IEEE transactions on power electronics, 2014.
8. Sze Sing Lee, Bing Chu, Nik Rumzi Nik Idris, Hui Hwang Goh, and Yeh En Heng, IEEE members, “Switched-Battery Boost-Multilevel Inverter with GA Optimized SHEPWM for Standalone Application”, IEEE transactions on industrial electronics, 2015.
9. Zainal Salam, Ahmed Majed, Abdul Moed Amjad, University Technology Malaysia, “Design and implementation of 15-level cascaded multi-level voltage source inverter with harmonics elimination pulse-width modulation using differential evolution Method”, IET research article, 2015.
10. Geervarghes Mathew Kurian, Jerlin Mathew, Prof P. Aruna Jeyenth, Prof. D. Devaraj Dept. of EEE, Kalasalingam University, “Standalone Multilevel Inverter Using DVR for Power Quality Improvement”, IEEE Conference INCOS 2019.
11. Fang Z. Peng, “Flexible AC transmission systems (FACTS) and resilient AC distribution systems (RACDS) in smart grid”, proceedings of IEEE, 2017.
12. Manik Prdhan, Mahesh K. Mishra, “Dual P- Q theory based energy optimized Dynamic voltage restorer for power quality improvement in a distribution system”, IEEE transactions on industrial electronics, 2019.
13. Wu, Jinn-Chang, and Chia-Wei Chou. "A Solar Power Generation System with a Seven-Level Inverter", IEEE Transactions on Power Electronics, 2014.

VI. SIMULATION RESULTS
To confirm the ability of the proposed scheme, the system is simulated using the MATLAB/Simulink. For the simulation purpose, we are using three different DC sources instead of solar panel inputs. The input voltage source of voltages is 20V, 40V and 60 V. We can adjust the voltage levels. As the number of stepped levels increases then the harmonics will be decreases. In this system used low frequency switching mechanism, so the harmonics issues will be reduced. By implementing the proposed inverter, we can reduce the percentage of total harmonics to 6.9% which is shown in figure 5. However, it is noticed that the sag and swell are still there in the transmitted voltage. The voltage injected by the DVR compensates it. The THD given by the DVR is 5.04%, which is nearly at the IEEE standard 519.

VII. CONCLUSION
The paper describes the application of a 15 level multilevel inverter. It is used to eliminate the harmonics in the system. As the levels increases, the percentage of total harmonics decreases. The THD found at the multilevel inverter will be 6.9%. As the power, quality is being reduced as the power is transferred to a distance, it will be improved by the use of DVR in the load side. The voltage sag and swell will be reduced. The THD measured in the load side will be 5.04%. The proposed system satisfies the IEEE 519 standard.

FUTURE SCOPE
The system can be implemented for the development of rural areas. We can use solar panel as the source of energy to the inverter. This renewable energy can be implemented...
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Geevarghese Mathew Kurian Completed B-tech in Electrical and Electronics engineering in 2007 from Mahatma Gandhi University, and ME in Power systems from Anna University. Two years experience in Kerala state Electricity Board. I am working as assistant professor in Kottayam Institute of Technology and science 2012. Currently pursing PhD in Power Quality at Kalasalingam University Tamil Nadu.

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