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Evolutionary Computation in Coded Communications: An Implementation of Viterbi Algorithm

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1. Introduction

Quantum Computing hopefully is the future of computing systems. It still on its first steps. The development of some quantum algorithms gives the quantum computing a boost on its importance. These algorithms (such as Shor’s and Grover’s algorithms) proved to have superior performance over classical algorithms [1-4]. The recent findings, that quantum error correction can be used, showed that the decoherence problem can be solved and hence the quantum computers can be realized [5-7]. The quantum algorithms are based on the use of special gates applied on one, two or more qubits (quantum bits). The classical computer uses different gates (NOT, AND, NAND, OR and XOR). Quantum gates are in many aspects different from classical gates where all gates must be reversible. This makes the quantum gates act as $2^n \times 2^n$ transformation operators, where we have $n$ input qubits and $n$ output qubits.

To understand the quantum bits and gates we describe the group of amplitudes that describes the state of a quantum register as a vector. A qubit with state $|0\rangle$, which is guaranteed to read logic 0 when measured, is represented by the vector $\begin{pmatrix} 1 \\ 0 \end{pmatrix}$, and a qubit with state $|1\rangle$ which is guaranteed to read logic 1 when measured is represented by the vector $\begin{pmatrix} 0 \\ 1 \end{pmatrix}$. An arbitrary qubit state is then represented by the vector $\begin{pmatrix} \alpha \\ \beta \end{pmatrix}$ as:

$$|\varphi\rangle = \alpha|0\rangle + \beta|1\rangle$$ \hspace{1cm} (1)

where $\alpha$ and $\beta$ are complex numbers and $|\alpha|^2 + |\beta|^2 = 1$.

One important quantum gate is the Hadamard gate given by:

$$H = \frac{1}{\sqrt{2}} \begin{pmatrix} 1 & 1 \\ 1 & -1 \end{pmatrix}$$ \hspace{1cm} (2)
When the input is \( |0\rangle \), Hadamard gate changes the state of the qubit to:

\[
|\varphi\rangle = \frac{1}{\sqrt{2}} \begin{pmatrix} 1 \\ 1 \\ -1/\sqrt{2} \\ 1/\sqrt{2} \end{pmatrix} |0\rangle = \frac{1}{\sqrt{2}} \begin{pmatrix} 1 \\ 1 \\ -1/\sqrt{2} \\ 1/\sqrt{2} \end{pmatrix} = \frac{1}{\sqrt{2}} \begin{pmatrix} 1 \\ 1 \\ -1/\sqrt{2} \\ 1/\sqrt{2} \end{pmatrix}
\]

that is, \( |\varphi\rangle = \frac{1}{\sqrt{2}} |0\rangle + \frac{1}{\sqrt{2}} |1\rangle \). So when reading the qubit at the end, we have exactly 50% chance of seeing a 0, and an equal chance of seeing a 1. Generalizing the above example, if an \( n \)-qubits register originally contains the value \( |0^n\rangle \), it can be transformed using the Hadamard gate to the superpositional state:

\[
\frac{1}{\sqrt{2^n}} \sum_{x=0}^{2^n-1} |x\rangle
\]

where we would see each of the \( 2^n \) binary numbers \( x \) with equal probability when we observe the register. Other gates operate similar to Hadamard gate with different matrices, where Pauli gates are given by:

\[
X = \begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix}, \quad Y = \begin{bmatrix} 0 & -j \\ j & 0 \end{bmatrix}, \quad Z = \begin{bmatrix} 1 & 0 \\ 0 & -1 \end{bmatrix}
\]

and phase gates:

\[
S = \begin{bmatrix} 1 & 0 \\ 0 & j \end{bmatrix}, \quad U = \begin{bmatrix} 1 & 0 \\ 0 & e^{i\theta} \end{bmatrix}
\]

The quantum computers use quantum gates to produce results in a faster and more efficient way than the classical computers. Implementation of quantum computers is still in its very beginning state, therefore, in this chapter, we need not to worry about the implementation issues. In addition to entanglement, the strength of quantum computing comes from the parallelism feature of the quantum computers and the fact that the quantum state is a superposition state. Using classical bits an \( n \) bit vector can represent one of \( 2^n \) symbols, but in quantum bits an \( n \) qubits vector can represent the \( 2^n \) symbols simultaneously.

Quantum Algorithms are usually organized in the following three steps:

**Step 1.** Initialize the quantum states.

**Step 2.** Apply the oracle quantum core as many times as needed.

**Step 3.** Measure the output states (results).

In many classical algorithms especially those used for searching mechanisms, speed and complexity are the main limiting factors in their implementation. Viterbi decoding algorithm is an important algorithm that is used to decode the received data when using Convolutional or Turbo codes at the transmitter. These codes are superior in their performance over many other types of codes. In the following we devise a quantum algorithm to implement the Viterbi algorithm (VA) [8-15].
2. Coded communication and quantum computation

Coded communication uses one type of channel coding that introduces a redundancy in the transmitted data. At the receiver different techniques are used to detect the transmitted information by correcting errors if occurred. All these techniques can be replaced by exhaustive search for the maximum likely data that is assumed to be the correct one.

Several quantum algorithms have been designed to perform classical algorithms with remarkable speedups. In adiabatic algorithms, the solution of the problem is encoded to the problem Hamiltonian. Since the mechanics of the Oracle remains unknown, the encoding process of the Hamiltonian in the adiabatic algorithm is unclear. Instead, just like Grover’s algorithm did, the adiabatic search algorithm forms the Hamiltonian directly from the solution state, which means we have to know the state in prior and then perform an algorithm to show it.

Like many quantum computer algorithms, Grover’s algorithm is probabilistic in the sense that it gives the correct answer with high probability. The probability of failure can be decreased by repeating the algorithm. Grover’s algorithm can be used for estimating the mean and median of a set of numbers. In addition, it can be used to solve NP-complete problems by performing exhaustive searches over the set of possible solutions. This, while requiring prohibitive space for large input, would result in a considerable speed-up over classical algorithms.

3. The classical Viterbi algorithm

In classical communication systems a channel error correcting codes is used to detect and/or correct errors introduced to the data while travelling in a noisy channel. One important class of these codes is the Convolutional Code, where the data is convolved with the code generating polynomials prior to transmitting such data to the receiver. At the receiver, a decoding mechanism is used to recover the transmitted data, and detect/correct errors if they happen. An optimal decoding algorithm was used for this class of codes introduced by Viterbi [10,11]. This algorithm solves the searching problem in the trellis to obtain the maximum likelihood path that best represents the transmitted data. This search grows rapidly with the size of the tree and the length of the data frame. Faster search algorithms will speed up the overall speed of the decoding algorithm. Quantum search algorithm introduced by Grover suggested an optimal searching method that can be used in Viterbi algorithm to speed its execution.

Viterbi decoder uses a tree search procedure to optimally detect the received sequence of data. It performs maximum likelihood (ML) decoding. It calculates a measure of similarity between the received signal and all the trellis paths entering each state at time. Remove all the candidates that are not possible based on the maximum likelihood choice. When two paths enter the same state, the one with the best path metrics (the sum of the distance of all branches) along the path is chosen. This path is called the surviving path. This selection of surviving paths is done for all the states and makes decisions to eliminate some of the least likely paths in early calculation stages to reduce the decoding complexity.

The Viterbi algorithm (VA) calculates the branch metrics at each signalling interval and searches for the minimum branch metric. It searches for the maximum possible correct branch out of all possible branches. The total number of searches for each path (for L signalling intervals) is given by [8,9]:
Fig. 1. Trellis Diagram Showing the Survivor Path.

\[ N_T = L 2^{k+m} \]  

(7)

where \( 2^k \) is the number of possible branches from each state and \( 2^m \) is the number of trellis states. Note that, these searches must be conducted even in the fastest classical implementation to VA. It is worth mentioning here that the larger the number of states the better the performance of the code (i.e. less Bit Error Rate (BER)) [10, 11]. For large number of states and longer signalling intervals the processing delay become a limiting factor in the implementation of such codes, especially the Turbo-Codes where more computations and searching is required [12]. Hence the use of Quantum search algorithm might be the solution for higher dimensionality codes as it promises in the Encryption field.

We will use an example to explain the Viterbi search technique. In this example, we transmit a sequence of \( L \) data bits (say: 1011000) over a noisy channel. Some bits will be corrupted during transmission, just like when you misinterpret a few words when listening to a lousy phone connection or a noisy radio transmission. In such case, instead of receiving the above \( L \)-sequence of bits (called hidden states), the receiver might obtain a new erroneous sequence (called the observed sequence).

To overcome such a problem, the transmitter (called convolutional encoder) operates in a state machine pattern, in which it can exist in a finite number of states, and instead of transmitting the above sequence of bits, the transmitter uses that bit sequence to drive it through the state machine. The encoder tells the receiver (the decoder) about its movement through the state machine by transmitting a codeword (a new bit sequence) that is a result of the state machine transitions.

The Viterbi Algorithm at the decoder side operates on that state machine assumption, and even though the transmitted codeword (representing how the encoder went though the different states) might be corrupted, the Viterbi Algorithm examines all possible sequences of states (called paths) to find the one that is the most likely transmitted sequence. In VA terminology, this is called the survivor path.

Let us consider the (rate 1/2, \( m = 3 \)) convolutional code, the state machine of which is shown in Figure 2. The notation (rate \( n/k \), \( m \)) is widely used for convolutional codes, where the parameter \( k \) represents the number of input bits that control movement in the state machine, the parameter \( n \) represents the number of output bits resulting from each state machine transition, and finally the parameter \( k(m - 1) \) (called the constraint length of the code) is related to the number of states \( S \) in the state machine, where \( S = 2^m \). In our example code, we have \( S = 2^3 = 8 \) different states.
As illustrated in Figure 2, the transmitter can exist in only one state at any given time. The transmitter always starts from state 000. To transmit one bit from the input data sequence, the transmitter looks up the only two possible transitions available from the current state. For example, if the transmitter is currently in state 100, and the input data bit is 1, the transmitter follows the solid-line transition to state 110 and sends the codeword 00 on the output channel. However, if the input bit is 0, the transmitter follows the dotted-line transition to state 010 and sends the codeword 11 to the decoder.

The transmitter remains in the new state until it is time to transmit the next bit in the input data sequence. The output codeword for any transition is decided by the numbers on the state diagram which are predetermined by certain generating polynomials. The ones we use in Figure 2 are $G_1 = [1 1 0 1]$ and $G_2 = [1 1 1 0]$. Notice that one transition occurs in the state machine for each input bit ($k = 1$), which results in two bits being transmitted by the encoder over the channel ($n = 2$), resulting in the code rate of $k/n = 1/2$. If we use the input data sequence of 1011000 in the above state machine, we get the transmitted codeword of 11111010101110.

One might think that implementing the above state machine using hardware is a complex task, but it is actually very simple since it can be implemented using a shift register of size $(K)(k)$ bits and an $n$ group of XOR gates (to give the $n$ output bits). The shift register and the XOR gates are connected based on the desired generating polynomials. A total of $k$ input bits are shifted into the register each time tick to force the state machine into another state. Since the transmitted codeword is based on a pattern (transitions in the state machine), the receiver can predict the movement in that machine even if some of the transmitted bits are corrupted by noise in the channel. To do that, the receiver uses the help of a trellis diagram as shown in Figure 3.

Figure 3 illustrates how the receiver decodes two incoming bits each time tick to decide which of the two possible transitions (the solid-line or the dotted-line) to be undertaken. Figure 4 shows the transitions that occur in the trellis for the transmitted codeword of 1111010101110, which corresponds to the input data sequence of 1011000.
If no errors occur while transmitting the encoder-generated codeword, the decoder will traverse the trellis without any problems and will be able to read the original data sequence of 1011000. However, due to errors in the channel, the receiver might read a different bit sequence. Since the input data sequence (1011000) is composed of 7 bits, the challenge the decoder faces is to know which of the $2^7$ possibilities did the transmitter intend to send.

The idea is that because each 7 bit code will generate a unique transmitted codeword, the decoder will search through all the $2^7$ possible codewords that can be generated by the state machine to see which one is closest to the received sequence. This is called the brute-force search method. 

Fig. 3. Decoder trellis diagram for the convolutional code (rate 1/2, $m = 3$).

| Input data sequence: | 1 | 0 | 1 | 1 | 0 | 0 | 0 |
|----------------------|---|---|---|---|---|---|---|
| Transmitted codeword: | 11 | 11 | 10 | 10 | 10 | 11 | 10 |
| Received sequence:   | 11 | 11 | 10 | 10 | 10 | 11 | 10 |

Fig. 4. Decoder trellis diagram showing the path for the input codeword of 11111010101110.
search method, which is computationally expensive. For example, imagine the case of 256-bit data bit sequence, which will force the receiver to compare the received codeword againsts $2^{256} \approx 1.2 \times 10^{77}$ possibilities.

A more efficient method compared to the brute-force search is the Viterbi algorithm. In such method, and using the trellis shown in Figure 3, we narrow the investigated codewords (called paths) systematically each signalling interval. The algorithm goes like this: As the decoder examines an entire received codeword of a given length, the decoder computes a metric for each possible path in the trellis. The metric is cumulative across the whole path. All paths are followed until two paths converge at a trellis state. Then the path with the higher metric is kept (called the survivor path) and the one with the lower metric is discarded. Since the path with the highest metric is kept, the decoder is classified as a maximum-likelihood receiver.

There are different metrics that can be used to compare the received codeword with different valid codewords, the simplest of which is the Hamming distance, which is the dot product between the received codeword and the original codeword (i.e., the bit agreements between the two bit sequences). The table below shows some examples of how to calculate the Hamming distance.

| Data Bit Sequence | Corresponding Codeword | Received Sequence (due to errors) | Bit Agreement (Hamming metric) |
|-------------------|------------------------|----------------------------------|-------------------------------|
| 0101111           | 00 11 11 10 10 01 11   | 10 11 10 10 10 11 10             | 10/14                         |
| 0101100           | 00 11 11 10 10 10 11   | 10 11 10 10 10 11 10             | 10/14                         |
| 1011000           | 11 11 10 10 10 11 10   | 10 11 10 10 10 11 10             | 13/14                         |
| 1010110           | 11 11 10 01 10 10 10   | 10 11 10 10 10 11 10             | 10/14                         |
| 1011011           | 11 11 10 10 00 10      | 10 11 10 10 10 11 10             | 11/14                         |

To illustrate the idea of Viterbi search, we show in Figure 5 the case of decoding the codeword 1111010101110 corresponding to the data bit sequence 1011000. An error occurs in the second bit, thus leading to the received bit sequence of 1011010101110.

The decoder starts at state 000, just like the encoder. From this point it has two possible paths available, which should be decided by the incoming bits. Unfortunately, the two incoming bits are 01, which do not match 00 or 11. Hence, the decoder computes the path metric (Hamming distance) for both transitions and continues along both of these paths. The metric for both paths is now equal to 1, which means that only one of the two bits was matched with the incoming 01 sequence.

As the algorithm progresses while traversing multiple paths, we notice that certain paths start to converge at different states of the trellis (see the fourth, fifth, sixth, … signalling intervals). The metrics are shown in Figure 5. Since maximum likelihood is employed, the decoder discards the path with the lower metric because it is least likely. This discarding of paths at each trellis state helps to reduce the number of paths that have to be examined and gives the Viterbi method its efficiency. The survivor paths are shown in darker color in Figure 5.

Once the whole codeword is traversed through the trellis, the path with the highest metric is chosen as the final path, which is 1111010101110 in our example corresponding to the input data sequence of 1011000.
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Fig. 5. Decoder trellis diagram showing the path for the input codeword of 10111010101110.

It is worth mentioning that building the receiver hardware is much more difficult compared to the transmitter hardware. This is because we need to save path history for the algorithm to work. The length of the trellis diagram decides the memory storage requirements for the decoder, which is (in classical hardware) usually reduced by a truncation process. Truncation also reduces latency since decoding need not be delayed until the end of the transmitted codeword. Such truncation can be avoided if a parallel search is done through multiple paths at the same time.

4. The quantum Grover’s algorithm

Grover’s search algorithm in quantum computing gives an optimal, quadratic speedup in the search for a single object in a large unsorted database [1-4]. It can be generalized in a Hilbert-space framework for both continuous and discrete time cases. As shown in Figure 6, Grover’s algorithm initializes the search space by creating an equal superposition of $n$ qubits $|\phi^n\rangle$ (the superscript denotes $n$ qubits) by applying the Hadamard Transform $H$. Then it applies the function $f(x)$ (The Oracle) that is equivalent to a gate operator called the $C$ gate. This function is given by:

$$f(x) = \begin{cases} 
0 & |\phi_i\rangle \neq |x_r\rangle \\
1 & |\phi_i\rangle = |x_r\rangle 
\end{cases}$$

(8)

where $|x_r\rangle$ is the required search result. The $C$ operator will reverse the sign of the state (rotate by $\pi$) $|\phi_i\rangle$ that most represents the required search result (this argument will be used
later in defining the oracle function used in our proposed implementation) and leave all other states unchanged.

\[ P = \begin{bmatrix} e^{i\phi_1} & 0 \\ 0 & e^{i\phi_2} \end{bmatrix} \] 

(9)

Then in matrix form we can see that:

\[ |x_{out}\rangle = U^N |\varphi_1\rangle \] 

(10)

Where:

\[ U^N = H^N P^N H^N C^N \] 

(11)

The superscript \( N \) denotes that it is an \( NxN \) operator. Applying the Grover’s gate \( G \) \( r \) times to find the final search result, where:

\[ r = \frac{\cos^{-1}\left(\frac{1}{\sqrt{N_{paths}}}\right)}{2 \sin^{-1}\left(\frac{1}{\sqrt{N_{paths}}}\right)} \] 

(12)

we see that as \( N_{paths} \to \infty \) the searching order \( r \to O(\sqrt{N_{paths}}) \). Next we discuss the use of Grover’s algorithm for implementing Viterbi algorithm.
5. Implementation of VA using Grover’s Search Algorithm

Viterbi decoder searches all possible branches at each signalling interval in the code tree. This requires storage of all paths including the survivor path, computation of each branch metric and a decision making to select the survivor path. In quantum theory the components are somehow different, we have a register containing a certain number of qubits, an arbitrary transformation applied on these qubits and a measuring mechanism that measures the state of each particular qubit.

The quantum VA can be viewed as a global search in all the possible paths in the decoding trellis. This means that for a classical Convolutional Code with \((n,k,m)\) parameters, \(L\) signalling symbols and the number of trellis states is \(2^m\), from equation (7) we see that the total number of possible paths is \(N_T = L 2^{m+k}\) when using the VA. We propose a single search quantum viterbi algorithm (SSQVA) and a multi search quantum viterbi algorithm (MSQVA).

6. Single Search Quantum Viterbi Algorithm (SSQVA)

In this algorithm all the \(N_T\) paths are searched at once for the closest state to the received signal qubits producing a search order of \(O(\sqrt{N_T})\). Then the total number of searches is given by:

\[
N_{SSQVA} = \sqrt{N_T} = \sqrt{L 2^{m+k}}
\]  

This requires large storage (especially for huge number of signalling intervals and number of trellis states). Figure 7 shows the block diagram of the SSQVA.

![Fig. 7. Block Diagram of Single Search Quantum Viterbi Algorithm (SSQVA).](www.intechopen.com)

The SSQVA can be summarized as:

1. Convert the received classical data bits into qubits \(\ket{X^{n}}\).
2. Initialize the Grover’s search engine.
3. Apply the search on all possible paths.
4. Measure the output and map it back to classical bits.

Following the nature of the classical VA, where multi-stages are implemented, we devise the Multi Search version (MSQVA) as follows:
7. Multi Search Quantum Viterbi Algorithm (MSQVA)

The $N_T$ paths can be searched for the closest state to the received signal qubits in distributed fashion (Multi Search). For each stage we search only the corresponding tree branches and then combine the searches to the survivor path. This will produce a search order of $O(N_{MSQVA})$. Where the total number of searches is given by:

$$N_{MSQVA} = L \frac{m+k}{2}$$  \hspace{1cm} (14)

This requires less storage but as we can see larger number of searches. Figure 8 shows the block diagram of the MSQVA.

The MSQVA can also be implemented iteratively using the same one stage (the repeated stage shown in Figure 8).

The MSQVA can be summarized as:

1. Convert the received classical $k$ data bits into groups of $k$ qubits $x^k_r$.
2. Initialize the Grover’s search engine.
3. Apply the search on all possible paths.
4. Measure the output and map it back to classical bits.
5. Go to step 1.

The MSQVA version is implementing the VA iteratively as its classical nature; therefore, it has higher searching order. The advantage of the MSQVA is that the same hardware setup is used every iteration.

In both SSQVA and MSQVA the function $f(x)$ is the black box of the algorithm. It differs according to the application of the VA. For example if VA is used as a Convolutional Code Decoder the function $f(x)$ will decide in favour of the state that is closest to the received state. This means that the equality might not hold for all the received qubits. Then the function becomes:
Then, the flipping around the mean transformation will amplify the most probable state and hence the result will be the maximum likelihood decision. Note that, $f(x)$ still has the same implementation as in Grover’s algorithm, since Grover’s algorithm produces the most probable search result.

The implementation of quantum algorithms is in its first steps; therefore, only theoretical performance evaluation is possible. Figure 9 shows a comparison of the classical VA and the two devised algorithms at different frame lengths and number of states.

![Figure 9: Search Order as a function of Frame Length (L) for the Classical VA, MSQVA and SSQVA for $m=4$ and $k=4$.](image)

### 8. Results and conclusions

In this chapter we discussed the use of quantum search algorithm introduced by Grover to speed the execution of Viterbi’s algorithm. Two methods were discussed to implement VA using quantum search algorithm: The first is the SSQVA where the search domain contains all possible paths in the code tree. The number of paths depends on the signalling length (received data frame length). This domain becomes very large when long frames are used. The SSQVA is optimal in the global sense, because it uses all possible solutions and obtains the best one that has the minimum number of differences. The second method is the MSQVA where the search is divided into multiple stages just like in the classical algorithm. This method produces a sub optimal solution from speed point of view, since it uses the search algorithm partially at each signalling interval.
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Bibliography

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This book presents several recent advances on Evolutionary Computation, specially evolution-based optimization methods and hybrid algorithms for several applications, from optimization and learning to pattern recognition and bioinformatics. This book also presents new algorithms based on several analogies and metafores, where one of them is based on philosophy, specifically on the philosophy of praxis and dialectics. In this book it is also presented interesting applications on bioinformatics, specially the use of particle swarms to discover gene expression patterns in DNA microarrays. Therefore, this book features representative work on the field of evolutionary computation and applied sciences. The intended audience is graduate, undergraduate, researchers, and anyone who wishes to become familiar with the latest research work on this field.

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