Article

Simulation of Extended Wavelength Avalanche Photodiode with the Type-II Superlattice Absorption Layer

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Abstract: The relationship between the performance of avalanche photodiode (APD) and structural parameters of the absorption, grading, and multiplication layers has been thoroughly simulated and discussed using the equivalent materials approach and Crosslight software. Based on separate absorption, grading, charge, and multiplication (SAGCM) structure, the absorption layer of APD was replaced with InGaAs/GaAsSb superlattice compared to conventional InGaAs/InP SAGCM APD. The results indicated that the breakdown voltage increased with the doping concentration of the absorption layer. When the thickness of the multiplication layer increased from 0.1 μm to 0.6 μm, the linear range of punchthrough voltage increased from 16 V to 48 V, and the breakdown voltage decreased at first and then increased when the multiplication layer reached the critical thickness at 0.35 μm. The grading layer could not only slow down the hole carrier, but also adjust the electric field. The dark current was reduced to about 10 nA and the gain was over 100 when the APD was cooled to 240 K. The response wavelength APD could be extended to 2.8 μm by fine tuning the superlattice parameters. The simulation results indicated that the APD using superlattice materials has potential to achieve a long wavelength response, a high gain, and a low dark current.

Keywords: APD; extended wavelength; InGaAs/GaAsSb superlattice; I-V characteristics; electric field distribution

1. Introduction

In recent years, with the growing concern about eye safety in light detection and ranging (LiDAR) [1] and capacity saturation in traditional communication wavebands, the extended wavelength avalanche photodiode has attracted increasing attention. By extending the operating wavelength from conventional 1.55 μm to more than 2 μm, the extended wavelength avalanche photodiode has important applications in LiDAR. Avalanche photodiodes (APDs) are ideal for the operation in this spectrum range because its internal gain provides a higher sensitivity than conventional photodiodes, which significantly improves the sensitivity of the receiver. At present, the main materials that can respond to 2 μm or longer wavelength include InGaAs [2–4] with high indium components, HgCdTe, and InAsAlSb. When the InGaAs devices are used with response wavelength exceeding 2 μm, the indium content must be increased to nearly 80 %, leading to a lattice mismatch with the InP material and increasing material defects. The dark current density is several orders higher than standard In0.53Ga0.47As devices. In addition, extended wavelength InGaAs devices have a lower quantum efficiency (QE) of 50–60 % [5]. The HgCdTe material can provide an adjustable band gap in the range of 0–1.6 eV, making it an excellent absorption material for all infrared bands. It has a high QE (> 70 %, at room
temperature) in the range of 1–3-μm short-wave infrared (SWIR), but the CdZnTe substrates required for the growth of HgCdTe material are expensive [6]. Compared with InP-based III-V devices, the HgCdTe device preparation process has difficulties and a low yield rate. Consequently, HgCdTe is facing challenges in many commercial applications because of its high ratio of cost to performance. InSb is a primary material candidate for 2-μm APD applications. Although this material exhibits very low excess noise, it must be operated at cryogenic temperatures to reduce dark current. Such operating temperatures prohibit compact receiver production, as the required cryogenic system is both complex and many times larger than the detector itself [7]. APDs based on group III–V, such as AlInAsSb and InGaAs/GaAsSb superlattice materials, have become a hot spot for domestic and international research due to their excellent performance and suitable detection wavelength range. In contrast, the type II superlattice (T2SL) material [8,9] is a material system under development in recent years. With outstanding growth uniformity [10] and excellent band structure engineering capability [11,12], T2SLs are versatile candidates for enhanced IR detection and imaging [9] from near-infrared [13,14] to long wavelength infrared (LWIR) and very long wavelength infrared (VLWIR)[15–18].

In this paper, we use Crosslight software to simulate the layer structure of T2SL extended wavelength APD and research the relationship between each layer of the APD. In the Crosslight simulation, the energy band structure is based on the Schrodinger equation, and the transport characteristics are based on the drift-diffusion model, the impact ionization model, and the Poisson equation. The device characteristics were simulated by Crosslight. In addition, the layer structure design of In0.53Ga0.47As/GaAs0.51Sb0.49 T2SL extended wavelength APD was also discussed.

2. Layer Structure Design

In this paper, we used the equivalent material method for dealing with the energy structure and physical parameters of superlattice materials. First of all, several periods of superlattice materials were used to build a layer structure model, the energy band structure was simulated, and the results were approximated as that of the equivalent bulk material. Then, the parameters of the equivalent bulk material corresponding to the whole absorption layer of the superlattice was brought to the SAGCM APD structure to simulate. This method has been used previously to calculate InAs/GaSb T2SL nBn detectors by Shi Yanli project group of Yunnan University in 2019 [19,20]. A group in the Beijing University of Posts and telecommunications has also used this method to calculate the energy band of superlattice materials in 2019 [21]. In this way, the equivalent bulk material was stimulated instead of hundreds of layers of superlattice materials, which will save a lot of time as it is easy to verify the results of the whole simulation materials.

Figure 1 shows the device layer structure diagram simulated in this paper. From top to bottom, there is the p+ InP layer, the i-InP multiplication layer, the n+ InP charge layer, the InGaAsP grading layer, the InGaAs/GaAsSb superlattice absorption layer, the InGaAs buffer layer, the n-type heavily doped InP substrate, and the ohmic contact metal electrode. Generally, the InGaAsP grading layer has 2–5 layers, which can slow down the hole barrier formed by the valence band shift between the InGaAs/GaAsSb absorption layer and the InP multiplication layer, thus reducing the accumulation of hole carriers at the heterojunction barrier. The n-InP charge layer is used to adjust the electric field distribution between the multiplication layer and the absorption layer, so that the absorption layer electric field can make sure the carriers overcome the heterojunction barrier between the absorption layer and the charge layer and drifting to the multiplication region, meanwhile reducing the absorption region electric field as much as possible to decrease the dark current. In this paper, the wide band gap material InP is used as the multiplication layer, which means the hole multiplication.
As shown in Figure 2, the energy band structure of a 5-nm-In$_{0.53}$Ga$_{0.47}$As/5nm-GaAs$_{0.51}$Sb$_{0.49}$ superlattice material was calculated with an 8-band K.P model. The red solid lines from top to bottom are L-band, Γ-band, heavy hole band, and light hole band. The two blue solid lines from top to bottom are the upper and lower edges of the bandgap of the equivalent material. The affinity of the equivalent material is equal to the affinity of the GaSb material plus the absolute value which is equal to (bottom of the conduction band of the equivalent material subtracts the bottom of the conduction band of the GaSb material). It can be seen from Figure 2 that the band gap of 5-nm-In$_{0.53}$Ga$_{0.47}$As/5nm-GaAs$_{0.51}$Sb$_{0.49}$ superlattice material is 0.43 eV. Figure 3 shows the energy band structure of the SAGCM APD device at equilibrium and breakdown with equivalent material method.
3. Discussion

The device structure simulation is performed by using equivalent material method. The effect of each layer structural parameters to the electric field and the I-V characteristics of the device are discussed in Table 1.

Table 1. The important parameters used in the simulation.

| Parameters                             | Value                          | Units |
|----------------------------------------|--------------------------------|-------|
| Absorption layer thickness             | 1.5, 2, 2.5, 3                 | μm    |
| Multiplication layer thickness         | 0.1, 0.2, 0.3, 0.4, 0.5, 0.6   | μm    |
| Doping concentration of grading layer  | 7 × 10^{22}, 8 × 10^{22}, 9 × 10^{22}, 1 × 10^{23} | m^{-3} |
| Temperature                            | 240, 270, 300                  | K     |

Firstly, for the absorption layer, the forbidden band width of the material has a relationship with the long wavelength limit as follows [22].

\[ \lambda(\mu m) = \frac{1.24}{E_g(eV)} \]  

(1)

\[ E_g \] is the band gap of the material, and the band gap of In_{0.53}Ga_{0.47}As/GaAs_{0.51}Sb_{0.49} superlattice material at 300 K is 0.43 eV, and the cut-off wavelength can be calculated from the above equation (1) to be about 2.8 μm. For photons with energy greater than the band gap, the absorbed efficiency of the absorption layer can be expressed by absorption coefficient and reflection coefficient as follows [22].

\[ P_{abs}(x) = (1-R)(1-\exp(-ax)) \]  

(2)

In the above equation, \( P_{abs}(x) \) represents the efficiency that the photon will be absorbed at the absorption position \( x \). \( R \) is the reflection coefficient of the material (Here \( R \) is approximately 0). \( a \) is the absorption coefficient of the material. The higher the \( P_{abs}(x) \), the higher the efficiency that a photon will be detected in the material. In_{0.53}Ga_{0.47}As/GaAs_{0.51}Sb_{0.49} superlattice material has an absorption coefficient of 6000 cm\(^{-1}\) at 1.55 μm and 2000 cm\(^{-1}\) at 2 μm [5]. These values are substituted into the above equation (2) to obtain the absorption efficiency distribution of photons in the In_{0.53}Ga_{0.47}As/GaAs_{0.51}Sb_{0.49} superlattice material (shown in Figure 4).

![Figure 4. Diagram of absorption efficiency as a function of thickness.](image)

It can be seen from Figure 4 that, for the photon at 2 μm with a wavelength indicated as the blue curve, the absorption efficiency is 30% when the thickness of the absorption layer is 1.5 μm, and the absorption efficiency is 80% when the thickness of the absorption layer is 2 μm.
layer is 1.5 μm, and the absorption efficiency increases by 15 % when the absorption thickness changes from 1.5 μm to 3 μm. Meanwhile, the material also has excellent absorption efficiency for a photon with a 1.5-μm wavelength, and the absorption efficiency can reach more than 80 % when the thickness of the absorption layer is 3 μm. From the above results, it can be seen that the photon absorption efficiency is increased by enlarging the absorption thickness, then tends to a limit value as the layer thickness increases, and the absorption layer cannot be made too thick in the actual avalanche photodiode. Figure 5 shows the simulated I-V characteristic curves of the device at 300 K. It can be seen that the photocurrent increases owing to the absorption layer thickness increases. The dark current increases from $2.2 \times 10^{-6}$ A (the absorption layer thickness is 1.5 μm) to $3.6 \times 10^{-6}$ A (the absorption layer is 3 μm). This is because the defects also increase when the thickness of the absorption layer increases. In addition, in practical situations, the response time can be raised by increasing of the absorption layer thickness. In single-photon detection mode, the increase in the absorption layer thickness will lead to the time jitter and dark count increasing.

![Figure 5](image)  
**Figure 5.** Simulation results of I-V characteristics of devices with different absorption layer thicknesses (Note. D: dark current; L: light current; the inserted figure is a partial enlargement of Figure 5).

The effect of multiplication layer thickness on the I-V characteristics are simulated, and the results are shown in Figure 6. The multiplication layer concentration is $1 \times 10^{21}$ m$^{-3}$ at 300 K. When the multiplication layer thickness is 0.1 μm, the dark current is $1.3 \times 10^{-6}$ A at the punchthrough voltage point; with the increase in bias voltage, the current of the detector appears obvious in this step-by-step phenomenon. Increasing the multiplication layer thickness, the dark current also increases, resulting from the generation-recombination current.

![Figure 6](image)  
**Figure 6.** I-V curve of the device with different multiplication thickness.
As shown in Figure 7, the relationship between the multiplication layer and the device punchthrough and break down voltages is obtained by adjusting the thickness of the multiplication layer. Here, the dark current of $1 \times 10^{-5}$ A is set as the breakdown voltage point ($V_{br}$). The breakdown voltage decreases from 46 V to 42 V as the multiplication layer thickness increases from 0.1 μm to 0.35 μm. Then, the breakdown voltage increases from 42 V to 47 V as the multiplication layer increases from 0.35 μm to 0.6 μm. This non-monotonic situation can be explained as follows. When the multiplication layer is less than 0.35 μm, the higher electrical field and external bias is required to obtain the sufficient multiplication for breakdown, so the multiplication layer is thinner and the breakdown voltage is higher. When the multiplication layer is relatively thick, the increase in breakdown voltage is a result of the reduction in the electric field of the multiplication layer, so external bias is also needed to increase the avalanche breakdown. The punchthrough voltage increases significantly due to the increased multiplication layer thickness; this is because higher external bias voltage is required to completely deplete the multiplication region. The electric field distribution for different multiplication layer thicknesses is depicted in Figure 8, and the interpretation can be well verified by looking at the electric field distribution near the breakdown.

![Figure 7.](image1)

**Figure 7.** The change in punchthrough voltage and breakdown voltage with the multiplication layer thickness (the breakdown voltage is set at the position where the dark current is $10^{-5}$ A).

![Figure 8.](image2)

**Figure 8.** The electric field distribution of the device varies with the multiplication layer thickness under breakdown bias.

In the SAGCM structure, the charge layer is InP whose forbidden band width is 1.35 eV, while the forbidden band width of the absorption layer In$_{0.55}$Ga$_{0.47}$As/GaAs$_{0.51}$Sb$_{0.49}$ T2SL is about 0.43 eV. The band gap between the two layers is too large, and it is easy to
form interfacial barriers to accumulate carriers. Therefore, a grading layer is added between the charge layer and the absorption layer to make the energy bands match between the two materials, where the band gap can be made to vary from 0.75 to 1.35 eV by adjusting the components of In$_{x}$Ga$_{1-x}$AsP materials. It is found from the simulation results that the grading layer can regulate the electric field, such as the InP charge layer. Figures 9 and 10 show the electric field and the I-V characteristic curves with different grading layer doping concentration, respectively. The electric field of the absorption layer becomes higher when the doping concentration of the grading layer is reduced from $1 \times 10^{23}$ m$^{-3}$ to $7 \times 10^{22}$ m$^{-3}$, and the punchthrough voltage decreases from 40 V to 36 V (change of 4 V) which comes from easily depletion of the lower doping concentration. On the contrary, the breakdown voltage of the device increases from 43.5 V to 47 V (increase of 3.5 V), which implies that the electrical filed in the multiplication layer decreases as the doping concentration of the grading layer decreases.

![Electric field distribution diagram under different doping concentration of grading layer](image1)

**Figure 9.** Electric field distribution diagram under different doping concentration of grading layer, doping concentration unit: m$^{-3}$. (Note. The inserted figure is a partial enlargement of Figure 9).

![Characteristic curves of I-V at different doping concentrations](image2)

**Figure 10.** Characteristic curves of I-V at different doping concentrations, doping concentration unit: m$^{-3}$. (Note. D: dark current; L: light current; the inserted figure is a partial enlargement of Figure 10).

### 4. Temperature Dependence

When the absorption layer is replaced by the InGaAs/GaAsSb superlattice other than InGaAs layer, the band gap of the absorption layer becomes narrower and the dark current increases. In order to decrease the dark current, the operation temperature is required to reduce. Therefore, the situation after cooling is simulated, and the results are shown in Figure 11. From the figure, it can be seen that the dark current decreases by an order of magnitude when the temperature decreases by 30 °C. When the temperature is 240 k, the dark current at the punchthrough voltage point is 10 nA. It can be seen that the change in
temperature affects the value of breakdown voltage, and the breakdown voltage becomes about 4V smaller as for every 30 °C decrease in temperature.

For APDs, the impact ionization is affected by temperature. The average free path of carriers increases as temperature decreases, which increases the impact ionization coefficient. It shows that the device is more prone to avalanche under a small bias voltage, that is, the breakdown voltage decreases with the temperature. The dependence of breakdown voltage on temperature is 0.13V/°C. This is about the same order of magnitude as the temperature drift coefficient of traditional InGaAs/InP APD [23].

\[
M = \frac{I_p - I_d}{I_{p0} - I_{d0}}
\]  

(3)

In formula (3), \( I_{p0} \) and \( I_{d0} \) are marked as the photocurrent and dark current at the unit gain point (\( M = 1 \)), where the unit gain point is at the punchthrough voltage point (29.5 V under 240 K). \( I_p \) and \( I_d \) are the photocurrent and dark current at the different bias. When the temperature drops to 240 K, the gain changes with the bias voltage (as shown in Figure 12), and the gain of the device at the breakdown point exceeds 300. In contrast, for comparison, Jones et al. have reported an AlInAsSb APD [25] with a gain of 60 at the condition of room temperature, 2-μm illumination, and a breakdown point of about 43-V bias. Dumas et al. have reported a HgCdTe APD [26] with a gain of 21 at the condition of 185 K, 2-μm illumination, and 12-V bias.

Figure 11. I-V characteristic curves at different temperatures.

Under the illumination of the 2-μm wavelength, the incident light power is 4 μw and the device diameter is 50 μm. The gain can be calculated by photocurrent and dark current [24].

\[
M = \frac{I_p - I_d}{I_{p0} - I_{d0}}
\]

(3)

In formula (3), \( I_{p0} \) and \( I_{d0} \) are marked as the photocurrent and dark current at the unit gain point (\( M = 1 \)), where the unit gain point is at the punchthrough voltage point (29.5 V under 240 K). \( I_p \) and \( I_d \) are the photocurrent and dark current at the different bias. When the temperature drops to 240 K, the gain changes with the bias voltage (as shown in Figure 12), and the gain of the device at the breakdown point exceeds 300. In contrast, for comparison, Jones et al. have reported an AlInAsSb APD [25] with a gain of 60 at the condition of room temperature, 2-μm illumination, and a breakdown point of about 43-V bias. Dumas et al. have reported a HgCdTe APD [26] with a gain of 21 at the condition of 185 K, 2-μm illumination, and 12-V bias.

Figure 12. The gain curve of the device at 240 K as a function of bias voltage.
5. Conclusions

In summary, an APD based on short-wave infrared InGaAs/GaAsSb T2SL was simulated using Crosslight software. The I-V characteristics and the electric field distribution of the absorption, grading, and multiplication layers were analyzed. Furthermore, the effect of temperature on the I-V characteristics and the gain of the device was discussed. The device had a cutoff wavelength of 2.8 μm. The dependence of the breakdown voltage on temperature was found to be 0.13 V / °C. The dark current of the device decreased by about one order of magnitude for every 30 °C decrease in temperature. The gain of the device at the breakdown point exceeded 300 at 240 K. The stimulation results indicate that the 5nm-In0.53Ga0.47As/5nm-GaAs0.55Sb0.45 superlattice material can be used as an absorption layer to achieve low dark current and high gain characteristics in the cutoff wavelength of 2.8 μm.

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