Design and Analysis of Dual Band Circular Slotted Patch Antenna at 2.45GHZ and 5.8GHZ for RFID Applications

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Abstract—In this project patch antenna has been used for Radio Frequency Identification (RFID) applications. Due to the advancement in the field of wireless communications day by day, RFID is the most attractive technology being used as an identification technology and is far better than other sources of identification and tracking i.e. Bar code technology. Different frequency bands are assigned for RFID antenna. But in this project work has been made in Microwave band ranging from (2.400–2.483) GHz and (5.7250–5.8750) GHz due to a number of applications and due to the higher transfer rate. In order to design slotted dual band patch for RFID 2.45GHz and 5.800 GHz applications, a 1.58 mm thicker RT/duriod-5880 is selected for substrate material having a relative permittivity and loss tangent of $\varepsilon_r=2.2$ and $\delta=0.0009$ respectively. To achieve dual band response different techniques can be adopted. First a conventional patch for 2.45GHz has been designed and then with the help of slotting techniques another frequency band was achieved that was radiating at 5.8 GHz. The dual band frequency of the antenna has been achieved by introducing the slot in the upper most layer patch with coaxial feed. The gain, efficiency and bandwidth of the antenna at (2.45 and 5.80) GHz are {6.661 dB, 89 %, 36 MHz} and {6.661 dB, 97 % and 341MHz}, respectively. The antenna can be potentially use for RFID and WLAN applications. All the simulations are carried out using CST MWS. The prototype has been fabricated to validate the results.

Keywords— Radio Frequency Identification (RFID), Giga hertz (GHz), Wireless lan (WLAN), Computer Simulation Technology Microwave Studio (CST MWS), Decibel (dB).

I. INTRODUCTION

Wireless communication is replacing the wired technologies. System antenna is an important module, without antenna wireless communication is impossible. Therefore the antenna must be more efficient to perform in a better way. Networked physical objects provide a great global infrastructure that connects virtually physical world with objects. Internet of Thing (IoT) is now a day’s most emerging technology with very vast futuristic vision in which internet embed itself with every objects through RFID in a transparent way [1].

The clue of the identification of things and the control of remote devices through Radio frequency identifiers, (RFID’s) was presented by H. Stockman in year 1948 [2]. RFID is an automated technology used for proof of identity and improved tracking of human as well as animals. It is also used for healthcare, security purposes, robotics, and collecting toll electronically vehicle speed measuring, and bioengineering [3]. It can identify, sensed object by sending a signal emerging from reader and ending at tag antenna where in the tag there is an integrated circuit (IC) where the information is stored so upon receiving an appropriate signal the information stored in the IC will send back through the tag antenna to the reader [4].

Antenna being a metallic device (as a wire or rod) while having the functionality of both receiving and transmitting electromagnetic energy. It can also be defined as something which can receive radio waves and can also radiate them [5].

According to the figure 1 ideal generator is the source. The impedance of antenna is signified by load ZA and transmission line bear connection with it, losses with in antenna is represented by RL known as dielectric losses and conduction, radiation resistance is denoted by Rr, and Xa also called reactance[5]. In ideal condition maximum power is transferred to the antenna from transmitting source. In applied systems, dielectric-losses appear in transmission line and mismatches appear between antenna impedances and antenna transmission line, which results in originating standing waves [5].

Less weight, small size, high performance, conformability and low cost are the key requirements of modern age and Patch antenna is the right choice. patch (antenna) contains a radiating-patch which is then attached on a dielectric layer (substrate) consequently supported by ground plane as xy

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presented in the figure 2. Metals like copper metals or gold can be used as a patch of any shape.

The operation of conventional patch antenna can be modified also by applying the techniques of slotting in the patch. The insertion of slot will introduce perturbation of different modes. In some extent the effect of modes are dependent upon the slot shape, size and the position of the slot. As a result of inserting slot in some certain position there will be produced some more modes. Compact linear and circular polarised antennas, dual band and wideband antennas can be produce by perturbing the correct modes. There are different ways to make slots in patch [6-7].

II. ANTENNA DESIGN AND ANALYSIS

Figure 3 and 4 shows the layout and aspects of dual-band patch (antenna) for RFID 2.45 GHz and 5.8 GHz related applications. In this design a Rogers RT5880 (lossy) is used as a substrate. Relative permittivity, thickness and tangent loss of the substrate are 1.575 mm, εᵣ = 2.2 and δ = 0.0009 respectively. The conducting material of the radiating ground and patch plane is taken as low loss copper metal to get better radiation efficiency. The proposed antenna is excited by 50 Ω, 4.5 mm wide coaxial feed line. The width & length of the feed-line as shown are calculated using the standard Transmission line theory. Slots we introduced to get the desired frequencies, dimensions of the slots can be seen in the same figure [8].

![Figure 2. Microstrip patch Antenna](image)

![Figure 3. Front View of Antenna](image)

![Figure 4. Rear View of Antenna](image)

![Figure 5. Return loss simulated result](image)

Width equation:

\[ W = \frac{c}{2f_r \sqrt{\varepsilon_r + 1}} \]  

(1)

Length equation:

\[ L = \frac{c}{2f_r \sqrt{\varepsilon_{ref}}} - 2\Delta L \]  

(2)

Frequency equation:

\[ f_r = \frac{c}{2\sqrt{\varepsilon_r + 1}} \]  

(3)

Coaxial feed line characteristic impedance:

\[ Z_{0∞} = \frac{60}{\sqrt{\varepsilon_r}} \ln \frac{D_e}{d_e} = \frac{138}{\sqrt{\varepsilon_r}} \log_{10} \frac{D_e}{d_e} \, \Omega \]  

(4)

III. RESULT AND DISCUSSION

Slotted dual-band circular patch antenna is simulated and designed in CST MWS and then fabricated. The Return loss is -31dB at 2.45 GHz and -23.7dB at 5.8 GHz as shown in the figure 5.
According to figure 6, the Voltage standing wave ratio of the proposed antenna is 1.5 and 1.135 at 2.45GHz and 5.8GHz respectively. Radiation Pattern Plots of the proposed antenna can be seen in figure 7 and 8 for frequency 2.45 and 5.8 GHz respectively. 3-D gain patterns of the simulated antenna can be seen in figure 9 and 10 for frequency 2.45 and 5.8 GHz respectively.
The antenna is then fabricated and can be seen in figure 11. The front view and figure 12. The rear view. Return loss of fabricated antenna can be seen in figure 13. Fabricated antenna results of VSWR can be seen from the figure 14 and 15 at 5.8 GHz. It is quite evident from both the simulated and fabricated antenna results that the antenna has dual band characteristics with less than -10db (BW of 36MHz, gain of 6.661db, total efficiency of 89%) and (BW of 341MHz, gain of 6.661db, Total efficiency of 97%) for 2.4GHz and 5.8GHz respectively which is attained through famous slotting technique.

CONCLUSION

Numerous parameters of the antenna discussed and analyzed like radiation pattern, gain, return loss and finally analysis of VSWR both through simulation and fabricated results for design's complete understanding. The results shows that gain and bandwidth for both 2.45GHz and 5.8GHz is much high and it makes the antenna in heavy competition with those in the field for the different RFID applications.

REFERENCES

[1] S. Mangal, Y.J. Kang and H. J. Lee. “Survey on security in Internet of Things: State of the art and challenges.” Advanced Communication Technology (ICACT), 2017 19th International Conference on. IEEE, 2017.
[2] M. Gaetano. "The art of UHF RFID antenna design: impedance matching and size-reduction techniques." IEEE Antennas and Propagation Magazine, pp. 66-79, 2008
[3] A. Ren; C. Wu; T. Wang; B. Yao, "A novel design for UHF near-field RFID reader antenna based on traveling wave," Communication Technology (ICCT), 2010 12th IEEE International Conference on , vol., no., pp.239,242, 11-14 Nov. 2010
[4] A.Iqbal, S.Shah, M. Amir, "Investigating Universal Filtered Multi-Carrier (UFMC) Performance analysis in 5G cognitive Radio Based Sensor Network (CSNs)" International Journal of Engineering Works,Vol. 4, Issue 1, PP. 5-9, Jan 2017.
[5] Z. N. Chen Fellow IEEE; X. Qing; Member IEEE and H. L. Chung, “A Universal UHF RFID Reader Antenna” IEEE Transactions on Microwave Theory and Techniques, vol.57, no.5, pp.1275, 1282, 27 March 2009
[6] C. A. Balanis Antenna Theory: analysis and design. Wiley-interscience, 2012.
[7] E. Michael. An investigation of reduced size planer fed microstrip patch antennas. Doctoral thesis Northumbria University, 2005.
[8] W.S. Chen, “Single-feed dual-frequency rectangular microstrip antenna with square slot,” Researcher own contribution – May 2019.