Optimized Harmonic Reduction PWM based Control Technique for Three-Phase Quasi Z-Source Inverter

Pritam Kumar Gayen¹*, Archak Sadhukhan ²

¹, ²Kalyani Government Engineering College, India
Email: ¹ pritamgayen@rediffmail.com, ² archksdkn@gmail.com

*Corresponding Author

Abstract—This paper proposes an optimized harmonic reduction pulse width modulation (HRPWM) control strategy for three-phase quasi Z-source inverter (qZSI). In traditional sinusoidal or space vector pulse width modulation techniques, the flexibility in adjustment of individual switching angles is not possible and thus, these techniques are not optimum choices for low switching frequency operations of high/medium power qZSI. In the proposed technique, adjustments of switching angles of HRPWM waveform are possible to achieve optimum performance. The optimum performance is targeted as maximization of boosting factor and simultaneous minimization of weighted total harmonic distortion (WTHD) at the output voltage of qZSI. The hybrid particle swarm optimization gravitational search algorithm (PSOGSA) is used for computation of optimum switching angles of suggested HRPWM waveform at various modulation indices. The obtained WTHDs up to 49th order harmonics and boosting factors of optimized HRPWM methodology are compared with that of the maximum boost control (MBC) technique for qZSI to justify superior performances of the suggested method in low switching frequency range. The proposed concept has been verified via simulation study. The experimentation (qZSI controlled by microcontroller) validates the working of optimized HRPWM based qZSI which agrees with software results.

Keywords—quasi Z-source inverter, Harmonic reduction pulse width modulation, Weighted total harmonic distortion, Voltage boost, PSOGSA optimization technique.

I. INTRODUCTION

In recent years, Z-source inverter (ZSI) / quasi Z-source inverter (qZSI) [1] based solar and fuel energy conversion systems [2-5] are getting importance for generation of electrical energy. Due to their growth in electrical power generation scenario, ZSI/qZSI is applied to grid level conversion process [6]. The ZSI/qZSI is single stage inverter topology having voltage boost capability. The circuit diagram of qZSI topology [7, 8] (improved version of ZSI) is shown in Fig. 1 which constitutes an impedance network in front of inverter bridge. The input side impedance network (Z-network) comprises of two capacitors (C1, C2), two inductors (L1, L2) and a diode (D) for boosting up the DC bus voltage for the inverter bridge.

The different control schemes [9] are developed as outcome of continuous research works in progress of time. In the context of control mechanism, type of pulse width modulation (PWM) technique decides the performance of qZSI. Various kinds of sine PWM (SPWM) or space vector PWM (SVPWM) method are proposed in published papers [3, 10-23]. Each kind of PWM method involves shoot-through duty ratio control to facilitate DC side voltage boosting process and also, simultaneous control of modulation index for obtaining expected voltage gain for qZSI. In earlier stage, the various SPWM techniques for qZSI are proposed as simple boost control (SBC) [3], maximum boost control (MBC) [11] and maximum constant boost control (MCBC) [14] PWMs. One unified control technique using space vector concept is presented in [16]. The comparative performance of theoretical and experimental evaluation of four no SVPWM techniques for qZSI are elaborately presented in the paper [17]. In very recent years, various improvements over traditional PWM methods, namely extended maximum boost control [21], improved PWM with maximum boost capability cum minimum switching frequency [22] and time-variant shoot-through (TVST) modulation [23] have been proposed. The harmonics productions in qZSI and comparative assessments are elaborately presented in articles [24, 25].

Various PWM schemes cited above are of high switching frequency. For medium or high power qZSI, comparatively lower switching frequency PWM scheme is desirable. But, low switching frequency PWM normally produces more harmonics which are required to be reduced by appropriate mechanism in PWM technique for avoiding/lowering filtering requirement. In addition to this, voltage boosting action is simultaneously necessary for the operation of qZSI. Therefore, the research work of this paper proposes an optimized harmonic reduction PWM (HRPWM) technique [26, 27] for qZSI in low switching frequency range. Here, the joint objectives are taken as minimization of weighted total harmonic distortion.
harmonic distortion (WTHD) and simultaneously maximization of boosting capability at various values of modulation index. In the logic, optimum switching angles of HRPWM signal are calculated using hybrid particle swarm optimization gravitational search algorithm (PSOGSA) [28] to meet the dual requirements. The MATLAB-SIMULINK 2014b software is used to validate the proposed methodology. Then, experimental work is also done in laboratory to justify the suggested concept. The values of WTHD up to 49th order and boosting factor at various modulation indices are compared with that of the MBC technique to prove the superiority of optimized HRPWM scheme in low switching frequency range.

The equations for describing the operations of qZSI are provided in section II. Section III discusses the proposed HRPWM methodology and allied analysis. Section IV presents both simulation and experimental studies to justify the superiority of suggested modulation concept. Finally, the core findings of the work are concluded in section V.

II. BASICS OF qZSI

The various equations for describing the operation of qZSI are presented in section 2. The qZSI has two operating states, namely shoot-through and non shoot-through states. The non-shoot through state covers both active and zero states. The shoot-through time span decides the amount of boosted voltage, whereas the non shoot-through state determines the output voltage of the inverter. Thus, any PWM logic for qZSI must incorporate shoot-through, active and/or zero states for the desirable operation of qZSI. Depending upon the type of PWM logic, the time duration of active state, zero state and shoot-through state varies. Hence, the performance of qZSI operated by any PWM logic is different from that of the other PWMs. The fundamental equations under two modes of operation can be summarized as follows:

The amplitude of input side boosted DC voltage during shoot-through time span is given by,

\[ V_m = \frac{1}{1 - 2D_{th}} V_s \]  

(1)

\[ B = \frac{1}{1 - 2D_{th}} \]  

(2)

Where \( D_{th} \) = shoot-through duty ratio, \( B \) = boosting factor. The peak value of fundamental component of output voltage is guided by the value of modulation index (\( M \)) and it can be expressed as,

\[ \tilde{V}_o = MBV_s / 2 \]  

(3)

Thus, the overall voltage gain (\( G \)) is defined from (3) as,

\[ G = MB = \frac{1}{1 - 2D_{th}} M \]  

(4)

III. PROPOSED OPTIMISED HRPWM METHODOLOGY

The fundamental idea of the proposed PWM method is the realization of combined objective as minimization of WTHD cum boosting factor maximization logic at each modulation index. This PWM pattern has comparatively lower switching transitions per cycle, thus it can be stated as efficient logic for high power converter. Both the objectives are targeted by the usage of optimization based approach to determine the switching angles (output variables for the mathematical problem). At first, the expressions of WTHD in terms of the switching angles are given. Then, the logic of incorporation of shoot-through span in the proposed PWM waveform and the determination of boosting factor are presented. Finally, PSOGSA based approach for achieving the aforementioned combined objective is presented as follows:

A. Expression of WTHD

The output pole voltage waveform of without incorporating shoot-through time span is shown in Fig. 2 which has \( m \) numbers of switching angles per quarter cycle waveform.

\[ v_o(t) = \sum_{n=1}^{\infty} \left( a_n \cos(n \omega t) + b_n \sin(n \omega t) \right) \]  

(5)

The \( \omega \) indicates harmonic order in (5). In (5), the coefficient \( a_n \) equals to zero due to odd symmetry of the waveform. The other coefficient \( b_n \) for the half-wave symmetry waveform can be calculated [29, 30] as,

\[ \begin{align*}
  b_n &= -\frac{4}{n \pi} \sum_{k=1}^{\infty} (-1)^k \cos(nk) \\
  b_n &= -\frac{4}{n \pi} \sum_{k=1}^{\infty} (-1)^k \cos(nk); n \neq 1, \text{even} \\
  b_n &= \frac{4}{n \pi} \sum_{k=1}^{\infty} (-1)^k \cos(nk); n = 1, \text{odd}
\end{align*} \]  

(6)

The coefficients in (6) are expressed in per unit (p.u. value. It is evident from (6) that the fundamental and harmonic components are dependent on the switching angles. For keeping harmonics values within tolerance level at a particular modulation index, (7) is formed as,

\[ \begin{align*}
  |b_n| &= M \\
  b_n &\leq \varepsilon; n \neq 1, \text{even, triplen}
\end{align*} \]  

(7)

The peak value of fundamental component of output voltage is guided by the value of modulation index (\( M \)) and it can be expressed as,

\[ \tilde{V}_o = MBV_s / 2 \]  

(3)

Thus, the overall voltage gain (\( G \)) is defined from (3) as,

\[ G = MB = \frac{1}{1 - 2D_{th}} M \]  

(4)

Triplen harmonics are omitted in (7) due to their absence in line-line voltage of three phase system under balanced condition. In this context, WTHD can be mathematically represented as,

\[ \text{WTHD} = \sqrt{\frac{1}{2} \sum_{n=3}^{\infty} \left( b_n / n \right)^2}; n = 6i \pm 1; i : 0 \rightarrow \infty \]  

(8)

Thus, WTHD indirectly depends on the value of switching angles via Fourier coefficients \( b_n \) using (8).

B. Identification and Incorporation of ST

It is expected that the shoot-through (ST) time span is inserted in the zero state of the proposed PWM signals for
attaining boosting action. The typical quarter cycle symmetrical PWM waveforms for power semiconductor switches of qZSI are shown in Fig. 3(a) which can be used for identifying the zero states of the PWM logic. When the PWM waveforms for upper three switches (S₁, S₃, S₅) have simultaneous high or low states (‘111’ or ‘000’ logic), the time span is considered as zero state of the PWM logic. It is evident from Fig. 3(a) that switching angles decide the amount of zero state time span i.e. the all one or all zero conditions of the upper switches in the PWM signals. The logical diagram for deciding the zero state as shown in Fig. 3(a) is presented in Fig. 3(b). Here, shoot-through state can be incorporated in PWM waveforms by covering whole zero states for maximization of boosting capability. The mean value of the cumulative shoot-through time span over the 1/6th of fundamental period can be computed from shoot through waveform to get the value of shoot-through duty ratio. Then, the boosting factor is calculated using (2) as shown in Fig. 3 (c).

C. Determination of Boosting Factor

Here, it is also necessary to measure the shoot-through duty ratio to quantify the obtained boosting factor. Therefore, at first, the various shoot-through time spans or pattern over a fundamental period are determined following logic shown in Fig. 3(b). Then, the mean of the total shoot-through time span over 1/6th of fundamental period are computed to get the value of shoot-through duty ratio. Finally, boosting factor is determined from the obtained shoot-through duty ratio using (2). These steps are shown in Fig. 3(c).

It can be commented from various discussions mentioned before under this section that the optimized choice of set of switching angles is necessary to maintain the balance between WTHD and boosting factor. In the context, hybrid particle swarm optimization gravitational search algorithm (PSOGSA) based approach is discussed in the next subsection.

D. PSOGSA Approach

From the above discussions, it can be stated here that the choice of switching angles plays crucial role for attaining minimum WTHD cum maximum boosting capability for each value of modulation index. Therefore, objective function (J ) combining the above targets are formed which is mathematically expressed in (9). The minimization of the objective function is considered in the study as reciprocal of boosting factor is taken in (9).

\[ J(\alpha) = \left[ \frac{1}{k_1} \sum_{i=2}^{m} (b_i/n)^2 \right]^{1/2} \cdot K_1 + (1/B) \cdot K_2 \tag{9} \]

In (9), the selection of weightage factors ‘k₁’ and ‘k₂’ decides accuracy of solution by maintaining balance between the two parts during minimization process. The corresponding constraints is written as,

\[ 0 < \alpha_1 < \alpha_2 \ldots \ldots < \alpha_m < \pi/2 \]

\[ D_{sh,\text{min}} < D_{sh} < D_{sh,\text{max}} \tag{10} \]

In (10), the additional constraint for shoot-through duty ratio is included in comparison to (7). For the constraint of duty ratio in (10), the upper and lower limits of shoot-through duty ratio (symbolized as \( D_{sh,\text{min}} \) and \( D_{sh,\text{max}} \) respectively) are included depending on the boosting range required in any specific application. In our case study, the limit of shoot-through duty ratio ranges between 0.1 and 0.5. The mathematical problem formed in (9) and (10) need the usage of optimization technique for determining switching angles to satisfy the both targets. In this paper, hybrid particle swarm optimization gravitational search algorithm (PSOGSA) technique is adopted for obtaining optimum values of switching angles. Here, the various steps for computation of switching angles by minimizing the objective function expressed in (9) using PSOGSA technique is presented in Fig. 4.

It can be summarized that the following steps are needed for production of optimized HRPWM waveforms:

1. ‘m’ numbers of switching angles per quarter cycle are to be decided as designer’s choice.
2. Optimum values of these switching angles are to be determined through simultaneous minimization of WTHD and maximization of BF using optimization algorithm.
3. Generation of three-phase PWM waveforms having quarter cycle symmetry using obtained optimum ‘m’ numbers of switching angles in step 2.
4. Production of ST pattern from quarter cycle symmetry PWM waveforms for upper three switches of qZSI.
5. Finally, HRPWM signals are generated via incorporation of obtained ST pattern into quarter cycle symmetry PWM waveforms for qZSI.
V. RESULTS

Both simulation study and experimental works are performed to validate the proposed optimization based PWM for the operation of qZSI. The passive parameters of qZSI used in these studies are given in Table I. The obtained results are presented as follows:

| TABLE I. Values of Various Parameters |
|-----------------------------|
| L<sub>1</sub>, L<sub>2</sub> | 4 mH |
| C<sub>1</sub>, C<sub>2</sub> | 470 μF |

A. Simulation Study

The programming for the calculation of switching angles using PSOGSA algorithm is written using MATLAB software. In the study, four switching angles per quarter wave are taken. The plot of obtained switching angles at various values of modulation index is presented in Fig. 5(a). The plot of WTHD versus modulation index is shown in Fig. 5(b). Here, WTHDs are computed up to 49th order harmonics. The corresponding values of the boost-up DC bus voltage, voltages of impedance network capacitors are presented in Fig. 8(c) and 8(d) respectively. Thus, the proposed concept is implemented in the simulation platform. In the next subsection, the hardware work is performed to validate the HRPWM concept.

Next, the performance of the proposed method is compared with that of the MBC scheme in Table 2 for various values of modulation index using SIMULINK software. The Table II indicates that superior performance in respect of BF and WTHD values are achieved in proposed case in comparison to MBC case at low switching frequency (450 Hz) operation.

Here, the generation of HRPWM signal is realized and applied to the switches of qZSI in SIMULINK platform. The input DC voltage is taken as 150 V. The PWM waveforms and allied shoot-through states at modulation index (M) of 0.8 p.u. (particular case study) are shown in Fig. 7. The plots of output phase voltage, current and FFT at the modulation index using SIMULINK software are given in Fig. 8. The phase voltage waveform is plotted in Fig. 8(a). The current waveform and its FFT spectrums are also shown in Fig. 8(b). The corresponding plot of boost-up DC bus voltage, voltages of impedance network capacitors are presented in Fig. 8(c) and 8(d) respectively. Thus, the proposed concept is implemented in the simulation platform. In the next subsection, the hardware work is performed to validate the HRPWM concept.
Pritam Kumar Gayen, Optimized Harmonic Reduction PWM based Control Technique for Three-Phase Quasi Z-Source Inverter

B. Experimental Study

The laboratory prototype is designed to implement optimized HRPWM for operation of IGBT (CT60AM-18F) based three phase qZSI. The load box having switched resistors and inductors are used for electrical loading purpose. No additional output filter is used. The overall experimental set up is presented in Fig. 9. The obtained optimized switching angles using MATLAB software are stored in the memory of microcontroller (PIC18F67K22) as look-up table. The HRPWM signals for six switches are produced using the
microcontroller from stored values of switching angles. The
gate drive PWM signals at modulation index of 0.8 p.u. are
shown in Fig. 10 (a), which is applied to the qZSI. The
corresponding responses of DC bus voltage, capacitors
voltages at the operating condition are shown in Fig. 10 (b).
The plots of output voltage, output current and FFT spectrum
of current are given in Fig. 10 (c) and (d) respectively. The 4-
channel, 200 MHz digital storage oscilloscope (KEYSIGHT:
DSOX2024A) is used to capture the various signals in
hardware platform. The overall experimental work justifies
the operation of qZSI with the proposed PWM technique.

From the studies, it can be stated the various significant
benefits of the proposed PWM method are as follows:

1. It can maintain optimum level of harmonics at its
   output as per standard [31] at low switching
   frequency range.

2. Higher boosting of voltage is noticed in comparison
   with MBC technique at a particular ‘M’.

3. Negotiation between WTHD and BF is possible
   using the proposed PWM logic.

Thus, the various notable features make this PWM
scheme effective for high power qZSI.

V. CONCLUSIONS

In this paper, Optimized HRPWM technique has been
proposed for three-phase qZSI. The suggested concept is
validated in both software and hardware platforms. The
optimized performance between minimization of WTHD and
maximization of voltage boosting capability has been
achieved at various values of modulation index. In this
context, the PSOGSA algorithm is used to obtain the
optimum switching angles at different values of modulation
index. The obtained optimum WTHDs up to 49th order
harmonics, boosting factors and sets of switching angles are
presented in the result section of this paper. It can be
concluded from various obtained results that the proposed
PWM approach is useful for medium or high power qZSI to
get superior performances in comparison with MBC
technique at low switching frequency operation.

REFERENCES

[1] A. Ayad, and R.Kennel, “A comparison of quasi-Z-source inverters
and conventional two-stage inverters for PV applications,” EPE
Y. P. Siwakoti, F. Z. Peng, F. Blaabjerg, P. C. Loh, and G. E. Town, “Impedance-source networks for electric power conversion part I: A topological review,” IEEE Trans. Power Electron., vol. 30, no. 2, pp. 699-716, Feb. 2015.

B. Ge, H. Abu-Rub, F. Z. Peng, Q. Li, A. T. de Almeida, F. J. T. E. Ferreira, D. Sun, and Y. Liu, “An energy stored quasi-Z-source inverter for application to photovoltaic power system,” IEEE Trans. Ind. Electron., vol. 60, no. 10, pp. 4468-4481, Oct. 2013.

O. Ellabban and H. Abu-Rub, “Z-source inverter: Topology improvements review,” IEEE Ind. Appl. Mag., vol. 10, no. 1, pp. 6-24, Mar. 2016.

F. Z. Peng, “Z-source inverter,” IEEE Trans. Ind. Appl., vol. 39, no. 2, pp. 504-510, Mar./Apr. 2003.

C. J. Gajananayake, D. M. Vilathgamuwa, P. C. Loh, R. Teodorescu, and F. Blaabjerg, “Z-source-inverter-based flexible distributed generation system solution for grid power quality improvement,” IEEE Trans. Energy Convers., vol. 24, no. 3, pp. 695-704, Sep. 2009.

Y. Li, J. Anderson, F. Z. Peng, and D. Liu, “Quasi-Z-source inverter for photovoltaic power generation systems,” in Proc. 24th Annual IEEE APEC, Feb. 15-19, 2009, pp. 918-924.

Y. Li, S. Jiang, J. G. Cintron-Rivera, and F. Z. Peng, “Modeling and control of quasi-Z-source inverter for distributed generation applications,” IEEE Trans. Ind. Electron., vol. 60, no. 4, pp. 1532-1541, Apr. 2013.

Y. P. Siwakoti, F. Z. Peng, F. Blaabjerg, P. C. Loh, G. E. Town, and S. Yang “Impedance Source Networks for Electric Power Dynamic Control Part II: Review of Control and Modulation Techniques,” IEEE Trans. Power Electron., vol. 30, no. 4, pp. 1887-1906, Apr. 2015.

A. Abdelhakim, F. Blaabjerg, and P. Mattavelli, “Modulation schemes of the three-phase impedance source inverters-Part I: Classification and review,” IEEE Trans. Ind. Electron., vol. 65, no. 8, pp. 6309-6320, Aug. 2018.

F. Z. Peng, M. Shen, and Z. Qian, “Maximum boost control of the Z-source inverter,” IEEE Trans. Power Electron., vol. 20, no. 4, pp. 833-838, Jul. 2005.

Q. Lei, D. Cao, and F. Z. Peng, “Novel loss and harmonic minimized vector modulation for a current-fed quasi-Z-source inverter in HEV motor drive application,” IEEE Trans. Power Electron., vol. 29, no. 3, pp. 1344-1357, Mar. 2014.

A. Abdelhakim, P. Davari, F. Blaabjerg, and P. Mattavelli, “Switching loss reduction in the three-phase quasi-Z-source inverters utilizing modified space vector modulation strategies,” IEEE Trans. Power Electron., vol. 33, no. 5, pp. 4045-4046, May 2018.

M. Shen, J. Wang, A. Joseph, F. Z. Peng, L. M. Tolbert, and D. J. Adams, “Maximum constant boost control of the Z-source inverter,” in Proc. IEEE Annu. Ind. Appl. Conf., Oct. 2004, pp. 142-147.

Y. Yang, L. C. Xie, and J. Ding, “Pulse width modulation of Z-source inverters with minimum inductor current ripple,” IEEE Trans. Ind. Electron., vol. 61, no. 1, pp. 98-106, Jan. 2014.

S. Yang, X. Ding, F. Zhang, F. Z. Peng, and Z. Qian, “Unified control technique for Z-source inverter,” in Proc. IEEE Power Electron. Spec. Conf., Jun. 2008, pp. 3236-3242.

Y. Liu, B. Ge, H. A. Rub, “Theoretical and Experimental Evaluation of Four Space-Vector Modulations Applied to Quasi-Z-Source Inverters,” IET Power Electron., vol. 6, no. 7, pp. 1257-1269, 2013.

Y. Liu, B. Ge, H. Abu-Rub, and F. Z. Peng, “Overview of space vector modulations for three-phase Z-source/quasi-Z-source inverters,” IEEE Trans. Power Electron., vol. 29, no. 4, pp. 2098-2108, Apr. 2014.

Y. He, Y. Xu, and J. Chen, “New space vector modulation strategies to reduce inductor current ripple of Z-source inverter,” IEEE Trans. Power Electron., vol. 33, no. 3, pp. 2643-2654, Mar. 2018.

S. Nassereddine, S. Mekhlef, and A. Masoud, “A simplified time-domain modulation scheme-based maximum boost control for three-phase quasi-Z-source inverters,” IEEE J. Emerg. Sel. Topics Power Electron., vol. 6, no. 2, pp. 760-769, Jun. 2018.

N. Sabeur, S. Mekhlef, A. Masoud, “Extended maximum boost control scheme based on single-phase modulator for three-phase Z-source inverter,” IET Power Electron., vol. 9, no. 4, pp. 669-679, 2016.

Y. Zhang, J. Liu, X. Li, X. Ma, S. Zhou, H. Wang, and Y. Liu, “An improved pwm strategy for z-source inverter with maximum boost capability and minimum switching frequency,” IEEE Trans. on Power Electron., vol. PP, no. 99, pp. 606-628, 2017.

Y. Zhou, Q. Wu, Z. Li, and F. Hong, “Research on a Time-variant Shoot-through Modulation Strategy for Quasi-Z-source Inverter,” IEEE Trans. on Power Electron., vol. 33, no. 11, pp. 9104-9109, Nov. 2018.

P. Liu, J. Xu, Y. Yang, H. Wang and F. Blaabjerg, “Impact of modulation strategies on the reliability and harmonics of impedance-source inverters,” in IEEE Journal of Emerging and Selected Topics in Power Electron., Early Access, pp. 1-1.

A. Abdelhakim, F. Blaabjerg, and P. Mattavelli, “Modulation schemes of the three-phase impedance source inverters-Part II: Comparative assessment,” IEEE Trans. Ind. Electron., vol. 65, no. 8, pp. 6321-6332, Aug. 2018.

R. Salehi, N. Farokhinia, M. Abedi, and S. H. Fathi, “Elimination of low order harmonics in multilevel inverters using genetic algorithm,” J. Power Electron., vol. 11, no.2 pp. 132-139, Mar. 2011.

Y. Chen, X. Guo, J. Xue, and Y. Chen, “Hybrid PWM Modulation Technology Applied to Three-Level Topology-Based PMSMs,” J. Power Electron., vol. 19, No. 1, pp. 146-157, Jan. 2019.

S. Mirjalili, S.Z.M.Hashim, “A new hybrid PSOGA algorithm for function optimization,” in Int. Conf. on Computer and Information Application (ICCAI 2010), 2010, pp. 374-377.

J. Chiasson, L. M. Tolbert, K. McKenzie, and Z. Du, “A complete solution to the harmonic elimination problem,” IEEE Trans. Power Electron., vol. 19, no. 2, pp. 491-499, Mar. 2004.

S. Ganguly, A. Sadhukhan, P. K. Gayen and S. Dolui, “Hardware implementation of single-phase full-bridge VSI switched by PSO based SHEPWM signals using embedded PIC microcontroller,” 2017 IEEE Calcutta Conference (CALCON), Kolkata, 2017, pp. 341-346. doi: 10.1109/CALCON.2017.820752.

C.K. Duffey, R.P. Stratford, “Update of harmonic standard IEEE-519: IEEE recommended practices and requirements for harmonic control in electric power systems,” IEEE Trans. Ind. Appl., vol. 25, no. 6, pp. 1025-1034, Nov./Dec. 1989.