DSTATCOM for Harmonic Mitigation in Distribution Lines using Two-Level Inverter

Vidya M., P. Pramila, A. M. Nagaraj

Abstract: Distribution systems have been facing serious problems of harmonics load current mainly due to advancement in power electronic based and other non-linear loads. The DSTATCOM has been widely used to mitigate the load current harmonics problems in distribution system. The power quality improvement is one of the major problems when the distribution side load increases with non-linear loads like electric vehicles, laptops, PCs etc. There are some power quality mitigation technique available at the load side where the electronic chargers works with unity power factor (UPC) control. But many DC loads are connected without the UPC. So, it is a need for a device which corrects the real and reactive power at the distribution level. The DSTATCOM is connected to the Indian distribution system with 415V, 50Hz. In this paper the linear loads and nonlinear loads are coupled to the system and analysis with DSTATCOM and without DSTATCOM cases are presented.

Keywords: DSTATCOM, Distribution systems, Unity power factor control, non-linear loads.

I. INTRODUCTION

Distribution system now days are facing several power quality problems like poor power factor, poor voltage regulation, harmonics current burden, unbalanced loading, neutral current etc. The usage of semiconductor devices in recent DC devices increases the power quality problems. For any loading condition the power quality has to be good [1]. Even the source values changes and load changes the power quality has to be consistent. The power quality issues due to source side changes are voltage sag and swell, At the load side current harmonics and flicker problems are the major problems. Harmonics current is mainly because of use of non-linear nature of load and poor power factor and poor voltage regulation is mainly due to the inductive load and line drop respectively. Many Custom Power Devices like DVR, UPQC are proposed for mitigating the power quality problems in distribution system. Distribution Static Compensator is shunt connected device which is able to mitigate load current based power quality problems. It injects compensating current such that load current based power quality problems like voltage sag, voltage swell, unbalance, loading, poor power factor, poor voltage regulation, harmonics etc. can be mitigated using the DSTATCOM. The multilevel inverter-based FACTs devices are used for reduction of the passive elements to get better power quality [2,3]. Here the two-level inverter is used as the DSTATCOM to mitigate the power quality problems like harmonics and the power factor. Hence the classical two-level inverter is used to improve the power quality in the distribution side of the power system.

II. BASIC OPERATING PRINCIPLE

The DSTATCOM single line illustration is shown in figure 2.1. The inverter which has the two-level switches is connected with the capacitor at DC side. The AC side of the inverter is connected with the isolating transformer and leakage inductance of the transformer.

![Fig 2.1 Single Line illustration of DSTATCOM](image)

The inverter is controlled as such the phase shift of the injected current angle changes. This change creates the changes in the reactive power. If there is lag of current with respect to voltage, then the DSTATCOM works as the inductive mode of operation. If the current leads than the voltage then it works as the capacitive injection mode. Most of the power system is loaded with inductive so the DSTATCOM generally works like a capacitor where the capacitance can be varied precise and maintain the voltage. To make sure the operation of the DSTATCOM the DQ0 based control technique is used. Here the D- takes care of real part of the power and Q takes care of the reactive part. The energy stored in the capacitor is used in the power system to compensate the harmonics.
III. CONVERTER SCHEMES USED FOR REACTIVE POWER GENERATION.

The 3.1 shows the reactive power converters used in practical. It can be single phase or three phase two level inverter or three phase three level. The converters are made with MOSFETS and the two level three phase converter is used in this paper.

![Diagram of converter schemes](image)

Fig:3.1 elementary converter schemes used for reactive power generation.

There is a requirement of pulse width modulation in the two-level inverter. Here sinusoidal pulse width modulation (SPWM) is used to switch the MOSFET. The sine wave is generated from the DQ0 control unit and the controlled DQ is given to DQ0-ABC converter to produce the modulating waveform. This modulating waveform is given to saw tooth comparison and the pulses are created. These pulses are given to MOSFET. The sine wave used for modulation is the output signal of controller.

IV. SIMULATION AND RESULTS

| Devices       | Parameters | Values |
|---------------|------------|--------|
| Linear Load   | Voltage in V | 415    |
|               | Frequency in Hz | 50    |
|               | Power in MVA | 100kVA |
| Non-linear load (rectifier) | Real power in W | 5000 |
|               | Reactive power VA | 3000 |
| Line parameters | R in ohms | 20     |
|               | L in H | 0.1    |
|               | C in F | 500u   |
| DSTATCOM      | X in mH | 0.05   |

Table:4.1 Parameters used for simulation

The test system is constructed in MATLAB with 415 V and 50Hz system. The base power of the system is 100kVA. The transmission line parameters are taken as 0.001 Ω. The reactance of the system is 0.05mH. The linear load of 5000W and 3000 VA are used. And as nonlinear load the rectifier bridge for three phases is used with load of 20Ω.

(i) Without DSTATCOM

![Without DSTATCOM](image)

Fig:4.1 Without DSTATCOM (test system)

![Grid voltage and Current](image)

Fig:4.2 Grid voltage and Current

![Total harmonic distortion (THD) for without DSTATCOM](image)

Fig:4.3 Total harmonic distortion (THD) for without DSTATCOM
This problem can be mitigated by using the DSTATCOM.

(ii) with dstatcom using 2-level inverter

![Diagram](image)

**Fig: 4.4 With 2-level inverter DSTATCOM (test system)**

![Graph](image)

**Grid Voltage and Current with DSTATCOM**

**Table: 4.2 Comparison Of Frequency And THD With And Without Dstatcom**

| Parameters          | Without DSTATCOM | With DSTATCOM |
|---------------------|------------------|---------------|
| Fundamental Frequency (50Hz) | 40.68            | 39.94         |
| THD                 | 21.88%           | 6.41%         |

![Graph](image)

**Fig: 4.6 total harmonic distortion (thd) for with dstatcom**

V. CONCLUSION

Custom Power (CP) devices can be used, at reasonable cost, to provide high power quality and improved power service. One such device used in this work is the DSTATCOM. These Custom Power devices provide solutions to power quality at the medium voltage distribution network level. This paper presents the modeling and simulation of DSTATCOM used in the distribution lines. The Indian system of 415 V, 50Hz system is constructed and the non-linear loads are connected to it. It produces a 21.88% of harmonics which is out of the IEEE standards. The DSTATCOM is placed using the 2-level inverter. This DSTATCOM changed the waveform shape of the current and the required compensation is done. It gives harmonics of 6.41%. Further the work can be extended using three-level inverter to get better results as compared to two-level inverter.
REFERENCES

1. D. Masand, S. Jain, G. Agnihotri "Control Strategies for Distribution Static Compensator for Power Quality Improvement", IETE Journal of Research, Industrial Electronics, Vol. 54, 2008, pp. 421-428.

2. R. Coteli, B. Dandil and F. Atia, Fuzzy-PI Current Controlled DSTATCOM, Gazi University Journal of Science 2 4-1 (2011), pp.91-99.

3. K. Amit, D. Kumar, and A. Yadav. "Power Quality Improvement of Power Distribution System Under Symmetrical and Unsymmetrical Faults Using D-STATCOM," Advances in Energy and Power Systems. Springer, Singapore, 2018, pp. 111-121.

4. A. Shukla, A. Ghosh, and A. Joshi, "A hysteresis current controlled 2 level multilevel inverter based DSTATCOM," in Proc. IEEE Power Eng. Soc. General Meeting, San Francisco, Jun. 12–16, 2005, pp. 1801–1808.

5. G. Ramya, V. Ganapathy, and P. Suresh. "Power quality improvement using multi-level inverter based DVR and DSTATCOM using neurofuzzy controller." International journal of power electronics and drive systems (IJPEDS) 8.1 (2017), pp. 316-324.

6. M. Perez, A. Bernet, S. Rodriguez, J. Kouro, and R. Lizana. "Circuit topologies, modeling, control schemes, and applications of modular multilevel converters". IEEE transactions on power electronics, 30(1) (2015), pp. 4-17.

7. Jin-Woo Jung, PhD Student, Space Vector PWM Inverter, Department of Electrical and Computer Engineering, Ohio State UNIVERSITY.

8. Lalili D., Berkouk E. M., Boudjema F., Louiei N., "Space vector pulse width modulation algorithm for three-level diode clamped inverter", 4th International Conference on Electrical Engineering, 07-08 November 2006, Batna, Algeria, pp. 443-448.

9. Design and Implementation of Three-Level Space Vector PWM IP Core for FPGA's Haibing Hu, Wenxi Yao, and Zhengyu Lu, Senior Member. IEEE TRANSACTIONS ON POWER ELECTRONICS, VOL. 22, NO. 6, NOVEMBER 2007

10. 32. MENDEL, J.M., Uncertain Rule-Based Fuzzy Logic Systems: Introduction and New Directions. Prentice Hall, 2001. [11] Buhler «Règles par logiquefloue » vol 2, Press polytechnique romandes, Suisse 1994.

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