Nanoelectronic thermometers optimised for sub-10 millikelvin operation

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We report the cooling of electrons in nanoelectronic Coulomb blockade thermometers below 4 mK. Above 7 mK the devices are in good thermal contact with the environment, well isolated from electrical noise, and not susceptible to self-heating. This is attributed to an optimised design that incorporates cooling fins with a high electron-phonon coupling and on-chip electronic filters, combined with a low-noise electronic measurement setup. Below 7 mK the electron temperature is seen to diverge from the ambient temperature. By immersing a Coulomb Blockade Thermometer in the $^3$He/$^4$He refrigerant of a dilution refrigerator, we measure a lowest electron temperature of 3.7 mK.

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Cooling nanoelectronic structures to millikelvin temperatures presents extreme challenges in maintaining thermal contact between the electrons in the device and an external cold bath\(^1\). It is typically found that the electrons in a nanoelectronic device cooled even to \(\sim 10\text{ mK}\) are significantly overheated. Understanding how to obtain and measure electron temperatures approaching \(1\text{ mK}\) will open a new regime for studying nanoelectronics, and pave the way towards pioneering sub-millikelvin techniques\(^2\). This would benefit, for example, investigations of the fractional quantum Hall effect in 2D electron gases\(^3,4\), and solid-state quantum technologies including superconducting and semiconducting qubits. To access these temperatures, one must minimise parasitic heating and internal Joule heating, maximise the coupling to cold contact wires and phonons in the host lattice, all the while overcoming the decrease in electron-phonon coupling and electrical heat conduction as temperatures drop.

Here we study Coulomb Blockade Thermometers (CBTs) that have been designed to operate significantly below \(10\text{ mK}\) and demonstrate cooling of electrons in a nanoelectronic device below \(4\text{ mK}\). This was achieved using CBTs as a diagnostic tool to quantify and optimise the thermal environment of the electrons in the device. A CBT consists of an array of Coulomb-blockaded metallic islands connected by tunnel junctions whose conductance is temperature dependent\(^5,6\). CBTs typically function over a decade of temperature and have previously been demonstrated to work at temperatures as low as \(7.5\text{ mK}\). Perhaps most importantly, they can be viewed as a primary thermometer of their internal electron temperature.

The structure of the CBTs studied here is shown in Fig. 1. Devices are fabricated using an ex situ tunnel junction process\(^8\) that provides excellent tunnel junction uniformity\(^9\), and has also been used to fabricate superconducting qubits\(^10\). In this work the CBTs are made from Al films with a thickness 250 nm and an isolating layer of 250 nm SiO\(_x\) deposited by PECVD. The tunnel junctions have a nominal diameter \(0.8\text{ \(\mu\text{m}\)}\) and a resistivity \(\sim 10\text{ k\(\Omega\)\(\mu\text{m}\)}^2\). The substrate is undoped Si with 300 nm thermal oxide on the surface.

Efficient thermalisation of electrons and phonons in the metallic islands of the CBT is critical for reaching low electron temperatures\(^11,12\). The electron-phonon heat flow \(P_{ep}\) is described by the material-dependent electron-phonon coupling constant \(\Sigma\) and the volume of the metallic island \(\Omega\),

\[
P_{ep} = \Sigma \Omega \left( T_e^5 - T_p^5 \right) \tag{1}
\]
FIG. 1. Optical micrograph of the CBT with equivalent circuit diagram (left) and schematic cross-section of the structure (right). The CBT is formed of $32 \times 20$ metallic islands of capacitance $C_\Sigma$ connected in an array by tunnel junctions of resistance $R_T$, as shown in the circuit diagram. Connection to the array is made via on-chip $RC$ filters comprising a meandering electrode sandwiched between large-area grounded metal films, separated by 250 nm SiO$_x$. Each filter has a distributed resistance $R \approx 500 \, \Omega$ and capacitance $C \approx 10 \, \text{pF}$. The schematic cross-section (right) shows one tunnel junction connecting two islands, with Au thermalisation blocks on top.

where $T_e$ is the electron temperature and $T_p$ is the phonon temperature$^{13}$. To minimise $T_e$, the island volume should be large and the material chosen to maximise $\Sigma$. The island thickness achievable with the ex situ tunnel junction process, or other deposition techniques used for tunnel junction devices, is typically up to 1 $\mu$m. Thicker films suffer from stress build-up, causing poor adhesion between the film and the substrate. This is a severe problem at mK temperatures where poor adhesion can lead to poor thermalisation and even mechanical failure due to thermal motion during cool-down. We avoid these problems by using a combination of the ex situ process followed by masked electroplating of Au on top of the CBT islands$^{14}$, which we refer to as thermalisation blocks. Electroplating can produce $\sim 10 \, \mu$m thick, low stress films, and here we choose a nominal thickness of 5 $\mu$m for the thermalisation blocks. The effective electron-phonon coupling in these islands, with nominal volume $5 \times 205 \times 38.5 \, \mu$m$^3$ and a high coupling constant$^{15}$ in Au $\Sigma = 2.4 \times 10^9 \, \text{W K}^{-5} \, \text{m}^{-3}$, is estimated to be more than two orders of magnitude larger than in previous CBTs fabricated using the ex situ junction process$^{16}$.

In addition to the thermalisation of the CBT itself it is important to thermalise the
incoming leads through robust thermal anchoring and heavy electromagnetic filtering\textsuperscript{17}. We improve the chain of thermalisation and filtering by including on-chip resistive meander structures in line with all electrical contacts. These form a distributed resistive-capacitive chain with a cut-off frequency of $\approx 40\,\text{MHz}$. Similar filters based on a large area capacitor and tunnel junctions in series have previously been incorporated in a CBT\textsuperscript{16}.

Figure 2 shows the behaviour of a CBT, fabricated using the process described above, focusing on temperatures between 7 mK and 80 mK. The sensor was measured in a commercial cryogen-free dilution refrigerator\textsuperscript{18} with a base temperature $\approx 7\,\text{mK}$ and later in a custom dilution refrigerator manufactured at Lancaster University\textsuperscript{19} with a base temperature $\approx 2.5\,\text{mK}$ (see below). The conductance of the CBT was measured in a current-driven four-wire configuration, with the drive current and voltage amplification provided by an Aivon PA10 amplifier. A small AC excitation (typically $5\,\text{pA} \leq I_{\text{AC}} \leq 100\,\text{pA}$) was added to the DC bias $I_{\text{DC}}$, allowing the differential conductance $G$ to be measured with a lock-in amplifier.

As shown in Fig. 2(a), the CBT conductance dips around zero bias, and the dip becomes deeper and narrower at lower temperatures. Its full-width at half maximum is related to temperature by $V_{1/2} \approx 5.439Nk_B T/e$, where $N$ is the number of tunnel junctions in series\textsuperscript{5}. This does not account for self-heating in the sensor and so is only applicable when $T = T_e = T_p$. A more practical parameter to determine temperature is the zero-bias conductance $G_0$, which has an approximate analytic relation to temperature\textsuperscript{20}

$$G_0 \approx G_T \left(1 - 1/6u_N - 1/60u_N^2 + 1/630u_N^3\right) \quad (2)$$

where $u_N \equiv E_C/k_BT$ is dimensionless inverse temperature, $E_C \equiv [(N-1)/N]e^2/C_\Sigma$ is the charging energy of the system, $C_\Sigma$ is the total capacitance of each island, and $G_T$ is the asymptotic conductance. When $u_n < 2.5$ the temperature measurement error is $< 2.5\%$\textsuperscript{12}. Thus, if $C_\Sigma$ and $G_T$ are known it is possible to determine $T$ by measuring only $G_0$. The most complete method to determine temperature is using a full tunnelling model to calculate $G(V_{\text{DC}})$ numerically\textsuperscript{5}. We use this last approach to find $C_\Sigma$ and $G_T$ for the device in order to then determine the temperature.

Numerical calculations of conductance are made using an algorithm derived from the free, open-source library pyCBT\textsuperscript{21}. In addition, we account for overheating in the sensor by predicting the electron temperature $T_e$ in the islands using a model for the heat flow into
FIG. 2. CBT behaviour between 80 mK and 7 mK combining measurements made in two dilution refrigerators. (a) CBT conductance $G$ versus measured bias voltage $V_{DC}$ at four temperatures. Symbols show measured values and lines show best fits to the calculated ideal conductance (see text for details of fitting). The warmest measurements (crosses, circles and squares) are fitted simultaneously to calibrate the sensor, giving $C_\Sigma = 236.6$ fF and $R_T = 22.42$ kΩ. The coldest measurement (triangles) was fitted using this calibration. The minimum electron temperatures $T_{e}^{\text{min}}$ are in close agreement with the refrigerator temperature measured by the RuO$_2$ thermometer: 59.9 mK, 40.1 mK, 20.0 mK, and 7.2 mK respectively. (b) CBT electron temperature $T_e$ vs refrigerator temperature $T_{\text{mxc}}$. Symbols show $T_e^{\text{min}}$ from fits to conductance dips measured in the cryogen-free refrigerator (circles) and the custom refrigerator (triangles). The solid line shows $T_e$ determined by monitoring the conductance at $V_{DC} = 0$ as the cryo-free fridge cooled over 35 min, showing that the CBT has a stronger thermal link to the refrigerator than the RuO$_2$ thermometer, leading to the thermal lag ($T_{\text{mxc}} \geq T_e$) during this time.
each island,

\[ P = \frac{V_{DC}^2}{R_T} - \Sigma\Omega \left( T_e^5 - T_p^5 \right) + P_0 \]  

(3)

where the first term is Joule heating at tunnel junctions of resistance \( R_T \), the second term is heat flow to phonons, and \( P_0 \) accounts for parasitic heating. For a given phonon temperature, the minimum electron temperature \( T_e^{\text{min}} \) is found at \( V_{DC} = 0 \). If \( P_0 \) is small, then \( T_e^{\text{min}} \approx T_p \).

Figure 2(a) shows the result of fitting the calculated \( G(V_{DC}) \) simultaneously to three measurements made between 20 mK and 60 mK. The fit parameters are \( G_T \), \( C_\Sigma \), and \( T_p \) for each measurement. The fit was found to be insensitive to the value of \( P_0 \), and hence parasitic heating is assumed to be negligible at these temperatures. Having calibrated the CBT, the fitted \( C_\Sigma \) was used to fit further measurements with \( T_p \) and \( G_T \) as the free parameters. An example is given in Fig. 2(a), where the electron temperature is found to be 7.3 mK at a refrigerator temperature of \( T_{mxc} = 7.2 \) mK, here measured by a calibrated RuO\(_2\) resistor\(^{22} \).

Figure 2(b) shows that the electron temperature measured by the CBT is in close agreement with the refrigerator temperature \( T_{mxc} \) in both refrigerators between 7 mK and 80 mK. In the custom refrigerator, \( T_{mxc} \) is measured using a conventional Vibrating Wire Resonator viscometer (VWR) immersed in the saturated dilute phase of the \(^3\)He/\(^4\)He refrigerant in the mixing chamber\(^{23,24} \). For this set of measurements, in both refrigerators the CBT is in vacuum and housed in a gold-plated copper package (Aivon Oy SH-1) that is attached to the mixing chamber plate. The package includes RC filters with a cut-off frequency \( \approx 300 \) kHz. Electrical contacts to the CBT are thermalised in additional cold RC filters potted in Ec-cosorb CR 124 (Aivon Oy ‘Therma’), which are also attached to the mixing chamber plate. The filters have a cut-off frequency \( \approx 15 \) kHz.

Subsequently we investigated the behaviour of the CBT below 7 mK in the custom dilution refrigerator. Figure 3(a) shows the electron temperature of the sensor, when in vacuum, gradually cooling below 4 mK while the refrigerator was held at \( T_{mxc} \approx 2.8 \) mK. Here \( T_e \) was determined from the value of \( G_0 \), which was found by measuring \( G \) over a small range of \( V_{DC} \) (\( \approx 30 \) µV) close to \( V_{DC} = 0 \). We observe an extremely long equilibration time (over 3 days) but a rapid cooling of the CBT following a heating event (a refill of the liquid helium bath that briefly heated the CBT above 5.5 mK and the fridge above 3.5 mK). This suggests that the thermal contact between the CBT and the refrigerator is relatively strong, and that its slow cooling is due to heat leak from an external warm object.

A second CBT was immersed in the \(^3\)He/\(^4\)He refrigerant of the custom dilution refrigerator
FIG. 3. Thermalisation of two CBTs as a function of time at a refrigerator temperature of $T_{\text{mxc}} \leq 2.8 \, \text{mK}$. (a) shows cooling of one CBT in vacuum (circles) and one immersed in the $^3\text{He}/^4\text{He}$ refrigerant of the dilution refrigerator (triangles). In both cases, the CBTs are cooling after being warmed above 10 mK by temporarily increasing the refrigerant temperature. The CBT in vacuum is extremely slow to thermalise. By comparison, the CBT immersed in $^3\text{He}/^4\text{He}$ thermalises significantly faster. The inset shows cooling of the immersed CBT after it has been heated by a large DC drive current (50 nA, 40 nA and 30 nA for run 1, 2 and 3 respectively). Fitting to an exponential decay (solid line) yields a time constant of 570 s and a saturation temperature of 3.8 mK. (b) shows schematic of the immersion cell.

to improve thermal coupling and better isolate external sources of heating. A schematic of the immersion cell is shown in the Fig. 3(b). Sintered silver blocks increase the thermal contact between the refrigerant and the CBT and contact wires. Several sinters was attached to the sensor package, to each of the four measurement wires, and a grounding wire for the package and the $RC$ filters. The immersed CBT was found to equilibrate much faster, as shown in Fig. 3(a), reaching $T_e = 3.8 \, \text{mK}$ at $T_{\text{mxc}} = 2.7 \, \text{mK}$.

In order to study the CBT at $|V_{\text{DC}}| \gg 0$, the time needed for the sensor to reach thermal equilibrium after a change of Joule heating needs to be known. The inset to Fig. 3(a) shows the relaxation of $T_e$ after the CBT has been heated by a large drive current for long enough to reach thermal equilibrium (> 30 min). The subsequent value of $T_e$ is measured by scanning close to $V_{\text{DC}} = 0$, where Joule heating should be negligible. The relaxation of $T_e$ is found to
FIG. 4. Characteristics of a CBT immersed in $^3$He/$^4$He refrigerant. (a) Fitting to the warmest three measurements gives $C^\Sigma = 209.5 \, \text{fF}$ and $R_T = 23.21 \, \text{k}\Omega$. The fitted minimum electron temperatures $T_{e}^{\text{min}}$ for the warmest three curves are in reasonable agreement with the refrigerator temperature as measured by the VWR thermometer: 29.4 mK, 19.0 mK, 10.5 mK respectively. (b) shows measured electron temperature in the CBT as the refrigerator cooled steadily from 10 mK to 2.7 mK over a period of 12 hours. The solid line shows a fit of the form $T_e^x = T_{\text{mxc}}^x + c$, yielding an exponent $x = 2.7$. The dot-dashed line shows a best fit of $T_e^5 = T_{\text{mxc}}^5 + c$. The inset to (b) shows the measured $T_e$ as the refrigerator was temporarily cooled in single-shot mode to 2.2 mK, reaching a lowest $T_e$ below 3.7 mK.

have a time constant of 570 s.

Figure 4(a) shows the calibration of the immersed sensor. The three warmest measurements were fitted simultaneously to determine $C^\Sigma = 209.5 \, \text{fF}$ and $R_T = 23.21 \, \text{k}\Omega$. The fitted temperatures agree with $T_{\text{mxc}}$ to within 6%. Given the agreement between the fitted $T_{e}^{\text{min}}$ and $T_{\text{mxc}}$, we can assume that parasitic heating is still negligible down to 10 mK.
The coldest measurement in Fig. 4(a) was fitted using the above values, yielding a minimum electron temperature of 3.9 mK. This measurement was made over a period of 7 hours to ensure that the CBT was in thermal equilibrium at each value of $V_{DC}$. At these temperatures the parasitic heating of the CBT is now significant and $T_{e}^{\min}$ does not match the refrigerator temperature of $T_{mx}=2.7$ mK. In order to fit this conductance dip the thermal model, Eq. 3, was used with $T_{p}=T_{mx}$, and with the parasitic heating $P_{0}$ and the electron-phonon coupling $\Sigma\Omega$ as free parameters.

The overheating of the sensor at $V_{DC}=0$ constrains the product $P_{0}\Sigma\Omega$; however, the parasitic heating is not large enough to reliably separate $P_{0}$ and $\Sigma\Omega$ in the fit. Qualitatively, the fits suggest that $P_{0} \geq 300$ aW per island and $\Sigma\Omega$ is at least four times larger than expected from the nominal size of the thermalisation blocks and the literature value of $\Sigma$ for Au$^{15}$. It was not possible to determine an upper bound on $P_{0}$. It is worth noting that the power required to measure the CBT conductance ($<1$ aW per island due to Joule heating from $I_{AC}$) is much lower than our estimate of $P_{0}$. As such, we believe that CBTs of this type can be operated at still lower temperatures by reducing parasitic heating.

Figure 4(b) shows how the CBT electron temperature diverges from the refrigerator temperature below $\approx 7$ mK. Here the value of $T_{e}$ is found by measuring $G_{0}$ close to $V_{DC}=0$ and so Joule heating can be neglected. The functional form of $T_{e}$ vs $T_{mx}$ should have the same temperature dependence as the dominant thermalisation mechanism, i.e. $T^{5}$ for electron-phonon coupling. However, other power laws have been observed$^{7}$. Here we find that the best fit of $T_{e}^{x}=T_{mx}^{x}+c$ gives $x=2.7$ and a saturated $T_{e}$ of $c^{1/x}=3.4$ mK. The fitted exponent $x$ cannot be confirmed by measurements of the conductance dip because the overheating is still relatively weak even at the lowest temperatures. We find that a thermal model with a $T^{3}$ thermalisation term fits the data equally well as a model using $T^{5}$. In order to understand the thermalisation mechanism in more detail, this sensor would need to be cooled closer to 1 mK.

In conclusion, CBTs of the structure described here have been shown to operate as reliable primary thermometers of electron temperature down to 3.7 mK. The large thermalisation blocks incorporated in the device, and a relatively low level of parasitic heating, ensure that the electron subsystem in the sensor is well coupled to the phonon subsystem down to $\approx 7$ mK. An immersion cell was shown to improve thermal coupling between a CBT and a dilution refrigerator. This allowed the onset of overheating to be observed below 7 mK, and
while the presence of overheating could be seen, the effect was sufficiently weak that the sensor will need to be cooled further to fully characterise the thermalisation mechanisms.

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