Design of Robust UPFC Controller Using $H_\infty$ Control Theory in Electric Power System

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Abstract: An industrial plant, such a power system, always contains parametric uncertainties. In the design of a controller the uncertainties have to be considered. Otherwise, if the real plant differs from the assumed plant model, a controller, designed based on classical controller design approaches, may not ensure the stability of the overall system. In this paper design of robust control for the UPFC controllers including power - flow and DC voltage regulator, using a $H_\infty$ loop-shaping design via a normalized coprime factorization approach, where loop-shape refers to magnitude of the loop transfer function $L = GK$ as function of frequency is presented. As an example, we have designed a case for the system to compare the proposed method with a conventional method (classical P-I controller). AS the results of the linear and nonlinear simulations, the validity of the proposed method has been confirmed.

Key words: Unified Power Flow Controller (UPFC); Flexible AC Transmission Systems (FACTS); power system oscillation; $H_\infty$ loop-shaping

INTRODUCTION

The Flexible AC Transmission Systems (FACTS) based on power electronics offer an opportunity to enhance controllability, stability, and power transfer capability of AC transmission systems [1]. The Unified Power Flow Controller (UPFC), which is the most versatile FACTS device, has the capabilities of controlling power flow in the transmission line, improving the transient stability, mitigating system oscillation and providing voltage support [2-4]. PID is the most commonly used control algorithm in the process industry. Also, this technique is used to control the FACTS devices [5]. However, the nonlinear nature of well as the uncertainties that exist in the system make it difficult to design an effective controller for the FACTS that guarantees fast and stable regulation under all operating conditions. A major source of difficulty is that open-loop plant may change. In particular, inaccuracy in plant may cause problems because the plant is part of the feedback loop. To deal with such a problem, instead of using a single model plant, an uncertain model should be considered. This problem has led to the study of applying adaptive controllers for instance [6, 7], nonlinear controllers for instance [8] in the power system stability control. Also, during past decade, the $H_\infty$ optimal robust control design has received increasing attention in power systems. Most of above methods have been applied in power systems and some of these efforts have contributed to the design of supplementary control for SVC using mixed sensitivity [9], applying $\mu$-synthesis for SVC in order to voltage control design [10] and supplementary control design for SVC and STATCOM [11]. For many control problems, a design procedure is required that offers more flexibility than mixed sensitivity, but should not be as complicated as $\mu$-synthesis and should not be limited in its application like LTR procedures. The $H_\infty$ loop-shaping design is such a controller procedure, used to design a robust controller for FACTS control to improve the system damping [12].

In this paper as an example, a Single Machine Infinite Bus (SMIB) power system installed with a UPFC is considered for case study and $H_\infty$ loop-shaping method is used to design a robust controller for UPFC controller including power-flow and DC-voltage regulator in this system. To show influence of proposed method, the proposed method is compared to conventional method (the parameters of conventional P-I controller are optimized using genetic algorithm). As the validity of the proposed method has been confirmed by linear and nonlinear time domain simulation results.

MATRIALS AND METHODS

Figure 1 shows a SMIB system equipped with a UPFC. The UPFC consists of an Excitation Transformer (ET), a Boosting Transformer (BT), two three-phase GTO based voltage source converters.

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Fig. 1: SMIB power system equipped with UPFC

(VSCs) and a DC link capacitors. The four input control signals to the UPFC are $m_E$, $m_B$, $E$, and $B$, where, $m_E$ is the excitation amplitude modulation ratio, $m_B$ is the boosting amplitude modulation ratio, $E$ is the excitation phase angle and $B$ is the boosting phase angle.

Non-Linear Dynamic Model: By applying Park’s transformation and neglecting the resistance and transients of the ET and BT transformers, the UPFC can be modeled as:

$$
\begin{align}
\begin{bmatrix}
\frac{v_{Ei}}{v_{Ei}} \\
\frac{v_{Bi}}{v_{Bi}}
\end{bmatrix} &=
\begin{bmatrix}
-x_E & 0 \\
0 & x_E
\end{bmatrix}
\begin{bmatrix}
i_{Ei} \\
i_{Bi}
\end{bmatrix}
+ \begin{bmatrix}
m_E \cos \delta_E v_{di} \\
m_E \sin \delta_E v_{di}
\end{bmatrix}
+ \begin{bmatrix}
m_B \cos \delta_B v_{bi} \\
m_B \sin \delta_B v_{bi}
\end{bmatrix}

\text{(1)}
\end{align}
$$

$$
\begin{align}
\begin{bmatrix}
\frac{v_{Bd}}{v_{Bd}} \\
\frac{v_{Bq}}{v_{Bq}}
\end{bmatrix} &=
\begin{bmatrix}
-x_B & 0 \\
0 & x_B
\end{bmatrix}
\begin{bmatrix}
i_{Bd} \\
i_{Bq}
\end{bmatrix}
+ \begin{bmatrix}
m_E \cos \delta_E v_{di} \\
m_E \sin \delta_E v_{di}
\end{bmatrix}
+ \begin{bmatrix}
m_B \cos \delta_B v_{bi} \\
m_B \sin \delta_B v_{bi}
\end{bmatrix}

\text{(2)}
\end{align}
$$

Where, $v_{Ei}$, $i_{Ei}$, $V_E$ and $i_B$ are the excitation voltage, excitation current, boosting voltage, and boosting current, respectively; $c_{dc}$ and $v_{dc}$ are the DC link capacitance and voltage, respectively. The nonlinear model of the SMIB system as shown in Fig. 1 is described by:

$$
\begin{align}
\omega &= (P_m - P_f - D\Delta \omega) / M \\
\dot{\delta} &= \omega_0 (1 - \omega) \\
E'_q &= (-E_q + E_{ix}) / T_d
\end{align}
$$

The equation for real power balance between the series and shunt converters is given by:

$$
\text{Re}(V_{Bh}^* - V_{Ei}^*) = 0
\text{(8)}
$$

Power system linearised model: A linear dynamic model is obtained by linearising the nonlinear model around an operating condition. The linearised model of power system as shown in Fig. 1 is given as follows:
\[ \Delta \dot{\delta} = \omega_c \Delta \omega \]  
\[ \Delta \omega = (-\Delta P_e - D \Delta \omega)/M \]  
\[ \Delta E_{i} = (-\Delta E_{i} + \Delta E_{id})/T_d \]  
\[ \Delta E_{id} = - \frac{1}{T_A} \Delta E_{id} - \frac{K_A}{T_A} \Delta V \]  
\[ \Delta V_{uk} = K_u \Delta \delta + K_p \Delta E_{i} + K_p \Delta V_{uk} + K_p \Delta m_e \]  
\[ + K_{id} \Delta \delta_{id} + K_{ib} \Delta m_{ib} + K_{ib} \Delta \delta_{ib} \]  

Where,

\[ K_1, K_2, \ldots, K_9, K_{pu}, K_{qu}, K_{cu} \] are linearization constants. The state-space model of power system is given by:

\[ \dot{x} = Ax + Bu \]  

Where, the state vector \( x \), control vector \( u \), \( A \) and \( B \) are:

\[ x = [\Delta \delta \quad \Delta \omega \quad \Delta E_{i} \quad \Delta E_{id} \quad \Delta V_{uk}] \]  
\[ u = [\Delta m_e \quad \Delta \delta_{id} \quad \Delta m_{ib} \quad \Delta \delta_{ib}]^T \]

The block diagram of the linearised dynamic model of the SMIB power system with UPFC is shown in Fig. 2.

**H_{s} Loop-shaping design:** The adopted control method is based on \( H_{s} \) robust stabilization combined with classical Loop-shaping, where loop-shape refers to the magnitude of the loop transfer function \( L = GK \) as a function of frequency. It is essentially a tow-step procedure, where in the first step; the singular values of the open-loop plant are shaped by pre and post compensators. In the second step, the resulting shaped plant is robustly stabilized with respect to coprime factor uncertainty using \( H_{s} \) optimization. An important advantage is that no problem-dependent uncertainty modeling, or weight selection, is required in this second step\(^{[16]}\).

The stabilization of a plant \( G \) is considered, that has a normalized left coprime factorization as follows\(^{[16]}\):

\[ G = M^{-1} \]  

A perturbed plant model \( G_{p} \) can then be written as:

\[ G_{p} = (M + \Delta M)^{-1} + (N + \Delta N) \]  

Where \( \Delta M \) and \( \Delta N \) represent the uncertainty in the nominal plant model \( G \). The objective of robust stabilization is to stabilize a family of perturbed plants defined by:

\[ G_{p} = \{(M + \Delta M)^{-1} + (N + \Delta N) : \|\Delta N - \Delta M\| < \varepsilon\} \]

Where \( \varepsilon > 0 \) is then the stability margin. For the perturbed feedback system of Fig. 3, the stability.
The lowest achievable value of $y$ and corresponding maximum stability margin $\varepsilon$ are given by:

$$\gamma_{\text{min}} = \varepsilon_{\text{MAX}} = \left\| \left[ \begin{array}{c} N \\ M \end{array} \right] \right\|_{\infty}^{1/2} = (1 + \rho(X_s))^{1/2} \quad (19)$$

Where $\left\| . \right\|_{\infty}$ denotes the Hankel norm, $p$ denotes radius (maximum eigenvalue), and for a minimal state space realization $(A, B, C, D)$ of $G$, $Z$ and $X$ are the unique positive definite solution to the algebraic Riccati equations:

$$Z : (A - BS^{-1}D^TC)Z + Z(A - BS^{-1}D^TC)^T - ZC^TR^{-1}CZ + BS^{-1}B^T = 0 \quad (20)$$
$$X : (A - BS^{-1}D^TC)X + X(A - BS^{-1}D^TC)^T - XBS^{-1}B^T + C^TR^{-1}C = 0 \quad (21)$$

Where $R = I + DD^T$ and $S = I + D^TD$. A controller, which guarantees that

$$\gamma = \left\| \left[ \begin{array}{c} K \\ I \end{array} \right] (I - GK)^{-1} M^{-1} \right\|_{\infty} \leq \frac{1}{\varepsilon} \quad (18)$$

For a specified $y > y$ is given by:

$$K = \begin{bmatrix} A + BF + \gamma'(L')^{-1}ZC^T(C + DF) & \gamma'(L')^{-1}ZC^T \\ B^TX & -D^T \end{bmatrix} \quad (22)$$

Where $F = -S^{-1}(D^TC + B^TX)$ and $L = (1 - \gamma)I + ZX$.

It is important to emphasize that, since $y_{\text{min}}$ is computed from (19) and an explicit solution has been derived by solving just two Riccati equations and the $y$ iteration needed to solve them, the general $H_\infty$ problem has been avoided\cite{16-17}.

The shaped plant and controller

The controller design procedure can be summarized as follows.

**Loop Shaping:** Using pre- and post compensators $W_1$ and $W_2$, the singular values of the plant are shaped to give a desired open loop shape as shown in Fig. 4. Some trial and error is involved. Here $W_2$ is usually chosen as a constant. $W_1$ contains dynamic shaping. Integral action for low frequency performance, phase-advance for reducing the roll-off rates at crossover, and phase-lag to increase the roll-off rates at high frequencies, should all be placed in $W_1$ if desired. The weights should be chosen so that no unstable hidden modes are created in $G_i$.

**Robust Stabilization:** Robustly stabilize the shaped plant $G_s$. First, calculate the maximum stability margin $\varepsilon_{\text{max}} > y_{\text{min}}$. If the margin is too small, $\varepsilon_{\text{max}} < 0.25$, return to step 1 and adjust the weight. Otherwise, select $y > y_{\text{min}}$ by about 10% and synthesis a suboptimal controller using (22). When $\varepsilon_{\text{max}} > 0.25$ (respectively, $y_{\text{min}} < 4$) the design is usually successful. A small value of $\varepsilon_{\text{max}}$ indicates that the chosen singular value loop shapes are incompatible with robust stability requirements. The loop shape does not change much following robust stabilization if $y$ is small\cite{16-17}.

If all the specification is not met: Return to step 1 and make further modification to the weights.

**Reduce the order of controller:** Check the frequency response plot of $K_{\text{red}}$ against that of $K_s$.

**Final feedback controller $K$:** This is achieved by combining $K_{\text{red}}$ with the shaping function $W_1$ and $W_2$ such that $K = W_1K_{\text{red}}W_2$.

**UPFC CONTROLLERS**

The UPFC control system comprises following controllers:

- Power flow controller
- DC voltage regulator controller
- Power system oscillation-damping controller
Fig. 5: Power flow controller with damping controller

Fig. 6: DC voltage regulator

Fig. 7: Transfer function block diagram of the UPFC based damping controller

Power flow and DC voltage regulator controllers:
The UPFC is installed in one of the two lines of the SMIB system. Figure 5 shows the transfer function of the power flow controller. The power flow controller regulates the power flow on this line. $k_{pp}$ and $k_{pi}$ are the proportional and integral gain of the power flow controller. The real power output of the shunt converter must be equal to the real power input of the series converter or vice versa. In order to maintain the power balance between the two converters, a DC-voltage regulator is incorporated. DC-voltage is regulated by modulating the phase angle of the shunt converter voltage. A P-I type DC-voltage regulator is considered Fig. 6. $k_{dp}$ and $k_{di}$ are the proportional and integral gain of the DC-voltage regulator.

Power system oscillation-damping controller: A damping controller is provided to improve the damping of power system oscillations. This controller may be considered as a lead-lag compensator\cite{18,19} or a fuzzy controller block. However an electrical torque in phase with the speed deviation is to be produced in order to improve damping of the system oscillation. The transfer function block diagram of the damping controller is shown in Fig. 7. It comprises gain block, signal-washout block and lead-lag compensator.

CONTROLLER DESIGN USING $H_\infty$ THEORY

Due to system parameters are given in Appendix, the initial d-q axes voltage, current components and torque angle are computed for the nominal operating condition as follows:

$E_d = 0.396 \, \text{pu}; E_q = 0.953 \, \text{pu}$

$V_d = 1.0233 \angle 26.9^\circ \, \text{pu}; V_q = 0.1047 \angle -55.87 \, \text{pu}$

$I_d = 0.4317 \, \text{pu}; I_q = 0.6601 \, \text{pu}; \delta = 51.61^\circ$.

Design of UPFC controller: The UPFC power-flow and DC voltage regulator are designed independently. Based on the procedures explained in section 4, the following controller design results are obtained. First, the singular values of the open loop are calculated and
plotted in Fig. 8. After trial and error, W1 and W2 for power flow controller are chosen:

\[
W_1 = \frac{S + 0.61}{S(S^2 + 11.8S + 18)} \quad W_2 = 1
\]

For DC voltage regulator, the same procedure is followed. The singular values of the open loop are calculated and plotted in Fig. 9. W1 and W2 for DC voltage regulator are chosen:

\[
W_1 = \frac{15(S+2)}{S(S+5)} \quad W_2 = 1
\]

The variable \((\gamma_{\text{min}})\) is the inverse of the magnitude of coprime uncertainty, which can be tolerated before getting instability. \(\gamma_{\text{min}} > 1\) should be as small as possible, and usually requires that \(\gamma_{\text{min}}\) is less than a value of 4\(^{[16-17]}\). By applying this, \(\gamma_{\text{min}} = 1.3104\) for power flow controller and \(\gamma_{\text{min}} = 1.3236\) for the DC voltage regulator are obtained. In order to show influence of \(H_v\) loop-shaping method, the proposed method is compared to conventional method. In conventional method, the parameters of the power-flow controller \((k_{pp} \text{ and } k_{pi})\) are optimized using genetic algorithm\[^{[20]}\]. Optimum values of the proportional and integral gain settings of the power-flow controller are obtained as \(k_{pp} = 2\) and \(k_{pi} = 0.35\).

The parameters of DC voltage regulator are now optimized using genetic algorithm. When the parameters of power-flow controller are set at their optimum values. The optimum gain setting of P-I type DC voltage regulator are \(k_{dp} = 0.25\) and \(k_{dq} = 0.35\).

Using a commercially available software package\[^{[11]}\], two controllers satisfying design objectives are obtained. For easy implementation, the order has been reduced by model reduction technique. The transfer functions of the controllers are:

\[
K_{pp} = \frac{-0.01917S^4 + 20.44S^3 + 27.43S^2 + 35.6S + 11.43S + 0.9874}{S^4 + 16.42S^3 + 18.69S^2 + 24.61S + 7.367S + 0.6474S}
\]

\[
K_{VDC} = \frac{8.406S^3 + 165.8S^2 + 355.6S + 373S + 125.1}{0.0001S^4 + 0.417S^3 + 20.55S^2 + 159.8S + 91.48}
\]

**Design of PSS:** The damping controllers are designed to produce an electrical torque in phase with the speed deviation according to phase compensation method. The four control parameters of the UPFC \((m_B, m_E, \delta_B \text{ and } \delta_E)\) can be modulated in order to produce the damping torque. In this paper \(m_B\) is modulated in order to damping controller design. The speed deviation \(\Delta \omega_s\) considered as the input to the damping controllers. The structure of UPFC based damping controller is shown in Fig. 7. It consists of gain, signal washout and phase compensator blocks. The parameters of the damping controller are obtained using the phase compensation technique. The detailed Step-by-step procedure for computing the parameters of the damping controllers using phase compensation technique is given\[^{[16, 17]}\]. Damping controller \(m_B\) was designed and obtained as follows (wash-out block is considered). Power flow controller damping with damping ratio of 0.5,

\[
\text{damping controller} = \frac{536.0145 \ s}{(s+0.1) (s+4.5)}
\]

**RESULTS AND DISCUSSION**

In order to examine the robustness of the UPFC power-flow and DC voltage regulator controller in the presence of wide variation in loading condition (three cases), the system load is varied over a wide range. Dynamic responses are obtained for the following three typical loading conditions for \(\Delta P_{\text{inj}} = 0.1 \text{pu} \text{and} \ \Delta T_m = 0.1 \text{pu}\)

Case a: \(P = 0.8 \text{pu}, Q = 0.15 \text{pu} \text{ (nominal load)}\)
Case b: \(P = 1.00 \text{ pu}, Q = 0.2 \text{ pu} \text{ (heavy load)}\)
Case c: \(P = 1.12 \text{ pu}, Q = 0.285 \text{ pu} \text{ (very heavy load)}\)

The performance of the designed \(H_v\)-UPFC and classic-UPFC controllers with damping controller \(m_B\) after sudden change in reference power on transmission line 2, reference mechanical power and reference voltage are shown in Figs. 10 to 15. It can be observed from these figures, which the proposed controller designed \((H_v\text{-UPFC})\) significantly damp power system oscillations compared to conventional (classical P-I) UPFC controllers (C-UPFC).

In order to investigate the performance of the proposed controller and the system behavior under large disturbances and various operating conditions, a transitory 3-phase fault of 5-10 ms duration at the generator terminals is considered. Dynamic performance is obtained using the non-linear model under the system of the nominal and heavy loading condition with \(H_v\)-based and optimal settings of the UPFC controllers (Power-flow controller, DC-voltage regulator and damping controller). Figure 16 shows the power system responses under the above operation condition. It can be observed from this figure, which the proposed controller designed significantly damp power system oscillations compared to conventional (classical P-I) UPFC controllers (C-UPFC).
CONCLUSION

In this paper, the design of robust controller based on $H_\infty$ theory with application to an UPFC has been carried out for power system. The performance of the controller has been evaluated in comparison with conventional UPFC by linear and nonlinear time domain simulations. The following issues have been addressed:

- representation of non-linear characteristics of the system by uncertainty model principle.
Fig. 13: Dynamic responses at operating point 1 (Nominal loading) during step change in $\Delta T_m=0.1$ pu; Solid ($H_\infty$) and Dashed (Classical a) Power flow deviation on line 2 b) DC- voltage deviation c) Speed deviation

Fig. 14: Dynamic responses at operating point 2 (Heavy loading) during step change in $\Delta T_m=0.1$ pu; Solid ($H_\infty$) and Dashed (Classical a) Power flow deviation on line 2 b) DC- voltage deviation c) Speed deviation

Fig. 15: Dynamic responses at operating point 3 (Very heavy loading) during step change in $\Delta T_m=0.1$ pu; Solid ($H_\infty$) and Dashed (Classical a) Power flow deviation on line 2 b) DC- voltage deviation c) Speed deviation

- selection of appropriate weighting functions based on the control system objectives, and:
- verification of the $H_\infty$ controller design by time domain simulations under various operating conditions.

The main conclusions are:
- The robust controller can improve the damping of power system oscillation.
- The non-linear characteristics of the system can easily be incorporated into the controller design by suitable selection of weighting functions.
- The results of these studies show that the proposed controller design using $H_\infty$ method compared to conventional method, has an excellent capability in damping of power system oscillations.
Performance of damping controllers under large perturbations show the superiority of proposed H_2-based controller over its conventional counterpart. Also, effectiveness of the proposed control strategy in damping the local low frequency oscillations with UPFC is confirmed.

APPENDIX

The nominal parameters and operating condition of the system are given below:

| Parameter          | Value       |
|--------------------|-------------|
| Generator          | M = 8 MJ/M  |
|                   | T_d = 5.044 |
|                   | S_d = 1 pu  |
| X_d = 0.6 P.U     |             |
| Excitation system  | K_a = 10    |
|                   | T_a = 0.05 sec |
| Transformers       | X_b = 0.1 pu |
|                   | X_d = 0.1 pu |
| Transmission line  | X_T1 = 1 pu |
| Operating condition| P = 0.8 pu  |
|                   | Q = 0.15 pu |
| DC link parameter  | V_DC = 2 pu |
|                   | C_DC = 3 pu |
| UPFC parameter     | m_b = 0.104 |
|                   | \delta_b = 55.87 |
|                   | \delta_b = 26.9° |
|                   | m_b = 1.0233 |

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