Deterministic Regular Expressions With Back-References*

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Abstract

Most modern libraries for regular expression matching allow back-references (i.e., repetition operators) that substantially increase expressive power, but also lead to intractability. In order to find a better balance between expressiveness and tractability, we combine these with the notion of determinism for regular expressions used in XML DTDs and XML Schema. This includes the definition of a suitable automaton model, and a generalization of the Glushkov construction. We demonstrate that, compared to their non-deterministic superclass, these deterministic regular expressions with back-references have desirable algorithmic properties (i.e., efficiently solvable membership problem and some decidable problems in static analysis), while, at the same time, their expressive power exceeds that of deterministic regular expressions without back-references.

1 Introduction

Regular expressions were introduced in 1956 by Kleene [34] and quickly found wide use in both theoretical and applied computer science, including applications in bioinformatics [41], programming languages [49], model checking [48], and XML schema languages [47]. While the theoretical interpretation of regular expressions remains mostly unchanged (as expressions that describe exactly the class of regular languages), modern applications use variants that vary greatly in expressive power and algorithmic properties. This paper tries to find common ground between two of these variants with opposing approaches to the balance between expressive power and tractability.

REGEX

The first variant that we consider are regex, regular expressions that are extended with a back-reference operator. This operator is used in almost all modern programming languages (like e.g. Java, PERL, and .NET). For example, the regex \((x: (a \lor b)^*) \cdot &x\) defines \(\{ww \mid w \in \{a,b\}^*\}\), as \((a \lor b)^*\) can create a \(w \in \{a,b\}^*\), which is then stored in the variable \(x\) and repeated with the reference \&x. Hence, back-references allow to define non-regular languages; but with the side effect that the membership problem is NP-complete (cf. Aho [2]).

Regex were first examined from a theoretical point of view by Aho [2], but without fully defining the semantics. There were various proposals for semantics, of which we mention the first by Câmpeanu, Salomaa, Yu [10], and the recent one by Schmid [46], which is the basis for this paper. Apart from defining the semantics, there was work on the expressive power [10, 11, 25], the static analysis [11, 23, 24], and the tractability of the membership problem (investigated in terms of a strongly restricted subclass of regex) [21, 22]. They have also been compared to related models in database theory, e.g. graph databases [4, 26] and information extraction [20, 24].

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Deterministic Regular Expressions

The second variant, deterministic regular expressions (also known as 1-unambiguous regular expressions), uses an opposite approach, and achieves a more efficient membership problem than regular expressions by defining only a strict subclass of the regular languages.

Intuitively, a regular expression is deterministic if, when matching a word from left to right with no lookahead, it is always clear where in the expression the next symbol must be matched. This property has a characterization via the Glushkov construction that converts every regular expression \( \alpha \) into a (potentially non-deterministic) finite automaton \( \mathcal{M}(\alpha) \), by treating each terminal position in \( \alpha \) as a state. Then \( \alpha \) is deterministic if \( \mathcal{M}(\alpha) \) is deterministic. As a consequence, the membership problem for deterministic regular expressions can be solved more efficiently than for regular expressions in general (more details can be found in [31]). Hence, in spite of their limited expressive power, deterministic regular expressions are used in actual applications: Originally defined for the ISO standard for SGML (see Brüggekmann-Klein and Wood [9]), they are a central part of the W3C recommendations on XML DTDs [7] and XML Schema [27] (see Murata et al. [42]). Following the original paper [9], deterministic regular expressions have been studied extensively. Aspects include computing the Glushkov automaton and deciding the membership problem (e.g. [8, 31, 44]), static analysis (cf. [40]), deciding whether a regular language is deterministic (e.g. [16, 31, 39]), closure properties and descriptive complexity [37], and learning (e.g. [5]). One noteworthy extension are counter operators (e.g. [29, 31, 36]), which we briefly address in Section 8.

Deterministic REGEX

The goal of this paper is finding common ground between these two variants, by combining the capability of backreferences with the concept of determinism in regular expressions. Generally, our definition of determinism for regex mimics that for classical regular expressions, i.e., we define a Glushkov-like conversion from regex into suitable automata and then say that a regex is deterministic if and only if its Glushkov-automaton is. The thus defined class of deterministic regex is denoted by DRX, and \( L(DRX) \) refers to the corresponding language class.

The underlying automaton model for this approach is a slight modification of the memory automata (MFA) proposed by Schmid [46] as a characterisation for the class of regex-languages. More precisely, we introduce memory automata with trap-state (TMFA), for which the deterministic variant, the DTMFA, is better suited for complementation than the deterministic MFA.

As indicated by the title of this subsection, it is our hope to preserve, on the one hand, the increased expressive power provided by backreferences, and, on the other hand, the tractability that usually comes with determinism. While it is not surprising that DRX do not achieve these goals to the full extent, a comprehensive study reveals that their expressive power clearly exceeds that of (deterministic) regular expressions, while, at the same time, being much more tractable than the full class of regex.

We shall now outline our main results according to these two aspects, and we start with the algorithmic features of DRX:

1. We can decide in time \( O(|\Sigma||\alpha|k) \), whether a regex \( \alpha \) with \( k \) variables and over alphabet \( \Sigma \) is deterministic (if so, we can construct its Glushkov-automaton in the same time).

2. We can decide in time \( O(|\Sigma||\alpha|n+k|w|) \), whether \( w \in \Sigma^* \) can be generated by an \( \alpha \in DRX \) with \( k \) variables and \( n \) occurrences of terminal symbols or variable references.

3. The intersection-emptiness problem for DRX is undecidable, but in \( \text{PSPACE} \) for variable-star-free\(^1\) DRX (as well as the inclusion and equivalence problem).

Results 1 and 2 are a consequence of the Glushkov-construction for regex. In view of the NP-hardness of the membership problem for the full class of regex, result 2 demonstrates that the membership problem for DRX can be solved almost as efficiently as for deterministic regular expression (which is possible in time \( O(|\Sigma||\alpha|+|w|) \) [8, 44] or \( O(|\alpha|+|w|\cdot \log \log |\alpha|) \) [31]). The positive results of 3 are based on encoding the intersection-emptiness problem in the existential

\(^1\)A regex is variable-star-free if each of its sub-regexes under a Kleene-star contains no variable operations (see Section 8).
theory of concatenation with regular constraints. With respect to \(3\), we observe that it is in fact the determinism, which makes the inclusion and equivalence problem for variable-star-free DRX decidable, since these problems are known to be undecidable for non-deterministic variable-star-free regex (see [23]). Moreover, result \(3\) also yields a PSPACE minimization algorithm for variable-star-free DRX (enumerate all smaller candidates and check equivalence).

Throughout the paper, there are numerous examples that demonstrate the expressive power of DRX. We also provide a tool for proving non-expressibility, which does not use a pumping argument. In fact, it can be shown that, despite the automata-theoretic characterisation of deterministic regex, \(\mathcal{L}(\text{DRX})\) contains infinite languages that cannot be pumped (in the sense as regular languages are “pumpable”). In addition, we show the following results with respect to DRX’s expressiveness:

1. There are regular languages that are not in \(\mathcal{L}(\text{DRX})\).
2. \(\mathcal{L}(\text{DRX})\) contains regular languages that are not deterministic regular.
3. \(\mathcal{L}(\text{DRX})\) contains all unary regular languages.
4. \(\mathcal{L}(\text{DRX})\) is not closed under union, concatenation, reversal, complement, homomorphism, inverse homomorphism, intersection, and intersection with deterministic regular languages.

While result \(4\) fits to the situation that there are also regular languages that are not deterministic regular languages (and, thus, points out that our definition of determinism restricts regex in a similar way as classical regular expressions), result \(2\) points out that in the case of regex this restriction is not as strong. With respect to result \(3\) note that not all unary regular languages are deterministic regular (see [37]). From a technical point of view, it is worth mentioning that in some of our proofs, we use subtleties of the back-reference operator in novel ways. Intuitively speaking, defining and referencing variables under a Kleene-star allows for shifting around factors between different variables (even arbitrarily often in loops), which makes it possible to abuse variables for generating seemingly non-deterministic regular structures in a deterministic way, instead of generating non-regular structures.

As a last strong point in favour of our definition of determinism for regex, we examine a natural relation of the definition of determinism (i.e., requiring determinism only with respect to a constant look-ahead). We prove that checking whether a regex is deterministic under this more general notion is intractable (even for the class of variable-star-free regex).

Summing up, from the perspective of deterministic regular expressions, we propose a natural extension that significantly increases the expressive power, while still having a tractable membership problem. From a regex point of view, we restrict regex to their deterministic core, thus obtaining a tractable subclass. Hence, the authors intend this paper as a starting point for further work, as it opens a new direction on research into making regex tractable.

Structure of the paper

In Section 2 we define some basic concepts and the syntax and semantics of regex; in addition, due to the fact that this aspect is often neglected in the existing literature, which caused misunderstandings, we also provide a thorough discussion of existing variants of regex in theory and practice. Section 3 is devoted to the definition of memory automata with trap state and their deterministic subclass. In addition, we provide an extensive automata-theoretic toolbox in this section that, besides showing interesting facts about TMFA, shall play an important role for our further results. Next, in Section 4 we define deterministic regex and provide the respective Glushkov-construction. The expressive power of DRX (and of related classes resulting from different variants of TMFA) is investigated in Section 5 and the decidability and hardness results of the static analysis of DRX are provided in Section 6. Finally, in Section 7 we discuss the above mentioned relaxation of determinism, and we conclude the paper by giving some conclusions in Section 8.
2 Preliminaries

We use $\varepsilon$ to denote the empty word. The subset and proper subset relation are denoted by $\subseteq$ and $\subset$, respectively. Let $\Sigma$ be a finite terminal alphabet (unless otherwise noted, we assume $|\Sigma| \geq 2$) and let $\Xi$ be an infinite variable alphabet with $\Xi \cap \Sigma = \emptyset$. For a word $w \in \Sigma^*$ and for every $i$, $1 \leq i \leq |w|$, $w[i]$ denotes the symbol at position $i$ of $w$. We define $w^i := \varepsilon$ and $w^{i+1} := w \cdot w$ for all $i \geq 0$, and, for $w = a_1 \cdots a_n$, with $a_i \in \Sigma$, let $w^{m \cdot 1} = w^m \cdot a_1 \cdots a_i$ for all $m \geq 0$ and all $i$ with $0 \leq i \leq n$. A $v \in \Sigma^*$ is a factor of $w$ if there exist $u_1, u_2 \in \Sigma^*$ with $w = u_1 v u_2$. If $u_2 = \varepsilon$, $v$ is also a prefix of $w$.

We use the notions of deterministic and non-deterministic finite automata (DFA and NFA) like in [2]. If an NFA can have $\varepsilon$-transitions, we call it an $\varepsilon$-NFA. Given a class $C$ of language description mechanisms (e.g., a class of automata or regular expressions), we use $L(C)$ to denote the class of all languages $L(C)$ with $C \in C$. The membership problem for $C$ is defined as follows: Given a $C \in C$ and a $w \in \Sigma^*$, is $w \in L(C)$?

Next, we define the syntax and semantics of regular expressions with backreferences.

Definition 1 (Syntax of regex). The set $RX$ of regex over $\Sigma$ and $\Xi$ is recursively defined as follows:

- **Terminals and $\varepsilon$:** $a \in RX$ and $var(a) = \emptyset$ for every $a \in (\Sigma \cup \{\varepsilon\})$.
- **Variable reference:** $\& x \in RX$ and $var(\& x) = \{x\}$ for every $x \in \Xi$.
- **Concatenation:** $(\alpha \cdot \beta) \in RX$ and $var(\alpha \cdot \beta) = var(\alpha) \cup var(\beta)$ if $\alpha, \beta \in RX$.
- **Disjunction:** $(\alpha \lor \beta) \in RX$ and $var(\alpha \lor \beta) = var(\alpha) \cup var(\beta)$ if $\alpha, \beta \in RX$.
- **Kleene plus:** $(\alpha^+) \in RX$ and $var(\alpha^+) = var(\alpha)$ if $\alpha \in RX$.
- **Variable binding:** $(x: \alpha) \in RX$ and $var(x: \alpha) = var(\alpha) \cup \{x\}$ if $\alpha \in RX$ with $x \in \Xi \setminus var(\alpha)$.

In addition, we allow $\emptyset$ as a regex (with $var(\emptyset) = \emptyset$), but we do not allow $\emptyset$ to occur in any other regex. An $\alpha \in RX$ with $var(\alpha) = \emptyset$ is called a proper regular expression, or just regular expression. We use REG to denote the set of all regular expressions. We add and omit parentheses freely, as long as the meaning remains clear. We use the Kleene star $\alpha^*$ as shorthand for $\varepsilon \lor \alpha^+$, and $A$ as shorthand for $\bigvee_{\alpha \in A} \alpha$ for non-empty $A \subseteq \Sigma$.

We define the semantics of regex using the ref-words (short for reference words) by Schmid [19]. A ref-word is a word over $(\Sigma \cup \Xi \cup \Gamma)$, where $\Gamma := \{[x], x \mid x \in \Xi\}$. Intuitively, the symbols $[x]$ and $x$ mark the beginning and the end of the match that is stored in the variable $x$, while an occurrence of $x$ represents a reference to that variable. Instead of defining the language of a regex $\alpha$ directly, we first treat $\alpha$ as a generator of ref-words by defining its ref-language $R(\alpha)$ as follows.

- For every $\alpha \in \Sigma \cup \{\varepsilon\}$, $R(\alpha) := \{\alpha\}$.
- For every $x \in \Xi$, $\alpha \in RX$,
  - $R(\& x) := \{x\}$,
  - $R(\langle x: \alpha \rangle) := \{[x, R(\alpha)]\}$.
- For every $\alpha, \beta \in RX$,
  - $R(\alpha \cdot \beta) := R(\alpha) \cdot R(\beta)$,
  - $R(\alpha \lor \beta) := R(\alpha) \cup R(\beta)$, and
  - $R(\alpha^+) := R(\alpha)^+$.

In particular, if $\alpha$ is a regular expression, then $L(\alpha) = R(\alpha)$. An alternative definition of the ref-language would be $R(\alpha) := L(\alpha_R)$, where $\alpha_R$ is the proper regular expression obtained from $\alpha$ by replacing each sub-regex $(x: \beta)$ by $[x, \beta_R]$, and each $\& x$ by $x$.

Intuitively speaking, every occurrence of a variable $x$ in some $r \in R(\alpha)$ functions as a pointer to the next factor $[x, v]$ to the left of this occurrence (or to $\varepsilon$ if no such factor exists). In this way, a ref-word $r$ compresses a word over $\Sigma$, the so-called dereference $D(r)$ of $r$, which can be obtained by replacing every variable occurrence $x$ by the corresponding factor $v$ (note that $v$ might again contain variable occurrences, which need to be replaced as well), and removing all
symbols \([x, x] \in \Gamma\) afterwards. See [46] for a more detailed definition, or the following Example 1 for an illustration. Finally, the language of a regex \(\alpha\) is defined by \(\mathcal{L}(\alpha) = \{D(r) \mid r \in \mathcal{R}(\alpha)\}\).

**Example 1.** Let \(\alpha := \langle x \cdot \lbrack a \vee b \rbrack \cdot x \cdot \rbrack \rangle \). Then
\[
\mathcal{R}(\alpha) = \{[x w_1] x \cdots [x w_n] x \mid n \geq 1, w_i \in \{a, b\}^+\},
\]
or, equivalently, \(\mathcal{L}(\alpha) = (L_{\text{copy}})^+\), with \(L_{\text{copy}} := \{ww \mid w \in \{a, b\}^\star\}\).

An interesting example is the regex \(\alpha_{\text{sq}} := \langle x \cdot \lbrack y \cdot \& x \cdot a \rbrack \rangle \) with ref-language \(\mathcal{R}(\alpha_{\text{sq}}) = \{r_i \mid i \geq 0\}, \) where \(r_i := ([x y] x \cdot [y x] a) \). For example, for
\[
r_3 = [x y] x \cdot [y x] a \cdot [x y] x \cdot [y x] a \cdot [x y] x \cdot [y x] a,
\]
we have \(r_3 \in \mathcal{R}(\alpha_{\text{sq}})\) with \(D(r_3) = a^9\). Using induction, we can verify that \(D(r_i) = a^{i2}\). Thus, \(\mathcal{L}(\alpha_{\text{sq}}) = \{a^{n^2} \mid n \geq 0\}\).

Hence, unlike regular expressions, regex can define non-regular languages. The expressive power comes at a price: their membership problem is NP-complete (follows from Angluin [3]), and various other problems are undecidable (Freydenberger [23]). Starting with Aho [2], there have been various approaches to specifying syntax and semantics of regex. While [2] only sketched the intuition behind the semantics, the first formal definition (using parse trees) was proposed by Câmpeanu, Salomaa, Yu [10], followed by the ref-words of Schmid [46]. In the following, we provide a more detailed discussion of the different approaches and actual implementations of regex.

### 2.1 Regex in Theory and Practice

In this section, we motivate the choice of the formalization of regex syntax and semantics that are used in the current paper, in particular in comparison to [10], and then connect these to the use of back-references in actual implementations. Note that in order to explain how our results and concepts can be adapted to various alternative definitions of syntax and semantics, we anticipate some of the technical content of our paper.

#### 2.1.1 Choices behind the definition

We begin with a discussion of semantics of back-references, which most actual implementations define in terms of the used matching algorithms [4]. For a theoretical analysis, this approach is not satisfactory.

To the authors’ knowledge, the first theoretical analysis of regular expressions is due to Aho [2], who defined the semantics informally. Câmpeanu, Salomaa, Yu [10] then proposed a definition using parse trees, which was precise, but rather technical and unwieldy. Schmid [46] then introduced the definition with ref-words that we use in the current paper. The two definitions differ only in some semantical particularities, which we discuss further down.

The most obvious difference in approaches to syntax is that some formalizations, like [10], do not use variables, but numbered back-references. For example, \((x: a^* b \cdot x)\) would be written as \((a^* b) \backslash 1\), where \(\backslash 1\) refers to the content of the first pair of parentheses (called the first group).

After working with this definition for some time, the authors of the present paper came to the conclusion that using numbered back-references instead of named variables is inconvenient (both when reading and writing regex). The developers of actual implementations seem to agree with this sentiment: While using numbered back-references was well-motivated when considering PERL at the time [10] was published, most current regex dialects allow the use of named groups, which basically act like our variables (depending on the actual dialect, see below). The choice between variables and numbered groups is independent of the choice of semantics, as parse trees can also be used with variables, see [23]. Hence, using variables instead of numbers is a natural choice.

\(^2\)From a theory point of view, this might be considered a rather generous use of the term “define”.

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Building on this, the next question is whether the same variable can be bound at different places in the regex (which is automatically excluded by the use of numbered groups as in [10]), i.e., whether one allows expressions like

\[(x: a^+)(y: b^+) \lor (y: b^+)(x: a^+) \lor x \cdot &y \cdot \varepsilon.\]

While some implementations that have developed from back-references forbid these constructions to certain degrees (see Section 2.1.2 below), there seems to be no particular reason for this decision when approaching this question without this historical baggage. In fact, one can argue from a point of applications that expressions like the following make sense (abstracting away some details that would be needed in actual use):

\[\Sigma^*\left(\text{Name}: (x: \Sigma^+) \text{ Title}: (y: \Sigma^+) \lor (\text{Title}: (y: \Sigma^+) \text{Name}: (x: \Sigma^+))\right)\Sigma^*\]

In fact, these constructions are explicitly allowed in the regex formulas of Fagin et al. [20], that are closely related to regex. In particular, both the semantic definitions (ref-words and parse trees) allow this choice. Thus, there seems to be no particular practical reason to disallow these constructions when considering only the model (instead of its algorithmic properties).

In addition to disallowing the repeated binding of the same variable described above, the regex definition in [10] also includes a syntactic restriction that changes the expressive power considerably: It requires that a backreference \(\backslash n\) can only appear in a regex if it occurs to the right of corresponding group number \(n\). In [10], otherwise, the expression is called a “semi-regex”. Consider \(\alpha_{a3} = \left((x: &y)(y: &x:a)\right)^*\) from Example 1. In the numbered notation of [10], this would be expressed as \(\beta = (2 \backslash 3) \backslash 2 \cdot (2 \cdot a)\), when adding group numbers to the groups to increase readability. But using definitions from [10], \(\beta\) is only a semi-regex, as the reference \(\backslash 3\) occurs to the left of group 3.

The motivation behind this restriction is not explained in [10]. While one might argue that this was chosen to avoid referencing unused groups, the definition of semantics in [10] still needs to deal with this problem in regexes like \((a \cdot 3) \backslash 2 \cdot (2 \cdot a)\), and handles them by assigning \(\varepsilon\) (like the definition from [10], which we use as well). Hence, even on “semi-regex”, the parse tree semantics behave like the ref-word semantics.

Arguably, the restriction has an advantage from a theoretical point of view, as it allows Câmpeanu, Salomaa, Yu [10] and Carle and Narendran [11] to define pumping lemmas for this class. Using these, it is possible to show that languages like \(L(\alpha_{a3})\) from Example 1 or the language from Lemma 3 cannot be expressed with the regex model from [10]. But in other areas, there seems to be no advantage in this choice: Even under this restriction, the membership problem is NP-complete (since it is still possible to describe Angluin’s pattern languages [3]), the undecidability results from [23] on various problems of static analysis are unaffected by this choice, and even the proof of Theorem 3 (the undecidability of the disjointness problem for deterministic RX) directly works on this subclass. In summary, the authors of the current paper see no reason to adapt this restriction.

For full disclosure, the second author points out that in his own articles [45, 46], using [10] as reference for a full definition of regex is not entirely correct, since the restrictions of [10] discussed above are not used in these papers; instead they talk about the language class \(L(RX)\) of the current paper.

The last choice in the definition that we need to address is how we deal with referencing undefined variables. Both [10] and [46] default those references to \(\varepsilon\) (as do others, like [23]); but there is also literature, like [11], that uses \(\emptyset\) as default value (under these semantics, a ref-word that contains a variable that de-references to \(\emptyset\) cannot generate any terminal words; the same holds for a parse tree that contains such a reference). This choice can easily be implemented in both semantics by discarding a ref-word or parse tree that contains such a reference; and a TMFA (see Section 3) can reject if a run encounters a reference to such an undefined memory.

While these “\(\emptyset\)-semantics” are also used in some actual implementations, the authors of the current paper are against this approach. One of the reasons is that using \(\emptyset\) as default allows the use of curious synchronization effects that distract from the main message of this paper. For
example, let \( \Sigma = \{a_1, \ldots, a_n\} \) for some \( n \geq 1 \), and define
\[
\alpha_n := (\bigvee_{i=1}^{n} (a_i \cdot (x_i : \epsilon))) & x_1 \cdots & x_n.
\]

If unbound variables default to \( \emptyset \), this regex generates the language
\[
\{a_{\pi(1)} \cdots a_{\pi(n)} \mid \pi \text{ is a permutation of } \{1, \ldots, n\}\},
\]
as every variable \( x_i \) needs to be assigned \( \epsilon \) exactly once (otherwise, a reference would return \( \emptyset \) and block). Hence, using this semantics, even variables that are bound only to \( \epsilon \) can be used for synchronization effects. While this can lead to interesting constructions, the authors think that it provides more insight to study the effects of back-references on lower bounds without relying on these additional features. This way, there is no question whether the hardness of the examined problems is due to the effects of the \( \emptyset \)-semantics.

Furthermore, all examples in the present paper can be adapted from the used \( \epsilon \)-semantics to \( \emptyset \)-semantics: Given an \( \alpha \in \text{RX} \) with the variables \( x_1, \ldots, x_k \), define \( \alpha' := \langle x_1 : \epsilon \rangle \cdots \langle x_k : \epsilon \rangle \cdot \alpha \).

First, we observe that the language that is defined by \( \alpha' \) under \( \emptyset \)-semantics is the same language that \( \alpha \) defines under \( \epsilon \)-semantics. Furthermore, note that if \( \alpha \) is deterministic (in the sense as shall be defined in Section 4), \( \alpha' \) is also deterministic. The analogous construction can be used for TMFA (and DTMFA).

While it is possible to adapt most of the results in the current paper directly to this alternative semantics, the authors chose to keep the paper focused on \( \epsilon \)-semantics.

### 2.1.2 Actual implementations

We now give a brief overview of how back-references are used in some actual implementations. For a good introduction on various dialects, the authors recommend \([30]\), in particular the section on back-references and named groups. As this behavior is often under-defined, badly documented, and implementation dependent, this can only be a very short and superficial summary of some behavior.

Before we go into details, we address why back-references are used, in spite of the resulting NP-hard membership problem: Most regex libraries use a backtracking algorithm that can have exponential running time, even on many proper regular expressions (see Cox \([15]\)). From this point of view, back-references can be added with little implementation effort and without changing the efficiency of the program.

Most modern dialects of regex not only support numbered back-references as used by \([10]\), but also named capture groups, which work like our variables. In some dialects like e. g. Python, PERL5, and PCRE, these act as aliases for back-references with numbers; hence, \( \langle x : a^* \rangle b \& x \) would be interpreted as \( \langle a^* \rangle b \& x \). As a consequence, each name resolves to a well-defined number.

As some of these dialects assign the empty set as default value of unbound back-references (or group names), the resulting behavior is similar implicitly requiring the restriction from \([10]\). This implementation of named capture groups seems to be mostly for historical reasons (as back-references were introduced earlier).

In contrast to this, there are other dialects that use numbered back-references and explicitly allow references to access groups that occur to their right in the expression. For example, the W3C recommendation for XPath and XQuery functions and operators \([33]\) defines regular expressions with back-references for the use in \texttt{fn:matches}. There, it is possible to refer to capture groups that occur to the right of the reference (although only for the capture groups 1 to 9, but not for 10 to 99, which might be considered a peculiar decision). As this dialect defaults unbound references to \( \epsilon \), it is possible to directly express \( \alpha_{sq} \) by renaming the variable references to back-references.

Furthermore, .NET allows the same name to be used for different groups, for example \( \langle (x : a^*) \rangle \texttt{v}(x : b^+) c \& x \rangle^\dagger \). While .NET defaults unset variables to \( \emptyset \), it is possible to express \( L(\alpha_{sq}) \), by using an expression like \( (x : \epsilon) \cdot \alpha_{sq} \). In the same way, every regex in the sense of our paper can be converted into an equivalent .NET regex.

Finally, in 2007 (just four years after the publication of \([10]\), PERL 5.10 introduced \textit{branch reset groups} (which were also adapted in PCRE). These reset the numbering inside disjunctions,
and allow expressions that behave like the expression \(((x: a^+) \lor (x: b^+)c)\& x^*\). This allows PERL regex to replicate a large part of the behavior of .NET regex.

In conclusion, it seems that almost every formalization of regex syntax and semantics can be justified by finding the right dialect; but every restriction might be superseded by the continual evolution of regex dialects. Hence, the current paper attempts to avoid restrictions; and when in doubt, we choose natural definitions over trying to directly emulate a single dialect. Therefore, we use variables instead of numbered back-references, and allow multiple uses of the same variable name.

The authors acknowledge that most actual implementations of “regular expressions” allow additional operators. Common features are counters, which allow constructions like e.g. \(a^2\&b\) that define the language \(\{a^i \mid i \in \{2, \ldots, 5\}\}\), character classes and ranges, which are shortcuts for sets like “all alphanumeric symbols” or “all letters from b to y”, and look ahead and look behind, which can be understood as allowing the expression to call another expressions as a kind of subroutine.

While these operators are outside of the scope of the current paper, we briefly address the issue of counters. These are used in XML DTDs and XML Schema, and were studied in connection to determinism. In particular, Gelade, Gyssens, Martens [29] described how counters can be added to finite automata and proposed an appropriate extension of determinism and Glushkov construction to this model. Although the current paper does not address this matter (in order to keep the paper focussed), the TMFA that we introduce in Section 5 can also be extended with counters (like the extension to NFA in [29]). Likewise, the Glushkov constructions of [29] and the current paper can be combined, as can the notions of determinism. The membership problem for the resulting class of deterministic regex with counters can then be solved as efficiently as for deterministic regex (see Theorem 5).

3 Memory Automata with Trap State

In this section, we define memory automata with trap-state, the deterministic variant of which will be the algorithmic foundation for deterministic regex. Before moving on to the actual definition of deterministic regex in Section 4 and the applications of memory automata with trap-state, we subject this automaton model to a thorough theoretical analysis. Most of the thus obtained insights will have immediate consequences and applications for proving the main results regarding deterministic regex, while others have the mere purpose of supporting our understanding of memory automata (and therefore the important class of regex languages).

Memory automata [20] are a simple automaton model that characterizes \(L(RX)\). Intuitively speaking, these are classical finite automata that can record consumed factors in memories, which can be recalled later on in order to consume the same factor again. However, for our applications, we need to slightly adapt this model to memory automata with trap-state.

Definition 2. For every \(k \in \mathbb{N}\), a \(k\)-memory automaton with trap-state, denoted by TMFA\((k)\), is a tuple \(M = (Q, \Sigma, \delta, q_0, F)\), where \(Q\) is a finite set of states that contains the trap-state \(\{\text{trap}\}\), \(\Sigma\) is a finite alphabet, \(q_0 \in Q\) is the initial state, \(F \subseteq Q\) is the set of final states and \(\delta: Q \times (\Sigma \cup \{\varepsilon\} \cup \{1, 2, \ldots, k\}) \rightarrow \mathcal{P}(Q \times \{\text{o, c, r, o}\})^k\) is the transition function (where \(\mathcal{P}(A)\) denotes the power set of a set \(A\)), which satisfies \(\delta([\text{trap}], b) = \{([\text{trap}], \text{o, o}, \ldots, \text{o})\}\), for every \(b \in \Sigma \cup \{\varepsilon\}\), and \(\delta([\text{trap}], i) = \emptyset\) for every \(i\), \(1 \leq i \leq k\). The elements \(\text{o, c, r}\) and \(\text{o}\) are called memory instructions (they stand for opening, closing and resetting a memory, respectively, and \(\text{o}\) leaves the memory unchanged).

A configuration of \(M\) is a tuple \((q, w, (u_1, r_1), \ldots, (u_k, r_k))\), where \(q \in Q\) is the current state, \(w\) is the remaining input and, for every \(i\), \(1 \leq i \leq k\), \((u_i, r_i)\) is the configuration of memory \(i\), where \(u_i \in \Sigma^*\) is the content of memory \(i\) and \(r_i \in \{\text{o, c}\}\) is the status of memory \(i\) (i.e., \(r_i = \text{o}\) means that memory \(i\) is open and \(r_i = \text{c}\) means that it is closed). The initial configuration of \(M\) (on input \(w\)) is the configuration \((q_0, w, (\varepsilon, \text{c}), \ldots, (\varepsilon, \text{c}))\), a configuration \((q, w, (u_1, r_1), \ldots, (u_k, r_k))\) is an accepting configuration if \(w = \varepsilon\) and \(q \in F\).

\(M\) can change from a configuration \(c = (q, w, (u_1, r_1), \ldots, (u_k, r_k))\) to a configuration \(c' = (p, w, (u'_1, r'_1), \ldots, (u'_k, r'_k))\), denoted by \(c \xrightarrow{\delta} M \ x c'\), if there exists a transition \(\delta(q, b) \supset (p, s_1, \ldots, s_k)\) with either \((b \in (\Sigma \cup \{\varepsilon\})\) and \(v = b)\) or \((b \in \{1, 2, \ldots, k\}, s_b = \text{c} \) and \(v = u_b)\), and, for every \(i\), \(1 \leq i \leq k\),
\[ (s_i = \circ) \land (r_i = 0) \Rightarrow (u_i, r_i') = (u, v, r), \]
\[ (s_i = \circ) \land (r_i = C) \Rightarrow (u_i, r_i') = (u, r), \]
\[ s_i = o \Rightarrow (u_i', r_i') = (v, 0), \]
\[ s_i = c \Rightarrow (u_i', r_i') = (u, c), \]
\[ s_i = x \Rightarrow (u_i', r_i') = (c, C). \]

Furthermore, \( M \) can change from a configuration \((q, v, (u_1, r_1), \ldots, (u_k, r_k))\) to the configuration \((q, w, (u_1, r_1), \ldots, (u_k, r_k))\) if \( \delta(q, b) \ni (p, s_1, \ldots, s_k) \) for some \( p \in Q, b \in \{1, 2, \ldots, k\} \) and \( s_k = c \), such that \( u_k = v v' \) with \( v' \neq \epsilon \) and \( v'[1] \neq u[1] \).

A transition \( \delta(q, b) \ni (p, s_1, s_2, \ldots, s_k) \) is an \( \epsilon \)-transition if \( b = \epsilon \) and is called consuming, otherwise (if all transitions are consuming, then \( M \) is called \( \epsilon \)-free). If \( b \in \{1, 2, \ldots, k\} \), it is called a memory recall transition and the situation that a memory recall transition leads to the state \( \text{[trap]} \), is called a memory recall failure.

The symbol \( \overset{\ast}{\rightarrow}_M \) denotes the reflexive and transitive closure of \( \rightarrow_M \). A \( w \in \Sigma^* \) is accepted by \( M \) if \( c_{\text{init}} \overset{\ast}{\rightarrow}_M c_f \), where \( c_{\text{init}} \) is the initial configuration of \( M \) on \( w \) and \( c_f \) is an accepting configuration. The set of words accepted by \( M \) is denoted by \( L(M) \).

Note that executing the open action \( o \) on a memory that already contains some word discards the previous contents of that memory. A crucial part of TMFA is the trap-state \( \text{[trap]} \), in which computations terminate, if a memory recall failure happens. If \( \text{[trap]} \) is not accepting, then TMFA are (apart from negligible formal differences) identical to the memory automata introduced in [40], which characterize the class of regex language. If, on the other hand, \( \text{[trap]} \) is accepting, then every computation with a memory recall failure is accepting (independent from the remaining input). While it seems counter-intuitive to define the words of a language via “failed” back-references, the possibility of having an accepting trap-state yields closure under complement for deterministic TMFA (see Theorem 3). It will be convenient to consider the partition of TMFA into TMFA\(^\circ \) and TMFA\(^{\circ \circ} \) (having a rejecting and an accepting trap-state, respectively).

Next, we illustrate the concept of memory automata with trap state by some example [41] (further illustrations can be found in [40]).

Intuitively speaking, in a single step of a computation of a TMFA, we first change the memory statuses according to the memory instructions \( s_i, 1 \leq i \leq k \), and then a (possibly empty) prefix \( v \) of the remaining input (\( v \) is either from \( \Sigma \cup \{\epsilon\} \) or it equals the content of some memory that, according to the definition, has been closed by the same transition) is consumed and appended to the content of every memory that is currently open (note that here the new statuses after applying the memory instructions count). The changes of memory configurations caused by a transition are illustrated in Figure 1.

**Example 2.** Consider the following TMFA\(^{\circ \circ} \) \( M \) with two memories over \( \Sigma = \{a, b\} \):

![Diagram](image)

This TMFA works as follows. First, in state \( q_1 \), we record a non-empty word over \( \{a, b\} \) in the first memory, then, in state \( q_2 \), a non-empty word over \( \{a, b\} \) in the second memory, and then, by moving through states \( q_3 \) and \( q_4 \), these words are repeated in reverse order by first recalling the second and then the first memory (note that in the transition from \( q_3 \) to \( q_4 \), an already closed memory is closed again, since according to Definition 3 every memory that is recalled must be closed in the same transition). Due to the \( \epsilon \)-transition from \( q_4 \) to \( q_0 \), \( M \) describes the Kleene-plus

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\(^3\)For the sake of convenience, we present TMFA in the form of the usual automata diagrams (initial states are marked by an unlabeled incoming arc, accepting states by an additional circle and arcs are labelled with the transition tuples).
of such words, i.e., \( \mathcal{L}(M) = \mathcal{L}(\alpha) \), where \( \alpha = ((x: (a \lor b)^+) / (y: (a \lor b)^+) \cdot kxy \cdot &x)^+ = (\{uvwu | u, v \in \{a, b\}^+\})^+ \).

Note that each of the two memory recall transitions closes the respective memory. This is required by definition, as a transition can only recall a memory if it ensures that it is closed.

Example 3. Consider the following TMFA\(^{10}\) \( M \) with two memories over \( \Sigma = \{a, b, d\} \):

\[
\begin{array}{c}
\varepsilon, o, o \\
\rightarrow \\
q_0 \\
\end{array}
\begin{array}{c}
b, o, o \\
\rightarrow \\
q_1 \\
\end{array}
\begin{array}{c}
d, o, o \\
\rightarrow \\
q_2 \\
\end{array}
\begin{array}{c}
e, o, o \\
\rightarrow \\
q_3 \\
\end{array}
\begin{array}{c}
e, c, o \\
\rightarrow \\
q_4 \\
\end{array}
\begin{array}{c}
e, o, c \\
\rightarrow \\
q_5 \\
\end{array}
\begin{array}{c}
l, c, o \\
\rightarrow \\
q_6 \\
\end{array}
\begin{array}{c}
2, o, c \\
\rightarrow \\
q_7 \\
\end{array}
\begin{array}{c}
r, q, r \\
\rightarrow \\
q_8 \\
\end{array}
\]

The behavior of \( M \) can be described as follows: First, \( M \) opens memory 1 and reads \( a_i, i \geq 0 \). After that, \( M \) opens the second memory, reads \( b_j \) (which is stored in both memories), \( j \geq 0 \), closes the first memory, reads \( d^k, k \geq 0 \), and closes the second memory. Hence, after reading \( a^ib^jd^k \), the first memory contains \( a^ib^j \), and the second \( b^jd^k \). Finally, \( M \) recalls memory 1 and then 2. Hence, \( \mathcal{L}(M) = \{a^ib^jd^k \mid i, j, k \geq 0\} \).

Now, note that in each input word \( w \), memory 2 is opened and closed after memory 1. Hence, if \( j > 0 \), the areas in \( w \) where the two memories are open overlap, instead of being nested. This cannot happen in a regex, as it is ensured from the syntax of variable bindings that these “areas” in the word are properly nested. For this reason, it seems impossible to express \( \mathcal{L}(M) \) with a regex with only two variables. But this does not mean that \( \mathcal{L}(M) \) is not a regex language, as \( \mathcal{L}(M) = \mathcal{L}(\alpha) \) for \( \alpha = ((x: a^i)/ (y: b^j) / (z: d^k) / kxy / &k) \). In other words, the key idea is expressing each memory with two variables (one for the overlapping parts of the memories, and one for each rest).\(^4\)

Next, we shall see that every TMFA\(^{acc}\) can be transformed into an equivalent TMFA\(^{eq}\), which implies \( \mathcal{L}(\text{TMFA}) = \mathcal{L}(\text{TMFA}\^{eq}) \); thus, it follows from \(^{16}\) that TMFA characterize \( \mathcal{L}(\text{RX}) \). The idea of this construction is as follows. Every memory \( i \) is simulated by two memories \((i, 1)\) and \((i, 2)\), which store a (non-deterministically guessed) factorisation of the content of memory \( i \). This allows us to guess and verify if a memory recall failure occurs, i.e., \((i, 1)\) stores the longest prefix that can be matched and \((i, 2)\) starts with the first mismatch. For correctness, it is crucial that every possible factorisation of the content of a memory \( i \) can be guessed.

We first need the following definition. An \( M \in \text{TMFA} \) is in normal form if no empty memory is recalled, no open memory is opened, no memory is reset, and, for every transition \( \delta(q, b) \geq (p, s_1, \ldots, s_k) \),

- if \( b \neq \varepsilon \), then \( s_i = \circ, 1 \leq i \leq k \),
- if \( s_i \neq \circ \), for some \( i, 1 \leq i \leq k \), then \( b = \varepsilon \) and \( s_j = \circ \), for every \( j, 1 \leq j \leq k, i \neq j \).

\(^4\)Proving that these overlaps can always be resolved is the main step in showing the equivalence of \( \mathcal{L}(\text{RX}) \) and \( \mathcal{L}(\text{TMFA}) \), which is provided in \(^{16}\) (see also the discussion at the end of the proof of Theorem\(^1\)).
Proposition 1. Any TMFA can be transformed into an equivalent TMFA in normal form.

Proof. An arbitrary TMFA can be changed into an equivalent one in normal form as follows. By introducing \( \varepsilon \)-transitions, we can make sure that every transition is of the form stated in the proposition. Furthermore, by adding states, we can keep track of the memory configurations (i.e., their status and whether or not they are empty; this simple technique is also explained in more detail in the proof of Theorem [3]). This allows us to replace transitions that are recalling an empty memory by \( \varepsilon \)-transitions. Furthermore, transitions that open an open memory \( i \) are replaced by transitions applying the memory instructions \( c \) and \( o \) in this order to memory \( i \), and transitions that reset a memory \( i \) are replaced by transitions applying the memory instructions \( c \cdot o \cdot c \) or \( c \) in this order to memory \( i \) (the correctness of this can be easily checked with the help of Figure [1]). The TMFA is then in normal form and, by definition, these modifications do not change the accepted language. \( \square \)

Now, we can formally prove the claimed characterisation.

Theorem 1. \( \mathcal{L}(\text{TMFA}) = \mathcal{L}(\text{TMFA}^{\text{eq}}) = \mathcal{L}(\text{RX}). \)

Proof. We first note that \( \mathcal{L}(\text{TMFA}^{\text{eq}}) = \mathcal{L}(\text{RX}) \) follows from [46] (we briefly discuss this at the end of the proof). Since \( \mathcal{L}(\text{TMFA}^{\text{eq}}) \subseteq \mathcal{L}(\text{TMFA}) \) and TMFA = TMFA\(^{\text{eq}}\) \( \cup \) TMFA\(^{\text{acc}}\); it only remains to prove \( \mathcal{L}(\text{TMFA}^{\text{acc}}) \subseteq \mathcal{L}(\text{TMFA}^{\text{eq}}) \). To this end, let \( M \) be a TMFA\(^{\text{acc}}\) in normal form. First, we replace every memory \( i, 1 \leq i \leq k \), by two memories \((i, 1)\) and \((i, 2)\) and we implement in the finite state control a list \((x_1, x_2, \ldots, x_\ell)\) with entries from \( \Sigma \cup \{\varepsilon\} \), which initially satisfies \( x_i = \varepsilon, 1 \leq i \leq k \). Then, we change the transitions of \( M \) such that the new memories \((i, 1)\) and \((i, 2)\) simulate the old memory \( i \), i.e., memory \( i \) stores some word \( w \) if and only if memories \((i, 1)\) and \((i, 2)\) store \( u_1 \) and \( u_2 \), respectively, with \( u = u_1u_2 \). Moreover, the element \( x_i \) always equals the first symbol of the content of memory \((i, 2)\). More precisely, this can be done as follows. Let \( \delta(q, b) \supseteq (p, s_1, \ldots, s_k) \) be an original transition of \( M \).

- If \( s_i = \varepsilon \) or \( s_i = c \), for some \( i, 1 \leq i \leq k \), then instead we open memory \((i, 1)\) or close memory \((i, 2)\), respectively.
- If \( b \in \Sigma \), then, for every open memory \((i, 1)\), we nondeterministically choose to close it and open memory \((i, 2)\) instead and set \( x_i = b \). Then we read \( b \) from the input and change to state \( p \).
- If \( b \in \{1, 2, \ldots, k\} \), then we first recall memory \((b, 1)\) and then, for every open memory \((i, 1)\), we nondeterministically choose to close it and open memory \((i, 2)\) instead and set \( x_i = x_b \). Then, we recall memory \((b, 2)\) and change to state \( p \).

All these modifications can be done by introducing intermediate states and using \( \varepsilon \)-transitions and the accepted language of \( M \) does not change.

The automaton \( M \) now stores some content \( u \) of an original memory \( i \) factorised into two factors \( u_1 \) and \( u_2 \) in the memories \((i, 1)\) and \((i, 2)\), respectively. For the sake of convenience, we simply say that \( u \) is stored in \((i, 1) \cdot (i, 2)\) in order to describe this situation. Next, we show that if \( u \) is stored in \((i, 1) \cdot (i, 2)\), then any way of how \( u \) is factorised into the content of \((i, 1)\) and \((i, 2)\) is possible. More precisely, we show that, for every \( w, u, u_1, u_2 \in \Sigma^* \) with \( u_1u_2 = u \), \( M \) can reach state \( p \) by consuming \( w \) with \( u \) stored in \((i, 1) \cdot (i, 2)\) if and only if \( M \) can reach state \( p \) by consuming \( w \) with \( u_1 \) and \( u_2 \) stored in \((i, 1)\) and \((i, 2)\), respectively.

The \( \text{iif} \) part of this statement is trivial. We now assume that \( M \) can reach state \( p \) by consuming \( w \) with \( u \) stored in \((i, 1) \cdot (i, 2)\). This implies that we reach the situation that \((i, 1)\) is open, currently stores \( u_1' \) and the next consuming transition consumes \( u_1'u_2' \), where \( u_1 = u_1'u_1'' \) and \( u_2 = u_2'u_2'' \) with \( u_2'' \neq \varepsilon \). If \( u_1'' = \varepsilon \), then \( M \) can choose to close \((i, 1)\) and then open \((i, 2)\), which results in \( u_1 \) and \( u_2 \) being stored in \((i, 1)\) and \((i, 2)\), respectively. If, on the other hand, \( u_1'' \neq \varepsilon \), then the next transition recalls memories \((j, 1)\) and \((j, 2)\) such that \( u_1''u_2' \) is stored in \((j, 1) \cdot (j, 2)\). If \( u_1' \) and \( u_2' \) are stored in \((j, 1)\) and \((j, 2)\), respectively, then \( M \) first recalls \((j, 1)\), chooses to close \((i, 1)\) and open \((i, 2)\), and then recalls \((j, 2)\), which results in \( u_1 \) and \( u_2 \) being stored in \((i, 1)\) and \((i, 2)\), respectively. Consequently, we have to repeat this argument for memories \((j, 1)\) and \((j, 2)\), i.e., we have to show that it is possible that \( u_1''u_2' \) is stored in \((j, 1) \cdot (j, 2)\) in such a way that \( u_1' \) is stored in \((j, 1)\) and \( u_2' \) is stored in \((j, 2)\). Repeating this argument, we will eventually
arrive at a memory that is not filled by any memory recalls; thus, we necessarily have the case $u''_1 = \varepsilon$.

Now, we turn $M$ into a TMFA\textsuperscript{\{\#\}} $M'$, i.e., the state [trap] becomes non-accepting, and, in addition, we add a new accepting state $q_t$ (simulating the old accepting [trap]) with $\delta(q_t, x) = \{(q_t, \circ, \ldots, \circ)\}, x \in \Sigma$, and we change all ordinary transitions (i.e., transitions that are not recall failure transitions) of the former $M$ that lead to [trap] such that they now lead to $q_t$. Furthermore, we change this $M'$ such that for every memory recall, there is also the nondeterministic choice to only recall $(i, 1)$, then check whether $x_i$ does not equal the next symbol on the input and, if this is the case, enter state $q_t$. Obviously, this simulates the memory recall failure of $M$.

Every word accepted by $M$ without memory recall failures can be accepted by $M'$ in the same way, every word accepted by $M$ due to a recall failure can be accepted by $M'$ by guessing and simulating this memory recall failure. On the other hand, if $M'$ accepts a word with a simulated memory recall failure, then $M$ will accept this word by a proper memory recall failure, and if $M'$ accepts a word without a simulated memory recall failure, then, since $M' \in$ TMFA\textsuperscript{\{\#\}}, there is no memory recall failure in the computation and $M$ can accept the word by the same computation.

This completes the proof of $L$(TMFA\textsuperscript{\{\#\}}) $\subseteq L$(TMFA\textsuperscript{\{\#\}}).

We shall conclude this proof by briefly sketching why $L$(TMFA\textsuperscript{\{\#\}}) = $L$(RX) holds. For an $\alpha \in$ RX, it is straightforward to obtain an equivalent TMFA\textsuperscript{\{\#\}}. Transform $\alpha$ into a proper regular expression $\alpha_R$ with $L(\alpha_R) = R(\alpha)$ (by just renaming variable bindings and references), then transform $\alpha_R$ into an equivalent NFA $M$, and finally interpret $M$ as a TMFA\textsuperscript{\{\#\}} by interpreting transition labels $[x, x]$ as memory instructions and transition labels $x$ as memory recalls. The other direction relies on first resolving overlaps of memories (i.e., the case that two memories store factors that overlap in the input word, see also Example 3) and then transforming the TMFA\textsuperscript{\{\#\}} $M$ into a proper regular expression for a ref-language that dereferences to $L(M)$, which can then directly be interpreted as a regex (due to the non-overlapping property of memories, which translates into a well-formed nesting of the parentheses $[x, x]$). This works in the same way as for the case of memory automata without trap-states (see [14] for details).

A consequence of the proof is that TMFA inherits the NP-hardness of the membership problem from RX. We do not devote more attention to this, as we focus on deterministic TMFA.

### 3.1 Deterministic TMFA

A TMFA is deterministic (or a DTMFA, for short) if $\delta$ satisfies $|\delta(q, b)| \leq 1$, for every $q \in Q$ and $b \in \Sigma \cup \{\varepsilon\} \cup \{1, 2, \ldots, k\}$ (for the sake of convenience, we then interpret $\delta$ as a partial function with range $Q \times \{o, c, r, \circ\}$), and, furthermore, for every $q \in Q$, if $\delta(q, x)$ is defined for some $x \in \{1, 2, \ldots, k\} \cup \{\varepsilon\}$, then, for every $y \in (\Sigma \cup \{\varepsilon\} \cup \{1, 2, \ldots, k\}) \setminus \{x\}$, $\delta(q, y)$ is undefined.\footnote{Note that in [15] deterministic memory automata without trap-state are considered. }

Analogously to TMFA, we partition DTMFA into DTMFA\textsuperscript{\{\#\}} and TMFA\textsuperscript{\{\#\}}.

Clearly, the TMFA of Examples 2 and 3 are not deterministic (in Example 2 there are different transitions for the same state that consume the same symbol and in Example 3 there are states for which $\varepsilon$-transitions exists in addition to other transitions). By minor changes of the TMFA of Example 2 a DTMFA can be easily constructed for the language $\{\{u \varepsilon \varepsilon v u | u, v \in \{a, b\}^+\}\}^+$, the details are left to the reader.

The algorithmically most important feature of DTMFA is that their membership can be solved efficiently by running the automaton on the input word. However, for each processed input symbol, there might be a delay of at most $|Q|$ steps, due to $\varepsilon$-transitions and recalls of empty memories, which leads to $O(|Q| |w|)$. Removing such non-consuming transitions first, is possible, but problematic. In particular, recalls of empty memories depend on the specific input word and could only be determined beforehand by storing for each memory whether it is empty, which is too expensive. However, by an $O(|Q|^2)$ preprocessing, we can compute the information that is needed in order to determine in $O(k)$ where to jump if certain memories are empty, and which memories are currently empty can be determined on-the-fly while processing the input. This leads to a delay of only $k$, the number of memories.

**Theorem 2.** Given $M \in$ DTMFA with $n$ states and $k$ memories, and $w \in \Sigma^*$, we can decide whether or not $w \in L(M)$
• in time $O(n|w|)$ without preprocessing, or
• in time $O(k|w|)$ after an $O(n^2)$ preprocessing.

Proof. We first modify $M$ with respect to its $\varepsilon$-transitions as follows. Let $p \in Q$ be a state with an $\varepsilon$-transition that is followed by another $\varepsilon$-transition. If $p$ is contained in a cycle $q_1, q_2, \ldots, q_n$ of $\varepsilon$-transitions, we simply replace this cycle by a single state $q'$ (i.e., all incoming edges of any $q_i$, $1 \leq i \leq n$, then point to $q'$) that is accepting if and only if some $q_i$, $1 \leq i \leq n$, is (note that, since $M$ is deterministic, no $q_i$ has any other transition). Otherwise, there are states $q_1, q_2, \ldots, q_n$, $p = q_1$, with transitions $\delta(q_i, \varepsilon) = (q_{i+1}, s_{i,1}, \ldots, s_{i,k})$, $1 \leq i \leq n - 1$, such that $q_n$ has no $\varepsilon$-transition. We can now remove the transition $\delta(q_1, \varepsilon) = (q_2, s_{1,1}, \ldots, s_{1,k})$ and add a transition $\delta(q_1, \varepsilon) = (q_n, t_1, \ldots, t_k)$, where, for every $j$, $1 \leq j \leq k$, $t_j$ is a memory instruction that has the same effect as applying instructions $s_{1,j}, s_{2,j}, \ldots, s_{n-1,j}$ in this order. Moreover, if, for some $i$, $2 \leq i \leq n - 1$, $q_i \in F$, then we define $q_1$ as accepting. By applying this modification for every $\varepsilon$-transition that is followed by another $\varepsilon$-transition, we can modify $M$ such that no $\varepsilon$-transition is followed by another $\varepsilon$-transition. Hence, since $M$ is deterministic, there are at most $|Q|$ $\varepsilon$-transitions and for each, we have to determine the states $q_1, q_2, \ldots, q_n$ and perform the modifications described above, which can be done in time $O(|Q|)$, as well. Consequently, the whole procedure can be carried out in $O(|Q|^2)$.

Next, we consider states with a memory recall transition. Similar as for states with $\varepsilon$-transition, such states are followed by a (possibly empty) sequence of consecutive memory recall or $\varepsilon$-transitions that either ends in a state with neither memory recall nor $\varepsilon$-transition or eventually forms a loop. We first consider the case, where this sequence does not contain any $\varepsilon$-transitions and does not form a loop. Let $q_1$ be the state with memory recall transition and let $(q_1, \ell_1), (q_2, \ell_2), \ldots, (q_n, \ell_n), q_{n+1}$ be the sequence of the following states with consecutive memory recall transitions along with the memory that is recalled. More precisely, the transition from $q_i$ to $q_{i+1}$, $1 \leq i \leq n$, recalls $\ell_i$ and the last element $q_{n+1}$ is the first state without memory recall transition (and, by assumption, also without $\varepsilon$-transition). We now contract this list by the following algorithm. Initially, let $A = \emptyset$. Then we move through the list from left to right and for every element $(q_i, \ell_i)$ (except for $q_{n+1}$), we proceed as follows. If $\ell_i \in A$, then we remove $(q_i, \ell_i)$ and if $\ell_i \notin A$, then we keep $(q_i, \ell_i)$ and add $\ell_i$ to $A$. Obviously, this results in a list $(p_1, r_1), \ldots, (p_n, r_n), q_{n+1}$ with $n' \leq k$. The idea is that if we move from left to right through this new list, it tells us which state to enter if the memory of the current memory recall is empty, i.e., if memory $r_1$ is non-empty, we recall it in state $p_1$, if memory $r_1$ is empty, we can directly jump to state $p_2$ and either recall $r_2$, if it is non-empty, or jump to $p_3$ otherwise, and so on. If all memories (that occur somewhere in the list) are empty, we end up in state $q_{n+1}$.

In the presence of $\varepsilon$-transitions, we simply ignore these and always only consider the next transition that recalls a memory, i.e., it is possible that for elements $(q_i, \ell_i)$ and $(q_{i+1}, \ell_{i+1})$ of the non-contracted list, there is an intermediate state $p$ with recall transition from $q_i$ to $p$ and $\varepsilon$-transitions from $p$ to $q_{i+1}$ (note that due to the construction from above, there are no consecutive $\varepsilon$-transitions), but the contraction works in the same way. Moreover, if $(q_i, \ell_j)$ and $(q_j, \ell_j)$ (or $q_{n+1}$, the last element) with $i < j$ are consecutive elements of the contracted list (i.e., all elements $(q_i, \ell_j)$, $i + 1 \leq r \leq j - 1$, have been deleted by the algorithm), then we replace $(q_i, \ell_j)$ by $(q_{i+1}^{\text{acc}}, \ell_j)$ (where the marker ACC means that we can accept), if for some $r$, $i + 1 \leq r \leq j$, $q_r \in F$. Note that this is analogous to the modification from above, where we define states as accepting, if they are connected to an accepting state by a sequence of $\varepsilon$-transitions, but here we cannot change acceptance of the actual states, since it depends on the current contents of memories, whether we can reach an accepting state by only recalls of empty memories or $\varepsilon$-transitions.

If the sequence of memory recall transitions enters a loop, we construct the list only up to the first time a state is repeated, say $p$, and have $(p, \text{LOOP})$ as the last element of the list. Then we apply the contraction in the same way as before, where $(p, \text{LOOP})$ plays the role of $q_{n+1}$. Similarly as before, we mark elements $(q_i, \ell_j)$ as accepting if a pair was removed that contained an accepting state.

In addition to the states, we also store in the list the memory instructions that have to be applied in order to jump to the next state (this can be done similar as for contracting the $\varepsilon$-transitions above). We construct such a list for every state with a memory recall transition. Every single list can be constructed in time $O(|Q|)$, so we need time $O(|Q|^2)$ in total.
Now we check whether or not $w \in \mathcal{L}(M)$ by running (the modified) $M$ on input $w$ in a special way. We first initialise a list $((1, \mathcal{C}, \varepsilon), (2, \mathcal{C}, \varepsilon), \ldots, (k, \mathcal{C}, \varepsilon))$ indicating that every memory is closed and empty. Then we simulate $M$ on input $w$ as follows. Every transition that consumes a single symbol as well as every $\varepsilon$-transition is just carried out. Whenever a memory status is changed, we store this in the list and also store whether a memory is currently empty or not (note that we have to know the current statuses in order to do this). When a memory is recalled in state $q$, then we move through the list stored for state $q$ until we find a recall of a memory that is currently non-empty, jump in the automaton to the corresponding state and apply the memory instructions. Whenever we reach an element $(q^\text{acc}, \ell_i)$ in the list, then we check whether the input has been fully consumed and if yes, we conclude $w \in \mathcal{L}(M)$. If we reach in a list an element $(p, \text{LOOP})$, then we conclude $w \notin \mathcal{L}(M)$, if $p \in F$ and $w \notin \mathcal{L}(M)$ otherwise. If in the computation the input has been completely consumed, then we conclude $w \in \mathcal{L}(M)$ if and only if $M$ is in an accepting state.

Since there are no consecutive $\varepsilon$-transitions, every consumption of a single symbol from the input by a transition is done in constant time. Every consumption by a memory recall transition requires time $O(k)$, since we have to move through a list of size $O(k)$. Consequently, the total running time is $O(|Q|^2 + k|w|)$. 

Note that the preprocessing in the proof of Theorem 2 is only required once, which implies the following corollary.

**Corollary 1.** Given $M \in \text{DTMFA}$ with $n$ states and $k$ memories, and words $w_i \in \Sigma^*$, $1 \leq i \leq \ell$, we can decide whether or not $w_i \in \mathcal{L}(M)$, $1 \leq i \leq \ell$, in total time $O(n^2 + k \sum |w_i|)$. 

Moreover, if it is guaranteed that no empty memories are recalled, then membership can be solved in $O(n + |w|)$ (where $O(n)$ is needed in order to remove $\varepsilon$-transitions).

Similar to DFA, it is possible to complement DTMFA by toggling the acceptance of states. However, for DTMFA, we have to remove $\varepsilon$-transitions and recalls of empty memories. In particular, our construction uses the finite control to store whether memories are empty or not, which causes a blow-up that is exponential in the number of memories.

We first extend the notion of completeness from DFA to DTMFA, by saying that a DTMFA is complete if, for every $q \in Q$, either $\delta(q, x)$ is defined, for every $x \in \Sigma$, or $\delta(q, \varepsilon)$ is defined, for some $i$, $1 \leq i \leq k$, or $\delta(q, \varepsilon)$ is defined. This means that a complete DTMFA has, for every state, either exactly $|\Sigma|$ transitions (which are all consuming transitions, but not memory recall transitions), exactly one memory recall transition, or exactly one $\varepsilon$-transition.

For deterministic automata, it is usually possible to apply the state complementation technique (i.e., toggling acceptance of states) in order to show closure under complement. However, we also need completeness and $\varepsilon$-freeness, since otherwise it may happen that a word is not accepted because its computation gets stuck or enters an infinite $\varepsilon$-loop and therefore is not entirely processed, which leads to a word which is accepted neither by the original nor by the complement automaton. The requirement of completeness and $\varepsilon$-freeness is not a restriction for DTMFA, since these properties can be achieved by classical techniques. However, recalling empty memories, which are special cases of $\varepsilon$-transition, can cause the same problems and therefore we have to get rid of them as well. This can be done by storing in the finite-state control whether the memories are currently empty or non-empty and then treating recalls of empty memories as $\varepsilon$-transitions and remove them along with the other $\varepsilon$-transition in the classical way (note that the trick of handling empty memories that has been used in the context of Theorem 2 cannot applied here, since the automaton needs to store the information for all possible runs on input words).

We need a few more definitions: Let $\Gamma = \{\phi, c, r, o\}$ and let $\otimes$ be a binary operator on $\Gamma$ defined by $x \otimes y = y$, if $y \neq \phi$ and $x \otimes y = x$, if $y = \phi$. Furthermore, we extend $\otimes$ to $\Gamma^k$ by $(x_1, \ldots, x_k) \otimes (y_1, \ldots, y_k) = (x_1 \otimes y_1, \ldots, x_k \otimes y_k)$. We note that $\otimes$ is associative and some memory instructions $s_1, s_2, \ldots, s_n \in \Gamma$ applied to some memory in this order have the same result as the memory instruction $s_1 \otimes s_2 \otimes \ldots \otimes s_n$ (this can be easily verified with the help of Figure 1).

Next, we prove a sequence of propositions (that are all proved in a straightforward way by applying classical automata constructions):
Proposition 2. Let $M \in \mathit{DTMFA}$. For every $w \in \Sigma^*$ and every configuration $c$ for $M$, there exists at most one configuration $c'$ with $c \vdash_M c'$.

Proof. Let $c = (q,v,(u_1,r_1),\ldots,(u_k,r_k))$. If no $\delta(q,i), 1 \leq i \leq k$, is defined, then there is obviously at most one $c'$ with $c \vdash_M c'$. If $\delta(q,i) = (p,s_1,\ldots,s_k)$, then either $v = u_iv'$, which implies that $c \vdash_M (p,v',(u_1',r_1'),\ldots,(u_k',r_k'))$, where $(u_i',r_i')$, $1 \leq j \leq k$, are uniquely determined by $u_i$ and the $s_j, 1 \leq j \leq k$, or $u_i$ is not a prefix of $v$, which implies that $c \vdash_M ((\text{trap}),v'',(u_1,r_1),\ldots,(u_k,r_k))$, where $v = v'v''$ and $v'$ is the largest common prefix of $v$ and $u_i$. In both cases, there is at most one configuration $c'$ with $c \vdash_M c'$.

Proposition 3. For every $M \in \mathit{DTMFA}$ there exists an $\varepsilon$-free $M' \in \mathit{DTMFA}$ with $\mathcal{L}(M) = \mathcal{L}(M')$.

Proof. Let $M = (Q,\Sigma,\delta,q_0,F)$. For every $p \in Q$, if, for some $q \in Q$, $\delta(p,\varepsilon) = (q,s_1,\ldots,s_k)$, then we define $S_{\varepsilon,1}(p) = q$ and $M(p,q) = (s_1,\ldots,s_k)$. For every $p \in Q$ and every $i, 2 \leq i \leq |Q| - 1$, we define $S_{\varepsilon,i}(p) = S_{\varepsilon,i-1}(p)$ and, if $S_{\varepsilon,i}(p)$ is defined, we define (or redefine) $M(p,S_{\varepsilon,i}(p)) = M(p,S_{\varepsilon,i-1}(p)) \odot (s_1,\ldots,s_k)$, where $\delta(S_{\varepsilon,i-1}(p),\varepsilon) = (S_{\varepsilon,i}(p),s_1,\ldots,s_k)$.

For every $p \in Q$ with $\delta(p,\varepsilon)$ defined, we now remove the $\varepsilon$-transitions as follows. Let $i, 1 \leq i \leq |Q| - 1$, be such that $S_{\varepsilon,i}(p) = q$ and $S_{\varepsilon,i+1}(p)$ is undefined. Furthermore, let $\delta(q,x_j) = (t_j,s_j,1,\ldots,s_j,k), 1 \leq j \leq \ell$, for some $\ell$ with $0 \leq \ell \leq |\Sigma|$, be all the transitions from $q$ (note that $\ell = 1$ and $x_j \in \{1,2,\ldots,k\}$ covers the case of a single memory recall transition and, furthermore, $x_j = \varepsilon$ is by definition not possible). We now add new transitions $\delta(p,x_j) = (t_j,s_1 \odot s_j,\ldots,s_\ell \odot s_k)$, where $M(p,q) = (s_1,\ldots,s_k)$. Then, we simply delete all $\varepsilon$-transitions (note that this may produce states that are not reachable anymore, which are deleted as well). It can be easily verified that this results in an $M' \in \mathit{DTMFA}$ with $\mathcal{L}(M) = \mathcal{L}(M')$.

Proposition 4. For every $M \in \mathit{DTMFA}$ there exists a complete $M' \in \mathit{DTMFA}$ with $\mathcal{L}(M) = \mathcal{L}(M')$.

Proof. Let $M = (Q,\Sigma,\delta,q_0,F)$. We transform $M$ into $M'$ by adding a new non-accepting state $t$ with $\delta(t,x) = (t,\varepsilon,\ldots,\varepsilon)$, for every $x \in \Sigma$, and we add transitions for every state $q \in Q$ as follows. If $\delta(q,i)$ is undefined, for every $i, 1 \leq i \leq k$, and $\delta(q,\varepsilon)$ is undefined, then, for every $x \in \Sigma$ with $\delta(q,x)$ undefined, we set $\delta(q,x) = (t,\varepsilon,\ldots,\varepsilon)$. On the other hand, if $\delta(q,i)$ is defined, for some $i, 1 \leq i \leq k$, or $\delta(q,\varepsilon)$ is defined, then we do not add any transition. By definition, $M'$ is complete and, since $t$ is non-accepting, $\mathcal{L}(M) = \mathcal{L}(M')$.

Remark 1. We note that the construction of the proof of Proposition 3 preserves completeness, i.e., if $M$ is a complete DTMFA, then we obtain an equivalent complete DTMFA without $\varepsilon$-transitions. Moreover, the construction of the proof of Proposition 4 does not introduce $\varepsilon$-transitions; thus, it turns an $\varepsilon$-free DTMFA into an equivalent complete DTMFA that is still $\varepsilon$-free.

We are now ready to show closure of $\mathcal{L}(\mathit{DTMFA})$ under complementation.

Theorem 3. $\mathcal{L}(\mathit{DTMFA})$ is closed under complement.

Proof. Let $M = (Q,\Sigma,\delta,q_0,F) \in \mathit{DTMFA}$. By Proposition 4 we can assume that $M$ is complete. Due to Proposition 2 for any input $w$, there is a unique computation of $M$ on $w$. Hence, the idea is now to toggle the acceptance of all the states of $M$ in order to obtain a DTMFA that accepts $\mathcal{L}(\overline{M})$. However, this only works if $M$ is $\varepsilon$-free, since otherwise it is possible that some word $w \in \Sigma^*$ cannot be fully consumed by $M$ (for example, if it leads into a loop in which all transitions are $\varepsilon$-transitions and no state is accepting); thus, $w$ is neither accepted by $M$ nor by the DTMFA obtained by toggling the acceptance of states. While we can remove $\varepsilon$-transitions due to Proposition 3 we encounter the problem that a memory recall transition with respect to an empty memory behaves just like an $\varepsilon$-transition and, thus, can cause the same problems. Hence, we first have to transform such memory recall transition into ordinary $\varepsilon$-transitions, which can then be removed according to Proposition 3.

To this end, we modify $M$ such that the finite state control stores, for every $i, 1 \leq i \leq k$, whether or not memory $i$ is open and whether or not memory $i$ stores the empty word. More precisely, we obtain an $M_1 \in \mathit{DTMFA}$ by modifying $M$ as follows. Every state $q$ is replaced by
2^k new states \([q, (r_1, c_1), \ldots, (r_k, c_k)]\), where \(r_i \in \{0, \infty\}, c_i \in \{\varepsilon, \square\}, 1 \leq i \leq k\), and we change the transitions such that if \(M_1\) reaches a configuration with state \([q, (r_1, c_1), \ldots, (r_k, c_k)]\), then, in the current configuration, for every \(i, 1 \leq i \leq k\), \(r_i\) is the status of memory \(i\) and memory \(i\) is empty if and only if \(c_i = \varepsilon\). For example, if \(M_1\) is in state \([p, (r_1, c_1), \ldots, (r_k, c_k)]\) with \((r_i, c_i) = (\varepsilon, \square)\) and \(\delta(p, x) = (q, s_1, \ldots, s_k)\) with \(x \in \Sigma\) and \(s_i = \infty\), then, if \(x\) is the next symbol of the input, \(M_1\) changes to a state \([q, (r_1', c_1'), \ldots, (r_k', c_k')]\) with \((r_i', c_i') = (0, \square)\). We note that \(M_1\) is still deterministic and completely consumes the input. This directly implies \(M_2\) has at least one memory \(i\) such that for every \(y \in \Sigma \cup \{1, 2, \ldots, k\}\), \(\delta(y, y)\) is undefined. Consequently, \(M_2\) is still deterministic and it never happens that an empty memory is recalled. Next, by Proposition \(3\), we can transform \(M_2\) into a complete \(M_3 \in \text{DTMFA}\) without \(\varepsilon\)-transitions (see Remark \(\square\)) that still has the property that no empty memories are recalled.

Let \(M \in \text{DTMFA}\) be obtained from \(M_3\) by toggling the acceptance of the states, i.e., if \(Q_3\) and \(F_3\) are the sets of states and accepting states, respectively, of \(M_3\), then \(\overline{M}\) is obtained from \(M_3\) by replacing \(F_3\) by \(Q_3 \setminus F_3\). Obviously, for every \(w \in \Sigma^*\), both \(M_3\) and \(\overline{M}\), on input \(w\), reach the same state and completely consume the input. This directly implies \(L(M) = L(\overline{M})\). \(\square\)

We next discuss expressive power: If there is a constant upper bound on the lengths of contents of memories that are recalled in accepting computations of an \(M \in \text{DTMFA}\), then memories can be simulated by the finite state control; thus, \(L(M) \in L(\text{REG})\). Consequently, if \(L(M) \notin L(\text{REG})\), there is a word \(ww\) that is accepted by recalling some memory with an arbitrarily large content \(v\). Moreover, if \([\text{trap}]\) is non-accepting, then no word can be accepted that contains \(u\) as a prefix, but not \(uw\), since this will cause a memory recall failure. Intuitively speaking, a \(\text{DTMFA}^{[i]}\) for a non-regular language makes arbitrarily large "jumps":

**Lemma 1** (Jumping Lemma). Let \(L \in L(\text{DTMFA}^{[i]})\). Then either \(L\) is regular, or for every \(m \geq 0\), there exist \(n \geq m\) and \(p_n, v_n \in \Sigma^*\) such that

1. \(|v_n| = n\),
2. \(v_n\) is a factor of \(p_n\),
3. \(p_n v_n\) is a prefix of a word from \(L\),
4. for all \(u \in \Sigma^*\), \(p_n u \not\in L\) only if \(v_n\) is a prefix of \(u\).

**Proof.** As \(L \in L(\text{DTMFA}^{[i]})\), there exists an \(M \in \text{DTMFA}^{[i]}\) with \(L(M) = L\). If there is an \(m \geq 0\) such that in every accepting run of \(M\), each memory stores only a word of length at most \(m\), then \(L\) is regular (as we can rewrite \(M\) into a DFA that stores the contents of the memories in its states). Likewise, if memories can store words of unbounded length, but are then never recalled, these memories can be eliminated, which also allows us to turn \(M\) into a DFA for \(L\).

Hence, if \(L\) is not regular, \(M\) has at least one memory \(x\) such that for every \(m \geq 0\), there is an accepting run of \(M\) on a word \(w\) during which \(x\) stores a word of length \(n \geq m\), and this memory is recalled with this content. Let \(p_n\) be the part of the accepting run that \(M\) has processed up to a state \(q\) where it recalls \(x\) at a point where this memory contains a word \(v_n\) of length \(n\). As \(v_n\) must have been consumed while processing \(p_n\), \(|p_n| \geq n\) holds, and \(v_n\) must be a factor of \(p_n\).

If \(M\) succeeds at recalling \(x\) at this point (i.e., it consumes \(v_n\)), it can continue to accept \(w\), which means that \(p_n v_n\) is a prefix of \(w \in L\). On the other hand, on an input \(p_n u\) for some \(u \in \Sigma^*\) such that \(v_n\) is not a prefix of \(u\), \(M\) encounters a memory recall failure and rejects. As \(M\) is deterministic, the recall transition for \(x\) must be the only transition that leaves the state \(q\). Hence, \(p_n u \not\in L\) for \(u\) that do not have \(v_n\) as prefix. \(\square\)
The Jumping Lemma is a convenient tool for proving that languages cannot be accepted by a DTMFA\(^{\text{DTMFA}}\), which shall be illustrated by some examples.

**Example 4.** Let \( L := \{ uv \mid |u| \geq 2 \} \) with \(|\Sigma| \geq 2\), which is well-known to be not regular. Assume \( L \in \mathcal{L}(\text{DTMFA}^{\text{DTMFA}}) \) and choose \( m := 1 \). Then there exist \( n \geq 1 \) and \( p_n, v_n \in \Sigma^* \) that satisfy the conditions of Lemma \( \text{[L]} \). Choose \( a \in \Sigma \) that is not the first letter of \( v_n \), and define \( u := ap_n a \). Then \( v_n \) is not a prefix of \( u \), but \( p_n u = (p_n a)^2 \in L \), which is a contradiction.

**Example 5.** Let \( L := \{ a^i b a^j \mid i > j \geq 0 \} \). Using textbook methods, it is easily shown that \( L \) is not regular. Now, assume that \( L \in \mathcal{L}(\text{DTMFA}^{\text{DTMFA}}) \), choose \( m := 4 \). Then there exist \( n \geq 4 \) and \( p_n, v_n \in \Sigma^* \) that satisfy the conditions of Lemma \( \text{[L]} \). As \( p_n v_n \) is a prefix of a word in \( L \), either \( p_n = a^4 \) or \( p_n = a^i b a^j \) with \( i, j \geq 0 \) (and \( i \geq 4 \) or \( i + j \geq 3 \)). In the first case, consider \( u := b a \). Then \( p_n u = a^i b a \) with \( i \geq 4 \); hence, \( p_n u \in L \). But \( u \) starts with \( b \), and \( v_n \) is a factor of \( p_n = a^i \), which leads to a contradiction, as \( v_n \) cannot be a prefix of \( u \). For the second case, let \( u := a \). As \( p_n v_n \) is a prefix of a word in \( L \), and \( |v_n| = n, i > j + n \geq j + 4 \) must hold. Hence, \( p_n u = a^i b a^{j+1} \), and \( p_n u \in L \), which, as \( v_n \) is not a prefix of \( u \), leads again to a contradiction.

For unary languages, there is an alternative to Lemma \( \text{[L]} \) that is easier to apply and that characterizes unary DTMFA\(^{\text{DTMFA}}\)-languages. It is built on the following definition: A language \( L \subseteq \{a\}^* \) is an infinite arithmetic progression if \( L = \{ a^{bi+c} \mid i \geq 0 \} \) for some \( b \geq 1, c \geq 0 \).

**Lemma 2.** Let \( L \in \mathcal{L}(\text{DTMFA}^{\text{DTMFA}}) \) be an infinite language with \( L \subseteq \{a\}^* \). The following conditions are equivalent:

1. \( L \) is regular.
2. \( L \) contains an infinite arithmetic progression.
3. There is \( b \geq 1 \) such that, for every \( n \geq 0 \), there exists some \( c_n \geq 0 \) with \( a^{bi+c_n} \in L \) for all \( 0 \leq i \leq n \).

**Proof.** We show that \((1) \implies (2) \implies (3) \implies (1) \). The first two of these steps are simple: Assume that \( L \) is regular. Every regular language over a single letter alphabet can be expressed as a finite union of arithmetic progressions (cf., e.g., Chrobak [13] [14]). As \( L \) is infinite, it must contain an infinite arithmetic progression. But if \( L \) contains an infinite arithmetic progression \( a^{bi+c} \), then the third condition is satisfied by definition.

The step from \((3) \) to \((1) \) is more involved. Before we prove this, note that there are unary languages (which are not DTMFA\(^{\text{DTMFA}}\)-languages), for which condition \((3) \) does not imply the existence of an infinite arithmetic progression, see Example \( \text{[E]} \) below.

Assume that \( L \subseteq \{a\}^* \) is infinite, \( L \in \mathcal{L}(\text{DTMFA}^{\text{DTMFA}}) \), and condition \((3) \) is met for some \( b \geq 1 \). By definition, there is an \( M \in \text{DTMFA}^{\text{DTMFA}} \) with \( \mathcal{L}(M) = L \). As \( M \) is deterministic, each of its states can have at most one outgoing transition; and as \( L \) is infinite, each state must have exactly one outgoing transition. Hence, like a DFA for a unary language (see e.g. the proof of Theorem \( \text{[T]} \) in particular Figure \( \text{[F]} \)), \( M \) consists of a chain and a cycle. Let \( m \) be the number of accepting states on the cycle, and let \( k \) be the number of memory locations that are accessed in the cycle (by recalling them, or by performing memory instructions).

Now consider an \( n > m(b+1)^k \) such that there exists some \( c_n \) with \( w_i := a^{bi+c_n} \in L \) for all \( 0 \leq i \leq n \), and reading \( x_0 = a^m \) takes \( M \) into the cycle (as condition \((3) \) holds for all \( n \), such a \( c_n \) exists for every \( n \) that is sufficiently large). Let \( q \) be the accepting state that is reached by \( w_0 \).

In the following, by an iteration of the cycle, we mean the situation that \( M \) is in state \( q \) and then consumes input symbols until it reaches \( q \) for the next time. The iteration of the cycle that starts after having fully consumed \( w_0 \) is called iteration 1. Now, for every \( j \geq 1 \), we define a function \( \tilde{v}_j : \{1, \ldots, k\} \to \mathbb{N} \) that describes the content of each memory after completing iteration \( j \).

In the remainder of the proof, we show that there is a constant upper bound for the values \( \tilde{v}_j(x), 1 \leq x \leq k, j \geq 1 \). Note that if the length of the content of each memory is bounded, then \( M \) can be rewritten into an equivalent DFA that simulates all memories in its states. Hence, \( L \) must be a regular language, which shows that condition \((3) \) implies condition \((1) \).

As \( M \) has to accept all words \( w_i \) with \( 0 \leq i \leq n \), and as each iteration of the cycle can accept only \( m \) words, we know that \( M \) has to perform at least \( I := \frac{n}{m} > (b+1)^k \) iterations of the cycle.
in order to accept \( w_n \). During these iterations, \( M \) cannot consume more than \( a^k \) between each pair of accepting states—otherwise, \( M \) would skip at least one of the \( w_i \) (as \( M \) is deterministic, the run for \( w_n \) must be an extension of each run for a \( w_i \) with \( i < n \)). In particular, this means that each memory that is recalled during these iterations cannot contain more than \( a^k \); thus, there are only \( b+1 \) possible contents for each memory. Furthermore, as \( M \) is deterministic, we know that each memory that is not recalled during these iterations will not be recalled during any later iterations of the cycle, which means that it can be removed from the cycle (and, as the chain is of finite length, it can also be removed from the chain). Hence, without loss of generality, we can assume that \( \vec{v}_j(x) \leq b \), \( 1 \leq x \leq k' \), \( 1 \leq j \leq I \), where the cycle contains exactly the memories \( 1, \ldots, k \).

As \( I > (b+1)^k \) and as there are only \( (b+1)^k \) possible choices of \( \vec{v}_j \), there exist \( j, j' \) with \( 0 \leq j' < j' \leq I \) and \( \vec{v}_j = \vec{v}_{j'} \). As \( M \) is deterministic, this allows us to conclude \( \vec{v}_{j+t} = \vec{v}_{j'+t} \) for all \( t \geq 0 \). In other words, the sequence of transitions from iteration \( j \) to iteration \( j' \) will be repeated forever, using exactly the same memory contents, which means that \( \vec{v}_i(x) \leq b \) for all \( l \geq 0 \) and all \( 1 \leq x \leq k \). As explained above, this concludes the proof.

Just like the Jumping Lemma, Lemma\(^2\) can be used to prove DTMFA\(^{\text{reg}}\)-inexpressibility for unary languages. See the following examples.

**Example 6.** We define a \( L_{\text{ex}} \subset \{a\}^* \) together with its complement \( \overline{L}_{\text{ex}} \) in the following way: First, add \( a \) to \( L_{\text{ex}} \), then add the two words \( a^2 \) and \( a^3 \) to \( \overline{L}_{\text{ex}} \), and the three words \( a^4 \) to \( a^6 \) to \( L_{\text{ex}} \), and so on. In other words, in each step \( i \), we add the next \( i \) words of \( \{a\}^* \) to one of the languages; namely \( L_{\text{ex}} \) if \( i \) is odd, and \( \overline{L}_{\text{ex}} \) if \( i \) is even. Then \( L_{\text{ex}} \) satisfies condition \( 3 \) of Lemma\(^3\), but it does not contain any infinite arithmetic progression. Hence, \( L_{\text{ex}} \notin \mathcal{L}(\text{DTMFA}^{\text{reg}}) \); and \( \overline{L}_{\text{ex}} \notin \mathcal{L}(\text{DTMFA}^{\text{reg}}) \) follows analogously.

**Example 7.** Let \( \alpha := (x: a^*)/(kx)^+ \) (this regex is also known as “Abigail’s expression” \( ^{[1]} \) in the PERL community). Then \( \mathcal{L}(\alpha) = \{a^m : m, n \geq 2 \} \). In other words, \( \alpha \) generates the language of all \( a^i \) such that \( i \) is a composite number (i.e., not a prime number). As \( \mathcal{L}(\alpha) \) is not regular and contains the arithmetic progression \( 2i + 4 \), Lemma\(^3\) yields \( \mathcal{L}(\alpha) \notin \mathcal{L}(\text{DTMFA}^{\text{reg}}) \).

The following result is a curious consequence of Lemma\(^2\).

**Proposition 5.** Over unary alphabets, we have

\[
\mathcal{L}(\text{DTMFA}^{\text{reg}}) \cap \mathcal{L}(\text{DTMFA}^{\text{acc}}) = \mathcal{L}(\text{REG}).
\]

**Proof.** We first observe that \( \mathcal{L}(\text{REG}) \subseteq \mathcal{L}(\text{DTMFA}^{\text{reg}}) \cap \mathcal{L}(\text{DTMFA}^{\text{acc}}) \) holds by definition. Next, we assume that there is a non-regular language \( L \in \mathcal{L}(\text{DTMFA}^{\text{reg}}) \cap \mathcal{L}(\text{DTMFA}^{\text{acc}}) \) over \( \{a\}^* \). In particular, this implies that both \( L \) and its complement \( \overline{L} := \{a\}^* \setminus L \) are infinite and, furthermore, by Theorem\(^1\), \( L \in \mathcal{L}(\text{DTMFA}^{\text{reg}}) \) implies \( \overline{L} \in \mathcal{L}(\text{DTMFA}^{\text{reg}}) \). Since \( \overline{L} \in \mathcal{L}(\text{DTMFA}^{\text{reg}}) \) is a non-regular DTMFA\(^{\text{reg}} \) language, Lemma\(^2\) allows us to conclude that for every \( m \geq 0 \), there exist an \( n \geq m \) and a \( p_n \geq n \) such that \( a^i \in \overline{L} \) for all \( p_n \leq i < p_n + n \). Hence, \( \overline{L} \) contains finite arithmetic progressions of unbounded length; and as \( \overline{L} \) is infinite, Lemma\(^2\) states that \( \overline{L} \) is regular, which is a contradiction.

## 4 Deterministic Regex

In order to define deterministic regex as an extension of deterministic regular expressions, we first extend the notion of a marked alphabet that is commonly used for the latter: For every alphabet \( A \), let \( A := \{a(a) \mid a \in A, n \geq 1 \} \). For every \( \alpha \in \text{RX} \), we define \( \hat{\alpha} \) as a regex that is obtained by taking \( \alpha \) over the regular expression over \( \Sigma \cup \Xi \cup \Gamma \) that generates the ref-language \( \mathcal{R}(\alpha) \), and marking each occurrence of \( \chi \in (\Sigma \cup \Xi \cup \Gamma) \) by a unique number (to make this well-defined, we assume that the markings start at \( 1 \) and are increased stepwise). For example, if \( \alpha := (y: (a \lor &x)^+ \cdot (e \lor b \cdot a) \cdot &cy, then \( \hat{\alpha} = [a(1) (a(2) \lor x(3))^* \cdot (e \lor b(4) \cdot a(5))]v(6) \cdot h(7). \) We also use these markings in the ref-words: For example, \( [a(1) a(2) b(3) a(4) b(5) v(6) h(7)] \in \mathcal{R}(\hat{\alpha}). \)

Before we explain this definition and use it to define deterministic regex, we first discuss the special case of deterministic regular expressions: A proper regular expression \( \alpha \) is not deterministic if there exist words \( u, v_1, v_2 \in \hat{\Sigma}, a \in \Sigma \) and positions \( i \neq j \) such that \( u \alpha_{i,j} v_1 \)
and \( u a(i) v_2 \) are elements of \( \mathcal{L}(\tilde{\alpha}) \) (see e.g. [9, 31]). Otherwise, it is a \textit{deterministic proper regular expression} (or, for short, just \textit{deterministic regular expression}).

The intuition behind this definition is based on the Glushkov construction for the conversion of regular expressions into finite automata, as a regular expression \( \alpha \) is deterministic if and only if its \textit{Glushkov automaton} \( \mathcal{M}(\alpha) \) is deterministic. Given a regular expression \( \alpha \), we define \( \mathcal{M}(\alpha) \) in the following way: First, we use the marked regular expression \( \tilde{\alpha} \) to construct its \textit{occurrence graph} \( G_{\tilde{\alpha}} \), a directed graph that has a source node \( \text{src} \), a sink node \( \text{snk} \), and one node for each \( a(i) \) in \( \tilde{\alpha} \). The edges are constructed in the following way: Each node \( a(i) \) has an incoming edge from \( \text{src} \) if \( a(i) \) can be the first letter of a word in \( \mathcal{L}(\tilde{\alpha}) \), and an outgoing edge to \( \text{snk} \) if it can be the last letter of such a word. Furthermore, for each factor \( a(i)b(j) \) that occurs in a word of \( \mathcal{L}(\tilde{\alpha}) \), there is an edge from \( a(i) \) to \( b(j) \). As a consequence, there is a one-to-one-correspondence between marked words in \( \mathcal{L}(\tilde{\alpha}) \) and paths from \( \text{src} \) to \( \text{snk} \) in \( G_{\tilde{\alpha}} \). To obtain \( \mathcal{M}(\alpha) \), we directly interpret \( G_{\tilde{\alpha}} \) as NFA over \( \Sigma \): The source \( \text{src} \) is the starting state, each node \( a(i) \) is a state \( q_i \), and an edge from \( a(i) \) to \( b(j) \) corresponds to a transition from \( q_i \) to \( q_j \) when reading \( b \). The sink \( \text{snk} \) does not become a state; instead, each node with an edge to \( \text{snk} \) is a final state (hence, \( \mathcal{M}(\alpha) \) contains the source state, and one state for every terminal in \( \alpha \)). This interpretation allows us to treat occurrence graphs as an alternative notation for a subclass of NFA (namely those where the starting state is not reachable from other states, and for each state \( q \), there is a characteristic terminal \( a_q \) such that all transitions to \( q \) read \( a_q \)). When doing so, we usually omit the occurrence markings on the nodes in graphical representations.

Intuitively, \( \mathcal{M}(\alpha) \) treats each terminal of \( \alpha \) as a state. Recall that \( \alpha \) is not deterministic if there exists words \( u a(i) v_1 \) and \( u a(j) v_2 \) in \( \mathcal{L}(\tilde{\alpha}) \) with \( i \neq j \). This corresponds to the situation where, after reading \( u \), \( \mathcal{M}(\alpha) \) has to decide between states \( a(i) \) and \( a(j) \) for the input letter \( a \).

**Example 8.** Let \( \alpha := (\varepsilon \lor (a \lor b)^+ a)_1 \). Then \( \tilde{\alpha} = (\varepsilon \lor ((a_1 \lor b_2)^+ a_3)) \), and the Glushkov automaton \( \mathcal{M}(\alpha) \) of \( \alpha \) is defined as follows:

![Diagram](image)

To the left, \( \mathcal{M}(\alpha) \) is represented as an occurrence graph, to the right in standard NFA notation. Then \( \mathcal{M}(\alpha) \) and \( \alpha \) are both not deterministic: For \( \mathcal{M}(\alpha) \), consider state 1; for \( \alpha \), consider \( u = a_1 \), \( v_1 = a_3 \), \( v_2 = \varepsilon \), and the words \( u a_1 v_1 \) and \( u a_3 v_2 \).

As shown in [9], \( \mathcal{L}(\text{DREQ}) \subset \mathcal{L}(\text{REG}) \) (also see [16, 39], or Lemma 5 below). Like for determinism of regular expressions, the key idea behind our definition of deterministic regex is that a matcher for the expression treats terminals (and variable references) as states. Then an expression is deterministic if the current symbol of the input word always uniquely determines the next state and all necessary variable actions. For regular expressions, non-determinism can only occur when the matcher has to decide between two occurrences of the same terminal symbol; but as regex also need to account for non-determinism that is caused by variable operations or references, their definition of non-determinism is more complicated.

**Definition 3.** An \( \alpha \in \text{RX} \) is not deterministic if there exist \( \rho_1, \rho_2 \in \mathcal{R}(\tilde{\alpha}) \) such that any of the following conditions is met for some \( r, s_1, s_2 \in (\Sigma \cup \Xi \cup \tilde{\Gamma})^* \) and \( \gamma_1, \gamma_2 \in \tilde{\Gamma}^* \):

1. \( \rho_1 = r \cdot \gamma_1 \cdot a(i) \cdot s_1 \) and \( \rho_2 = r \cdot \gamma_2 \cdot a(j) \cdot s_2 \) with \( a \in \Sigma \) and \( i \neq j \),
2. \( \rho_1 = r \cdot \gamma_1 \cdot x(i) \cdot s_1 \) and \( \rho_2 = r \cdot \gamma_2 \cdot x(j) \cdot s_2 \) with \( x \in \Xi \), \( x \in (\Sigma \cup \Xi) \) and \( i \neq j \),
3. \( \rho_1 = r \cdot \gamma_1 \cdot \chi(i) \cdot s_1 \) and \( \rho_2 = r \cdot \gamma_2 \cdot \chi(j) \cdot s_2 \) with \( \chi \in (\Sigma \cup \Xi) \) and \( \gamma_1 \neq \gamma_2 \),
4. \( \rho_1 = r \cdot \gamma_1 \) and \( \rho_2 = r \cdot \gamma_2 \) with \( \gamma_1 \neq \gamma_2 \).

Otherwise, \( \alpha \) is deterministic. We use \( \text{DRX} \) to denote the set of all deterministic regex, and define \( \text{DREG} := \text{DRX} \cap \text{REG} \) as the set of deterministic regular expressions (as discussed below, Condition 2 of Definition 2 covers the case of deterministic proper regular expression).

Most literature, like [9], defines the occurrence graph only implicitly by using sets first, last, and follow, which correspond to the edge from \( \text{src} \), the edges to \( \text{snk} \), or to the other edges of the graph, respectively. The explicit use of a graph is taken from the \( k \)-occurrence automata by Rex et al. [5]. We shall see that an advantage of graphs is that they can be easily extended by adding memory actions to the edges.
Example 9. We define \( \alpha_1 := ((x \cdot a) \lor a), \alpha_2 := (a \lor \& x), \alpha_3 := ((x \cdot \varepsilon) \lor \varepsilon) a \) and \( \alpha_4 := ((x \cdot \varepsilon) \lor \varepsilon) \). None of these regex are deterministic, as each \( \alpha_i \) meets the \( i \)-th condition of Definition 3. We discuss this for \( \alpha_1 \): Observe \( \alpha_1 = \left( [x(1) \cdot a(2)] x(3) \right) \lor a(4) \). Then choosing \( \mu_1 = [x(1) \cdot a(2)] x(3) \) and \( \rho_1 = a(4) \), with \( r = \varepsilon \), \( \gamma_1 = [x(1) \lor \varepsilon] \), \( s_1 = [x(3) \lor \varepsilon] \), and \( \gamma_2 = s_2 = \varepsilon \) shows the condition is met.

Let \( \beta_1 := ((x \cdot a) \lor b) ^* c \cdot \& x \), and \( \beta_2 := ((x \cdot \& y) \lor (y \cdot \& x \cdot a) ^+) \). Both regex are deterministic, with \( \mathcal{L}(\beta_1) := \{ w c w \mid w \in \{a, b\} ^* \} \) and \( \mathcal{L}(\beta_2) = \{ a ^n b \mid n \geq 0 \} \) (see Example 7).

Condition 1 of Definition 3 describes cases where non-determinism is caused by two occurrences of variable references and terminals, each label \( \nu \) the state \( M \alpha \) is deterministic if and only if \( G \) has \( n \) and each label \( \nu \) the state \( M \alpha \) is deterministic if and only if \( G \) has \( n \) and each variable reference or terminal. The labeled edges are of the form \( (\gamma \lor \epsilon, \nu) \) in analogy to the occurrence graph, each edge is labeled with a ref-word from \( \tilde{\Gamma} \). We use marked ref-words instead of unmarked ref-words to describe the memory actions (hence, there can be multiple edges from one node to another). In analogy to the occurrence graph, each memory occurrence graph can be directly interpreted as an \( \varepsilon \)-free TMFA.

Example 10. Let \( \Sigma = \{0, 1\} \) and \( \alpha := 1^+ (x \cdot 0^*) (1^+ \lor \& x)^+ 1^+ \). This regex was introduced by Fagin et al. [20], who call its language the “uniform-0-chunk language”. Obviously, \( \alpha \) is not deterministic (in fact, it satisfies conditions \( 1 \) and \( 2 \) of Definition 3). Nonetheless, it is possible to express \( \mathcal{L}(\alpha) \) with the deterministic regex \( 1 (1^+ \lor (0 (x \cdot 0^*) 1^+ (0 \cdot \& x \cdot 1^+)) ^+) \).

We now discuss the conversion from DRX to DTMFA[19], which generalizes the Glushkov construction of \( \mathcal{M}(\alpha) \) for regular expressions. The core idea is extending the occurrence graph to a memory occurrence graph \( G_\alpha \), which has two crucial differences: First, instead of only considering terminals, each terminal and each variable reference of a regex \( \alpha \) becomes a node. Second, each edge is labeled with a ref-word from \( \tilde{\Gamma} \) that describes the memory actions (hence, there can be multiple edges from one node to another). Analogous to the occurrence graph, each memory occurrence graph can be directly interpreted as an TMFA[19].

Theorem 4. Let \( \alpha \in \text{RX} \), and let \( n \) denote the number of occurrences of terminals and variable references in \( \alpha \). We can construct an \( n + 2 \) state TMFA[19] \( \mathcal{M}(\alpha) \) with \( \mathcal{L}(\mathcal{M}(\alpha)) = \mathcal{L}(\alpha) \) that is deterministic if and only if \( \alpha \) is deterministic. In time \( O(|\Sigma| |\alpha| n) \), the algorithm either 1. computes \( \mathcal{M}(\alpha) \) if \( \alpha \) is deterministic, or 2. detects that \( \alpha \) is not deterministic.

Proof. We construct \( \mathcal{M}(\alpha) \) by first constructing a graph \( G_\alpha \) from the marked regex \( \tilde{\alpha} \). As \( G_\alpha \) is a generalization of the occurrence graphs for proper regular expressions, we call this the memory occurrence graph. Analogously to proper regular expressions, this graph can be directly interpreted as an \( \mathcal{M}(\alpha) \in \text{TMFA}[19] \) that is deterministic if and only if \( \alpha \) is deterministic.

**Memory occurrence graph** \( G_\tilde{\alpha} \): Given a marked regex \( \tilde{\alpha} \), we define a memory occurrence graph \( G_\tilde{\alpha} := (V_\tilde{\alpha}, E_\tilde{\alpha}) \) with a source node src, a sink node snk, and one node for each marked variable reference or terminal. The labeled edges are of the form \( (u, \nu, v) \), where \( u, v \in V_\alpha \), and each label \( \nu \) is a marked ref-word \( \nu \in \tilde{\Gamma} \). We use marked ref-words instead of unmarked ref-words to fulfill the promise that \( \mathcal{M}(\alpha) \) is deterministic if and only if \( \alpha \) is deterministic. If \( \alpha \) has \( n \) occurrences of variable references and terminals, \( \mathcal{M}(\alpha) \) has \( n + 2 \) states: the initial state, the state [trap] for memory recall failures, and one state for each of the \( n \) occurrences in \( \alpha \).

If we only want to construct an algorithm that turns a deterministic regex into a DTMDFA[19] and rejects non-deterministic regexes, we can use unmarked edge labels instead (see the section at the end of this proof).

When interpreting \( G_\tilde{\alpha} \) as a TMFA \( \mathcal{M}(\alpha) \), we first remove the markings from the edge labels, and interpret these as memory actions of a TMFA, e.g., \([z] \) corresponds to opening the memory for \( x \). In order to simplify the construction, we take into account that different ref-words over \( \Gamma \) can have the same net effect on variables, and can be represented by the same single transition
in a TMFA. For example, \( [x[2]x[x_1] \quad \text{and} \quad ]_x[x] \quad \text{and} \quad [x \quad \text{all have the same effect as performing} \circ \quad \text{on the memory for} x \). Following this intuition, given a ref-word \( \nu \in \Gamma^* \), we define the net variable action of \( \nu \) as a function \( \text{net}_\nu : \Xi \to \{ \circ, c, r, \circ \} \), where for each \( x \in \Xi \), \( \text{net}_\nu(x) := \circ \) if no element of \( \Gamma_x := \{ [x_1] \} \) occurs in \( \nu \), and \( \text{net}_\nu(x) := \circ \) if the rightmost occurrence of an element of \( \Gamma_x \) is \( [x \). Furthermore, if the rightmost occurrence of an element of \( \Gamma_x \) is \( [x \), we define \( \text{net}_\nu(x) := r \) if \( x \) contains \( [x \), and \( \text{net}_\nu(x) := c \) otherwise. In the construction further down, we also consider concatenations of labels. We observe the following for all \( \nu, \nu_1, \nu_2 \in \Gamma^* \) and all \( x \in \Xi \): If \( \text{net}_\nu(x) = \circ \), then \( \text{net}_{\nu_1 \nu_2}(x) = \text{net}_{\nu_1}(x) \) and \( \text{net}_{\nu \nu_2} = \text{net}_{\nu_2}(x) \). If \( \text{net}_{\nu_1}(x) \in \{ \circ, r \} \), then \( \text{net}_{\nu_1 \nu_2}(x) = \text{net}_{\nu_2}(x) \).

We also use the following notion of minimal representations: For all \( \nu \in \Gamma^* \) and \( x \in \Xi \), we define \( \min_{\nu}(x) \in \Gamma^* \) by \( \min_{\nu}(x) := [x \) if \( \text{net}_\nu(x) = \circ \), \( \min_{\nu}(x) := [x \) if \( \text{net}_\nu(x) = c \), \( \min_{\nu}(x) := [x \) if \( \text{net}_\nu(x) = r \), and \( \min_{\nu}(x) := \circ \) if \( \text{net}_\nu(x) = \circ \). For any \( \nu \in \Gamma^* \), its minimal representation \( \min_{\nu}(x) \) is defined as any concatenation of all \( \min_{\nu}(x) \) for all \( x \in \Xi \) (as \( \text{net}_\nu \neq \circ \) holds only for finitely many \( x \in \Xi \), this is not problematic). In other words, for each \( \nu \in \Gamma^* \), \( \min_{\nu}(x) \) is one of the shortest words in \( \Gamma^* \) that satisfies \( \text{net}_{\min_{\nu}(x)} = \text{net}_{\nu} \). By unmark: \( (\Sigma \cup \Xi \cup \Gamma) \to (\Sigma \cup \Xi \cup \Gamma)^* \), we denote the morphism that removes the markings from marked letters.

By using net, we can directly interpret a memory occurrence graph \( G_\alpha \) as a TMFA \( M(\alpha) : = (Q, \Sigma, \delta, \var{src}, F) \), analogously to the occurrence graph for proper regular expressions. The components of \( M(\alpha) \) are obtained as follows: First, we rename the variables such that \( G_\alpha \) contains exactly the variables \( \{ 1, \ldots, k \} \) for some \( k \geq 0 \) (hence, for each \( 1 \leq i \leq k \), there is a variable \( x_i \in \var(\alpha) \) such that \( x_i \) is represented by \( i \)). We then define

\[
Q : = (V_0 \setminus \{ \text{snk} \}) \cup \{ \text{trap} \},
\]
\[
F : = \{ u \in Q \mid (u, \nu, \text{snk}) \in E_\alpha \text{ for some } \nu \}.
\]

In other words, all nodes except \( \text{snk} \) are states, and all nodes that have an edge to \( \text{snk} \) are final states (as in the occurrence graph). Following this intuition, each edge \( (u, \nu, \nu) \) with \( \nu \neq \text{snk} \) corresponds to a transition from state \( u \) to state \( v \), while performing the memory actions of \( \text{net}_\nu(x) \) on each \( x \in \var(\alpha) \). In order to allow recursive applications of the construction, each edge \( (u, \nu, \text{snk}) \) not only marks that \( u \) is an accepting state, but also that the memory actions of \( \nu \) need to be performed before accepting. Formally, we define \( \delta \) to include exactly the following transitions:

1. If \( (u, \nu, a_i(\alpha)) \in E_\alpha \) with \( a \in \Sigma \), then \((a_i(\alpha), s_1, \ldots, s_k) \in \delta(u, a) \).
2. If \( (u, \nu, x_i(\alpha)) \in E_\alpha \) with \( x \in \var(\alpha) \), then \((x_i(\alpha), s_1, \ldots, s_k) \in \delta(u, x) \),

where each \( 1 \leq i \leq k, s_i := \text{net}_{\text{unmark}(\nu)}(x_i) \) (unless the transition recalls memory \( i \); then we choose \( s_i := c \) as required by Definition 2). As we shall see, in order to satisfy the condition that \( M(\alpha) \) is deterministic only if \( \alpha \) is deterministic, we need to slightly adapt this definition.

Following this interpretation, we say that a memory occurrence graph \( G_\alpha \) is not deterministic if there exists a \( u \in V_0 \) such that any of the following conditions is met:

1. \( E_\alpha \) contains edges \((u, \nu_1, a_i(\alpha)) \) and \((u, \nu_2, a_j(\alpha)) \) with \( i \neq j \) and \( a \in \Sigma \).
2. \( E_\alpha \) contains edges \((u, \nu_1, x_i(\alpha)) \) and \((u, \nu_2, x_j(\alpha)) \) with \( i \neq j, x \in \Xi, \chi \in (\Xi \cup \Sigma) \),
3. \( E_\alpha \) contains edges \((u, \nu_1, \chi(\alpha)) \) and \((u, \nu_2, \chi(\alpha)) \) with \( \nu_1 \neq \nu_2 \) and \( \chi \in (\Xi \cup \Sigma) \),
4. \( E_\alpha \) contains edges \((u, \nu_1, \text{snk}) \) and \((u, \nu_2, \text{snk}) \) with \( \nu_1 \neq \nu_2 \).

Otherwise, we call \( G_\alpha \) deterministic. It is easily seen that if \( G_\alpha \) is deterministic, \( M(\alpha) \) is also deterministic. For the other direction, we need to account for two problems: First, it is possible that two labeled ref-words \( \nu_1 \) and \( \nu_2 \) of the same memory action \( \text{net}_{\text{unmark}(\nu_1)} = \text{net}_{\text{unmark}(\nu_2)} \), e.g., \( \nu_1 = [x_1]x_2 \) and \( \nu_2 = [x_3]x_4[x_5]x_6 \), which can occur in regex like \( \alpha_1 := (\circ v : \circ v) \circ (v : v : v) \circ (v : v : v) \). Second, as \( M(\alpha) \) has no \( \varepsilon \)-transitions, it does not model the difference between distinct edges to \( \text{snk} \), as they appear when converting regex like \( \alpha_2 := (\varepsilon v : \varepsilon v) \). As DTMFA cannot detect explicitly that the end of the input has been reached, they cannot simulate the memory actions of edges to \( \text{snk} \), which means that the construction ignores this.
In both cases, the accepted language is correct; but this has the side effect that the resulting $\mathcal{M}(\alpha)$ is deterministic, although $\alpha$ and $G_\tilde{\alpha}$ are not. Hence, to ensure that $\mathcal{M}(\alpha)$ is deterministic only if $\alpha$ is deterministic, we proceed as follows: If $G_\tilde{\alpha}$ contains any of these edges, we pick any transition $\delta(q, b) \ni (p, s_1, \ldots, s_k)$ with $b \in \Sigma \cup \{1, 2, \ldots, k\}$. We then add a new state $p_{\text{ndet}}$, a transition $\delta(q, b) \ni (p_{\text{ndet}}, s_1, \ldots, s_k)$, and $p_{\text{ndet}}$ has the same outgoing transitions as $p$. If we want to construct an algorithm that rejects non-deterministic regex, we can simply omit this technical crutch, and detect these cases in the construction of $G_\tilde{\alpha}$ as discussed below.

**Constructing $G_\tilde{\alpha}$:** We now define $G_\tilde{\alpha} = (V_\tilde{\alpha}, E_\tilde{\alpha})$ recursively.

1. **Empty word:** If $\tilde{\alpha} = \epsilon$, we define
   
   $$V_\tilde{\alpha} := \{\text{src, snk}\}, $$
   
   $$E_\tilde{\alpha} := \{\langle \text{src}, \epsilon, \text{snk} \rangle\}. $$

   This case is completely straightforward: An edge from src to snk is how occurrence graphs model $\epsilon$, and the marking $\epsilon$ means that this transition performs no memory actions.

2. **Terminals and variable references:** If $\tilde{\alpha} = \chi(i)$ with $\chi \in (\Sigma \cup \Xi)$, we define
   
   $$V_\tilde{\alpha} := \{\text{src, } \chi(i), \text{snk}\}, $$
   
   $$E_\tilde{\alpha} := \{\langle \text{src}, \epsilon, \chi(i) \rangle, \langle \chi(i), \epsilon, \text{snk} \rangle\}. $$

   Similar to the case for $\epsilon$, this models that the terminal is read, or that a variable reference is processed, by recalling the appropriate memory.

3. **Variable bindings:** If $\tilde{\alpha} = \langle [x:i], \beta [x:j] \rangle$ with $x \in \Xi$, we define $V_\tilde{\alpha} := V_\beta$ and
   
   $$E_\tilde{\alpha} := \{\langle \text{src}, [x:i], \nu_m, v \rangle \mid \langle \text{src}, \nu_m, v \rangle \in E_\beta, v \neq \text{snk} \}
   
   \cup \{\langle u, \nu, v \rangle \mid \langle u, \nu, v \rangle \in E_\beta, u \neq \text{src}, v \neq \text{snk} \}
   
   \cup \{\langle u, \nu_{\text{out}}, [x:j] \rangle, \nu \rangle \mid \langle u, \nu_{\text{out}}, \nu \rangle \in E_\beta, u \neq \text{src} \}
   
   \cup \{\langle \text{src}, [x:i], \nu_e \rangle, \nu \rangle \mid \langle \nu_e, \nu \rangle \in E_\beta \}, $$

   Less formally, we take the memory occurrence graph for $\beta$ and add opening (and closing) of $x$ to all edges from src (and to snk, respectively); while all other edges remain unchanged. Note that for edges from src to snk, we could also use $\nu_e \cdot [x]_{r} \nu$ or $[x]_{r} \nu_e$, as by Definition[1] $<x: \beta>$ is only a regex if $x \not\in \text{var}(\beta)$, which implies that no marked $[x]_{r}$ occurs in $\nu_e$.

4. **Disjunction:** If $\tilde{\alpha} = \langle \beta \lor \varphi \rangle$, we define $V_\tilde{\alpha} := V_\beta \cup V_\varphi$ and $E_\tilde{\alpha} := E_\beta \cup E_\varphi$.

   As the markings define a one to one correspondence between the nodes in $V_\tilde{\alpha}$ and the terminals and the variable references in $\alpha$, we know that $V_\tilde{\alpha} \cap V_\varphi = \{\text{src, snk}\}$. Therefore, the resulting memory occurrence graph $G_\tilde{\alpha}$ computes the union of $G_\beta$ and $G_\varphi$.

5. **Concatenation:** If $\tilde{\alpha} = \langle \beta \cdot \varphi \rangle$, we define
   
   $$V_\tilde{\alpha} := V_\beta \cup V_\varphi, $$
   
   $$E_\tilde{\alpha} := \{\langle u, \nu, v \rangle \mid \langle u, \nu, v \rangle \in E_\beta, v \neq \text{snk} \}
   
   \cup \{\langle u, \nu, v \rangle \mid \langle u, \nu, v \rangle \in E_\varphi, u \neq \text{src} \}
   
   \cup \{\langle u, \nu_1 \cdot \nu_2, v \rangle \mid \langle u, \nu_1, \nu_2 \rangle \in E_\beta, \langle \text{src, } \nu_1, \nu_2, v \rangle \in E_\varphi \}, $$

   Again, we use the fact that $V_\beta \cap V_\varphi = \{\text{src, snk}\}$. The memory occurrence graph $G_\tilde{\alpha}$ first simulates $G_\beta$, until the latter would accept by processing an edge $\langle u, \nu_1, \text{snk} \rangle \in E_\beta$. Instead of following this edge to snk, $G_\tilde{\alpha}$ then starts its simulation of $G_\varphi$, by picking any edge $\langle \text{src, } \nu_2, v \rangle \in E_\varphi$, which is merged with $\langle u, \nu_1, \text{snk} \rangle$ into a single edge from $u$ to $v$, and its label is $\langle \nu_1 \cdot \nu_2 \rangle$. Hence, it is easy to see that $G_\tilde{\alpha}$ computes the concatenation of $G_\beta$ and $G_\varphi$.
6. **Kleene plus:** Assume \( \bar{\alpha} = \bar{\beta}^+ \). This case requires some additional definitions. Let \( N_\nu \) denote the set of all \( \nu \) with \((\text{src}, \nu, \text{snk}) \in E_\beta^-\) and let \( N^{(\ast)} := \{ \min(\nu) \mid \nu \in N_\nu^+ \} \), where we assume that the elements of \( N^{(\ast)} \) have some arbitrary markings (as we shall see, this definition matters only for non-deterministic regrex, which means that we do not need markings to detect non-determinism).

Note that, as \( N_\nu \) is finite, there are only finitely many \( x \in \Sigma \) such that \( \text{net}_x(x) \neq \emptyset \) for a \( \nu \in N_\nu \), which implies that \( N^{(\ast)} \) is finite. In order to avoid hiding non-determinism in some very special cases, we assume that \( N^{(\ast)} \) always contains at least two elements (this is possible without loss of generality, as we can always add some \( \nu^2 \) for a \( \nu \in N^{(\ast)} \) without changing the behavior). We now define \( V_\alpha := V_\beta^- \), as well as

\[
E_\alpha := E_\beta^- \cup \{(\text{src}, \nu \cdot \nu, \nu, v) \mid (\text{src}, \nu, v) \in E_\beta^-\}, \nu \in N^{(\ast)}
\]

\[
\cup \{(u, \nu_{\text{out}} \cdot \nu, \text{snk}) \mid (u, \nu_{\text{out}}, \text{snk}) \in E_\beta^-\}, \nu \in N^{(\ast)}
\]

\[
\cup \{(u, \nu_{\text{out}} \cdot \nu, \nu, v) \mid (u, \nu_{\text{out}}, \text{snk}) \in E_\beta^-\}, (\text{src}, \nu, v) \in E_\beta^-\}, \nu \in N^{(\ast)}
\].

Similar to the construction for concatenation, the idea is that \( G_\alpha \) simulates \( G_\beta^-\); and whenever the latter could accept by taking an edge to \( \text{snk} \), the former can loop back to the beginning. The only difficult part is when \( G_\beta^-\) contains edges from \( \text{src} \) to \( \text{snk} \) with memory actions. As the Kleene plus allows us to use an arbitrary amount of these edges before taking an edge from \( \text{src} \) or to \( \text{snk} \), we need to include \( N_\nu^+ \) in the functions. This set is generally infinite; but it can be compacted to the finite set \( N^{(\ast)}\).

For deterministic regrex, this construction collapses to a far simpler case that does not use \( N^{(\ast)}\): First, note that if \( \alpha \) is deterministic and contains \( \beta^+ \), \((\text{src}, \nu, \text{snk}) \in E_\beta^-\) implies \( \nu = \varepsilon \), as otherwise \( \beta \) would satisfy condition 4 of Definition 3 and \( \alpha \) would satisfy condition 3 or 4. Hence, if \( \alpha \) is deterministic, we can assume that \( N_\nu = \{ \varepsilon \} \) or \( N_\nu = \emptyset \); both cases lead to \( N^{(\ast)} = \{ \varepsilon \} \). This allows us to use the following simplified definition:

\[
E_\alpha := E_\beta^- \cup \{(u, \nu_{\text{out}} \cdot \nu, \nu, v) \mid (u, \nu_{\text{out}}, \text{snk}) \in E_\beta^-\}, (\text{src}, \nu, v) \in E_\beta^-\}
\].

Hence, when constructing \( G_\alpha \) inductively, we first check if \( E_\beta^-\) contains an edge \((u, \nu, v)\) with \( \nu \neq \varepsilon \). If this is the case, we can reject \( \alpha \) as not deterministic. Otherwise, we use this simplified definition.

**Correctness and determinism:** The correctness of the construction is easily seen by a lengthy but straightforward induction, using the explanations provided with the definitions above. In particular, note that if \( \alpha \) does not contain a Kleene plus (or contains a Kleene plus and is deterministic), each path from \( \text{src} \) to \( \text{snk} \) through \( G_\alpha \) corresponds to a marked ref-word from \( R(\alpha) \), and vice versa. If \( \alpha \) is not deterministic and contains a Kleene plus, the correspondence is a little bit less strict, as ref-words \( \gamma \in \Gamma^+ \) are compressed to the equivalent \( \min(\gamma) \).

To see that \( M(\alpha) \) is deterministic if and only if \( \alpha \) is deterministic, recall that we established above that \( M(\alpha) \) is deterministic if and only if \( G_\alpha \) is deterministic. Hence, it suffices to show that determinism in \( G_\alpha \) is equivalent to determinism in \( \alpha \). But this follows immediately from our observation that there is a one-to-one correspondence between paths in \( G_\alpha \) and the marked ref-words in \( R(\alpha) \), and the fact that each node \( \chi(i) \in V_\alpha \setminus \{\text{src}, \text{snk}\} \) corresponds to the same \( \chi(i) \) in \( \bar{\alpha} \). Thus, if \( G_\alpha \) satisfies a condition \( i \) for non-determinism, \( \alpha \) satisfies the same condition \( i \) in Definition 3 and vice versa.

**Complexity:** Given a regrex \( \alpha \), let \( n \) denote the number of occurrences of terminals and variable references in \( \alpha \). We examine two steps of the computation: Computing \( G_\alpha \), and converting it to \( M(\alpha) \).

For the first step, observe that \( G_\alpha \) has \( n+2 \) nodes, and if \( \alpha \) is deterministic, each node has at most \( \min(n, |\Sigma|) \) outgoing edges, which means that we can bound this number with \( |\Sigma| \). Hence, if \( \alpha \) is deterministic, \( G_\alpha \) can be computed in time \( O(|\Sigma| \alpha(n)) \) by directly following the recursive definition of \( G_\alpha \): If \( \alpha \) is represented as a tree, it has at most \( |\alpha| \) nodes, which means that the recursive rules have to be applied \( O(|\alpha|) \) times. Each rule application requires the creation of at most \( O(|\Sigma|n) \) edges, each of which uses a concatenation.

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For the conversion, we need to process each edge \((u, \nu, v) \in E_{\tilde{G}}\), and compute its function \(\mathsf{net}_{\mathsf{unmark}(\nu)}\). From the recursive definition, we can immediately conclude that \(|\nu| \in O(|\alpha|)\) (as \(\alpha\) is deterministic, we do not even need to take into account that the definition for Kleene plus uses \(\mathsf{min}\)). Hence, each edge can be turned into a transition in time \(O(|\alpha|)\). As \(\alpha\) is deterministic, there are \(O(|\Sigma||\alpha|)\) edges, which gives us a total time of \(O(|\Sigma||\alpha|n)\) for this step.

As we have the same estimation for both steps, we conclude that the total running time is \(O(|\Sigma||\alpha|n)\).

If \(\alpha\) is not deterministic, this can be discovered during the construction, as soon as the recursive definition computes a non-deterministic memory occurrence graph \(G_{\tilde{G}}\) for a non-deterministic subexpression \(\beta\) of \(\alpha\), or if hidden non-determinism is detected.

**Unmarked edge labels:** As mentioned above, if the goal is not to construct an \(M(\alpha)\) that is deterministic if and only if \(\alpha\) is deterministic, but to turn every deterministic \(\alpha\) in a deterministic \(M(\alpha)\) and to reject non-deterministic \(\alpha\), we can construct \(G_{\tilde{G}}\) by using unmarked ref-words on the labels. The only cases where using unmarked ref-words can hide non-determinism (in the sense that \(G_{\tilde{G}}\) is deterministic, although \(\alpha\) is not) is in the rule for union. For example, consider \(\alpha := (x: \varepsilon) \lor (x: \varepsilon)\), which satisfies condition 4 of Definition 3 as \(L(\tilde{\alpha})\) contains \([x(1)]x(2)\) and \([x(3)]x(4)\), due to \(\tilde{\alpha} = ([x(1)]x(2)) \lor ([x(3)]x(4))\). If we use unmarked ref-words, \(G_{\tilde{G}}\) consists only of a single edge from src to snk with label \([x]_x\), which is clearly deterministic. Nonetheless, we can detect this hidden non-determinism when recursively constructing \(G_{\tilde{G}}\), by checking whether there exist edges \((\text{src}, \nu_1, \text{snk}) \in E_{\tilde{G}}\) and \((\text{src}, \nu_2, \text{snk}) \in E_{\tilde{G}}\) with \(\nu_1 \neq \varepsilon\) or \(\nu_2 \neq \varepsilon\). Hence, if the conversion algorithm encounters this case, it can reject the regex as non-deterministic.

Note that concatenation cannot hide non-determinism: For \(u \in V_{\tilde{G}}\) and \(v \in V_{\tilde{G}}\), define \(N_u := \{\nu \mid (u, \nu, \text{snk}) \in E_{\tilde{G}}\} \) and \(N_v := \{\nu \mid (\text{src}, \nu, v) \in E_{\tilde{G}}\}\). Assume that at least one of the two sets \(N_u\) and \(N_v\) contains more than one element. Then \(E_{\tilde{G}}\) contains at least two edges from \(u\) to \(v\), which means that \(G_{\tilde{G}}\) is not deterministic. Finally, Kleene star is also unaffected by this change, as the presence of any edge \((\text{src}, \nu, \text{snk})\) with \(\nu \neq \varepsilon\) causes non-determinism regardless of whether \(\nu\) is marked or not.

Furthermore, note that an implementation of this construction can also represent each label \(\nu\) in reduced form as \(\mathsf{net}_{\text{min}(\nu)}\), if it ensures that no hidden non-determinism is present.

Let us illustrate the construction of the proof of Theorem 3 by an example.

**Example 11.** Consider the deterministic regex \(\alpha := (x: (a \lor b)^+) \cdot d \cdot \varepsilon x\). Applying the markings yields \(\tilde{\alpha} := [x(1)(a(3) \lor b(3))^+] \cdot d(5) \cdot x(6)\), and \(M(\alpha)\) is the following automaton:

![Diagram](image)

To the left, \(M(\alpha)\) is represented as the memory occurrence graph \(G_{\tilde{G}}\), to the right as the DTMFA that can be directly derived from this graph (which uses memory 1 for \(x\)).

The construction from the proof of Theorem 3 behaves like the Glushkov construction for regular expressions, with one important difference: On regex that are not deterministic, its running time may be exponential in the number of variables; as there are non-deterministic regex where conversion into a TMFA without \(\varepsilon\)-transitions requires an exponential amount of transitions. For example, for \(k \geq 1\), let \(\alpha := a \cdot (\varepsilon \lor (x_1: \varepsilon)) \cdots (\varepsilon \lor (x_k: \varepsilon)) \cdot b\) and \(\beta := a(\bigvee_{1 \leq i \leq k} (x_i: \varepsilon))^\dagger b\). An automaton that is derived with a Glushkov style conversion then contains states \(q_1\) and \(q_2\) that correspond to the terminals; and between these two states, there must be \(2^k\) different transitions to account for all possible combinations of actions on the variables. This suggests that converting a regex into a TMFA without \(\varepsilon\)-edges is only efficient for deterministic regex; while in general, it is probably advisable to use a construction with \(\varepsilon\)-edges.

By combining Theorems 4 and 2 due to \(n \leq |\alpha|\), we immediately obtain the following:
Theorem 5. Given $\alpha \in \text{DRX}$ with $n$ occurrences of terminal symbols or variable references and $k$ variables, and $w \in \Sigma^*$, we can decide in time $O(|\sigma| n + |w|)$, whether $w \in \mathcal{L}(\alpha)$.

If we ensure that recalled variables never contain $\varepsilon$ (or that only a bounded number of variables references are possible in a row), we can even drop the factor $k$. For comparison, the membership problem for DREG can be decided in time $O(|\Sigma| |\sigma| + |w|)$ when using optimized versions of the Glushkov construction (see [8, 44]), and in $O(|\sigma| + |w| \cdot \log \log |\sigma|)$ with the algorithm by Groz and Maneth [31] that does not compute an automaton.

5 Expressive Power

Although Câmpeanu, Salomaa, Yu [10] as well as Carle and Narendran [11] state pumping lemmas for a class of regex, these do not apply to regex as defined in this paper (see Section 2.1.1). However, Lemmas 3 and 3 introduced in Section 3 shall be helpful for proving inexpressibility. A consequence of Lemma 3 is that there are infinite unary DTMFA\textsuperscript{\#}-languages that are not pumpable (in the sense that certain factors can be repeated arbitrarily often), as this would always lead to an arithmetic progression. It is also possible to demonstrate this phenomenon on larger alphabets, without relying on a trivial modification of the unary case.

The Fibonacci word $F_\omega$ is the infinite word that is the limit of the sequence of words $F_0 := b$, $F_1 := a$, and $F_{n+2} = F_n + F_{n+1}$ for all $n \geq 0$. The Fibonacci word has a number of curious properties. In particular, it is cube-free, which means that it does not contain cubes (i.e., factors $www$, with $w \neq \varepsilon$). This and various other properties are explained throughout Lothaire [35].

In the following, we demonstrate that a finite subset $L$ of $\{F_i \mid i \geq 0\}$ can be generated by a deterministic regex. Since the cube-freeness of $F_\omega$ particularly implies that all $F_i$, $i \geq 0$, are cube-free, this demonstrates that $L$ is a DRX-language that cannot be pumped by repeating factors of sufficiently large words arbitrarily often. This is a rather counter-intuitive phenomenon with respect to a class of formal languages that is characterised by a natural extension of classical finite automata.

Lemma 3. Let $L := \{F_{i+3} \mid i \geq 0\}$. Then $L = \mathcal{L}(\beta)$ holds for the deterministic regex

$$
\beta := a(x_0 : b)(x_1 : a) \cdot (\beta_{\text{shift}})^*,
$$

$$
\beta_{\text{shift}} := (x_2 : &x_3 &x_0)(x_3 : &x_3 &x_0 &x_1)(x_0 : &x_3 &x_2)(x_1 : &x_3 &x_2 &x_3).
$$

Proof. First, we observe that $\mathcal{R}(\beta) = \{r_i \mid i \geq 0\}$, where the ref-words $r_i$ are defined by $r_0 := a[x_0 b][x_1 a][x_1]$ and $r_{i+1} := r_i \cdot \hat{r}$ for all $i \geq 0$, where

$$
\hat{r} := [x_2 x_1 x_0][x_2][x_3 x_1 x_0 x_1][x_3][x_0 x_3 x_2][x_0][x_1 x_3 x_2 x_3][x_1].
$$

We now prove by induction that, for each $i \geq 0$, $\mathcal{D}(r_i) = F_{4i+3}$, and the rightmost values that are assigned to $x_0$ and $x_1$ are $F_{4i}$ and $F_{4i+1}$, respectively. For $i = 0$, this is obviously true: $\mathcal{D}(r_0) = aba$ and $x_0$ is assigned $b = F_3$, and $x_1$ is assigned $a = F_1$.

Now assume that the claim holds for some $i \geq 0$, and consider $r_i$. Then we can observe that $\mathcal{D}(r_{i+1}) = \mathcal{D}(r_i \cdot \hat{r}) = \mathcal{D}(r_i) \cdot \mathcal{D}(s)$, where the ref-word $s$ is obtained from $\hat{r}$ by replacing $x_0$ and $x_1$ with their respective values $F_{4i}$ and $F_{4i+1}$. Hence,

$$
s = [x_2 F_{4i+1} \cdot F_{4i}][x_2][x_3 F_{4i+1} \cdot F_{4i} \cdot F_{4i+1}][x_0 x_3 x_2][x_0][x_1 x_3 x_2 x_3][x_1].
$$

The second part of this equation uses that $F_{n+3} = F_{n+2} \cdot F_{n+1} = F_{n+1} \cdot F_n \cdot F_{n+1}$ holds for all $n \geq 0$. We now construct a ref-word $t$ by replacing the variables $x_2$ and $x_3$ in $s$ with their respective values. Hence,

$$
t = [x_2 F_{4i+2} \cdot F_{4i+2}][x_2][x_3 F_{4i+3}][x_0 F_{4i+3} \cdot F_{4i+2}][x_0][x_1 F_{4i+3} \cdot F_{4i+2} \cdot F_{4i+3}][x_1].
$$

Then $\mathcal{D}(r_{i+1}) = \mathcal{D}(r_i \cdot t)$ holds, and $r_{i+1}$ assigns $x_0$ and $x_1$ as $t$ does. Hence, $x_0$ is assigned $F_{4(i+1)}$, and $x_1$ is assigned $F_{4(i+1)+1}$, as required by the claim. To see that $\mathcal{D}(r_{i+1}) = F_{4(i+1)+3}$,
we observe that
\[
D(r_{i+1}) = D(r_i \cdot t) \\
= F_{4i+3} \cdot D([x_2 F_{4i+2} x_2 [x_3 F_{4i+3} x_3 [x_0 F_{4i+4} x_0 [x_1 F_{4i+5} x_1]]) \\
= F_{4i+3} \cdot F_{4i+2} \cdot F_{4i+3} \cdot F_{4i+4} \cdot F_{4i+5} \\
= F_{4i+5} \cdot F_{4i+4} \cdot F_{4i+5} \\
= F_{4i+7} = F_{4(i+1)+3}.
\]

This concludes the proof. \(\square\)

Also note that we can construct an \(M \in \text{DTMFA}^{\neg e}\) with \(\mathcal{L}(M) = L_F := \{F_n \mid n \geq 1\}\) from \(M(\beta)\). More specifically, this can be achieved by making the states that have an outgoing transition that opens a variable recall transition. In terms of regex, this corresponds to being able to accept before each variable binding (the correctness of this construction follows from the proof of Lemma \([3]\)). Note that the cycle in \(M\) has multiple accepting states. The authors conjecture that there is no \(\text{DTMFA}\) for \(L_F\) that has a cycle with exactly one accepting state, and that \(L_F\) is not a DRX-language.

For further separations, we use the following language:

**Example 12.** Let \(\alpha := a^2 \cdot (x : a^2) \cdot ((y : k \cdot x) \cdot (k \cdot x \cdot k \cdot y))^*\). Then \(\mathcal{L}(\alpha) = \{a^{4i} \mid i \geq 1\}\). This can be proven with a straightforward induction that is left to the reader.

From this, we define an \(L \in \mathcal{L}(\text{TMFA})\) with neither \(L \in \mathcal{L}(\text{DTMFA}^{\neg e})\), nor \(L \in \mathcal{L}(\text{DTMFA}^{\neg e})\):

**Lemma 4.** Let \(L := \{a^{4i+1} \mid i \geq 0\} \cup \{a^{4i} \mid i \geq 1\}\). Then \(L \in \mathcal{L}(\text{TMFA}) \setminus \mathcal{L}(\text{DTMFA})\).

**Proof.** To show that \(L \in \mathcal{L}(\text{TMFA})\), we construct a regex \(\alpha\) for \(L\), by \(\alpha := \alpha_1 \lor \alpha_2\), where \(\alpha_1 := a(a^4)^*\), and \(\alpha_2\) is the deterministic regex with \(\mathcal{L}(\alpha_2) = \{a^{4i} \mid i \geq 1\}\) from Example \([2]\).

Next, observe that \(L\) contains the arithmetic progression \(\{a^{4i+1} \mid i \geq 0\}\), and \(\overline{L} := \{a\}^* \setminus L\) contains the arithmetic progression \(\{a^{4i+2} \mid i \geq 0\}\). Assume that \(L\) is a \(\text{DTMFA}\)-language. Then there is an \(A \in \text{DTMFA}^{\neg e}\) that accepts \(L\) or \(\overline{L}\). Moreover, by Lemma \([2]\) \(L\) is regular (note that in case \(\mathcal{L}(\alpha) = \overline{L}\), we also use that the class of regular languages is closed under complementation). This leads to a contradiction, since, as we shall see next, \(L\) is not regular. To show that \(L\) is not regular, first assume the contrary. Then \(\mathcal{L}(\alpha_2) = L \cap \{a^{4i}\}^*\) would be regular, as the class of regular languages is closed under intersection. But \(\mathcal{L}(\alpha_2)\) is not regular, as for every pair \(i \neq j\), \(a^{2i}\) and \(a^{2j}\) are not Nerode-equivalent. \(\square\)

While ineffectiveness through \(\text{DTMFA}^{\neg e}\) provides us with a powerful sufficient criterion for DRX-ineffectiveness, it is not powerful enough to cover all cases of DRX-ineffectiveness. In particular, there are even regular languages that are no DRX-languages:

**Lemma 5.** Let \(L := \mathcal{L}((ab)^* (a \lor \epsilon)) = \{(ab)^{2i} \mid i \geq 0\}\). Then \(L \in \mathcal{L}(\text{REG}) \setminus \mathcal{L}(\text{DRX})\).

**Proof.** Before we assume the existence of an \(\alpha \in \text{DRX}\) with \(\mathcal{L}(\alpha) = L\) (and use this to obtain a contradiction), we first examine the structure of any \(M \in \text{DTMFA}^{\neg e}\) with \(\mathcal{L}(M) = L\). We observe that, with the exemption of states that are unreachable or cannot reach an accepting state, \(M\) must consist of a chain (which might be empty) that is followed by a cycle that contains at least one final state (like a DFA for a unary language, see the proof of Theorem \([6]\) in particular Figure \([2]\).

This is for the following reason: First, like for every \(\text{DTMFA}\), each state of \(M\) that has an outgoing memory recall transition cannot have any other outgoing transitions. The same holds for \(\varepsilon\)-transitions. Furthermore, due to the structure of \(L\), in \(M\) no state can have an outgoing transition that consumes \(a\) and an outgoing transition that consumes \(b\) at the same time. For \(\text{DTMFA}\), this is not problematic. In fact, as \(L\) is regular, we can interpret any DFA for \(L\) as a \(\text{DTMFA}\) for \(L\). For example, consider the following minimal incomplete DFA for \(L\), and its corresponding notation as an occurrence graph (without markings):
If we consider the occurrence graph notation, we see that this DFA cannot be obtained from a deterministic regular expression (at least not using the Glushkov construction, which — in the absence of variables — is identical to the construction from the proof of Theorem 1). Note that the states for a and b belong to the same strongly connected component. Hence, if there is an \( \alpha \in \text{DREG} \) such that this automaton is \( \mathcal{M}(\alpha) \), then \( \alpha \) must contain a subexpression \( \beta^+ \) with the occurrences \( a_{(j)} \) and \( b_{(j)} \) that correspond to these states. Then \( G_{\beta} \) must contain edges from \( a_{(j)} \) and \( b_{(j)} \) to \( \text{snk} \), and from \( \text{src} \) to \( a_{(j)} \). Using the rule for Kleene plus from the proof of Theorem 4, we see that \( G_{\alpha} \) must contain an edge from \( a_{(j)} \) to itself. This is a contradiction. Of course, this argument only shows that this DFA cannot be obtained from a deterministic regular expression; but it can be generalized to show that there is no \( \alpha \in \text{DREG} \) with \( L(\alpha) = L \) (see e. g. [9], and in particular [12], which explains how to apply the technique from [9] on this language).

We shall now use a similar line of reasoning to obtain a contradiction from the assumption that there is an \( \alpha \in \text{DRX} \) with \( L(\alpha) = L \). As explained above, the DTMFA \( \mathcal{M}(\alpha) \) must consist of a chain and a cycle (by definition, each state of \( \mathcal{M}(\alpha) \) is reachable; and from each state, we can reach an accepting state). This means that \( G_{\alpha} \) contains a cycle \( v_1, \ldots, v_n \) for some \( n \geq 1 \) and \( v_i \in (\Sigma \cup \mathbb{B}) \) such that there is an edge from \( v_i \) to \( v_{i+1} \) for \( 1 \leq i < n \) and from \( v_n \) to \( v_1 \). Hence, each \( v_i \) has at most two outgoing edges: One to the next node in the cycle, and (if it is an accepting state) one to the sink node \( \text{snk} \). Furthermore, exactly one \( v_i \) has an incoming edge from outside the cycle, let this be \( v_1 \).

From the construction of \( G_{\alpha} \), this cycle must have been generated from a Kleene plus in \( \alpha \). But this allows us to conclude that only exactly one \( v_i \) can have an edge to \( \text{snk} \); and furthermore, that this must be \( v_n \). This can be concluded from the following reasoning: If there existed nodes \( v_i, v_j \) with \( i \neq j \), and both have an edge to \( \text{snk} \), then the construction for Kleene plus would require edges from both \( v_i \) and \( v_j \) to \( v_1 \), which would break the cycle structure. Likewise, if \( i \neq n \), then there must be an edge from \( v_i \) to \( v_1 \), and from \( v_i \) to \( v_{i+1} \), which is a contradiction to our previous observations.

Hence, each iteration of the cycle must consume exactly one terminal letter (otherwise, we would skip over words of \( L \), alternating between a and b. Thus, \( v_i \in \Xi \) must hold for all \( 1 \leq i \leq n \) (as \( v_i = a_{(j)} \) with \( a_{(j)} \in \Sigma \) would consume \( a \) in every iteration, which would contradict the fact that the iterations alternate between consuming a and b).

Now assume that we enter an iteration that consumes a (the same reasoning shall hold for b). Then no variable that is recalled can contain b, and no variable can be bound to b, as otherwise, the iteration would consume more than a. But in the next iteration, the same variables are recalled, and as neither of them contains b, the iteration cannot consume b. Therefore, we arrive at a contradiction, and conclude that there is no \( \alpha \in \text{DRX} \) with \( L(\alpha) = L \). \( \square \)

The language \( L \) from Lemma 5 is also known to be a non-deterministic regular language (see e. g. [9]). Our proof can be seen as taking the idea behind the characterization of deterministic regular languages from [9], applying it to the specific language \( L \), and also taking variables into account. While this accomplishes the task of proving that deterministic regex share some of the limitations of deterministic regular expressions, the approach does not generalize (at least not in a straightforward manner). In particular, deterministic regex can express regular languages that are not deterministic regular, and are also quite similar to \( L \):

**Example 13.** Let \( L := \{(ab)^n | \ i \geq 0\} \). Then \( L \) is generated by the non-deterministic regular expression \((abab)^*\varepsilon \vee (aba))\), and one can show that \( L \) is not a deterministic regular language by using the BKW-algorithm [12] (also [16, 39]) on the minimal DFA \( M \) for \( L \). But for

\[ \alpha := a(y: b)\langle x: a\rangle \{(z: &y)(y: &x)\langle x: &z\rangle\}^* \],

we have \( \alpha \in \text{DRX} \) and \( L(\alpha) = L \) (to see this, note that in the iterations of the Kleene-star, the variable contents alternate between \( z = b \), \( y = a \), \( x = b \) and \( z = a \), \( y = b \), \( x = a \)).

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The “shifting gadget” that is used in Example [13] can be extended to show a far more general result for unary languages. Considering that \( \mathcal{L}(\text{DREG}) \subseteq \mathcal{L}(\text{REG}) \) holds even over unary alphabets (cf. Losemann et al. [37]), the following result might seem surprising:

**Theorem 6.** For every regular language \( L \) over a unary alphabet, \( L \in \mathcal{L}(\text{DRX}) \).

**Proof.** Assume that \( L \in \mathcal{L}(\text{REG}) \) with \( L \subseteq \{a\}^* \). Our goal is to construct an \( \alpha \in \text{DRX} \) with \( \mathcal{L}(\alpha) = L \). For technical reasons, we assume that \( \varepsilon \notin L \) (this is no problem, as for any \( \alpha \in \text{DRX} \) with \( \varepsilon \notin \mathcal{L}(\alpha) \), \( \alpha \cap \varepsilon \in \text{DRX} \)). If \( L \) is finite, \( L \in \mathcal{L}(\text{DREG}) \), and hence \( L \in \mathcal{L}(\text{DRX}) \). (As we shall see, our construction can also be used for finite languages, by replacing \( \alpha \) below with \( \varepsilon \). But to streamline the argument, we only consider infinite \( L \).)

Let \( M \) be a DFA with \( \mathcal{L}(M) = L \). Assume that all states of \( M \) are reachable, and that from each state, an accepting state can be reached. Then \( M \) has the form as shown in Figure 2. We refer to the states \( p_0 \) to \( p_m \) as the chain, and to the states \( q_1, \ldots, q_{n-1} \) and \( p_m \) as the cycle. Without loss of generality, we can assume that \( p_m \) is accepting (the cycle contains at least an accepting state; and as the automaton does not have to be minimal, we can extend the chain of \( p_i \) by unrolling the cycle until it starts with an accepting state).

Now there exists a number \( k \geq 1 \) and \( c_1, \ldots, c_k \geq 1 \) such that the words \( a^{c_1}, a^{c_1+c_2}, \ldots, a^{c_1+\cdots+c_k} \) are exactly the words that are accepted in the chain (recall that we assume that \( p_m \) is accepting, and that \( p_0 \) is not accepting, as \( \varepsilon \notin L \)). Furthermore, there exists an \( \ell \geq 1 \) and \( b_1, \ldots, b_\ell \geq 1 \) such that the words \( a^{b_1}, a^{b_1+b_2}, \ldots, a^{b_1+\cdots+b_\ell} \) are exactly the words that advance the cycle from \( p_m \) to each of the accepting states. These conditions also imply \( m = \sum_{i=1}^k c_i \) and \( n = \sum_{i=1}^\ell b_i \).

As an additional restriction, we assume that \( b_1 \geq 2 \). This is possible for the following reasons: If \( b_i = 1 \) for all \( i \), we can replace the cycle with the deterministic regular expression \( a^* \) and are done. Furthermore, if \( b_1 = 1 \), but there is a \( b_i \geq 2 \), we can unroll the cycle into the chain until \( b_1 \geq 2 \) (for the “new” \( b_1 \)).

We define \( \alpha := \alpha_1^{\text{chain}} \), where, for \( 1 \leq i < k \),

\[
\alpha_i^{\text{chain}} := a^{c_i} (\varepsilon \lor \alpha_{i+1}^{\text{chain}}),
\]

\[
\alpha_k^{\text{chain}} := \langle x\colon a \rangle a^{c_k-1} (\varepsilon \lor \alpha_{\text{cycle}}).
\]

Before we define \( \alpha_{\text{cycle}} \), note that if we disregard the words that can be generated by the subexpression \( \alpha_{\text{cycle}} \), \( \mathcal{L}(\alpha) \) contains exactly the words that are accepted by the chain. Furthermore, if we assume that \( \alpha_{\text{cycle}} \) is a deterministic regex and that its language does not contain \( \varepsilon \), we can conclude \( \alpha \in \text{DRX} \). Finally, note that if we first enter \( \alpha_{\text{cycle}} \), the variable \( x\ell \) contains \( a \).

The central part of the construction is defining \( \alpha_{\text{cycle}} \) in such a way that it is deterministic and it simulates the cycle of the DFA. We define

\[
\alpha_{\text{cycle}} := (\alpha_{\text{shift}} \cdot \alpha_{\text{cont}})^+,
\]

\[
\alpha_{\text{shift}} := \langle x_0 \cdot \& x_1 \rangle \langle x_\ell \cdot \& x_{\ell-1} \rangle \cdots \langle x_2 \cdot \& x_1 \rangle \langle x_1 \cdot \& x_0 \rangle,
\]

\[
\alpha_{\text{cont}} := \& x_1^{b_1-2} \cdot \& x_2^{b_2-1} \cdots \cdot \& x_\ell^{b_\ell-1}.
\]

The idea behind this definition is as follows. The expression \( \alpha_{\text{cycle}} \) should be able to generate the words that are generated in the cycle of the DFA, i.e., all the words \( a^i \), where \( i = r \sum_{j=1}^{\ell} b_j \).
Theorem 7.

\[ \mathcal{L}(TMFA) = \mathcal{L}(RX) \]

\[ \mathcal{L}(DTMFA) \]

\[ \mathcal{L}(DTMFA^{eq}) \]

\[ \mathcal{L}(DTMFA^{acc}) \]

\[ \mathcal{L}(DRX) \]

\[ \mathcal{L}(REG) \]

\[ \mathcal{L}(DREG) \]

Figure 3: The proper inclusions from Theorem 7. Arrows point from sub- to superset.

for some \( r \geq 1 \) and \( \ell' \) with \( 1 \leq \ell' \leq \ell \) (more precisely, \( r \) is the iteration of the cycle and in the current iteration, the word terminates in the \( (\ell')^{th} \) accepting state). This is done by producing, in every iteration of the expression \( (\alpha_{\text{shift}} \cdot \alpha_{\text{cont}})\), another factor \( a^b \) as follows: Before the first iteration, \( x_\ell \) contains \( a \), and all other variables default to \( \varepsilon \). Now, note that passing through \( \alpha_{\text{shift}} \), the \( a \) from \( x_\ell \) is shifted to \( x_1 \), or from \( x_i \) to \( x_{i+1} \) for \( 1 \leq i < \ell \). This means that in every application of \( \alpha_{\text{cont}} \), exactly one of the variables \( x_1, x_2, \ldots, x_\ell \) contains \( a \), which is responsible for producing the next \( a^b \) factor, while all the other variables are empty. However, this shifting mechanism needs an additional variable, namely \( x_0 \), which, in each iteration that charges \( x_\ell \) with \( a \), produces an additional occurrence of \( a \) (this explains the assumption \( b_1 \geq 2 \) above and that \( \& x_1 \) is repeated for only \( b_1 - 2 \) times in \( \alpha_{\text{cont}} \) instead of \( b_1 - 1 \), as the other variable references). In summary, in the \( i \)-th iteration of the plus, the variable \( x_j \) with \( j : = ((i - 1) \mod \ell) + 1 \) is set to \( a \), and if \( j = 1 \), then \( x_0 \) is also set to \( a \). All other variables are set to \( \varepsilon \). This means that \( \alpha_{\text{shift}} \) produces \( a^2 \) in the \( i \)-th iteration if \( (i - 1) \mod \ell = 1 \), and \( a \) in all other iterations. If \( j = 1 \), then the \( \& x_1^{b_1 - 2} \) in \( \alpha_{\text{cont}} \) produces \( a^{b_1 - 2} \), which means that in this iteration, \( \alpha_{\text{shift}} \cdot \alpha_{\text{cont}} \) produces \( a \cdot a^{b_1 - 2} = a^{b_1} \) (recall that all other variables are set to \( \varepsilon \)). If \( j > 1 \), then \( \alpha_{\text{cont}} \) used \( \& x_j^{b_j - 1} \) to produce \( a^{b_j - 1} \), which means that \( \alpha_{\text{shift}} \cdot \alpha_{\text{cont}} \) produces \( a \cdot a^{b_j - 1} = a^{b_j} \).

In conclusion, the \( i \)-th iteration of the Kleene plus in \( \alpha_{\text{cycle}} \) adds the word \( a^{b_j} \) for \( j : = ((i - 1) \mod \ell) + 1 \); which means that \( \alpha_{\text{cycle}} \) simulates the cycle. Hence, \( \mathcal{L}(\alpha) = L \). As \( \alpha_{\text{cycle}} \) contains no disjunctions or Kleene plus (except for the surrounding plus), it is deterministic. As remarked above, this allows us to conclude \( a \in DRX \). \( \square \)

As, in the proof of Theorem 6 a DFA with \( n \) states is converted into a deterministic regex of length \( O(n) \), this construction is even efficient. We summarize our observations (also see Figure 3):

Theorem 7.

\[ \mathcal{L}(DREG) \subset \mathcal{L}(DRX) \subset \mathcal{L}(DTMFA^{eq}) \subset \mathcal{L}(DTMFA) \subset \mathcal{L}(TMFA) = \mathcal{L}(RX) \]

The following pairs of classes are incomparable: \( \mathcal{L}(DRX) \) and \( \mathcal{L}(REG) \), \( \mathcal{L}(DRX) \) and \( \mathcal{L}(DTMFA^{acc}) \), as well as \( \mathcal{L}(DTMFA^{eq}) \) and \( \mathcal{L}(DTMFA^{acc}) \).

Proof. This follows from our previous observations as follows:

1. \( \mathcal{L}(DREG) \subset \mathcal{L}(DRX) \): The inclusion follows from the fact that our definition of determinism for regex is an extension of the notion of determinism for proper regular expressions. To see that the inclusion is proper, we recall any of the non-regular DRX-languages that we have seen, for example \( \{ w w w \mid w \in \{a, b\}^* \} \) and \( \{a^n \mid n \geq 0 \} \) from Example 9.

2. \( \mathcal{L}(DRX) \subset \mathcal{L}(DTMFA^{eq}) \): The inclusion follows from Theorem 4, it is proper due to the language \( \{ (ab)^n \mid i \geq 0 \} \), see Lemma 5.
3. $\mathcal{L}(\text{DTMFA}^{\text{reg}}) \subseteq \mathcal{L}(\text{DTMFA})$: The inclusion holds by definition. It is proper as the inclusion $\mathcal{L}(\text{DTMFA}^{\text{acc}}) \subseteq \mathcal{L}(\text{DTMFA})$ also holds by definition, and as $\mathcal{L}(\text{DTMFA}^{\text{reg}})$ and $\mathcal{L}(\text{DTMFA}^{\text{acc}})$ are incomparable (see below).

4. $\mathcal{L}(\text{DTMFA}) \subset \mathcal{L}(\text{TMFA})$: Again, the inclusion holds by definition. Languages that separate the two classes are for example the language of all $ww$ (where $w$ is from a non- unary alphabet, see Example[1], and the language $\{a^{4i+1} | i \geq 0\} \cup \{a^4 | i \geq 1\}$ from Lemma[4].

5. $\mathcal{L}(\text{TMFA}) = \mathcal{L}(\text{RX})$ is the statement of Theorem[1].

6. $\mathcal{L}(\text{DRX})$ and $\mathcal{L}(\text{REG})$ are incomparable: Again, we can use $\{(ab)^{2i} | i \geq 0\}$ from Lemma[5] and a non- regular DRX-language, like $\{wcw | w \in \{a, b\}^*\}$.

7. $\mathcal{L}(\text{DTMFA}^{\text{reg}})$ and $\mathcal{L}(\text{DTMFA}^{\text{acc}})$ are incomparable: Due to Proposition[5] over a unary alphabet, for every non-regular language $L \in \mathcal{L}(\text{DTMFA}^{\text{reg}})$, we have $L \subseteq \mathcal{L}(\text{DTMFA}^{\text{reg}}) \setminus \mathcal{L}(\text{DTMFA}^{\text{acc}})$, and $\overline{L} \subseteq \mathcal{L}(\text{DTMFA}^{\text{acc}}) \setminus \mathcal{L}(\text{DTMFA}^{\text{reg}})$). Hence, we can choose e. g. $L = \{a^{n^2} | n \geq 0\}$ (which, as shown in Example[9], is in $\mathcal{L}(\text{DRX}) \subset \mathcal{L}(\text{DTMFA}^{\text{reg}})$) and its complement to show the two classes to be incomparable.

8. $\mathcal{L}(\text{DRX})$ and $\mathcal{L}(\text{DTMFA}^{\text{acc}})$ are incomparable: Since $\mathcal{L}(\text{REG}) \subseteq \mathcal{L}(\text{DTMFA}^{\text{acc}})$, the language $\{(ab)^{2i} | i \geq 0\}$ is in the class $\mathcal{L}(\text{DTMFA}^{\text{acc}})$, but, due to Lemma[5], not in $\mathcal{L}(\text{DRX})$. Moreover, $L = \{a^{n^2} | n \geq 0\} \in \mathcal{L}(\text{DRX})$, see Example[9] but if $L \in \mathcal{L}(\text{DTMFA}^{\text{acc}})$, then, due to Theorem[4], also $L \in \mathcal{L}(\text{DTMFA}^{\text{reg}}) \cap \mathcal{L}(\text{DTMFA}^{\text{acc}})$, which, by Proposition[5], leads to the contradiction $L \in \mathcal{L}(\text{REG})$.

This concludes the proof. 

We can also use the examples from this section to show that the classes $\mathcal{L}(\text{DRX})$ and $\mathcal{L}(\text{DTMFA}^{\text{reg}})$ are not closed under most of the commonly studied operations on languages:

**Theorem 8.** $\mathcal{L}(\text{DRX})$ and $\mathcal{L}(\text{DTMFA}^{\text{reg}})$ are not closed under the following operations: union, concatenation, reversal, complement, homomorphism, and inverse homomorphism. $\mathcal{L}(\text{DRX})$ is also not closed under intersection, and intersection with DREG-languages.

**Proof.** The proofs for the operations that apply to both classes follow the same basic scheme: We start with one (or more) DRX-language(s), and show that applying the operation yields a language that is not a DTMFA-language:

**Union:** We use $L_1 := \{a^{4i+1} | i \geq 0\}$ and $L_2 := \{a^i | i \geq 1\}$, which are defined by the deterministic regex $\alpha_1 := a(\alpha_4)^{\ast}$ and $\alpha_2 := a^2 \cdot \langle x : a^2 \rangle \cdot \langle y : &x \cdot x \rangle \cdot \langle x : &y \cdot y \rangle$ (see Example[12]). Then $L_1 \cup L_2 \notin \mathcal{L}(\text{DTMFA}^{\text{reg}})$, as shown in Lemma[3].

**Concatenation:** Define the deterministic regexes $\alpha_3 := a^+ \cdot a^* \cdot \langle x : a^* \rangle \cdot b \cdot &x$. Then $\mathcal{L}(\alpha_4) = \{a^i b a^j | i \geq 0\}$, and $\mathcal{L}(\alpha_3) \cdot \mathcal{L}(\alpha_4) = \{a^i b a^j | i > j \geq 0\}$. As shown in Example[5], $\mathcal{L}(\alpha_3) \cdot \mathcal{L}(\alpha_4)$ is not a DTMFA-language.

**Reversal:** Let $\alpha_5 := \langle x : a^* \rangle \cdot b \cdot &x \cdot a^+$. Then $\alpha_5 \in \mathcal{L}(\text{DRX})$, and $\mathcal{L}(\alpha_5) = \{a^i b a^j | i \geq j \geq 0\}$. Reversing $\mathcal{L}(\alpha_5)$ again gives us the language from Example[5].

**Complement:** This follows directly from Proposition[5]. Consider e. g. $\{a^{n^2} | n \geq 0\}$.

**Homomorphism:** Let $\alpha_6 := ((\mathcal{C} \cdot \alpha_1) \cup (\mathcal{D} \cdot \alpha_2))$. Then $\alpha_6 \in \mathcal{L}(\text{DRX})$, but $h(\mathcal{L}(\alpha_6)) = L_1 \cup L_2$ for the morphism $h$ that is defined by $h(x) := x$ if $x \in \{a, b\}$ and $h(x) := \varepsilon$ if $x \in \{c, d\}$.

**Inverse homomorphism:** Define a morphism $g$ by $g(a) := g(b) := a$, and $g(c) := b$. Then let $L_7 := g^{-1}(\mathcal{L}(\alpha_4)) = \{u \cdot c \cdot v | u, v \in \{a, b\}^{\ast}, |u| = |v|\}$. We use Lemma[1] to show that $L_7 \notin \mathcal{L}(\text{DTMFA}^{\text{reg}})$. Assume to the contrary that it is, and choose $m \geq 1$. Then there exist $n \geq m$ and words $p_n, v_n$ that satisfy the conditions of Lemma[1]. We now distinguish the following cases: First, assume that $p_n$ does not contain the letter $c$. Then choose a $d \in \{a, b\}$ that is not the first letter of $v_n$, and define $u := d \cdot c \cdot a^{|p_n|+1}$. Then $p_n u \in L_7$;
but as } v_n \text{ is not a prefix of } u, \text{ this contradicts Lemma } 1. \text{ Now assume that } p_n \text{ contains } c. \text{ Then } p_n = w_1 c w_2 \text{ with } w_1, w_2 \in \{a, b\}^*, \text{ and } |w_1| \geq |w_2| + n \geq |w_2| + m. \text{ Again, choose } d \in \{a, b\} \text{ such that } d \text{ is not the first letter of } v_n, \text{ and define } u := d \cdot a^{[w_1]-|w_2|-1}. \text{ Then } p_n u = w_1 c w_3 \text{ for } w_3 = w_2 d \cdot a^{[w_1]-|w_2|-1}, \text{ and } |w_3| = |w_1|. \text{ Hence, } p_n u \in L_7, \text{ but as } v_n \text{ is not a prefix of } u, \text{ this contradicts Lemma } 1. \text{ } 7

**Intersection:** In order to show both claims on the intersection of } L(\text{DRX}), \text{ it suffices to show that we can obtain a language that is not a DRX-language by intersecting two deterministic regular languages. Accordingly, we define deterministic regular expressions } \beta_1 := (a(b \lor \epsilon))^* \text{ and } \beta_2 := \epsilon \lor (a(b(a \lor \epsilon))^*)^* \text{ (these expressions have been obtained by very minor modifications to the expressions that Caron, Han, Mignot } [12] \text{ use to show that } L(\text{DREG}) \text{ is not closed under intersection).}

Let } L_8 = \{(ab)^{i+1} \mid i \geq 0\}. \text{ To show that } L_8 = L(\beta_1) \cap L(\beta_2), \text{ we follow the approach from } [12], \text{ and first consider } M(\beta_1) \text{ and the corresponding minimal incomplete DFA:}

Likewise, we consider } M(\beta_2) \text{ and the corresponding minimal incomplete DFA (which merges the two states for a):}

Now it is easily seen that } L_8 = L(\beta_1) \cap L(\beta_2). \text{ From Lemma } 5 \text{ we know that } L_8 \notin L(\text{DRX}). \text{ Hence, the class of deterministic regex languages is not closed under intersection with deterministic regular languages, which also implies that it is not closed under intersection.} \]

We leave open whether } L(\text{DTMFA}^{[n]}) \text{ is closed under intersection (with itself or with } L(\text{DREG})), \text{ but we conjecture that this is not the case. In this regard, note that while } L(\text{TMFA}) \text{ is closed under intersection with } L(\text{REG}) \text{ (as shown in } [10]), \text{ it is open whether } L(\text{TMFA}) \text{ closed under intersection with itself.}

We also leave open whether } L(\text{DRX}) \text{ and } L(\text{DTMFA}^{[n]}) \text{ are closed under Kleene plus or star.

6 Static Analysis

In this section, we examine a restriction DRX and DTMFA, which we motivate with the following observation: As shown by Carle and Narendran [11], the intersection emptiness problem for regex is undecidable [4]. For DRX, that proof cannot be used, but the result still holds (and by Theorem 3 this extends to DTMFA):

**Theorem 9.** Given } \alpha, \beta \in \text{DRX}, \text{ it is undecidable whether } L(\alpha) \cap L(\beta) = \emptyset.

**Proof.** We show this with a reduction from Post’s Correspondence Problem (PCP, for short). Let } (u_1, v_1), \ldots, (u_k, v_k) \in \Sigma^* \times \Sigma^*, \text{ } k \geq 1, \text{ be a PCP instance. Our goal is to construct } \alpha, \beta \in \text{DRX} \text{ such that } L(\alpha) \cap L(\beta) \neq \emptyset \text{ if and only if there exists a sequence } i_1, \ldots, i_n, n \geq 1 \text{ and } 1 \leq i_j \leq k, \text{ such that } u_{i_1} \cdots u_{i_n} = v_{i_1} \cdots v_{i_n}. \text{ To do so, we first introduce an alphabet } A := \{a_1, \ldots, a_k\}

\[ Although that proof refers to a subclass of our definition of regex, see Section 2.1.1 it directly translates to RX. \]
such that \( A, \Sigma, \) and \( \{\#, \$, \&\} \) are pairwise disjoint (at the end of the proof, we discuss how this construction can be adapted to binary terminal alphabets). We then define

\[
\alpha := \left( \bigvee_{i=1}^{k} a_i \# u_i(x: \Sigma^*) \# v_i(y: \Sigma^*) \$ & x \# & y \$ \right)^*,
\]

\[
\beta := A \# \langle z: \Sigma^* \rangle \# & z \$ \langle x: \Sigma^* \rangle \# \langle y: \Sigma^* \rangle \$ A \# & x \# & y \$ \# \$.
\]

To see that \( \alpha \) is deterministic, note that the disjunction ranges over the letters from \( A \). For \( \beta \), observe that after each iteration of the starred subexpression, we read either a letter from \( \Sigma \), and start a new iteration, or \( \# \), which means that this was the last iteration.

We now claim that \( w \in L(\alpha) \cap L(\beta) \) if and only if there exist an \( n \geq 1 \) and \( i_1, \ldots, i_n \) with \( 1 \leq i_j \leq k \) such that \( u_{i_1} \cdots u_{i_n} = v_{i_1} \cdots v_{i_n} \) and \( w = w_1 \$ w_2 \$ \cdots w_n \$ \), where

\[
w_j = a_{i_j} \# u_{i_j} \cdots u_{i_n} \# v_{i_j} \cdots v_{i_n} \$ u_{i_{j+1}} \cdots u_{i_n} \# v_{i_{j+1}} \cdots v_{i_n}.
\]

Take note that \( w_n \) always ends on \$ \$. Informally explained, \( w \) encodes how a solution of the PCP instance is constructed, where the finished solution is in \( w_1 \), and the start of the construction is at \( w_n \). Starting at \( w_1 \), the sequence of \( w_j \) can be understood as splitting off pairs of prefixes \((u_j, v_j)\) from the solution, where each word \( w_j \) also encodes which tuple \((u_j, v_j)\) is processed (by using the preceding symbol \( a_j \) as a marker), and the words before and after the pair is split off (to the left and right of \$, respectively).

Here, \( \alpha \) ensures that in each \( w_j \), \( u_j \) and \( v_j \) are split off correctly, while \( \beta \) ensures that the “after” words of \( w_j \) are the “before” words of \( w_{j+1} \). Hence, such a \( w \) exists if and only if the instance of the PCP has a solution. As the existence of the latter is undecidable (see e.g. Hopcroft and Ullman [32]), deciding \( L(\alpha) \cap L(\beta) \neq \emptyset \) is also undecidable.

To adapt the construction to a binary alphabet (say, \( \{a, b\} \)), we use a morphism \( h: (A \cup \Sigma \cup \{\#, \$, \&\})^* \rightarrow \{a, b\}^* \) that is defined as follows (we assume an arbitrary ordering on \( \Sigma \) with \( \Sigma = \{b_1, \ldots, b_n\}\)):

- \( h(a_i) := ab^i a \) for all \( a_i \in A \),
- \( h(b_i) := ab^i a \),
- \( h(\#) := bab \), \( h(\$) := ba^2 b \), and \( h(\&) := ba^3 b \).

If we apply \( h \) to \( \alpha \) and \( \beta \) by applying \( h \) to each terminal, we obtain rexh(\( \alpha \)) and rexh(\( \beta \)) such that \( L(h(\alpha)) \cap L(h(\beta)) \neq \emptyset \) if and only if the instance of the PCP has a solution. The only problem is that these regex are not deterministic, as there are disjunctions that start with the same terminal letter. But each of these disjunctions can be rewritten into a deterministic disjunction by nesting the branches. For example, consider the disjunction \((a_1 \lor a_2 \lor a_3)\). Using \( h \), this becomes \((aba \lor ab^2 a \lor ab^3 a)\), which is not deterministic, but can be rewritten to the equivalent \((ab(a \lor ba) \lor ba a))\).

Now, note that if we apply this rewriting to the disjunctions to \( h(\alpha) \) and \( h(\beta) \) (including the disjunctions that are hidden in shorthand notations \( A \) and \( \Sigma \)), we obtain deterministic regex. In particular, note that Kleene plus and Kleene star are only used on elements of \( A \cup \Sigma \), and are always followed by either \# or \&. As the encodings of the former start with \( a \), while the encodings of the latter start with \( b \), rewriting the disjunctions is enough to ensure determinism.

As a consequence, DTMFA intersection emptiness problem is also undecidable. Theorem 9 applies even to very restricted DRX, as no variable binding contains a reference to another variable. \(|\var(\alpha)| = 2\), and \(|\var(\beta)| = 3\). Hence, bounding the number of variables does not make the problem decidable. Instead, the key part seems to be that the variables occur under Kleene stars, which means that they can be reassigned an unbounded amount of times. Following similar observations, Freydenberger and Holldack [25] introduced the following concept: A regex is variable-star-free (vstar-free) if each of its plussed sub-regexes contains neither variable references, nor variable bindings. Analogously, we call a TMFA memory-cycle-free if it contains no cycle with a memory transition (a transition in a TMFA that is a memory recall, or that contains memory actions other than \( \_ \)). Let RX vsf be the set of all vstar-free regex,
and \( \text{DRX}_{\text{mcf}} = \text{RX}_{\text{mcf}} \cap \text{DRX} \). Let \( \text{TMFA}_{\text{mcf}} \) be the set of all memory-cycle-free TMFA, and define \( \text{DTMFA}_{\text{mcf}} \) analogously. The proof of Theorem \( 4 \) allows us to conclude that \( M(\alpha) \in \text{DTMFA}_{\text{mcf}} \) holds for every \( \alpha \in \text{DRX}_{\text{mcf}} \). Likewise, we can use the proof of Theorem \( 4 \) to conclude \( L(\text{TMFA}_{\text{mcf}}) = L(\text{RX}_{\text{mcf}}) \). Note that for \( \varepsilon \)-free \( \text{DTMFA}_{\text{mcf}} \), the membership problem can be decided in time \( O(|Q| + |w|) \), as the preprocessing step of Theorem \( 2 \) is not necessary (as only a bounded number of variable references is possible in each run). Likewise, we can drop the factor \( k \) from Theorem \( 5 \) when restricted to \( \text{DRX}_{\text{mcf}} \).

As shown by Freydenberger \( [24] \), it is decidable in \( \text{PSPACE} \) whether \( \bigcap_{i=1}^{n} L(\alpha_i) = \emptyset \) for \( \alpha_1, \ldots, \alpha_n \in \text{RX}_{\text{mcf}} \). By combining the proof for this with some ideas from another construction from \( [24] \), we encode the intersection emptiness problem for \( \text{TMFA}_{\text{mcf}} \) in the existential theory of concatenation with regular constraints (a \( \text{PSPACE} \)-decidable, positive logic on words, see Diekert \( [17] \), Diekert, Jeż, Plandowski \( [19] \)). This yields the following:

**Theorem 10.** Given \( M_1, \ldots, M_n \in \text{TMFA}_{\text{mcf}} \), we can decide \( \bigcap_{i=1}^{n} L(M_i) = \emptyset \) in \( \text{PSPACE} \). The problem is \( \text{PSPACE} \)-hard, even if restricted to \( L(\alpha) \cap L(\beta) \), \( \alpha \in \text{DRX}_{\text{mcf}} \) and \( \beta \in \text{DREG} \) (if the size of \( \Sigma \) is not bounded), or to \( L(\alpha) \cap L(M) \), \( \alpha \in \text{DRX}_{\text{mcf}} \) and \( M \in \text{DFA} \).

**Proof.** We begin with the first lower bound: As shown by Martens, Neven, and Schwentick \( [10] \) (Theorem 3.10), the intersection emptiness problem for deterministic regular expressions is \( \text{PSPACE} \)-complete (if \( \Sigma \) is not bounded; the paper does not discuss the unbounded case, and the proof cannot be adapted directly). This problem is defined as follows: Given \( \beta_1, \ldots, \beta_n \in \text{DREG} \) for some \( n \geq 2 \), is \( \bigcap_{i=1}^{n} L(\beta_i) = \emptyset \)? We use a new terminal letter \( \# \not\in \Sigma \), and define

\[
\alpha := (x: \Sigma^*) \# (\& x \#)^{n-1} \\
\beta := \beta_1 \# \beta_2 \# \cdots \beta_n \#.
\]

First, observe that \( \alpha \) and \( \beta \) are deterministic, as \( \# \not\in \Sigma \). Now \( w \in L(\alpha) \) holds if and only if \( w = (\hat{w} \#)^n \) for some \( \hat{w} \in \Sigma^* \), and \( w \in L(\beta) \) if and only if there exist \( w_1, \ldots, w_n \in \Sigma^* \) with \( w_i \in L(\beta_i) \) and \( w = w_1 \# w_2 \# \cdots w_n \# \). Hence, \( (L(\alpha) \cap L(\beta)) \neq \emptyset \) if and only if \( \bigcap_{i=1}^{n} L(\beta_i) \neq \emptyset \). As this problem is \( \text{PSPACE} \)-complete, deciding \( (L(\alpha) \cap L(\beta)) \not\subseteq \emptyset \) is \( \text{PSPACE} \)-hard.

The second lower bound is a reduction from the intersection emptiness problem for DFA, which is defined as follows: Given \( M_1, \ldots, M_n \in \text{DFA} \) with \( n \geq 2 \), is there a \( w \in \Sigma^* \) with \( w \in L(M_i) \) for all \( 1 \leq i \leq n \)? This problem is \( \text{PSPACE} \)-complete (cf. Kozen \( [35] \)). We take a new terminal symbol \( \# \not\in \Sigma \), define \( \alpha \) as above, and choose \( M \) to be the DFA for the language \( L(M_1) \# L(M_2) \# \cdots \# L(M_n) \) (as \( \# \) does not occur in the languages of the DFA, this is trivially possible). The reasoning continues as above; but as the DFA can be defined on a binary alphabet, this proof does not require an unbounded alphabet.

The upper bound takes more work, including further definitions. Our goal is to encode the intersection emptiness problem for \( \text{TMFA}_{\text{mcf}} \) in \( \text{EC}\text{reg} \), the existential theory of concatenation with regular constraints, which we now introduce (for a more detailed definition and examples on \( \text{EC}\text{reg} \), see for example Freydenberger \( [24] \)).

One of the basic elements of \( \text{EC}\text{reg} \)-formulas are word equations: A pattern is a word \( \alpha \in (\Sigma \cup \Xi)^* \), and a word equation is a pair of patterns \( (\eta_L, \eta_R) \), which can also be written as \( \eta_L = \eta_R \) (hence the name equation). A pattern substitution is a homomorphism \( \sigma: (\Sigma \cup \Xi)^* \to \Sigma^* \) with \( \sigma(a) = a \) for all \( a \in \Sigma \). It is a solution of a word equation \( (\eta_L, \eta_R) \) if \( \sigma(\eta_L) = \sigma(\eta_R) \), and we write this as \( \sigma \models (\eta_L, \eta_R) \). Less formally, a pattern substitution replaces all variables with terminal words (where multiple occurrences of the variable have to be substituted in the same way), and it is a solution of an equation if both sides have the same terminal word as a result.

The other basic building block are constraint symbols: For every \( \varepsilon \text{-NFA} \ A \) and every \( x \in \Xi \), we can use a constraint symbol \( C_A(x) \). A pattern substitution \( \sigma \) satisfies \( C_A(x) \) if \( \sigma(x) \in L(A) \). We write this as \( \sigma \models C_A(x) \).

The existential theory of concatenation with regular constraints \( \text{EC}\text{reg} \) is obtained by combining word equations and constraint symbols using \( \land \), \( \lor \) and existential quantification over variables. Semantics are defined canonically: We have \( \sigma \models (\varphi_1 \land \varphi_2) \) if \( \sigma \models \varphi_1 \) and \( \sigma \models \varphi_2 \); and \( \sigma \models (\varphi_1 \lor \varphi_2) \) if \( \sigma \models \varphi_1 \) or \( \sigma \models \varphi_2 \). Finally, \( \sigma \models (\exists x: \varphi) \) if there exists a \( w \in \Sigma^* \) such that \( \sigma[w \rightarrow y] \models \varphi \), where the pattern substitution \( \sigma[w \rightarrow y] \) is defined by \( \sigma[w \rightarrow y] = w \) and \( \sigma[w \rightarrow y] = \sigma(y) \) if \( y \neq x \). In slight abuse of notation, we also write \( w \models \varphi(x) \) if \( \sigma \models \varphi(x) \) holds for the pattern substitution \( \sigma(x) := w \).
For example, let \( \varphi(x) := \exists y: ((x = yb) \land C_A(y)) \), where \( A \) is an NFA with \( L(A) = \{a^*\} \). Then \( w \models \varphi(x) \) if and only if \( w = a^nba^n \) for some \( n \geq 0 \).

Given an \( EC^{\mathsf{reg}} \)-formula \( \varphi \), deciding the existence of a pattern substitution \( \sigma \) with \( \sigma \models \varphi \) is \( \text{PSPACE} \)-complete, cf. Diekert [17].

We first prove the claim only for automata with rejecting trap states (as we shall see further down, the case for accepting trap states requires only a small modification). Before we proceed to the main idea of the construction, we first take a closer look at the accepting runs of memory-cycle-free TMFA.

Let \( M \in \text{TMFA}_{\text{med}} \) with \( M = (Q, \Sigma, \delta, q_0, F) \) and memories \( \{1, \ldots, k\} \), and consider any accepting run of \( M \). As \( M \) is memory cycle free, whenever it takes a memory transition from a state \( p \) to a state \( q \), we know that \( p \) cannot occur anywhere else in the run. Otherwise, it would be possible to repeat the memory transition from \( p \) to \( q \) arbitrarily often, which would contradict the assumption that \( M \) is memory cycle free. Hence, we know that every accepting run of \( M \) can use at most \( |Q| - 1 \) memory transitions.

This allows us to condense any accepting run of \( M \) by considering only its memory transitions. Formally, for some \( 0 \leq \ell < |Q| \), we define a condensed run \( \kappa = (\vec{q}, \vec{p}, \vec{\tau}) \) of length \( \ell \) as follows:

1. \( \vec{q} = (q_0, \ldots, q_\ell) \) is a sequence of states, where \( q_0 \) is the starting state of \( M \),
2. \( \vec{p} = (p_0, \ldots, p_\ell) \) is a sequence of states, with \( p_\ell \in F \),
3. \( \vec{\tau} = (\tau_1, \ldots, \tau_\ell) \) is a sequence of memory transitions, where for each \( 1 \leq i \leq \ell \), either
   \( \tau_i = (p_i, x_i, q_{i+1}, s_{i,1}, \ldots, s_{i,k}) \) with \( x_i \in \{1, \ldots, k\} \), or
   \( \tau_i = (p_i, b_i, q_{i+1}, s_{i,1}, \ldots, s_{i,k}) \) for some \( b_i \in \{\Sigma \cup \{\varepsilon\}\} \)
and \( s_{i,j} \in \{\sigma, c, r, o\} \), \( 1 \leq j \leq k \). In the second case, we also require that there is at least one \( j \) with \( s_{i,j} \neq \sigma \).
4. \( p_i \) is reachable from \( q_i \) without using memory transitions for all \( 0 \leq i \leq \ell \).

The intuition behind this construction is that we condense the run to a sequence of states \((q_0, p_0, q_1, p_1, \ldots, q_\ell, p_\ell)\) that only contains the starting state \( q_0 \), a final state \( p_\ell \), and the states before and after each memory transition (as \( M \) runs from \( q_i \) to \( p_i \) only without memory transitions, and each memory transition \( \tau_i \) takes the automaton from \( p_i \) to \( q_{i+1} \)).

As we shall see, each condensed run \( \kappa \) can be converted in polynomial time into an \( EC^{\mathsf{reg}} \) formula \( \varphi_\kappa(w) \) that defines exactly the language of all \( w \in L(M) \) for which there is an accepting run of \( M \) that can be condensed to \( \kappa \). In particular, the parts of the run between each pair of states \( q_i \) and \( p_i \) (which involve no memory transitions) shall be handled by appropriate regular constraints.

Now, given \( M_1, \ldots, M_n \in \text{TMFA}_{\text{med}} \), we proceed as follows to decide whether \( \bigcap_{i=1}^n L(M_i) = \emptyset \). First, we guess a condensed run \( \kappa_i \) for each \( M_i \) (as the length of each sequence is bounded by the number of states of \( M_i \) and as \( \text{PSPACE} = \text{NPSPACE} \), this is allowed). Next, we convert each \( \kappa_i \) into an \( EC^{\mathsf{reg}} \)-formula \( \varphi_i(w) \) (as we shall see, this is possible in polynomial time). Finally, we combine these into the formula \( \varphi(w) := \bigwedge_{i=1}^n \varphi_i(w) \), and decide whether \( \varphi \) is satisfiable (as mentioned above, this is possible in \( \text{PSPACE} \), see Diekert [17]). As \( \varphi \) is satisfiable if and only if there exists a \( w \in \bigcap_{i=1}^n L(M_i) \), this proves the claim for \( \text{TMFA}_{\text{med}} \) (as mentioned above, we shall discuss the case of accepting failure states at the end of the proof).

We now discuss how to construct \( \varphi_\kappa \) from \( \kappa \). Consider a word \( w \in L(M) \), and the condensed run \( \kappa \) of length \( \ell \) for any accepting run of \( M \) on \( w \). Then \( w \) can be decomposed into \( w = u_0u_1u_2 \cdots u_{\ell-1}u_\ell \) with \( u_i, v_i \in \Sigma^* \) such that \( u_i \) is the word that \( M \) consumes when processing from \( q_i \) to \( p_i \) (without using memory transitions), and \( v_i \) is the word that is consumed when processing from \( p_{i-1} \) to \( q_i \) (using the memory transition \( \tau_i \)). This is illustrated by the following picture:

\[
\begin{align*}
q_0 & \xrightarrow{u_0} p_0 \xrightarrow{v_1} q_1 & \xrightarrow{u_1} p_1 & \xrightarrow{v_2} q_2 & \cdots & \xrightarrow{u_{\ell-1}} p_{\ell-1} & \xrightarrow{v_{\ell}} q_\ell & \xrightarrow{u_\ell} p_\ell
\end{align*}
\]
Following this intuition, we define

\[ \varphi_\kappa(w) := \exists u_0, \ldots, u_\ell, v_1, \ldots, v_\ell : (w = u_0v_1u_1 \cdots v_\ell u_\ell) \land \bigwedge_{i=0}^{\ell} C_{M_{\eta, p_i}}(u_i) \land \bigwedge_{i \in T} (v_i = b_i) \land \bigwedge_{i \in R} (v_i = \eta_i), \]

where the following holds:

1. for \( p, q \in Q, M_{q, p} \) is the \( \varepsilon \)-NFA that is obtained from \( M \) by removing all memory transitions, using \( q \) as starting and \( p \) as only finite state,

2. \( T \subseteq \{1, \ldots, \ell\} \) is the set of all \( i \) such that \( \tau_i \) is not a memory recall (i.e., \( \tau_i \) consumes a terminal symbol \( b_i \)),

3. \( R \subseteq \{1, \ldots, \ell\} \) is the set of all \( i \) such that \( \tau_i \) is a memory recall (i.e., \( \tau_i \) recalls memory \( x_i \)),

4. for each \( i \in R \), we define \( \eta_i \) to describe the current content for \( x_i \) (as the memory actions are completely determined by the \( \tau_j \), this is directly possible by checking the \( \tau_j \) with \( j \leq i \)).

There are three possible cases:

(a) if \( x_i \) was never changed in a memory transition \( \tau_j \) with \( j < i \), then its value defaults to \( \varepsilon \), and we define \( \eta_i := \varepsilon \),

(b) if \( x_i \) was reset in a memory transition \( \tau_j \) with \( j < i \), and not changed between \( \tau_j \) and \( \tau_i \), we define \( \eta_i := \varepsilon \),

(c) otherwise, there exist well-defined \( 1 \leq j < j' \leq i \) such that \( x_i \) was opened in transition \( \tau_j \) and closed in \( \tau_{j'} \), and not changed between \( \tau_j \) and \( \tau_i \). Hence, we define \( \eta_i := v_j u_j \cdots u_{j'} \).

The constraints \( C_{M_{\eta, p_i}}(u_i) \) check that each \( u_i \) conforms to a sequence of transitions that takes \( M \) from \( q_i \) to \( p_i \), without using memory transitions. The conjunction over the \( i \in T \) ensures that the memory actions that are not memory recalls consume terminals correctly, and the conjunction over the \( i \in R \) ensures that each memory recall refers to the right part of the consumed input. Hence, for all \( w \in \Sigma^* \), \( w \models \varphi_\kappa \) if and only if \( w \in L(M) \), and there is an accepting run of \( M \) on \( w \) that can be condensed to \( \kappa \). It is easily seen that \( \varphi_\kappa \) can be constructed in polynomial time (and, hence, its size is polynomial in \(|Q|\)). We need to construct \( \ell + 1 \leq |Q| \) automata \( M_{q, p_i} \), each of which has at most \(|Q| \) states and at most \(|Q|^2 \) transitions. Determining each \( \eta_i \) is also possible in time \( O(|Q|) \), be checking the previous transitions \( \tau_j \).

This concludes the proof for the case of \( M_i \in \text{TMFA}_{\text{mcf}}^{|q|} \). For the case where the failure state is accepting, we need to add a small extension: Instead of only considering condensed runs for runs that reach a final state, we also need to consider the runs that end in a memory recall that fails. Hence, we consider a condensed run \( \kappa \) of length \( \ell < |Q| \), such that \( \tau_\ell \) is a memory recall transition for a variable \( x \in \{1, \ldots, k\} \). Then we construct \( \varphi_\kappa \) almost as explained above. The only difference is that we replace the equation \((v_\ell = \eta_\ell)\) in the conjunction over the elements of \( R \) with the following formula:

\[ \left( \exists z : (\eta_\ell = v_\ell z) \land C_A(v_\ell) \land C_A(z) \right) \lor \left( \bigvee_{a \in \Sigma} \bigvee_{b \in \Sigma \setminus \{a\}} \exists y, z_1, z_2 : (v_\ell = yaz_1) \land (v_\ell = ybz_2) \right), \]

where \( A \) is the minimal DFA for \( L(A) = \Sigma^+ \). The left part of this disjunctions describes all cases where \( v_\ell \) is non-empty and a proper prefix of the content of \( x \); the right part describes all cases where \( v_\ell \) and the content of \( x \) differ at at least one position (recall that the content of \( x \) at this point of the run is represented by \( \eta_\ell \)). Hence, this formula describes all cases where this condensed run ends in a memory recall failure.

This shows that the approach also works for \( M \in \text{TMFA}_{\text{mcf}}^{\varphi_\kappa} \). Hence, given \( M_1, \ldots, M_k \in \text{TMFA}_{\text{mcf}} \), we can decide whether the intersection of all \( L(M_i) \) is empty by guessing a condensed
run $\kappa_i$ for each $M_i$. If $M_i \in \text{TMFA}^{reg}_{\text{mct}}$, we only need to consider runs that end in final states; if $M_i \in \text{TMFA}^{conc}_{\text{mct}}$, we also need to consider runs that end in memory recall failures. Either way, the length of $\kappa_i$ is bounded by the number of states in $M_i$. We then transform in polynomial time each $\kappa_i$ into an $E\Sigma$-formula $\varphi_i$, and combine these into $\varphi := \bigwedge_{i=1}^n \varphi_i$. Then $w \models \varphi$ if and only if $w \in \mathcal{L}(M_i)$ for all $i$, and as satisfiability of $E\Sigma$-formulas can be decided in PSPACE, intersection emptiness is also decidable in PSPACE. As we already showed hardness at the very beginning of this proof, we conclude that the problem is PSPACE-complete. \hfill $\square$

The unbounded size of $\Sigma$ comes from the PSPACE-hardness of the intersection emptiness problem for DRX by Martens et al. [40], which has the same requirement. Using the existential theory of concatenation for the upper bound might seem conceptually excessive, considering how complicated even the satisfiability problem for word equations is (see Diekert [17, 18] for a detailed and a recent survey). But even intersection emptiness for $\text{DRX}_{\text{cf}}$ is at least as hard as the satisfiability problem for word equations:

**Proposition 6.** Given a word equation $\eta$ over $\Sigma$, we can construct in linear time $\alpha_L, \alpha_R \in \text{DRX}_{\text{cf}}$ over $\Sigma \cup \{\#\}$ such that $\mathcal{L}(\alpha_L) \cap \mathcal{L}(\alpha_R) \neq \emptyset$ holds if and only if $\eta$ has a solution.

**Proof.** Let $\eta = (\eta_L, \eta_R)$, and assume that $x_1, \ldots, x_k$ are the variables that occur in $\eta$. Let $\#$ be a new terminal letter, $\# \notin \Sigma$, and define

$$\alpha_L := \langle x_1 : \Sigma^* \rangle \# \langle x_2 : \Sigma^* \rangle \# \cdots \langle x_k : \Sigma^* \rangle \# \beta_L,$$

$$\alpha_R := \langle x_1 : \Sigma^* \rangle \# \langle x_2 : \Sigma^* \rangle \# \cdots \langle x_k : \Sigma^* \rangle \# \beta_R,$$

where $\beta_L$ and $\beta_R$ are obtained from $\eta_L$ and $\eta_R$ (respectively) by replacing each occurrence of a variable $x_i$ with the reference $&x_i$. As $\# \notin \Sigma$, and as $\beta_L$ and $\beta_R$ consist only of a chain of terminals and variable references, $\alpha_L, \alpha_R \in \text{DRX}$. Furthermore, $w \in \mathcal{L}(\alpha_L) \cap \mathcal{L}(\alpha_R)$ holds if and only if there is a homomorphism $\sigma : (\Sigma \cup \Xi)^* \to \Sigma^*$ with $\sigma(a) = a$ for all $a \in \Sigma$ such that

$$w = \sigma(x_1) \# \sigma(x_2) \# \cdots \sigma(x_k) \# \sigma(\eta_L)$$

$$= \sigma(x_1) \# \sigma(x_2) \# \cdots \sigma(x_k) \# \sigma(\eta_R),$$

which holds if and only if there is a solution $\sigma$ of $\eta$. \hfill $\square$

We now combine the proofs of Theorems 8 and 10, and observe:

**Theorem 11.** Given $M_1, M_2 \in \text{DTMFA}_{\text{mct}}$, $\mathcal{L}(M_1) \subseteq \mathcal{L}(M_2)$ can be decided in PSPACE.

**Proof.** We use the simple fact that $L_1 \subseteq L_2$ holds if and only if $L_1 \cap (\Sigma^* \setminus L_2) = \emptyset$. Due to Proposition 8, we can assume that $M_2$ is complete (see Section 3.1).

While we could use Theorem 10 together with Theorem 8 to show that the inclusion problem is decidable, the proof of Theorem 8 uses a construction that can lead to an exponential blowup in the number of states. The reason for this is that even in a complete DTMFA, we cannot simply toggle the acceptance behaviour of states, as the automaton might continue its computation by recalling memories that contain $\varepsilon$.

But as we shall see, it is possible to adapt the proof of Theorem 11 to handle this as well. First, note that we do not need to consider how to handle memory recall failures, as this is already part of the proof (we can simply add or remove the modifications that we discussed for DTMFA^{conc}). The first modification is that the algorithm now guesses a condensed run $\kappa$ that ends in a state $p_\ell$ that is not accepting. But to ensure that we can treat this state as an accepting state, we need to ensure that no accepting state can be reached from it. Instead of putting this into the formula, we make an additional guess: For each variable $x \in \{1, \ldots, k\}$, we also guess a language $L_x$ such that $L_x = \{\varepsilon\}$ or $L_x = \Sigma^+$. Formally, in addition to $\kappa$ and $\ell$, the algorithm guesses a function $f : \{1, \ldots, k\} \to \{M_\varepsilon, M_{\Sigma^+}\}$, where $M_\varepsilon$ and $M_{\Sigma^+}$ are NFA with $\mathcal{L}(M_\varepsilon) = \{\varepsilon\}$ and $\mathcal{L}(M_{\Sigma^+}) = \Sigma^+$.

It then checks whether it is possible to reach an accepting state from $q$, using only $\varepsilon$-transitions and memory recalls for variables $x$ with $f(x) = M_\varepsilon$. If that is the case, the algorithm rejects
the guess. Otherwise, it constructs a formula \( \varphi_{\kappa, f} \), which is obtained from \( \varphi_\kappa \) by adding the following formula to the conjunction:

\[
\exists y_1, \ldots, y_k: \bigwedge_{x \in \{1, \ldots, k\}} \left( (y_k = \hat{\eta}_x) \land C_f(x)(y_i) \right),
\]

where each \( \hat{\eta}_x \) is chosen to represent the content of the variable \( x \) in \( p_k \), like the \( \eta_j \) in the proof of Theorem 10. Hence, this formula checks whether the contents of the variables when reaching the state \( p_k \) conform to the guessed function \( f \).

Hence, for every word that would be rejected by \( M_2 \), we can guess appropriate \( \ell, \kappa \), and \( f \), which allows us to decide the intersection emptiness of \( \mathcal{L}(M_1) \) and \( (\Sigma^* \setminus \mathcal{L}(M_2)) \) in \( \text{PSPACE} \) as in the proof of Theorem 10. Hence, inclusion is decidable in \( \text{PSPACE} \).

Obviously, this implies that equivalence for \( \text{DTMFA}_{\text{mcf}} \) is decidable in \( \text{PSPACE} \), and, furthermore, this also holds for \( \text{DRX}_{\text{mcf}} \), which is an interesting contrast to non-deterministic \( \text{RX}_{\text{mcf}} \). As shown by Freydenberger [23], equivalence (and, hence, inclusion and minimization) are undecidable for \( \text{RX}_{\text{mcf}} \) (while [23] does not explicitly mention the concept, the regex in that proof are vsf-free, as discussed in [23]). Hence, Theorem 11 also yields a minimization algorithm for \( \text{DRX}_{\text{mcf}} \) and \( \text{DTMFA}_{\text{mcf}} \) that works in \( \text{PSPACE} \) (enumerate all smaller candidates and check equivalence). We leave open whether this is optimal, but observe that even for \( \text{DREG} \), minimization is \( \text{NP} \)-complete, see Niewerth [13].

7 A Relaxation of Determinism

One could argue that Definition 3 is overly restrictive; e.g., consider \( \alpha := (x: a^+)(y: b^+)(c \& x \lor cy) \). Then \( \alpha \) is not deterministic; but as the contents of \( x \) and \( y \) always start with \( a \) or \( b \) (respectively), deterministic choices between \( \& x \) and \( cy \) are possible by looking at the current letter of the input word. Analogous observations can be made for \( \text{TMFA} \). These observations lead to the following definition of \( \ell \)-determinism.

Let \( \ell \geq 1 \) and let \( u, v \in \Sigma^* \). The words \( u \) and \( v \) are \( \ell \)-prefix equivalent, denoted by \( u \equiv_\ell v \), if \( u \) is a prefix of \( v \), \( v \) is a prefix of \( u \) or their longest common prefix has a size of at least \( \ell \). By \( u \not\equiv_\ell v \), we denote that \( u \) and \( v \) are not \( \ell \)-prefix equivalent. Note that in order to check for two words \( u \) and \( v \) whether or not \( u \equiv_\ell v \), it is sufficient to compare the first \( \min\{k, |u|, |v|\} \) symbols of \( u \) and \( v \).

Based on this, we define the notion of \( \ell \)-deterministic \( \text{TMFA} \) (for short: \( \ell \)-\( \text{DTMFA} \)) as a relaxation of the criteria of \( \text{DTMFA} \): In contrast to the latter, an \( \ell \)-deterministic \( M \in \text{TMFA}(k) \) can have states \( q \) with multiple outgoing memory recall-transitions, as long as

1. these recall distinct memories, and
2. for every reachable configuration \( (q, v, (u_1, r_1), \ldots, (u_k, r_k)) \) of \( M \), \( u_i \not\equiv_\ell u_j \) holds for all \( i \neq j \) that appear on the recall transitions of \( q \).

For technical reasons, we define 0-\( \text{DTMFA} \) to coincide with \( \text{DTMFA} \).

Next, we note that this relaxation from determinism to \( \ell \)-determinism does not increase the expressive power of \( \text{DTMFA} \).

**Proposition 7.** \( \text{DTMFA} = \bigcup_{\ell \geq 0} \ell \text{-DTMFA} \).

**Proof.** The inclusion \( \text{DTMFA} \subseteq \bigcup_{\ell \geq 0} \ell \text{-DTMFA} \) holds by definition. In order to show the converse inclusion, let \( M \in \ell \text{-DTMFA}(k) \), for some \( \ell \geq 1 \). We transform \( M \) into an equivalent \( \text{DTMFA} M' \) as follows. We implement \( k \) auxiliary memories (called state-memories in the following) in the finite state control, which can store words of length at most \( \ell \), i.e., we replace every state \( q \) by states \( [q, m_1, m_2, \ldots, m_k] \), where \( m_i \in \Sigma^* \), \( |m_i| \leq \ell \), \( 1 \leq i \leq k \). The general idea is that \( M' \) simulates \( M \) in such a way that whenever \( M \) reaches a configuration \( (q, v, (u_1, r_1), \ldots, (u_k, r_k)) \), then \( M' \) reaches the configuration with state \( [q, m_1, m_2, \ldots, m_k] \) and memory configurations \( (u'_i, r_i) \), \( 1 \leq i \leq k \), such that, for every \( i \), \( 1 \leq i \leq k \), \( u_i = m_i u'_i \) and if \( u'_i \not\in \varepsilon \), then \( |m_i| = k \). This can be achieved as follows.

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Initially, all memories and state-memories are empty and closed (to this end, the finite state control contains a flag for each state-memory, indicating whether or not it is open). If \( M \) recalls memory \( i \), then \( M' \) consumes the content of the state-memory \( i \) from the input, symbol by symbol, and then applies a memory recall instruction on memory \( i \) (note that memory \( i \) might be empty). If the consumption of the content of state-memory \( i \) fails, i.e., it is not a prefix of the remaining input, then we move to the state [trap].

Whenever \( M \) opens memory \( i \), \( M' \) empties the state-memory \( i \) and marks it as open, but does not yet open memory \( i \). The scanned input is now stored as follows. If a single symbol is read and the state-memory currently stores a word of length at most \( \ell - 1 \), then this symbol is appended to the state-memory (furthermore, if the new symbol exhausts the state-memory’s capacity, then memory \( i \) is opened), and if the state-memory already stores a word of length \( \ell \), then the symbol is automatically stored in the open memory \( i \).

On the other hand, if \( M \) consumes a prefix \( u \) of the input by a memory recall instruction for some memory \( j \), i.e., in \( M' \), the state-memory \( j \) stores some \( u' \) and memory \( j \) stores some \( u'' \) with \( u = u'u'' \), then this is simulated by \( M' \) as follows. We start consuming \( u' \) symbol by symbol and store every symbol in state-memory \( i \). If this is possible without exhausting the capacity of state-memory \( i \) (i.e., state-memory \( i \) now stores a word of length at most \( \ell - 1 \)), then \( |u'| < \ell \), which implies \( u'' = \varepsilon \) and we are done. On the other hand, if the consumption of \( u' \) exhausts the state-memories capacity, i.e., \( u' = v'v'' \), where \( v' \) is the largest prefix that fits in state-memory \( i \) (note that \( v' = u \) is possible), then we open memory \( i \) and fill it with \( v'u'' \) by first consuming \( v'' \) symbol by symbol and then consulting memory \( j \).

We implement the modifications from above in such a way that whenever in \( M \) there is a nondeterministic choice of the form that, for some state \( q \) and several \( i_1, i_2, \ldots, i_s \), \( 1 \leq i_j \leq k \), \( 1 \leq j \leq s \), each \( \delta(q, i_j), 1 \leq j \leq s \), is defined (note that, since \( M \) is \( \ell \)-deterministic, these are the only possible non-deterministic choices), then this is implemented in \( M' \) by \( s \) many \( \varepsilon \)-transitions from the states \([q, m_1, m_2, \ldots, m_k]\). Since the modifications from above do not require any nondeterminism, there is a one-to-one correspondence between the nondeterministic choices of \( M \) and \( M' \). We further note that, for every \( j, 1 \leq j \leq s \), the \( \varepsilon \)-transition for consulting memory \( i_j \) is followed by a path of states, in which the content of state-memory \( i_j \) is consumed symbol by symbol, followed by a recall of memory \( i_j \) (and, simultaneously, for every open memory \( i \), the state-memory is filled with the consumed symbols until it is full and then memory \( i \) is opened). The memory recall performed by this path of states either fails, which can happen in the phase where the content of the state-memory is matched with the input or in the actual recall of the memory, or it successfully simulates the memory recall. We shall now describe how the nondeterministic choices of \( M' \) can be removed.

Instead of nondeterministically choosing one of these paths, we carry them out in parallel as follows. We start consuming a prefix of the remaining input and compare it, symbol by symbol, with the contents of the state-memories \( i_j \), \( 1 \leq j \leq s \). Whenever the next input symbol does not match the next symbol of a state-memory \( i_j \), we mark this memory as inactive and ignore it from now on. If all memories are inactive, we change to state [trap] and if there is exactly one active memory \( i_j \) left, we conclude the consultation of this memory (i.e., we match the remaining part of the state-memory \( i_j \) with the input and then consult memory \( i_j \)). In particular, we note that if a state-memory has been completely and successfully matched with a prefix of the input and there is another memory still active, then the contents of these memories are \( \ell \)-prefix equivalent, which is a contradiction to the \( \ell \)-determinism of \( M \). Consequently, we encounter the situation that either all memories are inactive or that exactly one active one is left, before a state-memory is completely matched with a prefix of the input. Obviously, this procedure is completely deterministic and it results in an equivalent automaton.

Next, we consider the problem to decide whether a given TMFA is \( \ell \)-deterministic for some given \( \ell \geq 1 \), and we shall see that this is a hard problem (even for TMFA_mcf). Moreover, extending the notion of \( \ell \)-determinism to RX by defining \( \alpha \in \text{RX} \) to be \( \ell \)-deterministic if and only if \( M(\alpha) \) is, we shall see that deciding \( \ell \)-determinism is also hard for RX (and even RX_mcf).

**Proposition 8.** For every \( \ell \geq 1 \), deciding whether a TMFA is \( \ell \)-deterministic is PSPACE-complete. The problem is coNP-complete if the input is restricted to TMFA_mcf. These lower bounds hold even if we restrict the input to RX and RX_mcf, respectively.
Proof. Before we proceed to the actual proof, we briefly discuss why it is possible to treat non-deterministic regex as an input for the problem, considering that the number of transitions in $M(\alpha)$ can be exponential (in the number of variables of $\alpha$). While this is true in general, the non-deterministic regex that have these blowups are also not $\ell$-deterministic: As soon as an $M \in \text{TMFA}$ has more than one transition from one state to another, it is not $\ell$-deterministic. Hence, we can use an algorithm that decides $\ell$-determinism for TMFA to decide $\ell$-determinism forRX by converting every input $\alpha \in \text{RX}$ into $M(\alpha)$ according to the proof of Theorem 13, but aborting if $G_\alpha$ contains nodes $u$ and $v$ with at least two edges from $u$ and $v$ (if these occur, $\alpha$ can be rejected as not $\ell$-deterministic, regardless which $\ell$ was chosen).

**Upper bounds:** In order to prove the upper bounds, let $M \in \text{TMFA}$ and $\ell \geq 1$. Assume that $M$ is not deterministic, but only violates the criteria by having states with multiple outgoing memory recall transitions for different variables (if any other violation of the criteria occurs, $M$ cannot be $\ell$-deterministic). Now, $M$ is not $\ell$-deterministic if and only if there exists a state $q$ in $M$ that has outgoing memory recall transitions for two different variables $x$ and $y$, and there is a run of $M$ that reaches $q$ while $x$ and $y$ contain words $w_x$ and $w_y$ (respectively) such that $w_x \equiv_{\ell} w_y$. We show this property can be decided in PSPACE in general, and in NP if $M$ is memory-cycle-free. The claim of the Proposition follows then directly if $M$ is memory-cycle-free, and from the closure of PSPACE under complementation in the general case.

We first consider the general case: The PSPACE algorithm guesses a state $q$ that has two outgoing memory recall transitions for variables $x$ and $y$. It then guesses its way from $q_0$ through the automaton, while storing for each variable $z$ of $M$ (not just $x$ and $y$) the first $\ell$ letters of the stored word $w_z$ (in order to determine these for a memory $z$, it suffices to know all terminal edges that are traversed while $z$ is open, and at most $\ell$ letters of each memory $z$' that is referenced while $z$ is open). If the algorithm reaches $q$ while $w_x \equiv_{\ell} w_y$, the algorithm correctly identifies $M$ as not $\ell$-deterministic. Hence, this can be decided in PSPACE.

For the memory-cycle-free case, we combine this with the condensed runs with the proof of Theorem 10. The NP-algorithm first guesses a condensed run $\kappa$ of $M$ that ends at $q$ with outgoing memory recall transitions for $x$ and $y$. In order to determine the first $\ell$ letters of each variable, it then guesses a prefix $u_i$ of length at most $\ell$ for each transition from a $q_i$ to a $p_i$, and checks whether there is a word in $L(M_{q_i,p_i})$ that has $u_i$ as a prefix (where the $\varepsilon$-NFA $M_{\varepsilon,p_i}$ is obtained as in the proof of Theorem 10). Remove all memory transitions from $M$, and take $q_i$ as starting and $p_i$ as only accepting state). It then computes the first $\ell$ letters of $w_x$ and $w_y$, which suffice to determine whether $w_x \equiv_{\ell} w_y$. If $w_x \equiv_{\ell} w_y$, the algorithm correctly identifies $M$ as not $\ell$-deterministic. Hence, for memory-cycle-free TMFA, the absence of $\ell$-determinism can be decided in NP, which means that $\ell$-determinism can be decided in coNP.

**Lower bound for TMFA_{mcf} and RX_{raf}** We prove this claim with a reduction from the 3-satisfiability problem, which is well-known to be NP complete (cf. Garey and Johnson 28). Let $\varphi$ be a formula in 3-conjunctive normal form, with variables $V = \{v_1, \ldots, v_n\}$, $k \geq 1$, where $\varphi := \bigwedge_{i=1}^{n} \varphi_i$ with $\varphi_i = (\lambda_{i,1} \lor \lambda_{i,2} \lor \lambda_{i,3})$, and $\lambda_{i,j} \in \{v, \neg v \mid v \in V\}$ for all $1 \leq i \leq n$ and $1 \leq j \leq 3$.

Our goal is to construct a $\beta \in \text{RX}_{raf}$ that is not $\ell$-deterministic if and only if there is an assignment to the variables in $V$ that satisfies $\varphi$. As the latter problem is NP-complete, deciding whether a vsf-regex is $\ell$-deterministic is coNP-hard. To this end, we first construct an $\alpha \in \text{DRX}_{raf}$ that has a variable $z$ that can only contain $\varepsilon$ if $\varphi$ has a satisfying assignment, and that otherwise contains a word from $\{a\}^+$. We then define $\beta := (z_1 \lor b)^{\varepsilon}(z_2 \lor \& z_1^\varepsilon).$ Note that $\beta$ is not $\ell$-deterministic if and only if $\varepsilon$ can be assigned to $z$; as otherwise, $z$ always contains some word from $a^+ b$, which means that $z_1$ and $z_2$ already differ on the first letter.

We implement this by modeling each variable $v_i \in V$ of $\varphi$ with two variables $x_i$ and $\bar{x}_i$ in $\alpha$, where an assignment of 1 to $v_i$ is modeled by setting $x_i$ to $\varepsilon$ and $\bar{x}_i$ to $a$, while assigning 0 is modeled by setting $x_i$ to $a$ and $\bar{x}_i$ to $\varepsilon$. Keeping this in mind, we define $\alpha := \alpha_{\text{init}} \cdot \alpha_{\text{sat}}$, where

\[
\alpha_{\text{init}} := \alpha_{\text{init}}^1 \cdots \alpha_{\text{init}}^k,
\]

\[
\alpha_{\text{sat}} := \left((A(x_i : \varepsilon)(\bar{x}_i : a)) \lor (B(x_i : a)(\bar{x}_i : \varepsilon))\right).
\]
for \(1 \leq i \leq k\), as well as

\[
\alpha_{\text{sat}} = \alpha^1_{\text{init}} \cdots \alpha^n_{\text{init}} \cdot (z \cdot \& y_1 \cdots \& y_n), \\
\alpha^t_{\text{sat}} = (a \cdot \alpha^t_{\text{init}}) \lor (b \cdot ((a \cdot \alpha^t_{\text{init}}) \lor (b \cdot \alpha^t_{\text{init}}))), \\
\alpha^i_{\text{init}} = \begin{cases} (y_i : \& x_i) & \text{if } \lambda_{i,j} = v_i, \\ (y_i : \& \tilde{x}_i) & \text{if } \lambda_{i,j} = \neg v_i \end{cases}
\]

for \(1 \leq i \leq n\) and \(1 \leq j \leq 3\).

Now, observe that \(\alpha\) is deterministic, as each part of a disjunction starts with a unique first letter (\(a\) or \(b\)); and \(\alpha\) is obviously \(v\)-star-free. To see that \(\alpha\) can assign \(\varepsilon\) to \(z\) if and only if \(\varphi\) has a satisfying assignment, we read \(\alpha\) from left to right: First, \(\alpha_{\text{init}}\) ensures that for each pair of variables \(x_i\) and \(\tilde{x}_i\), exactly one is bound to \(\varepsilon\), and the other to \(a\) (recall that setting \(x_i\) to \(\varepsilon\) corresponds to assigning 1 to \(v_i\)). Next, for each clause \(\varphi_i\), \(\alpha_{\text{sat}}\) stores the value of one of the literals \(\lambda_{i,j} \in \{v_i, \neg v_i\}\) under the chosen assignment \(y_i\), by recalling the appropriate \(x_i\) or \(\tilde{x}_i\). Thus, \(y_i\) can only contain \(\varepsilon\) if the assignment satisfies \(\varphi_i\). Finally, all \(y_i\) are concatenated, and the result is stored in \(z\). Hence, \(z\) can only contain \(\varepsilon\) if all clauses \(\varphi_i\) are satisfied, which means that \(\varphi\) is satisfied. Likewise, each satisfying assignment can be made to use the appropriate choices in the \(\alpha^t_{\text{init}}\) and \(\alpha^t_{\text{sat}}\) such that \(z\) contains \(\varepsilon\).

Hence, as explained above, \(\beta\) is \(\ell\)-deterministic if and only if \(\varphi\) has no satisfying assignment, which means that deciding whether a \(v\)-star-free regex is \(\ell\)-deterministic is \(\text{coNP}\)-hard. As we already showed the matching upper bound, the problem is \(\text{coNP}\)-complete.

**Lower bound for TMFA and RX:** We show this with a reduction from the intersection emptiness problem for DFA, which is defined as follows: Given \(M_1, \ldots, M_n \in \text{DFA}\) for some \(n \geq 2\), is there a \(w \in \Sigma^*\) with \(w \in L(M_i)\) for all \(1 \leq i \leq n\)? This problem is \(\text{PSPACE}\)-complete (cf. Kozen [23]).

As in the case for \(v\)-star-free regex, we first construct an \(\alpha \in \text{DRX}\) that has a variable \(z\) such that it is possible to assign \(\varepsilon\) to \(z\) if and only if the intersection of the \(L(M_i)\) is not empty (and which is set to a word from \(\{a\}^*\) otherwise), and then define

\[
\beta := \alpha \cdot (z_1 : b^t \langle z_2 : \& z \cdot b^t \rangle \langle z_1 \lor \& z_2 \rangle).
\]

Again, \(\beta\) is not \(\ell\)-deterministic if and only if \(\varepsilon\) can be assigned to \(z\); as otherwise, \(z\) always contains some word from \(a^* b\).

Consider \(M_1, \ldots, M_n \in \text{DFA}\) with \(M_i = (\Sigma, Q_i, q_{i,0}, \delta_i, F_i)\). In order to simplify the construction, we assume \(Q_i = \{q_{i,0}, \ldots, q_{i,m}\}\) for some \(m \geq 1\), and \(\Sigma \supseteq \{a, b, c_0, \ldots, c_{\max(m,n)}\}\). We shall discuss in the proof how the construction can be adapted to a binary alphabet, but using an unbounded alphabet is simpler.

The main idea of the construction is that each state \(q_{i,j}\) is represented by a variable \(x_{i,j}\), which can take either \(a\) or \(\varepsilon\) as values. The regex \(\alpha\) contains a subexpression \(\alpha_{\text{iter}}\) which uses a Kleene star to simulate all \(M_i\) in parallel on the same input. In particular, it ensures that \(x_{i,j}\) can be set to \(\varepsilon\) if and only if \(M_i\) can enter state \(q_{i,j}\) at the current point of the parallel simulation. Using this, we shall see that it is possible to set \(z\) to \(\varepsilon\) if and only if all \(M_i\) can reach an accepting state at the same time. We define

\[
\alpha := \alpha_{\text{init}} \cdot (a \cdot \alpha_{\text{iter}})^* \cdot b \cdot \alpha_{\text{acc}}
\]

Before we define the subexpressions of \(\alpha\), we observe that the use of the Kleene star does not affect determinism, as the terminals \(a\) and \(b\) signal whether there should be another iteration of the star or not (respectively). The subexpressions of \(\alpha\) are defined as follows:

\[
\alpha^1_{\text{init}} := \alpha^1_{\text{init}} \cdots \alpha^n_{\text{init}}, \\
\alpha^1_{\text{init}} := (x_{i,0} : \varepsilon)(x_{i,1} : a) \cdots (x_{i,m} : a),
\]

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for all $1 \leq i \leq n$. This represents that each automaton $M_i$ is in its starting state $q_{0,i}$. Furthermore, to simulate the behaviour of the automata $M_i$, we define

$$
\alpha_{\text{iter}} := \left( (a \cdot \alpha_{\text{step}}^a) \lor (b \cdot \alpha_{\text{step}}^b) \right) \cdot \alpha_{\text{switch}},
$$

$$
\alpha_{\text{step}}^d := \alpha_{\text{step}}^{d,1} \cdots \alpha_{\text{step}}^{d,n},
$$

$$
\alpha_{\text{step}}^{d,i} := \bigvee_{0 \leq j \leq m} \left( c_j \cdot \left( \bigvee_{0 \leq k \leq m, \delta_i(q_{0,i}, d) = q_{j,k}} \langle \hat{x}_{i,j} : \& x_{i,l} \rangle \right) \cdot \alpha_{\text{dump}}^{i,j} \right),
$$

$$
\alpha_{\text{dump}}^{i,j} := \langle \hat{x}_{i,0} : a \rangle \cdots \langle \hat{x}_{i,j-1} : a \rangle \langle \hat{x}_{i,j+1} : a \rangle \cdots \langle \hat{x}_{i,m} : a \rangle,
$$

$$
\alpha_{\text{switch}} := \alpha_{\text{switch}}^1 \cdots \alpha_{\text{switch}}^n,
$$

$$
\alpha_i^{\text{i,j}} := \langle x_{i,0} : \& \hat{x}_{i,0} \rangle \cdots \langle x_{i,m} : \& \hat{x}_{i,m} \rangle
$$

for $d \in \{a, b\}$, $1 \leq i \leq n$, and $0 \leq j \leq m$. Each $\alpha_{\text{step}}^{d,i}$ picks a pair of states $q_{i,j}$ and $q_{i,l}$ of $M_i$, such that $\delta(q_{i,l}, d) = q_{i,j}$. Less formally, $q_{i,j}$ is the successor state of $q_{i,l}$ on input $d$. The temporary variable $\hat{x}_{i,j}$ is then set to the content of $x_{i,l}$, while all other temporary variables $\hat{x}_{i,j'}$ with $j' \neq j$ are set to $a$, using $\alpha_{\text{dump}}^{i,j}$.

Hence, each iteration of $\alpha_{\text{step}}$ can set $\hat{x}_{i,j}$ to $\varepsilon$ if and only if $q_{i,j}$ is the successor state on input $d$ for a state $q_{i,l}$ such that $\hat{x}_{i,l}$ contains $\varepsilon$. In other words, each iteration of $\alpha_{\text{iter}}$ uses a subexpression $\alpha_{\text{switch}}^i$ to simulate all $M_i$ in parallel on the input letter $d \in \{a, b\}$, and $\alpha_{\text{switch}}$ sets each $\hat{x}_{i,j}$ to the same content as its corresponding temporary variable $\hat{x}_{i,j}$.

As an aside, note that it is possible to adapt the construction to a binary terminal alphabet. To do so, one replaces the disjunctions over the terminals $c_j$ and $c_l$ with nested disjunctions over $a$ and $b$, as in the expressions $\alpha_{\text{sat}}^i$ in the proof for the lower bound for RXcf above.

Regardless of the number of terminal letters, we define the remaining subexpressions as follows:

$$
\alpha_{\text{acc}} := \alpha_{\text{acc}}^1 \cdots \alpha_{\text{acc}}^n \cdot (z : \& y_1 \cdots \& y_n),
$$

$$
\alpha_{\text{acc}}^i := \bigvee_{0 \leq j \leq m, q_{i,j} \in P_i} \left( c_j \cdot (y_j : \& x_{i,j}) \right)
$$

for all $1 \leq i \leq n$. Again, this disjunction can be adapted to a binary terminal alphabet, as described above.

It is possible to set $z$ to $\varepsilon$ if and only if every $y_i$ can be set to $\varepsilon$. In turn, this is possible if and only if for every $M_i$, there is an accepting state $q_{i,i}$ such that $x_{i,i}$ can be set to $\varepsilon$. As established above, $\alpha_{\text{iter}}$ ensures that this is only possible if these states can be reached by simulating all $M_i$ in parallel on the same input. Hence, $z$ can be set to $\varepsilon$ if and only if the intersection of all $\mathcal{L}(M_i)$ is not empty.

As discussed above, $\alpha$ is deterministic (we discussed the use of the Kleene star above, and all branches disjunctions start with characteristic letters). Hence, $\beta$ is $\ell$-deterministic if and only if the intersection of all $\mathcal{L}(M_i)$ is empty. As $\beta$ can obviously be constructed in polynomial time, this shows that deciding whether a regex is $\ell$-deterministic is PSPACE-hard. As we already established the matching upper bound, this concludes the whole proof.

Furthermore, while this definition of $\ell$-determinism is only concerned with choices between different variables, it is also possible to adapt the notion of $1$-determinism to include the distinction between a variable and a terminal. For example, the expression $\langle x : a^+ \rangle b (b \lor x)^*$ is not deterministic; but as the content of $x$ always starts with $a$, such cases could be considered $1$-deterministic. Propositions 7 and 8 can be directly adapted to this extended notion of $1$-determinism.

8 Conclusions and Further Directions

Based on TMFA, an automaton model for regex, we extended the notion of determinism from regular expressions to regex. Although the resulting language class cannot express all regular
languages, it is still rich; and by using a generalization of the Glushkov construction, deterministic regex can be converted into a DTMFA, and the membership problem can then be solved quite efficiently. Although we did not discuss this, the construction is also compatible with the Glushkov construction with counters by Gelade, Gyssens, Martens [29]. Hence, one can add counters to DRX and DTMFA without affecting the complexity of membership.

Many challenging questions remain open, for example: Can the more advanced results for DREG be adapted to DRX, i.e., can $\mathcal{M}(\alpha)$ be computed more efficiently (as in [8, 14]), or is it even possible, like in [31], to avoid computing $\mathcal{M}(\alpha)$? Is effective minimization possible for DTMFA or DRX? Is it decidable whether a DTMFA defines a DRX-language? Are inclusion and equivalence decidable for DRX or DTMFA? Can determinism be generalized to larger classes of regex without making the membership problem intractable?

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