LOW-RANK TRAINING OF DEEP NEURAL NETWORKS FOR EMERGING MEMORY TECHNOLOGY

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ABSTRACT
The recent success of neural networks for solving difficult decision tasks has incentivized incorporating smart decision making “at the edge.” However, this work has traditionally focused on neural network inference, rather than training, due to memory and compute limitations, especially in emerging non-volatile memory systems, where writes are energetically costly and reduce lifespan. Yet, the ability to train at the edge is becoming increasingly important as it enables real-time adaptability to device drift and environmental variation, user customization, and federated learning across devices. In this work, we address two key challenges for training on edge devices with non-volatile memory: low write density and low auxiliary memory. We present a low-rank training scheme that addresses these challenges while maintaining computational efficiency. We then demonstrate the technique on a representative convolutional neural network across several adaptation problems, where it out-performs standard SGD both in accuracy and in number of weight writes.

1 INTRODUCTION
Deep neural networks have shown remarkable performance on a variety of challenging inference tasks. As the energy efficiency of deep-learning inference accelerators improves, some models are now being deployed directly to edge devices to take advantage of increased privacy, reduced network bandwidth, and lower inference latency. Despite edge deployment, training happens predominately in the cloud. This limits the privacy advantages of running models on-device and results in static models that do not adapt to evolving data distributions in the field.

Efforts aimed at on-device training address some of these challenges. Federated learning aims to keep data on-device by training models in a distributed fashion (Konecný et al., 2016). On-device model customization has been achieved by techniques such as weight-imprinting (Qi et al., 2018), or by retraining limited sets of layers. On-chip training has also been demonstrated for handling hardware imperfections (Zhang et al., 2017; Gonugondla et al., 2018). Despite this progress with small models, on-chip training of larger models is bottlenecked by the limited memory size and compute horsepower of edge processors.

Emerging non-volatile (NVM) memories such as resistive random access memory (RRAM) have shown great promise for energy and area-efficient inference (Yu, 2018). In Figure 1, NVM used in solution C is able to solve the weight-movement energy drawbacks of traditional solution A while also alleviating the chip area drawbacks of solution B. The benefits offered by NVM for neural network inference suggest it may become an important component of future smart edge devices. However, while solution C offers advantages for inference, it can make training even more difficult. On-chip training requires a large number of writes to the memory, and RRAM writes cost significantly more energy than reads (e.g., 10.9 pJ/bit versus 1.76 pJ/bit (Wu et al., 2019)). Additionally, RRAM endurance is on the order of $10^6$ writes (Grossi et al., 2019), shortening the lifetime of a device due to memory writes for on-chip training. In anticipation of growing numbers of inference-optimized NVM-based edge devices, we ask what can be done to enable training as well.

In this paper, we present an online training scheme amenable to NVM memory solutions. Our contributions are (1) an algorithm called Low Rank Training (LRT), and its analysis, which addresses the two key challenges of low write density and low auxiliary memory; (2) two techniques “gradient max-norm” and “streaming batch norm” to help training specifically in the online setting; (3) a suite of adaptation experiments to demonstrate the advantages of our approach.

2 RELATED WORK
Efficient training for resistive arrays. Several works have aimed at improving the efficiency of training algorithms on resistive arrays. Of the three weight-computations required
in training (forward, backprop, and weight update), weight updates are the hardest to parallelize using the array structure. Stochastic weight updates (Gokmen & Vlasov, 2016) allow programming of all cells in a crossbar at once, as opposed to row/column-wise updating. Online Manhattan rule updating (Zamanidoost et al., 2015) can also be used to update all the weights at once. Several works have proposed new memory structures to improve the efficiency of training (Soudry et al., 2015; Ambrogio et al., 2018). The number of writes has also been quantified in the context of chip-in-the-loop training (Yu et al., 2016).

**Distributed gradient descent.** Distributed training in the data center is another problem that suffers from expensive weight updates. Here, the model is replicated onto many compute nodes and in each training iteration, the mini-batch is split across the nodes to compute gradients. The distributed gradients are then accumulated on a central node that computes the updated weights and broadcasts them. These systems can be limited by communication bandwidth, and compressed gradient techniques (Aji & Heafield, 2017) have therefore been developed. In Lin et al. (2017), the gradients are accumulated over multiple training iterations on each compute node and only gradients that exceed a threshold are communicated back to the central node. In the context of on-chip training with NVM, this method helps reduce the number of weight updates. However, the gradient accumulator requires as much memory as the weights themselves, which negates the density benefits of NVM.

**Low-Rank Training.** Our work draws heavily from previous low-rank training schemes that have largely been developed for use in recurrent neural networks to uncouple the training memory requirements from the number of time steps inherent to the standard truncated backpropagation through time (TBPTT) training algorithm. Algorithms developed since then to address the memory problem include Real-Time Recurrent Learning (RTRL) (Williams & Zipser, 1989), Unbiased Online Recurrent Optimization (UORO) (Tallec & Ollivier, 2017), Kronecker Factored RTRL (KF-RTRL) (Mujika et al., 2018), and Optimal Kronecker Sums (OK) (Benzing et al., 2019). These latter few techniques rely on the weight gradients in a weight-vector product looking like a sum of outer products (i.e., Kronecker sums) of input vectors with backpropagated errors. Instead of storing a growing number of these sums, they can be approximated with a low-rank representation involving fewer sums.

## 3 Training Non-Volatile Memory

The meat of most deep learning systems are many weight matrix - activation vector products $W \cdot a$. Fully-connected (dense) layers use them explicitly: $a^{[\ell]} = \sigma (W^{[\ell]}a^{[\ell-1]} + b^{[\ell]})$ for layer $\ell$, where $\sigma$ is a non-linear activation function (more details are discussed in detail in Appendix B.1). Recurrent neural networks use one or many matrix-vector products per recurrent cell. Convolutional layers can also be interpreted in terms of matrix-vector products by unrolling the input feature map into strided convolution-kernel-size slices. Then, each matrix-vector product takes one such input slice and maps it to all channels of the corresponding output pixel (more details are discussed in Appendix B.2).

The ubiquity of matrix-vector products allows us to adapt the techniques discussed in “Low-Rank Training” of Section 2 to other network architectures. Instead of reducing the memory across time steps, we can reduce the memory across training samples in the case of a traditional feedforward neural network. However, in traditional training (e.g., on a GPU), this technique does not confer advantages. Traditional training platforms often have ample memory to store a batch of activations and backpropagated gradients, and the weight updates $\Delta W$ can be applied directly to the weights $W$ once they are computed, allowing temporary activation memory to be deleted. The benefits of low-rank training only become apparent when looking at the challenges of proposed NVM devices:

**Low write density (LWD).** In NVM, writing to weights at every sample is costly in energy, time, and endurance. These concerns are exacerbated in multilevel cells, which require several steps of an iterative write-verify cycle to program the desired level. We therefore want to minimize the number of writes to NVM.

**Low auxiliary memory (LAM).** NVM is the densest form of memory. In 40nm technology, RRAM 1T-1R bitcells @ 0.085 um$^2$ (Chou et al., 2018) are 2.8x smaller than 6T SRAM cells @ 0.242 um$^2$ (TSMC, 2019). Therefore, NVM
should be used to store the memory-intensive weights. By the same token, no other on-chip memory should come close to the size of the on-chip NVM. In particular, if our \(b\)-bit NVM stores a weight matrix of size \(n_o \times n_i\), we should use at most \(r(n_i + n_o)\) auxiliary non-NVM memory, where \(r\) is a small constant. Despite these space limitations, the reason we might opt to use auxiliary (large, high endurance, low energy) memory is because there are places where writes are frequent, violating LWD if we were to use NVM.

In the traditional minibatch SGD setting with batch size \(B\), an upper limit on the write density per cell per sample is easily seen: \(1/B\). However, to store such a batch of updates without intermediate writes to NVM would require auxiliary memory proportional to \(B\). Therefore, a trade-off becomes apparent. If \(B\) is reduced, LAM is satisfied at the cost of LWD. If \(B\) is raised, LWD is satisfied at the cost of LAM. Using low-rank training techniques, the auxiliary memory requirements are decoupled from the batch size, allowing us to increase \(B\) while satisfying both LWD and LAM\(^1\). Additionally, because the low-rank representation uses so little memory, a larger bitwidth can be used, potentially allowing for gradient accumulation in a way that is not possible with low bitwidth NVM weights.

Figure 3 illustrates how typical learning algorithms exhibit strong coupling between the number of writes and the amount of auxiliary memory. In contrast, LRT aims to decouple these, achieving the low writes of large batch training methods with the low memory of small batch training methods. In the next section, we elaborate on the low-rank training method.

\(^1\)This can alternately be achieved by sub-sampling the training data by \(r/B\) where \(r\) is the OK rank. The purpose of using a low-rank estimate is that for the same memory cost, it is significantly more information that the sub-sampled data, allowing for faster training convergence.

### 4 Low-Rank Training Method

Let \(\mathbf{z}^{(i)} = \mathbf{W} \mathbf{a}^{(i)} + \mathbf{b}\) be the standard affine transformation building block of some larger network, e.g., \(y_p^{(i)} = f_{post}(\mathbf{z}^{(i)})\) and \(\mathbf{a}^{(i)} = f_{pre}(\mathbf{x}^{(i)})\) with prediction loss \(\mathcal{L}(y_p^{(i)}, y_i^{(i)})\), where \((\mathbf{x}^{(i)}, y_i^{(i)})\) is the \(i\)th training sample pair. Then weight gradient \(\nabla_\mathbf{W} \mathcal{L}^{(i)} = \nabla_\mathbf{z}^{(i)} (\mathbf{a}^{(i)})^\top = \nabla_\mathbf{z}^{(i)} \otimes \mathbf{a}^{(i)}\) where \(\Delta \mathbf{W} = -\eta \sum_{i=1}^B \Delta \mathbf{z}^{(i)} \otimes \mathbf{a}^{(i)}\) for learning rate \(\eta\).

For a rank-\(r\) training scheme, approximate the sum \(\sum_{i=1}^B \Delta \mathbf{z}^{(i)} \otimes \mathbf{a}^{(i)}\) by iteratively updating two rank-\(r\) matrices \(\mathbf{L} \in \mathbb{R}^{n_o \times r}\), \(\mathbf{R} \in \mathbb{R}^{n_i \times r}\) with each new outer product:
The core of low-rank training is an efficient method of computing the SVD of a batch. For the product of two long, skinny matrices $\tilde{L}$ and $\tilde{R}$, an efficient SVD can be computed by first decomposing them with a QR factorization, then running an SVD on just the small matrix $R_L R_R$.

Figure 4 depicts the key operations in this step. Let $L = [\tilde{L}, dz^{(i)}]$ and $R = [\tilde{R}, a^{(i)}]$ so that $LR^\top = LR^\top + dz^{(i)} \otimes a^{(i)}$. Recall that rankReduce should turn rank-$q$ LR into an updated rank-$r$ $LR^\top$.

4.1 Optimal Kronecker Sum Approximation (OK)

One option for rankReduce($X$) to convert from rank $q = r + 1$ X to rank $r$ is a minimum L2 error estimator, which is implemented by selecting the top $r$ components of a singular value decomposition (SVD) of $X$. However, a naïve implementation is computationally infeasible and biased: $\mathbb{E}[\text{rankReduce}(X)] \neq X$. Benzing et al. (2019) solves these problems by proposing a minimum variance unbiased estimator for rankReduce, which they call the OK algorithm.$^2$

The OK algorithm can be understood in two key steps: first, an efficient method of computing the SVD of a Kronecker sum; second, a method of splitting the singular value matrix $\Sigma$ into two rank-$r$ matrices whose outer product is a minimum-variance, unbiased estimate of $\Sigma$. Details can be found in their paper; however, we include a high-level explanation in Sections 4.1.1 and 4.1.2 to aid our later discussions. Note that our variable notation differs from Benzing et al. (2019).

$^2$Their target application differs slightly in that they handle matrix - vector Kronecker sums rather than vector - vector Kronecker sums.

4.1.1 Efficient SVD of Kronecker Sums

Figure 4 depicts the key operations in this step. Let $L = [\tilde{L}, dz^{(i)}]$ and $R = [\tilde{R}, a^{(i)}]$ so that $LR^\top = LR^\top + dz^{(i)} \otimes a^{(i)}$. Recall that rankReduce should turn rank-$q$ LR into an updated rank-$r$ $LR^\top$.

QR-factorize $L = Q_L R_L$ and $R = Q_R R_R$ where $Q_L \in \mathbb{R}^{n_o \times q}$, $Q_R \in \mathbb{R}^{n_i \times q}$ are orthogonal so that $LR^\top = Q_L (R_L R_R^\top) Q_R^\top$. Let $C = R_L R_R^\top \in \mathbb{R}^{q \times q}$. Then we can find the SVD of $C = U_C \Sigma V_C^\top$ in $O(q^3)$ time (Cline & Dhillon, 2006), making it computationally feasible on small devices. Now we have:

$$LR^\top = Q_L (U_C \Sigma V_C^\top) Q_R^\top = (Q_L U_C) \Sigma (Q_R V_C)^\top$$

which gives the SVD of $LR^\top$ since $Q_L U_C$ and $Q_R V_C$ are orthogonal and $\Sigma$ is diagonal. This SVD computation has a time complexity of $O((n_i + n_o + q)q^2)$ and a space complexity of $O((n_i + n_o + q)q)$.

4.1.2 Minimum Variance, Unbiased Estimate of $\Sigma$

In Benzing et al. (2019), it is shown that the problem of finding a rank-$r$ minimum variance unbiased estimator of $LR^\top$ can be reduced to the problem of finding a rank-$r$ minimum variance unbiased estimator of $\Sigma$ and plugging it in to (1).

Further, it is shown that such an optimal approximator for
The fact that it is unbiased: \( E[\Sigma] = \Sigma \) can be easily verified.
\[ Q_L \leftarrow Q_L U_c Q_x \]
\[ Q_R \leftarrow Q_R V_c Q_x \]
\[ c_x \leftarrow \text{diag}(R_x R_x^\top) \]

These matrix multiplies require \( O((n_i + n_o)q^2) \) multiplications, so this optimization does not improve asymptotic complexity bounds. This optimization may nonetheless be practically significant since matrix multiplies are easy to parallelize and would typically not be the bottleneck of the computation compared to Gram-Schmidt. The next section discusses how to orthogonalize \( \tilde{\Sigma}_L \) efficiently and why \((R_x R_x^\top)\) is diagonal.

### 4.2.2 Orthogonalization of \( \tilde{\Sigma}_L \)
Orthogonalization of \( \tilde{\Sigma}_L \) is relatively straightforward. From (2), the columns of \( \tilde{\Sigma}_L \) are orthogonal since \( Z \) is orthogonal. However, they do not have unit norm. We can therefore pull out the norm into a separate diagonal matrix \( R_x \) with diagonal elements \( \sqrt{c_x} \):

\[
Q_x = \begin{bmatrix}
I_{m-1} & 0 \\
0 & X_s
\end{bmatrix}
\]

\[
\sqrt{c_x} = (\sqrt{\sigma_1}, \ldots, \sqrt{\sigma_{m-1}}, \sqrt{s_1/k})^{(q-m+1) \text{ times}}
\]

### 4.2.3 Finding Orthonormal Basis \( X \)
We generated \( X \) by finding an orthonormal basis that was orthogonal to a vector \( x_0 \) so that we could have \( XX^\top = I - x_0 x_0^\top \). An efficient method of producing this basis is through Householder matrices \( (x_0, X) = I - 2 vv^\top/||v||^2 \) where \( v = x_0 - e^{(1)} \) and \((x_0, X) \) is a \( k+1 \times k+1 \) matrix with first column \( x_0 \) and remaining columns \( X \) (Householder, 1958; user1551, 2013).

### 4.2.4 Efficiency Comparisons to Standard Approach
The OK/LRT methods require \( O((n_i + n_o + q)q^2) \) operations per sample and \( O(n_i n_o q) \) operations after collecting \( B \) samples, giving an amortized cost of \( O((n_i + n_o + q)q^2 + n_i n_o q/B) \) operations per sample. Meanwhile, a standard approach expands the Kronecker sum at each sample, costing \( O(n_i n_o) \) operations per sample. If \( q \ll B, n_i, n_o \) then the low rank method is superior to minibatch SGD in both memory and computational cost.

### 4.2.5 LRT Variants
In this paper, we compare two variants of the LRT algorithm. The first one, biased LRT, is a version that compresses the \( q \)-rank representation to rank \( r \) by taking the top \( r \) singular values of the SVD with no mixing. The second one, unbiased LRT, is a version that follows the OK algorithm and includes mixing for minimum variance, unbiased estimates as discussed in Section 4.1.2. While the unbiased version does not add much computational complexity, it does require access to random bits and introduces more variance into the gradient estimates. These drawbacks must be traded off with the benefits of having an unbiased estimator.

## 5 Convex Convergence
LRT introduces variance into the gradient estimates, so here we analyze the implications for online convex convergence. We analyze the case of strongly convex loss landscapes \( f'(w^t) \) for flattened weight vector \( w^t \) and online sample \( t \). In Appendix A, we show that with inverse square-root learning rate, when the loss landscape Hessians satisfy \( 0 \prec cI \preceq \nabla^2 f'(w^t) \) and under constraint (4) for the size of gradient errors \( \epsilon_t \), where \( w^* \) is the optimal offline weight vector, the online regret (5) is sublinear in the number of online steps \( T \). We can approximate \( ||\epsilon_t|| \) and show that convex convergence is likely when (6) is satisfied for biased LRT, or when (7) is satisfied for unbiased LRT.

\[
||\epsilon_t|| \leq \frac{c}{2} ||w^t - w^*||
\]

\[
R(T) = \sum_{t=1}^{T} f'(w^t) - \sum_{t=1}^{T} f'(w^*)
\]

\[
\left(\sum_{i=1}^{B} \sigma_q^{(t,i)} \right)^2 \leq \frac{c^2}{4} ||w^t - w^*||^2
\]

\[
\sum_{i=1}^{B} \sigma_q^{(t,i)} \sigma_q^{(t,i)} \leq \frac{c^2}{8} ||w^t - w^*||^2
\]

Equations (6, 7) suggest conditions under which fast convergence may be more or less likely and also point to methods for improving convergence. We discuss these in more detail in Appendix A.3.

### 5.1 Convergence Experiments
We validate (4) with several linear regression experiments on a static input batch \( X \in \mathbb{R}^{1024 \times 100} \) and target \( Y_t \in \mathbb{R}^{256 \times 100} \). In Figure 5(a), Gaussian noise at different strengths (represented by different colors) is added to the true batch gradients at each update step. Notice that convergence slows significantly to the right of the dashed lines, which is the region where (4) no longer holds\(^4\).

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\(^4\)As discussed in Appendix A.1, \( B < n_i \), so we substitute c in \( c/2 ||w^t - w^*|| \) with the minimum non-zero Eigenvalue of the Hessian \( \hat{c} \) when plotting the RHS of (4).
In Figure 5(b), we validate Equations (4, 6, 7) by testing the biased and unbiased LRT cases with rank \( r = 10 \). In these particular experiments, unbiased LRT adds too much variance, causing it to operate to the right of the dashed lines. However, both biased and unbiased LRT can be seen to reduce their variance as training progresses. In the case of biased LRT, it is able to continue training as it tracks the right dashed line.

### 6 Implementation Details

**Quantization.** The NN is quantized in both the forward and backward directions with uniform power-of-2 quantization, where the clipping ranges are fixed at the start of training\(^5\). Weights are quantized to 8 bits between -1 and 1, biases to 16 bits between -8 and 8, activations to 8 bits between 0 and 2, and gradients to 8 bits between -1 and 1. Both the weights \( W \) and weight updates \( \Delta W \) are quantized to the same LSB so that weights cannot be used for accumulation beyond the fixed quantization dynamic range. This is in contrast to using high bitwidth (Zhou et al., 2016; Banner et al., 2018) or floating point accumulators. See Appendix C for more details on quantization.

**Gradient Max-Norming.** State-of-the-art methods in training, such as Adam (Kingma & Ba, 2014), use auxiliary memory per parameter to normalize the gradients. Unfortunately, we lack the memory budget to support these additional variables, especially if they must be updated every sample\(^6\). Instead, we propose dividing each gradient tensor by the maximum absolute value of its elements. This stabilizes the range of gradients across samples. See Appendix D for more details on gradient max-norming. In the experiments, we refer to this method as “max-norm” (opposite “no-norm”).

**Streaming Batch Normalization.** Batch normalization (Ioffe & Szegedy, 2015) is a powerful technique for improving training performance which has been suggested to work by smoothing the loss landscape (Santurkar et al., 2018). We hypothesize that this may be especially helpful when parameters are quantized as in our case. However, in the online setting, we receive samples one-at-a-time rather than in batches. We therefore propose a streaming batch norm that uses moving average statistics rather than batch statistics as described in detail in Appendix E.

### 7 Experiments

#### 7.1 Adaptation Experiments

To test the effectiveness of LRT, experiments are performed on a representative CNN with four \( 3 \times 3 \) convolution layers and two fully-connected layers. We generate “offline” and “online” datasets based on MNIST (see Section F), including one in which the statistical distribution shifts every 10k images. We then optimize the hyperparameters of both an online SGD and rank-4 LRT model for fair comparison (see Appendix G). To see the importance of different training

\[ a^2, dz^2, a, dz \] allows for a low-rank approximation of the variance of the gradients, however, this is unlikely to work well because of numerical stability (e.g., estimated variances might be negative).
Figure 6. Adaptation of various training schemes over four different training environments (a) to (d). In each training environment, the top plot shows the exponential moving averages (0.999) of the per-sample online accuracy of the five training schemes, while the bottom plot shows the maximum number of updates applied to any given convolution or fully-connected kernel memory cell. For the distribution shifts in (b), the enabled augmentations at each contiguous 10k samples is shown (CD = class distribution, ST = spatial transforms, BG = background gradients, WN = white noise).

techniques, we run several ablations in Section 7.2. Finally, we compare these different training schemes in different environments, meant to model real life. In these hypothetical scenarios, a model is first trained on the offline training set, and is then deployed to a number of devices at the edge that make supervised predictions (they make a prediction, then are told what the correct prediction would have been).

We present results on four hypothetical scenarios. First, a control case where both external/environment and internal/NVM drift statistics are exactly the same as during offline training. Second, a case where the input image statistical distribution shifts every 10k samples, selecting from augmentations such as spatial transforms and background gradients (see Section F). Third and fourth are cases where the NVM drifts from the programmed values, roughly modeling NVM memory degradation. In the third case, Gaussian noise is applied to the weights as if each weight was a single multi-level memory cell whose analog value drifted in a Brownian way. In the fourth case, random bit flips are applied as if each weight was represented by $b$ memory cells (see Appendix F for details). For each hypothetical scenario, we plot five different training schemes: pure quantized inference (no training), bias-only training, standard SGD training, LRT training, and LRT training with max-normed gradients. In SGD training and for training biases, parameters are updated at every step in an online fashion. These are seen as different colored curves in Figure 6.

Inference does best in the control case, but does poorly in adaptation experiments. SGD doesn’t improve significantly on bias-only training, likely because SGD cannot accumulate gradients less than a weight LSB. LRT, on the other hand, shows significant improvement, especially after several thousand samples in the weight drift cases. Additionally, LRT shows about three orders of magnitude improvement compared to SGD in the worst case number of weight updates. Much of this reduction is due to the convolutions, where updates are applied at each pixel. However, reduction in fully-connected layer is still important because of potential energy savings. LRT/max-norm performs best in terms of accuracy across all environments and has similar weight update cost to LRT/no-norm.

7.2 Ablations
In Figure 7, rank and weight bitwidth is swept for LRT with gradient max-norming. As expected, training accuracy
Table 1. Accuracy recovery beyond inference (%, mean with standard deviation from 5 random seeds) between different algorithms (all with max-norm; effective batch size $B = 100$ if applicable), tested at different ranks ($r$), and learning rates ($\eta$). Optimal learning rates are bolded.

| Algorithm | $\eta$ | $r$ | 0.003   | 0.010   | 0.030   | 0.100   | 0.300   |
|-----------|--------|-----|---------|---------|---------|---------|---------|
| SGD       | -      | 1   | +0.3 ± 0.2 | +0.3 ± 0.2 | +0.3 ± 0.2 | +0.9 ± 0.2 | -3.9 ± 0.8 |
| Biased LRT | 1      | 1   | +0.4 ± 0.2 | +0.3 ± 0.4 | -1.8 ± 0.9 | -7.6 ± 1.6 | -31.7 ± 1.6 |
|          | 2      | +1.4 ± 0.4 | +6.5 ± 0.7 | +6.3 ± 0.6 | -5.2 ± 0.9 | -36.3 ± 0.9 |
|          | 4      | +1.3 ± 0.4 | +6.5 ± 0.7 | +5.2 ± 0.8 | -3.3 ± 1.0 | -33.8 ± 0.8 |
|          | 8      | +1.4 ± 0.3 | +5.6 ± 0.8 | +4.3 ± 0.9 | -2.4 ± 1.0 | -32.8 ± 0.9 |
| Unbiased LRT | 1      | +0.3 ± 0.2 | +0.3 ± 0.2 | -0.7 ± 0.4 | -2.7 ± 1.7 | -26.5 ± 2.6 |
|          | 2      | +0.3 ± 0.2 | +0.4 ± 0.3 | -0.1 ± 0.4 | +1.3 ± 0.9 | -12.9 ± 1.1 |
|          | 4      | +0.4 ± 0.2 | +0.6 ± 0.2 | +1.9 ± 0.3 | +8.0 ± 1.1 | -5.1 ± 1.1 |
|          | 8      | +0.4 ± 0.2 | +1.1 ± 0.2 | +3.3 ± 0.7 | +4.8 ± 1.5 | -15.8 ± 1.7 |

Table 2. Importance of unbiased SVD. Accuracy is calculated from the last 500 samples of 10k samples trained from scratch. Mean and unbiased standard deviation are calculated from five runs of different random seeds.

| Conv LRT | FC LRT | Accuracy (no-norm) | Accuracy (max-norm) |
|----------|--------|--------------------|---------------------|
| Biased   | Biased | 79.7% ± 1.1%       | 82.7% ± 1.3%        |
| Biased   | Unbiased | 83.0% ± 0.9%       | 82.4% ± 1.2%        |
| Unbiased | Biased | 77.7% ± 1.5%       | 84.6% ± 2.0%        |
| Unbiased | Unbiased | 81.0% ± 0.9%       | 83.6% ± 2.5%        |

improves with both higher LRT rank and bitwidth. In dense NVM applications, higher bitwidths may be achievable, allowing for corresponding reductions in the LRT rank and therefore, reductions in the auxiliary memory requirements.

Figure 7. Accuracy across a variety of LRT ranks and weight bitwidths, showing the expected trends of increasing accuracy with rank and bitwidth. Accuracy is calculated by averaging the accuracy on the last 500 samples from a 2k portion of the training data. For bitwidths of 1 and 2, mid-rise quantization is used (e.g., 1 bit quantizes values to -0.5 and 0.5 instead of -1 and 0).

In Table 2, biased (zero-variance) and unbiased (low-variance) versions of LRT are compared. Accuracy improvements are generally seen moving from biased to unbiased LRT although the pattern differs between the no-norm and max-norm cases. In the no-norm case, a significant improvement is seen favoring unbiased LRT for fully-connected layers. In the max-norm case, the choice of biased or unbiased LRT has only a minor impact on accuracy. It might be expected that as the number of accumulated samples for a given batch increases, lower variance would be increasingly important at the expense of bias. For our network, this implies convolutions, which receive updates at every pixel of an output feature map, would preferentially have biased LRT, while the fully-connected layer would preferentially be unbiased. This hypothesis is supported by the no-norm experiments, but not by the max-norm experiments.

In Table 3, several ablations are performed on LRT with max-norm. Most notably, weight training is found to be extremely important for accuracy as bias-only training shows an $\approx 15 - 30\%$ accuracy hit depending on whether max-normalizing is used. Streaming batch norm is also found to be quite helpful, especially in the no-norm case.

Now, we explain the $\kappa_{2h}$ ablation. In Section 4.1.1, we found the SVD of a small matrix $C$ and its singular values $\sigma_1, \ldots, \sigma_q$. This allows us to easily find the condition number of $C$ as $\kappa(C) = \sigma_1 / \sigma_q$. We suspect high condition numbers provide relatively useless update information akin to noise, especially in the presence of $L, R$ quantization.
Table 3. Miscellaneous selected ablations. Accuracy is calculated from the last 500 samples of 10k samples trained from scratch. Mean and unbiased standard deviation are calculated from five runs of different random seeds.

| Modified Condition               | Accuracy (no-norm) | Accuracy (max-norm) |
|----------------------------------|--------------------|---------------------|
| baseline (no modifications)      | 80.2% ± 1.0%       | 83.0% ± 1.1%        |
| bias-only training               | 51.8% ± 3.2%       | 68.6% ± 1.4%        |
| no streaming batch norm          | 68.2% ± 1.9%       | 81.8% ± 1.3%        |
| no bias training                 | 81.3% ± 1.0%       | 83.0% ± 1.4%        |
| $\kappa_{th} = 10^8$ instead of 100 | 79.8% ± 1.4%       | 84.2% ± 1.4%        |

Therefore, we prefer not to update $L, R$ on samples whose condition number exceeds threshold $\kappa_{th}$. We can avoid performing an actual SVD (saving computation) by noting that $C$ is often nearly diagonal, leading to the approximation $\kappa(C) \approx C_{1,1}/C_{q,q}$. Empirically, this rough heuristic works well to reduce computation load while having minor impact on accuracy. In Table 3, $\kappa_{th} = 10^8$ does not appear to ubiquitously improve on the default $\kappa_{th} = 100$, despite being $\approx 2 \times$ slower to compute.

7.3 Transfer Learning and Algorithm Comparisons

To test the broader applicability of low rank training techniques, we run several experiments on ImageNet with ResNet-34 (Deng et al., 2009; He et al., 2016), a potentially realistic target for dense NVM inference on-chip. For ImageNet-size images, updating the low-rank approximation at each pixel quickly becomes infeasible, both because of the single-threaded nature of the algorithm, and because of the increased variance of the estimate at larger batch sizes. Instead, we focus on training the final layer weights ($1000 \times 512$). ResNet-34 weights are initialized to those from Paszke et al. (2017) and the convolution layers are used to generate feature vectors for 10k ImageNet training images, which are quantized and fed to a one-layer quantized neural network. To speed up experiments, the layer weights are initialized to the pretrain weights, modulated by random noise that causes inference top-1 accuracy to fall to 52.7% ± 0.9%. In Table 1, we see that the unbiased LRT has the strongest recovery accuracies, although biased LRT also does quite well. The high-variance UORO and true SGD have weak or non-existent recoveries.

8 Conclusion

We demonstrated the potential for LRT to solve the major challenges facing online training on NVM-based edge devices: low write density and low auxiliary memory. LRT is a computationally-efficient, memory-light algorithm capable of decoupling batch size from auxiliary memory, allowing larger effective batch sizes, and consequently lower write densities. Additionally, we noted that LRT may allow for training under severe weight quantization constraints as rudimentary gradient accumulations are handled by the $L, R$ matrices, which can have high bitwidths (as opposed to SGD, which may squash small gradients to 0).

We found expressions for when LRT might have better convergence properties. Across a variety of online adaptation problems and a large-scale transfer learning demonstration, LRT was shown to match or exceed the performance of SGD while using a small fraction of the number of updates.

Finally, we conclude with speculations about more general applications of the LRT technique. Auxiliary memory minimization may be analogous to communication minimization in training strategies such as federated learning, where gradient compression is important. Therefore, LRT could be a valuable tool for local training of networks of devices that communicate training information to each other, without the use of a central server.

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7 The decision to use training data is deliberate, however experiments on out-of-sample images, such as Recht et al. (2019) show similar behavior.

8 Quantization ranges are chosen to optimize accuracy and are different from those in Section 7.1.
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A Convex Convergence

In this section we will attempt to bound the regret (defined below) of an SGD algorithm using noisy LRT estimates \( \bar{g} = g + \varepsilon \) in the convex setting, where \( g \) are the true gradients and \( \varepsilon \) are the errors introduced by the low rank LRT approximation. Here, \( g \) is a vector of size \( N \) and can be thought of as a flattened/concatenated version of the gradient tensors (e.g., \( N = n_1 \cdot n_2 \)).

Our proof follows the proof in Zinkevich (2003). We define \( \mathcal{F} \) as the convex feasible set (valid settings for our weight tensors) and assume that \( \mathcal{F} \) is bounded with \( D = \max_{w,v \in \mathcal{F}} \|w - v\| \) being the maximum distance between two elements of \( \mathcal{F} \). Further, assume a batch of \( B \) samples out of \( T \) total batches corresponds to a loss landscape \( f^t(w^t) \) that is strongly convex in weight parameters \( w^t \), so that there are positive constants \( C \geq c > 0 \) such that \( cI \preceq \nabla^2 f^t(w^t) \preceq CI \) for all \( t \) (Boyd & Vandenberghe, 2004).

We define regret as

\[
R(T) = \sum_{t=1}^{T} f^t(w^t) - \sum_{t=1}^{T} f^t(w^*)
\]

where \( w^* = \arg\min_w \sum_{t=1}^{T} f^t(w) \) (i.e., it is an optimal offline minimizer of \( f^1, \ldots, f^T \)).

The gradients seen during SGD are \( g^t = \nabla f^t(w^t) \) and we assume they are bounded by \( G = \max_{w \in \mathcal{F}, t \in [1, T]} \|\nabla f^t(w)\| \). We also assume errors are bounded by \( E = \max_{t \in [1, T]} ||\tilde{g}^t|| \|\|w^t - w^*\|\| + ||\varepsilon^t|| \leq G + E \) by the triangle inequality.

**Theorem 1.** Assume LRT-based SGD is applied with learning rate \( \eta_t = 1/\sqrt{t} \). Then, under the additional constraint \( (w^t - w^*) - \frac{c}{2} ||w^t - w^*||_2^2 \leq \tilde{g}^t \cdot (w^t - w^*) \), we have sublinear regret:

\[
R(T) \leq \frac{D^2}{2} \sqrt{T} + (G + E)^2 \left( \sqrt{T} - 1/2 \right)
\]

**Proof.** From strong convexity \( cI \preceq \nabla^2 f^t(w^t) \preceq CI \) for all \( t \),

\[
f^t(w) + g^t \cdot (v - w) + \frac{c}{2} ||v - w||_2^2 \leq f^t(v) \quad \text{for all} \ v
\]

In particular, if we consider \( v = w^* \) and rearrange,

\[
f^t(w) - f^t(w^*) \leq g^t \cdot (w - w^*) - \frac{c}{2} ||w - w^*||_2^2
\]

\[
f^t(w) - f^t(w^*) \leq \tilde{g}^t \cdot (w^t - w^*)
\]

(9)

Consider a gradient update \( w^{t+1} = P_{\mathcal{F}}(w^t - \eta_t \tilde{g}^t) \), where \( P_{\mathcal{F}} \) projects the update back to \( \mathcal{F} \). Then,

\[
||w^{t+1} - w^*||_2^2 = ||P(w^t - \eta_t \tilde{g}^t) - w^*||_2^2
\]

\[
\leq ||w^t - \eta_t \tilde{g}^t - w^*||_2^2 = ||w^t - w^*||_2^2 - 2\eta_t (w^t - w^*) \cdot \tilde{g}^t + \eta_t^2 ||\tilde{g}^t||_2^2
\]

\[
\leq ||w^t - w^*||_2^2 - 2\eta_t (w^t - w^*) \cdot \tilde{g}^t + \eta_t^2 (G + E)^2
\]

(10)

From (9, 10),

\[
f^t(w^t) - f^t(w^*) \leq \frac{1}{2\eta_t} (||w^t - w^*||_2^2 - ||w^{t+1} - w^*||_2^2) + \frac{\eta_t}{2} (G + E)^2
\]

(11)

We now bound the regret:

\[
R(T) = \sum_{t=1}^{T} [f^t(w^t) - f^t(w^*)]
\]

\[
\leq \sum_{t=1}^{T} \left[ \frac{1}{2\eta_t} (||w^t - w^*||_2^2 - ||w^{t+1} - w^*||_2^2) + \frac{\eta_t}{2} (G + E)^2 \right]
\]

\[
= \frac{||w^1 - w^*||_2^2}{2\eta_1} - \frac{||w^{T+1} - w^*||_2^2}{2\eta_T} + \frac{1}{2} \sum_{t=2}^{T} \left( \frac{1}{\eta_t} - \frac{1}{\eta_{t-1}} \right) ||w^t - w^*||_2^2 + \frac{(G + E)^2}{2} \sum_{t=1}^{T} \eta_t
\]

\[
\leq \frac{D^2}{2\eta_1} + \frac{1}{2} \sum_{t=2}^{T} \left( \frac{1}{\eta_t} - \frac{1}{\eta_{t-1}} \right) D^2 + \frac{(G + E)^2}{2} \sum_{t=1}^{T} \eta_t
\]

\[
= \frac{D^2}{2\eta_T} + \frac{(G + E)^2}{2} \sum_{t=1}^{T} \eta_t
\]

(12)

If \( \eta_t = 1/\sqrt{t} \), then \( \sum_{t=1}^{T} \eta_t \leq 2\sqrt{T} - 1 \) (Zinkevich, 2003), so from (12),
\[ R(T) \leq \frac{D^2}{2} \sqrt{T} + (G + \varepsilon)^2 \left( \sqrt{T} - \frac{1}{2} \right) \]  

(13)

This is a sublinear regret and therefore, average regret \( R(T)/T \) is bounded above by 0 in the limit as \( T \to \infty \). To achieve this result, we constrained \( g^t \cdot (w^t - w^*) - \frac{c}{2} ||w^t - w^*||^2 \leq \hat{g}^t \cdot (w^t - w^*) \). We now examine sufficient conditions for this inequality to be satisfied.

\[ g^t \cdot (w^t - w^*) \leq \frac{c}{2} ||w^t - w^*||^2 \leq \hat{g}^t \cdot (w^t - w^*) \]

(14)

For the unbiased, minimum-variance case. Theorem A.4 from Benzing et al. (2019) states that the minimum variance is \( \sigma^2/(k+s_2) \), where \( s_1 = \sum_{i=m}^q \sigma_i \), \( s_2 = \sum_{i=m}^q \sigma_i^2 \), and \( k \), \( m \) are as defined in Section 4.1.2. Since \( m \) is chosen to minimize variance, we can upper bound the variance by choosing \( m = r \) and therefore \( k = 1, s_1 = \sigma_r + \sigma_g \), and \( s_2 = \sigma_r^2 + \sigma_g^2 \). Empirically, this tends to be a good approximation. Then, the average per-element variance added at sample \( i \) is approximately \( (2/N)(\sigma_r^2) \). Assuming errors between samples are uncorrelated, this leads to a total variance:

\[ \sigma^2 \approx \frac{2}{N} \sum_{i=1}^B \sigma_r^2 \]

(18)

For either case, \( \|\varepsilon\|^2 \approx N\sigma^2 \). For the \( t \)-th batch and \( i \)-th sample, we denote \( \sigma_q^{(t,i)} \) as the \( q \)-th singular value. For simplicity, we focus on the biased, zero-variance case (the unbiased case is similar). From (15), an approximately sufficient condition for sublinear-regret convergence is:

\[ \sum_{i=1}^B \sigma_q^{(t,i)} \approx \frac{c^2}{4} \|w^t - w^*\|^2 \]

(19)

A.3 Discussion on Convergence

Equation (19) suggests that as \( w^t \to w^* \), the constraints for achieving sublinear-regret convergence become more difficult to maintain. However, in practice this may be highly problem-dependent as the \( \sigma_q \) will also tend to decrease near optimal solutions. To get a better sense of the behavior of the left-hand side of (19), suppose that:

\[ \sum_{i=1}^B \sigma_q^{(t,i)} \approx \sum_{i=1}^B (\sigma_i(G^t))^2 \]

where \( G^t = \nabla_{W^t} f^t(W^t) \in \mathbb{R}^{n_x \times n_t} \) are the matrix weight \( W^t \) gradients at batch \( t \) and \( \| \cdot \|_F \) is a Frobenius norm. We therefore expect both the left (proportional to \( \|G^t\|^2_F \)) and the right (proportional to \( \|w^t - w^*\|^2 \)) of (19) to decrease during training as \( w^t \to w^* \). This behavior is in fact what is seen in Figure 5(b). If achieving convergence is found to be difficult, (19) provides some insight for convergence improvement methods.

One solution is to reduce batch size \( B \) to satisfy the inequality as necessary. This minimizes the weight updates
during more repetitive parts of training while allowing dense weight updates (possibly approaching standard SGD with small batch sizes) during more challenging parts of training.

Another solution is to reduce $\sigma_\epsilon$. One way to do this is to increase the rank $r$ so that the spectral energy of the updates are spread across more singular components. There may be alternate approaches based on conditioning the inputs to shape the distribution of singular values in a beneficial way.

A third method is to focus on $c$, the lower bound on curvature of the convex loss functions. Perhaps a technique such as weight regularization can increase $c$ by adding constant curvature in all Eigen-directions of the loss function Hessian (although this may also increase the LHS of (19)). Alternatively, perhaps low-curvature Eigen-directions are less important for loss minimization, allowing us to raise the $c$ that we effectively care about. This latter approach requires no particular action on our part, except the recognition that fast convergence may only be guaranteed for high-curvature directions. This is exemplified in Figure 5(b), where we can see biased LRT track the curve for $C$ more so than $c$.

Finally, we note that this analysis focuses solely on the errors introduced by a floating-point version of LRT. Quantization noise can add additional error into the $\epsilon^t$ term. We expect this to add a constant offset to the LHS of (19). For a weight LSB $\Delta$, quantization noise has variance $\Delta^2/12$, so we desire:

$$N \frac{\Delta^2}{12} + \sum_{i=1}^{B} \left( \sigma_{(t,i)}^2 \right)^2 \leq \frac{c^2}{4} \| w^t - w^* \|^2$$

(20)

B KRONOECKER SUMS IN NEURAL NETWORK LAYERS

B.1 Dense Layer

A dense or fully-connected layer transforms an input $a \in \mathbb{R}^{n \times 1}$ to an intermediate $z = W \cdot a + b$ to an output $y = \sigma(z) \in \mathbb{R}^{n_o \times 1}$ where $\sigma$ is a non-linear activation function. Gradients of the loss function with respect to the weight parameters can be found as:

$$\nabla_W \mathcal{L} = (\nabla_z \mathcal{L}) \odot (\nabla_W z) = dz \otimes a$$

(21)

which is exactly the per-sample Kronecker sum update we saw in linear regression. Thus, at every training sample, we can add $(dz^{(i)} \otimes a^{(i)})$ to our low rank estimate with LRT.

Note that while we already save an impressive factor of $B/q$ in memory when computing gradients for the dense layer, we save a much larger factor of $B h_{out} w_{out} / q$ in memory when computing gradients for the convolution layers, making the low rank training technique even more crucial here.

However, some care must be taken when considering activation memory for convolutions. For compute-constrained edge devices, image dimensions may be small and result in minimal intermediate feature map memory requirements. However, if image dimensions grow substantially, activation memory could dominate compared to weight storage. Clever dataflow strategies may provide a way to reduce intermediate activation storage even when performing back-propagation. 

B.2 Convolutional Layer

A convolutional layer transforms an input feature map $A \in \mathbb{R}^{h_{in} \times w_{in} \times c_{in}}$ through a 2D convolution with weight kernel $W_{kern} \in \mathbb{R}^{h_{out} \times w_{out} \times c_{out}}$. Then it computes an output feature map $y = \sigma(z)$ where $\sigma$ is a non-linear activation function.

Convolutions can be interpreted as matrix multiplications through the im2col operation which converts the input feature map $A$ into a matrix $A_{col} \in \mathbb{R}^{(h_{out} w_{out}) \times (k_h k_w c_{in})}$ where the $i^{th}$ row is a flattened version of the sub-tensor of $a$ which is dotted with $W_{kern}$ to produce the $i^{th}$ pixel of the output feature map (Ren & Xu, 2015). We can multiply $A_{col}$ by a flattened version of the kernel, $W \in \mathbb{R}^{c_{out} \times (k_h k_w c_{in})}$ to perform the $W_{kern} \ast A$ convolution operation with a matrix multiplication. Under the matrix multiplication interpretation, weight gradients can be represented as:

$$\nabla_W \mathcal{L} = \frac{\nabla_z \mathcal{L}}{dz} \odot \frac{\nabla_W Z}{dz} \odot A_{col}$$

$$= \sum_{i=1}^{h_{out} w_{out}} dZ_{col,i} \otimes A_{col,i}$$

(22)
C Hardware Quantization Model

In a real device, operations are expected to be performed in fixed point arithmetic. Therefore, all of our training experiments are conducted with quantization in the loop. Our model for quantization is shown in Figure 8. The green arrows describe the forward computation. Ignoring quantization for a moment, we would have $a^\ell = \text{ReLU}(a^\ell W^\ell * a^{\ell-1} + b^\ell)$, where $*$ can represent either a convolution or a matrix multiply depending on the layer type and $a^\ell$ is the closest power-of-2 to He initialization (He et al., 2015). For quantization, we rely on four basic quantizers: $Q_w, Q_b, Q_a, Q_g$, which describe weight quantization, bias and intermediate accumulator quantization, activation quantization, and gradient quantization, respectively. All quantizers use fixed clipping ranges as depicted on the figure. They may be consistently quantized to 0, preventing them from accumulating. To combat this, we only apply an update if a minimum update density $\rho_{\text{min}} = 0.01$ would be achieved, otherwise we continue accumulating samples in $L$ and $R$, which have much higher bitwidths. When an update does finally happen, the “effective batch size” will be a multiple of $B$ and we increase the learning rate correspondingly. In the literature, a linear scaling rule is suggested (see Goyal et al. (2017)), however we empirically find square-root scaling works better (see Appendix G).

D Gradient Max-Norming

Figure 9 plots the magnitude of gradients seen in a weight tensor over training steps. One apparent property of these gradients is that they have a large dynamic range, making them difficult to quantize. Even when looking at just
Figure 9. Maximum magnitude of weight gradients versus training step for standard SGD on a CNN trained on MNIST.

the spikes, they assume a wide range of magnitudes. One potential method of dealing with this dynamic range is to scale tensors so that their max absolute element is 1 (similar to a per-tensor AdaMax (Kingma & Ba, 2014) or Range Batch-Norm (Banner et al., 2018) applied to gradients). Optimizers such as Adam, which normalize by gradient variance, provide a justification for why this sort of scaling might work well, although they work at a per-element rather than per-tensor level. We choose max-norming rather than variance-based norming because the former is easier computational and potentially more amenable to quantization. However, a problem with the approach of normalizing tensors independently at each sample is that noise might be magnified during regions of quiet as seen in the Figure. What we therefore propose is normalization by the maximum of both the current max element and a moving average of the max element.

Explicitly, max-norm takes two parameters - a decay factor \( \beta = 0.999 \) and a gradient floor \( \epsilon = 10^{-4} \) and keeps two state variables - the number of evaluations \( k := 0 \) and the current maximum moving average \( x_{mv} := \epsilon \). Then for a given input \( x \), max-norm modifies its internal state and returns \( x_{\text{norm}} \):

\[
\begin{align*}
  k &:= k + 1 \\
  x_{max} &:= \max(|x|, \epsilon) \\
  x_{mv} &:= \beta \cdot x_{mv} + (1 - \beta) \cdot x_{max} \\
  \bar{x}_{mv} &:= \frac{x_{mv}}{1 - \beta^k} \\
  x_{\text{norm}} &:= \frac{x}{\max(x_{max}, \bar{x}_{mv})}
\end{align*}
\]

E Streaming Batch Normalization

Standard batch normalization (Ioffe & Szegedy, 2015) normalizes a tensor \( X \) along some axes, then applies a trainable affine transformation. For each slice \( X \) of \( X \) that is normalized independently:

\[
Y = \gamma \cdot \frac{X - \mu_b}{\sqrt{\sigma_b^2 + \epsilon}} + \beta
\]

where \( \mu_b, \sigma_b \) are mean and standard deviation statistics of a minibatch and \( \gamma, \beta \) are trainable affine transformation parameters.

In our case, we do not have the memory to hold a batch of samples at a time and must compute \( \mu_b, \sigma_b \) in an online fashion. To see how this works, suppose we knew the statistics of each sample \( \mu_i, \sigma_i \) for \( i = 1 \ldots B \) in a batch of \( B \) samples. For simplicity, assume the \( i^{th} \) sample is a vector \( X_{i,:} \in \mathbb{R}^n \) containing elements \( X_{i,j} \). Then:

\[
\begin{align*}
  \mu_b &= \frac{1}{B} \sum_{i=1}^{B} \mu_i \quad (23) \\
  \sigma_b^2 &= \frac{1}{B} \sum_{i=1}^{B} \left( \sum_{j=1}^{n} X_{i,j}^2 - \mu_b^2 \right) \\
  &= \frac{1}{B} \sum_{i=1}^{B} (\sigma_i^2 + \mu_i^2) - \mu_b^2 \\
  &\neq \frac{1}{B} \sum_{i=1}^{B} \sigma_i^2 \quad (24)
\end{align*}
\]

In other words, the batch variance is not equal to the average of the sample variances. However, if we keep track of the sum-of-square values of samples \( \sigma_i^2 + \mu_i^2 \), then we can compute \( \sigma_b^2 \) as in (24). We keep track of two state variables: \( \mu_s, sq_s \) which we update as \( \mu_s := \mu_s + \mu_i \) and \( sq_s := sq_s + \sigma_i^2 + \mu_i^2 \) for each sample \( i \). After \( B \) samples, we divide both state variables by \( B \) and apply (23, 24) to get the desired batch statistics. Unfortunately, in an online setting, all samples prior to the last one in a given batch will only see statistics generated from a portion of the batch, resulting in noisier estimates of \( \mu_b, \sigma_b \).

In streaming batch norm, we alter the above formula slightly. Notice that in online training, only the most recently viewed sample is used for training, so there is no reason to weight different samples of a given batch equally. Therefore we can use an exponential moving average instead of a true average to track \( \mu_s, sq_s \). Specifically, let:

\[
\begin{align*}
  \mu_s &:= \eta \cdot \mu_s + (1 - \eta) \cdot \mu_i \\
  sq_s &:= \eta \cdot sq_s + (1 - \eta) \cdot (\sigma_i^2 + \mu_i^2)
\end{align*}
\]

If we set \( \eta = 1 - 1/B \), a weighting of \( 1/B \) is seen on the current sample, just as in standard averages with a batch of size \( B \), but now all samples receive similarly clean batch statistic estimates, not just the last few samples in a batch.
For our experiments, we construct a dataset comprising an offline training, validation, and test set, as well as an online training set. Specifically, we start with the standard MNIST dataset of LeCun et al. (1998) and split the 60k training images into partitions of size 9k, 1k, and 50k. Elastic transforms (Simard et al., 2003; Ernestus, 2016) are used to augment each of these partitions to 50k offline training samples, 10k offline validation samples, and 100k online training samples, respectively. Elastic transforms are also applied to the 10k MNIST test images to generate the offline test samples.

The source images for the 100k online training samples are randomly drawn with replacement, so there is a certain amount of data leakage in that an online algorithm may be graded on an image that has been generated from the same image a previous sample it has trained on has been generated from. This is intentional and is meant to mimic a real-life scenario where a deployed device is likely to see a restrictive and repetitive set of training samples. Our experiments include comparisons to standard SGD to show that LRT’s improvement is not merely due to overfitting the source images.

From the online training set, we also generate a “distribution shift” dataset by applying unique additional augmentations to every contiguous 10k samples of the 100k online training samples. Four types of augmentations are explored. Class distribution clustering biases training samples belonging to similar classes to have similar indices. For example, the first thousand images may be primarily “0”s and “3”s, whereas the next thousand might have many “5”s. Spatial transforms rotate, scale, and shift images by random amounts. Background gradients both scale the contrast of the images and apply black-white gradients across the image. Finally, white noise is random Gaussian noise added to each pixel. Figure 10 shows some representative examples of what these augmentations look like. The augmentations are meant to mimic different external environments an edge devices might need to adapt to.

In addition to distribution shift for testing adaptation, we also look at internal statistical shift of weights in two ways - analog and digital. For analog weight drift, we apply independent additive Gaussian noise to each weight every $d = 10$ steps with $\sigma = \sigma_0/\sqrt{1M/d}$ where $\sigma_0 = 10$ and reclip the weights between -1 and 1. This can be interpreted as each cell having a Gaussian cumulative error with $\sigma = \sigma_0$ after 1M steps. For digital weight drift, we apply independent binary random flips to the weight matrix bits every $d$ steps with probability $p = p_0/(1M/d)$ where $p_0 = 10$. This can be interpreted as each cell flipping an average of $p_0$ times over 1M steps. Note that in real life, $\sigma_0, p_0$ depend on a host of issues such as the environmental conditions of the device (temperature, humidity, etc), as well as the rate of seeing training samples.
G Hyperparameter Selection

In order to compare standard SGD with the LRT approach, we sweep the learning rates of both to optimize accuracy. In Figure 11, we compare accuracies across a range of learning rates for four different cases: SGD or LRT with or without max-norming gradients. Optimal accuracies are found when learning rate is around 0.01 for all cases. For most experiments, 8b weights, activations, and gradients, and 16b biases are used. Experiments similar to those in Section 7.2 are used to select some of the hyperparameters related to the LRT method in particular. In most experiments, rank-4 LRT with batch sizes of 10 (for convolution layers) or 100 (for fully-connected layers) are used. Additional details can be found in the supplemental code.

Figure 11. The left two heat maps are used to select the base / standard SGD learning rate. The right two heat maps are used to select the LRT learning rate using the optimal SGD learning rate for bias training from the previous sweeps. For the LRT sweeps, the learning rate is scaled proportional to the square-root of the batch size $B$. This results in an approximately constant optimal learning rate across batch size, especially for the max-norm case. Accuracy is reported averaged over the last 500 samples from a 10k portion of the online training set, trained from scratch.