Characterization of a CdZnTe detector for a low-power CubeSat application

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ABSTRACT: We report spectral and imaging performance of a pixelated CdZnTe detector custom designed for the MeVCube project: a small Compton telescope on a CubeSat platform. MeVCube is expected to cover the energy range between 200 keV and 4 MeV, with a sensitivity comparable to the one of the last generation of larger satellites. In order to achieve this goal, an energy resolution of few percent in full width at half maximum (FWHM) and a 3-D spatial resolution of few millimeters for the individual detectors are needed. The severe power constraints present in small satellites require very low power read-out electronics for the detector. Our read-out is based on the VATA450.3 ASIC developed by Ideas, with a power consumption of only 0.25 mW/channel, which exhibits good performance in terms of dynamic range, noise and linearity. A 2.0 cm × 2.0 cm × 1.5 cm CdZnTe detector, with a custom 8 × 8 pixel anode structure read-out by a VATA450.3 ASIC, has been tested. A preliminary read-out system for the cathode, based on a discrete Amptek A250F charge sensitive pre-amplifier and a DRS4 ASIC, has been implemented. An energy resolution around 3% FWHM has been measured at a gamma energy of 662 keV; at 200 keV the average energy resolution is 6.5%, decreasing to ≤2% at energies above 1 MeV. A 3-D spatial resolution of ≈2 mm is achieved in each dimension.

KEYWORDS: Solid state detectors, Gamma detectors, Space instrumentation, Data analysis

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1 Introduction

Over the last few decades, Cadmium Zinc Telluride (CdZnTe or CZT) semiconductor detectors have gained increasing interest for X-ray and gamma-ray applications [1, 2]. Compared to other traditional semiconductor materials such as silicon (Si) and germanium (Ge), CdZnTe displays a higher atomic number and density, which translates into high radiation stopping power and detection efficiency. At the same time, the wider band gap and low leakage currents are favourable for low noise measurement and promise an excellent energy resolution performance at room temperature. In the current state of technology CdZnTe detectors can be produced with high quality up to few cm$^3$ in volume. Therefore CdZnTe detectors are well suited for the development of compact and reliable radiation detection systems, and appealing for a large variety of applications, from nuclear medicine and radiation monitoring, to X-ray and gamma-ray astronomy.

The most significant drawback of CdZnTe devices, with respect to Si and Ge, is the comparatively poor transport properties of charge carriers, in particular holes. In order to operate the detectors with optimal performance, special electrode configurations which rely on the electron charge collection and are insensitive to the hole contribution, need to be implemented. Such configurations, also referred to as single charge carrier devices, include the virtual Frisch grid [3], small pixel arrays or strips [4], the co-planar grid [5, 6] and orthogonal strips [7] geometries. A complete review of these configurations and principle of operation can be found, for example, in [8] or [9].

In this work we characterize the performance of a pixelated CdZnTe detector custom designed for application on a Compton telescope, named MeVCube [10]. The scientific payload is based on the
CubeSat standard, a class of nanosatellites with precise restrictions and limitations in size and form factor [11]. The MeVCube instrument consists of 128 CdZnTe detectors, arranged in a stack of two layers of 64 detectors each; the separation of the two layers is 6 cm. The telescope will scan the gamma-ray sky in the energy range from 200 keV to 4 MeV, in a nearly equatorial low-Earth orbit, with an altitude of ~ 550 km and an inclination angle ≤ 5°. Soft gamma rays in this energy range interact through Compton scattering in the first layer of CdZnTe detectors, where the recoil electron is quickly absorbed. The scattered photon deposits the rest of the energy in the second layer. Measuring the energies of the scattered photon and of the recoil electron, together with their interaction positions, the energy and direction of the incoming gamma ray can be reconstructed. Further details about the MeVCube geometry and its principle of operation are provided in a parallel paper [10]. Furthermore, the simulations performed in [10] have also shown that with an energy resolution from ~ 6.5% (FWHM) at 200 keV to ≤ 2.0% above 1 MeV, and a spatial resolution of $O(2\text{ mm})$ in each direction for the interaction position, MeVCube can reach an angular resolution of ~ 1.5°, a field-of-view of ~ 2 sr and a sensitivity level around $10^{-10}$ erg cm$^{-2}$ s$^{-1}$.

Due to power constraints present in CubeSats, low-power read-out electronics for the detectors is mandatory. The VATA450.3 ASIC, developed by Ideas, fulfils our requirements in terms of power consumption (0.255 mW/channel), dynamic range (up to 1.5-2 MeV deposited energy), noise and linearity (few percents). Further details about the read-out electronics are provided in Appendix A. Moreover, VATA450.3 has been used successfully for the read-out of CdTe sensors in the ASTRO-H space mission [12]. We first present in more detail the experimental set-up employed for the measurements, and then present spectral and imaging performance of the detector.

2 Pixel detector layout and experimental set-up

We characterize the performance of a pixelated CdZnTe detector, manufactured by Redlen Technologies, with a volume of 2.0 cm × 2.0 cm × 1.5 cm and a custom designed 8 × 8 pixel anode structure (see Figure 1). The pixel size is 2.25 × 2.25 mm$^2$ and the pixel pitch is 2.45 mm. On the opposite side, the planar cathode is biased at −2500 V (in our application). Gold contacts are used for both, the anode and the cathode.

According to the Shockley–Ramo theorem [13, 14] the signal induced in the electrodes can be computed based on quantities such as the weighting potential and the weighting field. In pixelated CdZnTe detectors, signals from the pixels are almost insensitive to the interaction position, while signals from the cathode linearly depend on the interaction depth. Therefore signals from triggered pixels provide a measurement of the energy deposited inside the detector, while the ratio between cathode and pixels signals provides a measurement for the depth-of-interaction. A detailed overview of signal formation in CdZnTe and semiconductor detectors can be found in [8]. A steering grid surrounds the pixels; the space of one pixel in the corner is used for the electrical contact of the steering grid. The presence of a steering electrode has shown to improve the charge collection efficiency of

1We do not consider Compton events in the same detector or detectors of the same layer, since these events leads to major uncertainties in the reconstruction of the direction of the incoming gamma ray.

2Application Specific Integrated Circuit.

3Integrated Detector Electronics AS, https://ideas.no/.

4https://redlen.com/.
Figure 1: Schematic of the anode pattern of the CdZnTe detector as implemented in the MeVCube project. Pixels are identified in a chess-like coordinate system, with rows ranging from 1 to 8, and columns ranging from A to H. The space of one pixel in the corner of the detector (H1) is left for the electrical contact of the steering grid. The pixels highlighted in yellow are considered in the following analysis to illustrate the performance of the detector. Pixel G4 is chosen as a representative of the best performing pixels, while H8 is selected as a representative of pixels with a comparatively poor performance, and is affected by distortions of the weighting potential and electric field typical for edge pixels (see Section 3 for further details).

CdZnTe detectors, as reported, e.g., in [15]. When the steering grid is biased to a slightly negative voltage with respect to the grounded pixels (around −50 V, see also Appendix B), electrons are forced to move towards the pixels when approaching the anode surface, reducing charge loss in the pixels gap. The results of our measurements regarding the effect of the steering grid are reported in Appendix B. The detector is attached using conductive epoxy to a 4-layer PCB\(^5\), hosting also filtering circuits for the high voltages of the cathode and the steering grid, and connectors to the VATA450.3 ASIC on its evaluation board for the pixels read-out (Figure 2). A Galao evaluation board configures the VATA450.3 ASIC for different working modes and controls data read-out and communication with the host computer. A comprehensive overview of the VATA450.3 ASIC, its principle of operation and experimental measurements verifying the manufacturer specifications are provided in Appendix A.

A preliminary read-out system for the cathode is based on a discrete Amptek A250F\(^6\) charge sensitive

\(^5\)Printed Circuit Board.
\(^6\)https://www.amptek.com/internal-products/a250f-and-a250fnf-high-density-charge-sensitive-preamplifiers.
Figure 2: Close-up of the custom designed CdZnTe detector. The pixels are attached to a PCB; exposed on the top is the planar cathode. On the front row input connectors to the evaluation board provided by *Ideas* are visible.

Figure 3: Block diagram of the experimental set-up and read-out chain.

pre-amplifier, on a separate carrier board. Waveforms are sampled and recorded by a DRS4 (*Domino Ring Sampler*, [16]) ASIC on its evaluation test-board⁷, for signal and baseline analysis. At a later stage of the project the entire read-out will be based on VATA450.3 ASICs⁸. Only information on the deposited charge is used in the following analysis; no additional information or corrections

⁷[https://www.psi.ch/en/drs/evaluation-board](https://www.psi.ch/en/drs/evaluation-board).

⁸A read-out system completely based on VATA450.3 would require the implementation of two ASICs, the development of dedicated carrier boards and our own first version of read-out firmware. The work is beyond the scope of this paper and will be carried out in the next stages of the *MeVCube* project.
involving cathode drift time or cathode-to-anode time difference has been implemented in the current set-up, in order to develop a framework consistent with a read-out system completely based on the VATA450.3. The pixels and the cathode are AC coupled to the read-out electronics. A block diagram of the experimental set-up and read-out chain is shown in Figure 3. The high resistivity, low bulk-capacitance and surface passivation of the CdZnTe and contacts result in a low dark current. However a large parasitic capacitance is expected in the current setup, due to the size of the evaluation boards and long traces in the PCB. Details about noise measurements for our setup are given in Appendix A. An optimization of the layout of the carrier boards, in order to improve the noise performance of the set-up is foreseen for the future.

The performance of the detector is evaluated using radioactive gamma-ray sources. Different radioactive sources were used, in order to test the spectral response of the detector on a wide energy range: a Cs-$^{137}$ source with a gamma energy line of 662 keV, a Ba-$^{133}$ source with lines at 81, 276.4, 302.9 and 356 keV, and a Co-$^{60}$ source with lines at 1.17 and 1.33 MeV. The detector has been tested at room temperature using a bias voltage of $-2500$ V.

3 Depth-of-interaction correction

The first radioactive source used for detector characterization is a Cs-$^{137}$ source. In order to characterize the performance of each pixel individually, we analyze events which trigger a single anode pixels (in coincidence with the cathode signal). Events in which two or more pixels are triggered simultaneously, are rejected from our analysis. For the Cs-$^{137}$ source, such events constitute about 15% of the data sample.

The spectral results for two selected pixels are shown in Figure 4. A common behaviour observed for the measured spectra of thick CdZnTe detectors is the presence of pronounced tails towards lower energies with respect to the photo-peaks (see, for example, [17] or [4]). The effect is due to incomplete charge collection and charge carrier trapping; imbalances of the electric field and potential for pixels on the edge of the detector might also contribute. Since the low-energy tails are a consequence of depth-dependent charge collection efficiency, depth information can be used to correct the signals, enhancing the overall spectral performance of the detector. This is achieved by the read-out of signals from the planar cathode, taken in coincidence with those of the anode pixels. The bottom of Figure 4 shows the measured relationship between the two signals. The distinct correlation profile of the 662 keV photo-peak line can be linearised in order to enhance the overall spectral performance of the detector, in a procedure called depth-of-interaction correction [4, 18]. A proper correction is provided by the following function:

$$E = a \cdot \frac{S_{\text{pixel}}}{1 - \exp \left( -b \cdot \frac{S_{\text{cathode}}}{S_{\text{pixel}}} \right)},$$

(3.1)

where $S_{\text{cathode}}$ and $S_{\text{pixel}}$ represent the integrated charge measured at the cathode and the pixel, respectively, and $a$ and $b$ are the fitting parameters.

The relationship between the cathode signal and the corrected pixel signal, obtained after depth-of-interaction correction is highlighted in the scatter plot of Figure 5. As observed from the blue histograms in Figure 5, the reduction of the low-energy tails is remarkable and the photo-peak line
Figure 4: Top: energy spectra of the Cs-137 radioactive source for two selected pixels, showing very different behaviour. Bottom: enhanced spectral performance can be achieved through a depth-of-interaction technique: signals from the cathode, taken in coincidence with those of the pixels, provide a correction for incomplete charge collection and trapping effects.

is more pronounced. The peak-to-valley ratio improves from 5.9 to 8.2 for the G4 pixel, and from 2.8 to 6.6 for the H8 pixel.

A degradation of the energy response of the detector is still observed for interactions very close to the anode surface (smaller than the pixel size), corresponding to the smallest values of the cathode signal. Since the fitting function in (3.1) only takes into account the trapping effect in the case of an ideal weighting potential, it does not provide an accurate description for interaction closer to the anode side, which might be affected by imbalances in potentials and electric field. Therefore the spectral resolution shown can be improved further, selecting interactions with:

\[
\frac{S_{\text{cathode}}}{S_{\text{pixel}}} > \text{threshold}.
\]
Figure 5: Bottom: scatter plot between the cathode signal and the new pixel signal obtained after depth-of-interaction correction. Top: the linearisation of the correlation profile leads to a sharper energy line for the Cs-137 photo-peak. A further improvement of the spectra can be achieved by imposing a cut in the interaction position, marked by the red lines in the scatter plots. The cut is introduced in order to reject events close to the anode surface, affected by the greatest distortions, with uniform efficiency in energy.

The value of the threshold is tuned channel by channel, in order to take into account the different distortions and behaviour of each pixel. The distribution of the threshold value for all pixels is provided in Figure 6. The highest values of the threshold are found for pixels at the edges and corners of the detector, affected by the largest distortions in the weighting potential and electric field. Since the ratio between the cathode and pixel signals is equal to the interaction depth (scaled to the total detector thickness), the relation is a cut in the interaction position, meant to provide uniform efficiency over the energy range of interest. The final spectra obtained for the two selected pixels are shown by the red histograms in Figure 5. The imposed cut limits the active volume of
the detector to ~ 80%, taking also into account that the space of one pixel is left for the electrical contact of the steering grid. In terms of efficiency, 13% of the counts are lost for pixel G4, while 22% of the counts are lost for the pixel H8.

4 Energy resolution of the CdZnTe detector

Figure 7 displays the energy resolution measured at 662 keV for all pixels, expressed in full width at half maximum (FWHM). As it can be seen, the spectral performance is very uniform throughout the detector: the energy resolution measured for all pixels in the bulk of the detector is ≤ 3.0% at 662 keV, while few pixels in the edges and corners of the detectors exhibit an energy resolution > 3.0%. The deterioration of the energy resolution for edge pixels is expected due to distortions of the electric field at the boundaries of the detector.

An overall “global” energy resolution for the detector is provided by the median over all 63 pixels. Figure 8 shows the distribution of the measured energy resolution in all pixels, with the median and mean marked respectively by the red and black vertical lines; the median value is ~ 2.8%.

In order to characterize the detector performance over a wider energy range, we made use of other radioactive sources, such as Ba-133 and Co-60, allowing for the measurement of the energy resolution from ~ 200 keV to above 1 MeV. Specifically the detectable energy lines are: 276.4 keV, 302.9 keV and 356 keV for Ba-133 and 1.17 MeV and 1.33 MeV for Co-60. Measurements of the 81 keV line from Ba-133 is only possible for the pixels at the sides of the detector directly facing the radioactive source, since at this energy 99.9% of the total radiation is stopped after ~ 0.4 cm in CdZnTe⁹. For these pixels an energy resolution between 15 and 20 keV FWHM is measured, mainly limited by electronic noise. In fact the total noise contribution from the measurement of

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⁹The attenuation coefficient for CdZnTe can be computed at https://physics.nist.gov/PhysRefData/Xcom/html/xcom1.html
Figure 7: Energy resolution (expressed in FWHM) measured for the 662 keV line of Cs-137.

Figure 8: Distribution of the measured energy resolution at 662 keV for all pixels. The median and the mean of the distribution are displayed with the red and black vertical lines, respectively.

develop

the width of the pedestal distribution is 15.4 keV FWHM, averaged for all pixels (see Figure 17 in Appendix A). The spectral performance of the detector, as a function of the energy of the gamma photo-peak lines, is plotted in Figure 9: the measured energy resolution is on average $\sim 6.5\%$ around 200 keV decreasing to $\leq 2\%$ at energies above 1 MeV.
Figure 9: Energy resolution of the pixelated CdZnTe detector as a function of energy, measured for different radioactive sources. For each gamma-ray line the resolution is obtained as the median value of the distribution for all 63 pixels.

5 Depth resolution of the pixelated CdZnTe detector

A second requirement for our application is a $O(2 \text{ mm})$ 3-D spatial resolution in each direction. The spatial resolution on the anode plane (interaction on the x-y plane) is dictated by the pixel pitch, equal to 2.45 mm, while the interaction depth (interaction location on the z axis) can be reconstructed from the ratio between the cathode and the pixel signals. The depth resolution of the detector is investigated with a Cs-137 radioactive source and a copper collimator, manufactured at DESY (see Figure 10).

The collimator has a length of 10 cm and a drill hole of 0.5 mm in diameter. In the current set-up the collimator stands to a distance of $\sim 3 \text{ cm}$ from the detector. A Geant4 [19] simulation has been performed, in order to evaluate the geometrical effects of the set-up on the spatial resolution, due to the finite size of the collimator beam and the distance between the collimator and the detector. For this purpose, an ideal detector with infinitely precise energy and spatial resolution has been considered. The results of the simulations are shown in Figure 11.

Pronounced tails superimposed to the gamma-ray peak can be observed, due to inefficient collimator shielding in a penumbra region around the hole. A proper fit of the data in Figure 11 is achieved with a double gaussian distribution, with identical mean value: the first one accounts for the “background” in the penumbra region, while the second one gives the desired collimator spatial resolution, $R_c$. The measured depth-of-interaction resolution of the system is given by:

$$R_{\text{sys}} = \sqrt{R_{\text{det}}^2 + R_c^2},$$

where $R_{\text{det}}$ is the intrinsic spatial resolution of the detector and $R_c$ the component due to the collimator geometry, evaluated through simulation.

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In, for narrow angles around the collimator’s hole a non-negligible fraction of the passing gammas can pass the copper material around the exit of the hole.
Figure 10: Overview of the experimental set-up employed for the depth of interaction measurements.

The depth resolution of the CdZnTe detector has been measured for different scanning positions, adjusted via a vertical stage with a stroke of 20 mm and a scale of 1 mm. The interaction depth is provided by the ratio between the cathode and the anode signals (corrected with the depth-of-interaction technique) as in (3.2), from those events corresponding to the Cs-137 photo-peak. The relation between the computed ratio and the collimator position is provided by Figure 12. An example of the reconstructed depth for three different scanning positions is shown in Figure 13. After subtraction of the geometrical component evaluated through simulations, described in (5.1), an average depth resolution of $\sim 1.8$ mm (in FWHM) is achieved for the detector, meeting the requirements of the MeVCube project.

6 Conclusions

The performance of a 2.0 cm $\times$ 2.0 cm $\times$ 1.5 cm pixelated CdZnTe detector has been evaluated. In our current set-up pixels are read-out by the low-power ASIC VATA450.3, while a read-out system based on the Amptek A250F charge sensitive pre-amplifier has been implemented for the cathode. Experimental measurements have shown that this combination can cover the energy range between 200 keV and 2.0 MeV, with an acceptable noise and an integral non-linearity of just a few percent. Energy and spatial resolution of the CdZnTe detector has been characterized by irradiation with different radioactive sources. A depth of interaction correction has been implemented, in order to obtain optimal charge collection and improved performance. After depth-of-interaction correction, an energy resolution of $\leq 3.0\%$ in FWHM is achieved at 662 keV, with a median value of 2.8%;
**Figure 11:** Interaction position as obtained from the Geant4 simulation with the collimator set-up, considering an ideal CdZnTe detector with infinitely precise energy and spatial resolution.

**Figure 12:** Linear relation between the collimator position and the computed ratio between the cathode and pixel signals. Errors on the x axis are given by the scale of the vertical stage, while the widths of the reconstructed depth distributions are provided for the y axis.
Figure 13: Reconstructed interaction depth for three different scanning positions.

10 pixels, located at the edges of the detector exhibit an energy resolution $> 3.0\%$. The median energy resolution decreases to about $\sim 6.5\%$ at 200 keV and increases to $\leq 2.0\%$ at energies above 1 MeV. The spatial resolution on the anode plane is dictated by the pixel pitch (2.45 mm), while a depth resolution of $\sim 1.8$ mm (FWHM) has been obtained, from the ratio between cathode and pixel signals.

The detector has been designed for implementation on a small Compton telescope on a CubeSat platform, named MeVCube. Given the measured energy and spatial resolution for CdZnTe detector, MeVCube sensitivity is comparable to the one achieved by the last generation of large satellites like COMPTEL and INTEGRAL, as shown in [10]. The next steps will include the design and test of new carrier boards for the CdZnTe detectors, with focus on optimization of their noise performance and qualification for space operation. Validation of the temperature dependence of the detector performance will be carried out in a thermal vacuum chamber. The development of a prototype Compton camera employing multiple CdZnTe detectors will allow a full evaluation of the Compton event reconstruction performance.

A VATA450.3 as a low-power read-out electronics for space operations

A.1 VATA450.3 overview

VATA450.3 is a 64 channel ASIC developed by Ideas, optimized for the front-end readout of CdTe and CdZnTe devices. A schematic representation of VATA450.3’s circuit diagram (from [12]) is shown in Figure 14, while Table 1 summarizes its main parameters and performance.

Each channel of the ASIC implements a charge-sensitive pre-amplifier (CSA) followed by a slow shaper for spectroscopy and a fast shaper for triggering. The fast shaper has a shaping time of $\sim 0.6$ $\mu$s and is followed by a discriminator to generate the trigger signal (TA component in Figure 14). A second shaper, which generates the output pulse, has a longer shaping time of about 4.0 $\mu$s. With a sample and hold circuit the pulse height is sampled at the time specified by an external hold signal, produced from the trigger signal with a configurable delay (referred as hold delay or
short shaping time is followed by a discriminator to form a trigger signal. The other shaper with a long shaping time is followed by a sample and hold circuit to hold the pulse height at the timing specified by an external hold signal. The hold signal is produced from the trigger signal with an appropriate delay.

Figure 8: Circuit diagram of the ASIC developed for the SGD. The circuits shown in a blue background are implemented in this development.

Many important functionalities are integrated in the ASIC for the SGD in order to minimize additional components required to readout the signal as shown in the circuit diagram with a blue background region. As a result, we only need an FPGA, several digital drivers and receivers, and passive components (resistors and capacitors) to operate 208 ASICs in a Compton camera. The signals in all channels on the ASIC are converted to digital values in parallel with Wilkinson-type analog-to-digital converters (ADCs) where the time duration of voltage ramp to cross the sampled voltage is counted by a counter. The conversion time is less than 100 µs using the external clock or less than 50 µs using the internal clock. (The conversion time depends on the pulse height of the signal.) In order to minimize the readout time, the only channels that are read-out are those above a data threshold that can be digitally set for each channel independently from the trigger threshold. We usually observe common mode noise from this type of ASIC at the level of \( \sim 1 \) keV (can be worse if power supplies and grounding are not appropriate). Common mode noise has to be subtracted to accurately apply the threshold for the zero suppression. Common mode noise level of each event is detected by taking an ADC value of the 32nd (a half of number of channel) pulse height, corresponding to a median value of all ADC values. With zero suppression, the readout time is 0.5 µs per ASIC when no data is readout and \((9 + n)\) µs when we readout \(n\) channels. Without zero suppression, the readout time becomes 73 µs per ASIC.

The ASIC produces all necessary analog bias currents and voltages on the chip by internal DACs (Digital to Analog Converters) except for the main bias current which sets the scale of all bias currents: this is provided by an external circuit on the FEC. Each bit of the registers for all internal DACs and other functions consists of three flip-flops and a majority selector for tolerance against single event upset (SEU). If the majority selector detects any discrepancies among three flip-flops, it will set a SEU flag which will be readout as a part of output data. The ASIC is fabricated on a wafer with an epitaxial layer which will improve immunity against latch up.

Table 2 summarizes specifications.

Figure 14: Circuit diagram of VATA450.3. Picture taken from [12].

Table 1: Summary of main design parameters and performance of VATA450.3, as specified by the manufacturer.

| Parameter                        | Value                                                                 |
|----------------------------------|----------------------------------------------------------------------|
| Supplier                         | Ideas                                                               |
| Technology                       | 0.35 \( \mu \)m CMOS                                               |
| Chip size                        | \(10000 \mu m \times 6500 \mu m \times 450 \mu m\)                   |
| Supply voltages                  | \(-2.0 \) V/\(+1.5 \) V                                            |
| Power consumption                | 255 \(\mu\)W/channel                                               |
| Max. readout rate                | 10 MHz                                                              |
| Shaping time                     | \~ 0.6 \(\mu\)s, fast shaper (triggering)                          |
|                                  | \~ 4.0 \(\mu\)s, slow shaper (spectroscopy)                        |
| Dynamic range                    | \(-30 \) fC/\(+16 \) fC, high gain mode                             |
|                                  | \(-60 \) fC/\(+50 \) fC, low gain mode                             |
| Non linearity (max.)             | 3.2\%                                                              |
| Equivalent noise charge (ENC)    | 45e\(^{-}\) high gain mode, no inputs bonded                        |
|                                  | 65e\(^{-}\) low gain mode, no inputs bonded                        |
fixed delay). Figure 15 illustrates this principle of operation. Each of the 64 analog signals is then converted to digital values with a 10-bit Wilkinson-type ADC (Analogue-to-Digital Converter); a multiplexer finally forwards all the 64 signal from the ADCs to the output data-stream.

VATA450.3 can operate with either negative or positive input charges and in two different gain modes (high gain mode and low gain mode). All amplifier inputs are protected against over-voltage and electrostatic discharge; input and output circuits are also designed to allow daisy-chaining of multiple ASICs.

**Figure 15**: Sketch illustrating the principle of operation of VATA450.3. The output signal from the charge-sensitive pre-amplifier is shaped by a fast shaper for triggering and slow shaper for spectroscopy. When an adjustable threshold voltage is reached for the fast shaper output, the trigger signal is generated. The hold signal, generated after a fixed delay (hold delay), is used to sample the output signal from the slow shaper. The hold delay value is chosen in order to probe this signal approximately at its maximum height.

### A.2 VATA450.3 performance: dynamic range, noise and linearity

Our tests have been performed on evaluation boards and control software directly provided by Ideas. A Galao evaluation board, with a Xilinx FPGA\(^{10}\) and bias generators, configures the VATA450.3 ASIC for different working modes and controls data read-out and communication with the host computer. The VATA test-board hosts one VATA450.3 ASIC, input connectors for detectors and test points for all key VATA450.3 signals.

Input calibration signals can be provided via an internal pulse directly generated by the evaluation board, or from an external pulse generator, connected to the input connectors of the VATA test-board, through different load capacitors. Figure 16a and Figure 16b show the dynamic range of VATA450.3

\(^{10}\)Field-Programmable Gate Array.
in the low-gain mode. Measurements have been performed with charge of both polarities: positive input charge and negative input charge, respectively. The different curves correspond to different settings of VATA450.3, like the ADC ramp speed ($I_{ramp}$) and offset ($I_{offset}$), the hold delay, and the bias voltages controlling the feedback MOS in the pre-amplifier and shapers ($ifp$, $ifss$, $ifsf$). The values are set by internal DACs\textsuperscript{12} governed via the control register. The figures verify the manufacturer specifications, that VATA450.3 can cover the range up to $+50$ fC for a positive input charge and $-60$ fC for a negative input charge, with an integral non-linearity of just few percents. Since the average electron-hole pair creation energy for CdZnTe is around $4.6$ eV, it follows that VATA450.3 can be coupled to the detector in order to measure charge deposits from gamma rays up to $1 - 2$ MeV, suitable for our application. For negative signals, at the expense of linearity, the dynamic range can be extended to $-80$ fC.

\begin{figure*}[h]
\centering
\begin{subfigure}{0.49\textwidth}
\includegraphics[width=\textwidth]{fig16a.png}
\caption{(a) Dynamic range of VATA450.3 for different settings of the ASIC in the low gain mode and for positive input charge.}
\end{subfigure}\hfill
\begin{subfigure}{0.49\textwidth}
\includegraphics[width=\textwidth]{fig16b.png}
\caption{(b) Dynamic range of VATA450.3 for different settings of the ASIC in the low gain mode and for negative input charge.}
\end{subfigure}
\end{figure*}

\begin{figure*}[h]
\centering
\begin{subfigure}{0.49\textwidth}
\includegraphics[width=\textwidth]{fig16c.png}
\caption{(c) Noise performance of VATA450.3 for different load capacitors in the low gain mode.}
\end{subfigure}\hfill
\begin{subfigure}{0.49\textwidth}
\includegraphics[width=\textwidth]{fig16d.png}
\caption{(d) Equivalent noise charge (black solid line) and integral non-linearity (red dash-dot line) for all 64 VATA450.3 channels. Measurements are performed for the specified settings corresponding to the red line in Figure 16b.}
\end{subfigure}
\end{figure*}

Figure 16: Main performance of the VATA450.3 ASIC: dynamic range, noise, integral non-linearity.

Figure 16c summarizes the noise performance of VATA450.3, expressed in equivalent noise charge

\textsuperscript{12}Digital to Analogue Converters.
Measurements have been taken for different load capacitors from 1.2 pF to 10 pF, computing the noise from the width of the calibration pulse (as 1σ unit). A plot of the integral non-linearity and the noise (for a 1.2 pF load capacitor) for all 64 channels of the ASIC is reported in Figure 16d, measured for the settings corresponding to the red line in Figure 16b. In the final set-up with the detector attached to the evaluation board, the total noise contribution of the system has been measured from the width of the pedestal distribution: the results for all 63 pixels are shown in Figure 17.

Figure 17: Total noise measured from the width of the pedestal distribution in the final set-up, expressed both in keV as FWHM and in equivalent noise charge (e−). The conversion between keV and e− is done taking into account an average electron-hole pair creation energy of 4.6 eV in CdZnTe; an additional factor of 2.355 takes into account the passage from σ to FWHM. The horizontal red line displays the average for all channels.

B Steering grid measurements

In pixelated CdZnTe detectors, charge sharing causes charge loss for events occurring in the gap between pixels, and is one of the factors that degrades the performance of the detector. The presence of a steering grid, surrounding the anode pixel and biased to a slightly negative voltage with respect to the grounded pixels, has shown to improve the performance of CdZnTe detectors [15, 20]: when the steering grid is biased to a slightly negative voltage, electrons are forced to move towards the pixels when approaching the anode surface, reducing charge loss in the pixels gap. Typical values for the steering grid voltage are ~ −50 V [15]: lower values do not lead to significant improvements in collection efficiency, while higher values result in an additional surface current and increased noise. The performance degradation due to charge sharing effect, may vary according to several

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[i]: i.e. the deviation of the ADC values from the best straight line describing the dynamic range of the device.
factors of the considered device, such as pixel size, width of the gap between pixels, size of the steering grid, as well as shaping performance and noise of the read-out electronics.

Figure 18: Comparison of the energy spectra obtained from measurements with the Cs-137 radioactive source for two selected pixels, with the steering grid floating and the steering grid biased at $-50\,\text{V}$. The histograms have been normalized to allow an easier comparison of the respective shapes.

In our set-up two sets of measurements were performed, in order to evaluate the effect of the steering grid: one with the steering grid floating and one with the steering grid biased at $-50\,\text{V}$. The detector was tested under irradiation with a Cs-137 source. In Figure 18 the spectra obtained with the steering grid floating and the steering grid biased at $-50\,\text{V}$ are compared. No overall improvement in the energy resolution is measured before and after depth-of-interaction correction. In order to quantify the improvement in efficiency due to the steering grid biasing, the detection rates have been considered. The detection rate is determined by counting the number of events in the photo-peak $N$, normalized to the observation time $t$. Then, the relative improvement in efficiency is computed as the difference between the detection rate measured with the steering grid biased at $-50\,\text{V}$ and the detection rate measured with the steering grid floating:

\[ \Delta \varepsilon = \frac{N_{-50\,\text{V}}}{t_{-50\,\text{V}}} - \frac{N_{\text{floating}}}{t_{\text{floating}}} . \quad (B.1) \]

The results of our analysis, for 10 selected pixels, are shown in Figure 19: as it can be observed the biasing of the steering grid results in a slightly improved detection efficiency. At the same time, however, the steering grid biasing introduces some design complications, such as an additional power supply and filtering circuit to provide the required negative voltage, and the loss of an active pixel due to the space left for the electrical contact of the steering grid. Therefore, for our application, the improvement derived from a biased steering election is thought to be too small to justify the additional system complexity deriving from it.
Figure 19: Improvement of the detection efficiency for 10 selected pixels when biasing the steering grid.

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