Utility Optimal Thread Assignment and Resource Allocation in Multi-Server Systems

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Abstract—Achieving high performance in many multi-server systems requires finding a good assignment of worker threads to servers and also effectively allocating each server’s resources to its assigned threads. The assignment and allocation components of this problem have been studied extensively but largely separately in the literature. In this paper, we introduce the assign and allocate (AA) problem, which seeks to simultaneously find an assignment and allocation that maximizes the total utility of the threads. Assigning and allocating the threads together can result in substantially better overall utility than performing the steps separately, as is traditionally done. We model each thread by a utility function giving its performance as a function of its assigned resources. We first prove that the AA problem is NP-hard. We then present a 2\(\sqrt{2}\) - 1 > 0.828 factor approximation algorithm for concave utility functions, which runs in \(O(n m^2 + n (\log m C)^2)\) time for \(n\) threads and \(m\) servers with \(C\) amount of resources each. We also give a faster algorithm with the same approximation ratio and \(O(n (\log m C)^2)\) time complexity. We then extend the problem to two more general settings. First, we consider threads with nonconcave utility functions, and show the algorithm achieves good empirical performance. We conduct extensive experiments to test the performance of our algorithms on threads with both synthetic and realistic utility functions, and find that they achieve over 92\% of the optimal utility on average. We also compare our algorithms with a number of practical heuristics, and find that our algorithms achieve up to 9 times higher total utility.

Keywords: Assignment and allocation; utility; algorithms; multi-server systems; web hosting center; cloud

I. INTRODUCTION

In this paper, we study efficient ways to execute a set of resource constrained worker threads on multiple servers. Our problem consists of two steps. First, each thread is assigned to a server. Subsequently, the resources at each server are allocated to the threads assigned to it. Each thread obtains a certain utility based on the resources it is allocated, which is captured in the form of a utility function. The goal is to maximize, over all possible assignments and allocations, the total utility of all the threads. We call this problem AA, for assign and allocate.

The AA problem can model a range of system settings. One example is a web hosting center, where a service provider operates websites on behalf of customers. Incoming web requests are serviced by threads which are run on center’s servers. As requests have different characteristics, the performance of the thread for each request depends on the amount of resources, such as processing or memory, which the thread is allocated. The goal of the service provider is to process the largest number of requests, and it does this by controlling both the servers which threads are assigned to and the resources allocated to the threads on each server, in a way which maximizes the overall system performance. Another application of the AA problem is in cloud computing. Clouds are a promising paradigm for providing configurable computing resources to users. A cloud provider sells virtual machine instances (corresponding to threads in AA) running on physical machines (corresponding to servers). Customers use utility functions to express their willingness to pay for instances consuming different amounts of resources, and the provider’s task is to assign and size the virtual machines to maximize her profit (utility). As a final application, consider a multicore processor, where each core corresponds to a server offering its shared cache as a resource to concurrently executing threads. Each thread is first bound to a core, after which cache partitioning \([4], [5]\) can enforce an allocation of the core’s cache among the assigned threads. A thread’s performance is often strongly dependent on its cache allocation \([5], [10], [31]\). A scheduler tries to maximize overall system performance through an efficient mapping of threads to the cores and effectively partitioning of each core’s cache.

The two steps in AA correspond to the thread assignment and resource allocation problems, both of which have been studied extensively in the literature. However, to the best of our knowledge, these problems have not been studied together in the unified context considered in our work. Existing works on resource allocation \([4], [5], [10], [11], [12]\) largely deal with dividing the resources on a single server among a given set of threads. It is not clear how to apply these algorithms when there are multiple servers, since there are many possible ways to initially assign the threads to servers, and certain assignments result in low overall performance regardless of how resources are subsequently allocated. For example, if there are two types of threads, one with high maximum utility and one with low utility, then assigning all the high utility threads to the same server will result in competition between them and depressed overall utility no matter how resources
are allocated. Likewise, existing works on thread assignment \[2, 3, 8, 9\] often overlook the resource allocation aspect. Typically in these works each thread requests a fixed amount of resource. Once assigned to a server, a thread is allocated precisely the resources it requested, without any adjustments made based on the requests of other threads assigned to the same server. This can also lead to suboptimal performance. For example, consider a thread which obtains \(x\beta\) utility when allocated \(x\) amount of resource, for some \(\beta \in (0, 1]\) \(^1\) and suppose the thread requests \(z > 0\) resources. Then when there are \(n\) threads and one server with \(C\) resources, typical thread assignment algorithms would give \(\frac{C}{n}\) threads \(z\) resources each while the rest receive 0, leading to a total utility of \(Cz^{\beta - 1}\); note that this quantity is constant in \(n\). However, the optimal allocation gives \(\frac{C}{n}\) resources to each thread and has total utility \(C\beta n^{1-\beta}\), which is arbitrarily better than the first allocation for large \(n\). To see this, consider an example of \(C = 100\), \(z = 10\), \(\beta = 0.5\), then the ratio between \(C\beta n^{1-\beta}\) and \(Cz^{\beta - 1}\) is \(\frac{\beta}{1-\beta}\), which is arbitrarily large for large \(n\).

The AA problem models each thread using a nondecreasing utility function giving its performance as a function of the resources it receives. In practice, many utility functions are concave, capturing a frequently observed diminishing returns property \[5\]. However, in certain settings, such as cache hit rates for different amounts of cache allocation, the utility function may also be nonconcave \[10\]. Furthermore, a thread’s utility may sometimes depend on multiple types of resources. The goal in AA is to simultaneously find assignments and allocations for all the threads which maximizes their total utility. To the best of our knowledge, we are the first to study the utility when \(\beta = \frac{1}{2}\), this is known as the “square root rule” \[6, 7\].

5) We conduct extensive experiments to test the performance of our algorithms. We use several types of synthetic and realistic utility functions, and show that our algorithms obtains over 92% of the maximum utility on average. We also compare our algorithms with several simple but practical heuristics, and show that they achieve up to 9 times higher utility for very heterogeneous threads.

The rest of paper is organized as follows. In Section II, we describe related works on thread assignment and resource allocation. Sections III formally defines our model and the AA problem. Section IV proves AA is NP-hard. Section V presents an approximation algorithm for concave utility functions and its analysis, and Section VI proposes a faster algorithm. Section VII presents and analyzes an algorithm for nonconcave utility functions. In Section VIII, we describe our experimental results. Section IX extends our proposed algorithms to multiprocessor settings. Finally, Section X concludes and discusses some future problems.

## II. Related Works

There is a large body of work on resource allocation for a single server. Fox et al. \[11\] considered concave utility functions and proposed a greedy algorithm to find an optimal allocation in \(O(nc)\) time, where \(n\) is the number of threads and \(C\) is the amount of resource on the server. Galil \[12\] proposed an improved algorithm with \(O(n\log C)^2\) running time, by doing a binary search to find an allocation in which the derivatives of all the threads’ utility functions are equal, and the total resources used by the allocation is \(C\). Resource allocation for nonconcave utility functions is blocked NP-complete. However, Lai and Fan \[10\] identified a structural property of real-world utility functions which leads to fast parameterized algorithms.

Our work is related to the application placement problem, in which applications with different resource requirements need to be mapped to servers while fulfilling certain quality of service guarantees. Urgaokar et al. \[2\] proposed offline and online approximation algorithms for application placement, and the offline algorithm achieves a \(\frac{1}{2}\) approximation ratio. \[3\] proposed algorithms to place web applications on servers with the goal of maximizing the amount of demand which can be satisfied. They model each thread using a single value corresponding to a fixed amount of allocated resource, instead of a utility function allowing a range of resource allocations as our paper does.

The AA problem is also related to the multiple knapsack and multiple-choice knapsack (MCKP) problems, for which there has been a number of studies. For the former, Neebe et al. \[16\] proposed a branch-and-bound algorithm, and Chekuri et al. \[17\] proposed a polynomial time approximation scheme. The multiple knapsack problem differs from AA in that each item, corresponding to a thread, has a single weight and value, corresponding to a single resource allocation and associated utility. In contrast, we use utility functions which allow threads a continuous range of allocations and utilities. The MCKP problem can model utility functions as it considers classes

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\(^1\)For \(\beta = \frac{1}{2}\), this is known as the “square root rule” \[6, 7\].
of items with different weights and values and chooses one item from each class; each class corresponds to a utility function. However, MCKP only considers a single knapsack, and thus corresponds to a restricted form of AA with one server. Kellerer et al. [13] proposed a greedy MCKP algorithm. Lawler [15] proposed a $1 - \epsilon$ approximate algorithm, while Gens and Levner [14] proposed a $\frac{1}{2}$-approximate algorithm with better running time. AA can be seen as a combined multiple-choice multiple-knapsack problem. We are not aware of any previous work on this problem. The model used in this paper corresponds to the case where there are items for every weight.

There has also been a large amount of work on resource provisioning for cloud computing and data centers, which are related to the multi-server setting we consider. [19], [20] analyzed online bin packing algorithms for the problem of dispatching cloud gaming requests to servers in order to minimize total cost. Bobroff et al. [23] proposed a first-fit heuristic which dynamically places virtual machines (VMs) on physical machines to minimize the number of machines required to support a workload at a specified allowable rate of SLA violations. Jennings et al. [24] surveyed many virtual machine placement schemes in clouds. These works typically follow a bin packing formulation, whereas the AA problem corresponds to a multiple-choice multiple knapsack problem. Lampe et al. [26] proposed an auction scheme to allocate VMs to users to maximize a provider’s profit. [27], [28] proposed auction schemes to allocate VMs to users to maximize social welfare. [27] focuses on the online problem while [28] focuses on the offline problem. In auction schemes, each user submits a bid stating their desired number of VM instances and their maximum willingness to pay, and the system provider decides whether to accept the users’ bids. Each user’s request for VMs is allowed to be assigned to more than one physical machine. However, in the AA problem, each thread can be assigned to only one server. Tang et al. [18] proposed a policy to allocate multiple resources to different users to achieve long-term fairness, but do not consider how to assign the resource requests to different servers as our paper does. Han et al. [30] proposed an algorithm to schedule (offload) jobs on mobile devices to edge servers to minimize the jobs’ response times in edge-clouds. Wei et al. [29] proposed an online algorithm to minimize cost for data centers with multiple servers and randomly arriving service requests by determining the state of each server among three types (i.e., active, idle and setup). Han [25] proposed an approximate dynamic VM management method to minimize power consumption in data centers. While all these works have as a basic goal—an efficient usage of servers and resources, they usually consider these aspects separately, and also do not model thread performance using utility functions.

III. Model

In this section we formally define our model and problem. We consider a set of $m$ homogenous servers $s_1, \ldots, s_m$. Each server has $C > 0$ amount of resources, where $C$ is a positive integer. We note that homogeneous servers, i.e., servers with the same processing capabilities and available resources, have been widely studied in the literature [2], [22] and accurately model a number of scenarios. For example, multicore processors typically contain shared caches of the same size, and datacenters often have many identically configured servers for ease of management. We also have $n$ threads $t_1, \ldots, t_n$. The set of threads is static, to capture threads performing long-running tasks. Let $S$ and $T$ denote the set of servers and threads, respectively. Each thread $t_i$ is associated with a utility function $f_i : [0, C] \rightarrow \mathbb{Z}^{\geq 0}$, giving its performance as a function of the resources it is allocated. We assume that $f_i$ is nonnegative and nondecreasing. We also assume that $f_i$ is either concave, or consists of a set of concave or convex segments. In the former case, the concavity assumption models a diminishing returns property frequently observed in practice [5], and is often used to model cache and memory performance [6], [7]. The latter case of a nonconcave function consisting of several concave or convex segments was introduced in [10], and can be used to model an arbitrary function $f : [0, C] \rightarrow \mathbb{Z}^{\geq 0}$. It was observed in [10] that most utility functions $f$, despite being possibly nonconcave, consist of a small number $s$ of segments $[0, b_1], (b_1, b_2], \ldots, (b_{s-1}, C]$, such that $f$ is either concave or convex in each segment $(b_i, b_{i+1}]$. Examples of such functions include cache hit rate functions from the SPEC CPU benchmarks, such as aspi or swim.

Our goal is to assign the threads to the servers in a way which respects the resource bounds and maximizes the total utility. While a solution to this problem involves both an assignment of threads and allocations of resources, for simplicity we use the term assignment to refer to both. Thus, an assignment is given by a vector $(r_1, c_1), \ldots, (r_n, c_n)$, indicating that each thread $t_i$ is allocated $c_i$ amount of resource on server $s_{r_i}$. Let $S_j$ be the set of threads assigned to server $s_j$. That is, $S_j = \{ i | r_i = j \}$. Then for all $1 \leq j \leq m$, we require $\sum_{i \in S_j} c_i \leq C$, so that the threads assigned to $s_j$ use at most $C$ resources. We assume that every thread is assigned to some server, even if it receives 0 resources on the server. The total utility from an assignment is $\sum_{j=1}^{m} \sum_{i \in S_j} f_i(c_i) = \sum_{i \in T} f_i(c_i)$. The AA (assign and allocate) problem is to find an assignment that maximizes the total utility.

IV. HARDNESS OF THE PROBLEM

In this section, we show that it is NP-hard to find an assignment maximizing the total utility, even when there are only two servers and the utility functions are all concave. Thus, it is unlikely there exists an efficient optimal algorithm for the AA problem. This motivates the approximation algorithms we present in Sections V and VI.

Theorem IV.1. Finding an optimal AA assignment is NP-hard, even when there are only two servers and all threads have concave utility functions.

Proof. We give a reduction from the NP-hard partition problem [21] to the concave AA problem with two servers. In the partition problem, we are given a set of numbers $S = \{c_1, \ldots, c_n\}$ and need to determine if there exists a partition of
S into sets $S_1$ and $S_2$ such that $\sum_{i \in S_1} c_i = \sum_{i \in S_2} c_i$. Given
an instance of partition, we create an AA instance $A$ with
two servers each with $C = \frac{1}{2} \sum_{i=1}^{n} c_i$ amount of resources.
There are $n$ threads $t_1, \ldots, t_n$, where the $i$'th thread has utility function $f_i$ defined by

$$f_i(x) = \begin{cases} x & \text{if } x \leq c_i \\ c_i & \text{otherwise} \end{cases}$$

The $f_i$ functions are nondecreasing and concave. We claim
the partition instance has a solution if and only if $A$'s maximum utility is $\sum_{i=1}^{n} c_i$. For the if direction, let $A^* = \{(r_1^*, c_1^*), \ldots, (r_n^*, c_n^*)\}$ denote an optimal solution for $A$, and let $S_1$ and $S_2$ be the set of threads assigned to the servers 1 and 2, respectively. We show that $S_1, S_2$ solve the partition problem. We first show that $c_i^* = c_i$ for all $i$. Indeed, if $c_i^* < c_i$ for some $i$, then $f_i(c_i^*) < c_i$, while $f_i(c_i) \leq c_j$, for all $j \neq i$. Thus, $\sum_{i=1}^{n} f_i(c_i^*) < \sum_{i=1}^{n} c_j$, which contradicts the assumption that $A^*$'s utility is $\sum_{i=1}^{n} c_j$. Next, suppose $c_i^* > c_i$ for some $i$. Then since $A^*$ is a valid assignment, we have $\sum_{i \in S_1} c_i^* + \sum_{i \in S_2} c_i^* \leq C + C = \sum_{i=1}^{n} c_i$, and so there exists $j \neq i$ such that $c_j^* < c_j$. But then $f_j(c_j^*) < c_j$ and $f_k(c_k^*) \leq c_k$ for all $k \neq j$, so $A^*$'s total utility is $\sum_{k=1}^{n} f_k(c_k^*) < \sum_{i=1}^{n} c_i$, again a contradiction. Thus, we have $c_i^* = c_i$ for all $i$, and so $\sum_{i \in S_1} c_i^* + \sum_{i \in S_2} c_i^* = \sum_{i=1}^{n} c_i = 2C$. So, since $\sum_{i \in S_1} c_i^* \leq C$ and $\sum_{i \in S_2} c_i^* \leq C$, then $\sum_{i \in S_1} c_i = \sum_{i \in S_2} c_i = C$. Hence, $\sum_{i \in S_1} c_i = \sum_{i \in S_2} c_i = C$, and $S_1, S_2$ solve the partition problem.

For the only if direction, suppose $S_1, S_2$ are a solution to
the partition instance. Then since $\sum_{i \in S_1} c_i = \sum_{i \in S_2} c_i = C$, we can assign the threads with indices in $S_1$ and $S_2$ to servers 1 and 2, respectively, and get a valid assignment with utility $\sum_{i=1}^{n} f_i(c_i) = \sum_{i=1}^{n} c_i$. This is a maximum utility assignment for $A$, since $f_i(x) \leq c_i$ for all $i$. Thus, the partition problem reduces to the AA problem, and so the latter is NP-hard for two servers.

V. APPROXIMATION ALGORITHM FOR CONCAVE FUNCTIONS

In this section, we present an algorithm for the AA problem
when the utility function for each thread is concave. The algorithm outputs an assignment with total utility at least $\alpha = 2(\sqrt{2} - 1) > 0.828$ times the optimal, and runs in $O(mn^2 + n(\log mC)^2)$ time. The algorithm consists of two main steps. The first step transforms the utility functions, which are arbitrary nondecreasing concave functions and difficult to work with algorithmically, into functions consisting of two linear segments, which are easier to handle. Next, we find an $\alpha$-approximate thread assignment for the linearized functions. We then show that this leads to an $\alpha$ approximate solution for the original concave problem. For ease of exposition, we list the notations used in the remainder of the paper in Table 1.

A. Linearization

To describe the linearization procedure, we start with the
following definition.

**Definition V.1.** Given an instance $A$ of the AA problem with
$m$ servers each with $C$ amount of resources, and $n$ threads
with utility functions $f_1, \ldots, f_n$, consider the quantity

$$\hat{F} = \max_{c_i, i \in [1, n]} \sum_{i=1}^{n} f_i(c_i)$$

subject to $\sum_{i=1}^{n} c_i \leq mC$. Let $\hat{c}_1, \ldots, \hat{c}_n$ be values for $c_1, \ldots, c_n$, respectively, which achieve the optimum $\hat{F}$. Then we call $\hat{F} = \sum_{i=1}^{n} f_i(\hat{c}_i)$ the super-optimal utility of $A$, and $\hat{c}_1, \ldots, \hat{c}_n$ the super-optimal allocation for $A$.

To motivate the above definition, note that for any valid assignment $[(r_1, c_1), \ldots, (r_n, c_n)]$ for $A$, we have $\sum_{i=1}^{n} c_i \leq mC$. Therefore, the utility of the assignment $\sum_{i=1}^{n} f_i(c_i)$ is at most $\hat{F}$. Let $F^*$ denote $A$'s maximum utility. Then we have the following.

**Lemma V.2.** $F^* \leq \hat{F}$.

Thus, to find an $\alpha$ approximate solution to $A$, it suffices to
find an assignment with total utility at least $\alpha \hat{F}$. We note that the problem of finding $\hat{F}$ and the associated super-optimal allocation can be solved in $O(n(\log mC)^2)$ time using the algorithm from [12], since the $f_i$ functions are concave. Also, since these functions are nondecreasing, we have the following basic property.
Lemma V.3. \( \sum_{i=1}^{n} \hat{c}_i = mC. \)

In the remainder of this section, fix \( A \) to be an AA problem consisting of \( m \) servers with \( C \) resources each and \( n \) threads with utility functions \( f_1, \ldots, f_n. \) Let \([\hat{c}_1, \ldots, \hat{c}_n]\) be a super-optimal allocation for \( A \) computed as in [12]. We define the linearized version of \( A \) to be another AA problem \( B \) with the same set of servers and threads, but where the threads have piecewise linear utility functions \( g_1, \ldots, g_n \) defined by

\[
g_i(x) = \begin{cases} f_i(\hat{c}_i) x \overline{c}_i & \text{if } x < \hat{c}_i \\ f_i(\hat{c}_i) & \text{otherwise} \end{cases} \tag{1}
\]

Figure 1 shows how \( f_i(x) \) is linearized to \( g_i(x). \) The blue curve is \( f_i(x) \) and the red curve is \( g_i(x). \) We first prove the following basic property.

Lemma V.4. For any \( i \in T \) and \( x \in [0, C], f_i(x) \geq g_i(x). \)

Proof. For \( x \in [0, \hat{c}_i], \) we have

\[
f_i(x) \geq f_i(0) + \frac{\hat{c}_i - x}{\hat{c}_i} f_i(\hat{c}_i) \geq g_i(x),
\]

where the first inequality follows because \( f_i \) is concave, and the second inequality follows because \( f_i(0) \geq 0. \) Also, for \( x > \hat{c}_i, f_i(x) \geq f_i(\hat{c}_i) = g_i(x). \) Hence, the lemma holds. \( \square \)

Lemma V.4 implies that to find an assignment with total utility at least \( \alpha \hat{F} \) at Problem \( A, \) it suffices to find an assignment with total utility at least \( \alpha \hat{F} \) at the linearized problem.

B. Approximation algorithm for linearized problem

We now describe an \( \alpha \) approximation algorithm for the linearized problem. The pseudocode is given in Algorithm 1. The algorithm takes as input a super-optimal allocation \([\hat{c}_1, \ldots, \hat{c}_n] \) for \( A \) and the resulting linearized utility functions \( g_1, \ldots, g_n, \) as described in Section V.A. Variable \( C_j \) represents the amount of resource left on server \( j, \) and \( R \) is the set of unassigned threads. The outer loop of the algorithm runs until all threads in \( R \) have been assigned. During each iteration, \( U \) is the set of (thread, server) pairs such that the server has at least as much remaining resource as the thread’s super-optimal allocation. If any such pairs exist, then in line 6 we find a thread in \( U \) with the greatest utility using its super-optimal allocation. Otherwise, in line 9 we find a thread that can obtain the greatest utility using the remaining resources of any server. In both cases we assign the thread in line 12 to a server giving it the greatest utility. Lastly, we update the server’s remaining resources accordingly.

### Algorithm 1

**Input:** Super-optimal allocation \([\hat{c}_1, \ldots, \hat{c}_n], \) and \( g_1, \ldots, g_n \) as defined in Equation [1]

1. \( C_j \leftarrow C \) for \( j = 1, \ldots, m \)
2. \( R \leftarrow \{1, \ldots, n\} \)
3. while \( R \neq \emptyset \) do
4. \( U \leftarrow \{(i, j) \mid (i \in R) \land (1 \leq j \leq m) \land (\hat{c}_i \leq C_j)\} \)
5. if \( U \neq \emptyset \) then
6. \( (i, j) \leftarrow \text{argmax}_{(i, j) \in U} g_i(\hat{c}_i) \)
7. \( c_i \leftarrow \hat{c}_i \)
8. else
9. \( (i, j) \leftarrow \text{argmax}_{i \in R, 1 \leq j \leq m} g_i(C_j) \)
10. \( c_i \leftarrow C_j \)
11. end if
12. \( r_i \leftarrow j \)
13. \( R \leftarrow R - \{i\} \)
14. \( C_j \leftarrow C_j - c_i \)
15. end while
16. return \((r_1, c_1), \ldots, (r_n, c_n)\)

1) An example execution: We first give a simple example to illustrate Algorithm 1. Suppose there are 2 servers, 4 threads, and each server has \( C = 7 \) units of resource. Let \( \hat{c}_1 = 2, \hat{c}_2 = 3, \hat{c}_3 = 3, \hat{c}_4 = 6, f_1(\hat{c}_1) = 3, f_2(\hat{c}_2) = 6, f_3(\hat{c}_3) = 4 \) and \( f_4(\hat{c}_4) = 7. \) Then \( g_1(\hat{c}_1) = 3, g_2(\hat{c}_2) = 6, g_3(\hat{c}_3) = 4, g_4(\hat{c}_4) = 7. \) Table II shows how Algorithm 1 produces the assignment and allocation for the linearized problem. The algorithm runs for \( n = 4 \) iterations. In each iteration, the column labeled \((i, j)\) shows the (thread, server) pair which increases the overall utility by the largest amount, as in Line 6, 9 of Algorithm 1, column Assignment shows the final allocation of the thread in the pair, and column Allocation shows the final assignment of the thread in the pair. Initially \( C_1 = 7, C_2 = 7, \) and \( R = \{1, 2, 3, 4\}. \) In the first iteration, \( U \) consists of all (thread, server) pairs. The pair \((4,1)\) results in the largest utility increase of 7. Thus, \((i, j) = (4,1)\) in line 6 and the algorithm assigns thread \( t_4 \) to server \( s_1 \) in line 13. It allocates 6 units of resource on server \( s_1 \) to \( t_4 \) in line 7, sets \( R = \{1, 2, 3\}, C_1 = 1 \) and leaves \( C_2 \) unchanged. In the second iteration, \( U = \{(1,2), (2,2), (3,2)\} \) in line 4, in the third iteration \( U = \{(3,2)\}, \) and in the fourth iteration \( U = \emptyset, \) after which the algorithm terminates.

C. Analyzing the linearized algorithm

We now analyze the quality of the assignment produced by Algorithm 1. We first define some notations. Let \( D = \{i \in T \mid c_i = \hat{c}_i\} \) be the set of threads whose allocation in Algorithm 1 equals its super-optimal allocation, and let \( E = T - D \) be the remaining threads. We say the threads in \( D \) are full, and the threads in \( E \) are unfull. Note that full threads are
the ones computed in line 6, and unfull threads are computed in line 9.

The full threads have the same utility in the super-optimal allocation and the allocation produced by Algorithm 1. Thus, to show Algorithm 1 achieves a good approximation ratio it suffices to show the utilities of the unfull threads in Algorithm 1 are sufficiently large compared to their utilities in the super-optimal allocation. We first show some basic properties about the unfull threads.

**Lemma V.5.** At most one thread from $E$ is assigned to any server.

*Proof.* Suppose for contradiction there are two threads $t_a, t_b$ with $a, b \in E$ assigned to a server $s_k$, and assume that $t_a$ was assigned before $t_b$. Consider the time of $t_a$’s assignment, and let $S_j$ denote the set of threads assigned to a server $s_j$. We have $\sum_{i \in S_k} c_i = C$, because $a \in E$, and so $t_a$ was allocated all of $s_k$’s remaining resources in lines 10 and 14 of Algorithm 1. Also, $\sum_{i \in S_j} c_i = C$ for any $j \neq k$. Indeed, if $\sum_{i \in S_j} c_i < C$ for any $j \neq k$, then $s_j$ has more remaining resources than $s_k$, and so $t_b$ would be assigned to $s_j$ instead of $s_k$ because it can obtain more utility. Thus, we have that when $t_b$ is assigned, $\sum_{i \in T} c_i \geq \sum_{j=1}^{m} \sum_{i \in S_j} c_i = mC$. Now, $c_i \leq \hat{c}_i$ for all $i \in T$. Also, since $a, b \in E$, then $c_a < \hat{c}_a$ and $c_b < \hat{c}_b$. Thus, we have $\sum_{i \in T} \hat{c}_i > \sum_{i \in T} c_i \geq mC$, which is a contradiction because $\sum_{i \in T} \hat{c}_i = mC$ by Lemma V.3. $\square$

**Lemma V.6.** $\vert E \vert \leq m - 1$.

*Proof.* Lemma V.5 implies that $\vert E \vert \leq m$, so it suffices to show $\vert E \vert \neq m$. Assume for contradiction $\vert E \vert = m$. Then by Lemma V.6 for each server $s_k$ there exists a thread $t_a, a \in E$ assigned to $s_k$, $t_a$ receives all of $s_k$’s remaining resources, and so $\sum_{i \in S_k} c_i = C$ after its assignment. Then after all $m$ threads in $E$ have been assigned, we have $\sum_{i \in T} c_i = mC$. But since $c_a < \hat{c}_a$ for all $a \in E$, and $c_i \leq \hat{c}_i$ for all $i \in T$, we have $\sum_{i \in T} \hat{c}_i > \sum_{i \in T} c_i \geq mC$, which is a contradiction. Thus, $\vert E \vert \neq m$ and the lemma follows. $\square$

The next lemma shows that the total resources allocated to the unfull threads in Algorithm 1 is not too small compared to their super-optimal allocation. We first briefly outline the main proof idea. Note that some servers may have unallocated resources after Algorithm 1 terminates, since there may be some unfull threads which do not get their super-optimal allocations. Let $D^*$ be the set of servers containing only full threads after Algorithm 1 terminates, and let $E^*$ be the remaining servers containing some unfull threads. Recall that $C_j$ represents the unallocated resources in a server $j$. Then for any thread $i \in E$ and any server $j \in D^*$, we have $c_i \geq C_j$, because in each iteration of thread assignment we assign a thread to a server giving it the largest utility. Thus, we can show $\sum_{i \in E} c_i + \sum_{j \in D^*} C_j \geq \vert E \vert + \vert D^* \vert$. Also, we can show $\sum_{i \in E} c_i + \sum_{j \in D^*} C_j = \sum_{i \in E} \hat{c}_i$. Additionally, the total number of unfull threads and unfull servers (i.e. $\vert E \vert + \vert D^* \vert$) is equal to the total number of servers $m$, since each unfull thread is distributed to a different server in $E^*$. From this, we can derive that $\sum_{i \in E} \hat{c}_i \geq \frac{\vert E \vert}{m}$, as stated in the following lemma.

**Lemma V.7.** $\sum_{i \in E} c_i \geq \frac{\vert E \vert}{m} \sum_{i \in E} \hat{c}_i$.

*Proof.* We first partition the servers into sets $U$ and $V$, where $U = \{j \in S \mid S_j \subseteq D\}$ is the set of servers containing only full threads, and $V = S - U$ are the servers containing some unfull threads. Let $C_j = C - \sum_{i \in S_j} c_i$ be the amount of unused resources on a server $s_j$ at the end of Algorithm 1. Then $C_j = 0$ for all $j \in V$, since the unfull thread in $S_j$ was allocated all the remaining resources on $s_j$. So, we have

$$\sum_{j \in U} C_j = \sum_{j \in S_j} (C - \sum_{i \in S_j} c_i) = mC - \sum_{i \in T} c_i,$$

and so

$$\sum_{i \in T} c_i = mC - \sum_{j \in U} C_j. \quad (2)$$

Next, we have

$$\sum_{i \in U} c_i = \sum_{i \in D} \hat{c}_i + \sum_{i \in E} c_i = mC - \sum_{i \in E} \hat{c}_i + \sum_{i \in E} c_i,$$

The first equality follows because $c_i < \hat{c}_i$ for $i \in D$, and the second equality follows because $D \cup E = T$ and $\sum_{i \in T} \hat{c}_i = mC$. Combining this with the earlier expression for $\sum_{i \in T} c_i$ in Equation 2, we have

$$mC - \sum_{j \in U} C_j = \sum_{i \in U} c_i = \sum_{i \in V} c_i + \sum_{i \in E} c_i, \quad (3)$$

and so

$$\sum_{i \in U} c_i = \sum_{i \in U} c_i + \sum_{i \in E} c_i. \quad (4)$$

Now, assume for contradiction that $\sum_{i \in E} c_i < \frac{\vert E \vert}{m} \sum_{i \in E} \hat{c}_i$. Then by Equation 4 we have

$$\sum_{i \in U} c_i > \frac{m - \vert E \vert}{m} \sum_{i \in E} \hat{c}_i. \quad (5)$$

We have $\vert V \vert = \vert E \vert$, since by Lemma V.5 each server in $V$ contains only one unfull thread. Thus $\vert U \vert = m - \vert V \vert = m - \vert E \vert$. Using this in Equation 5 we have that there exists an $j \in U$ with

$$C_j \geq \frac{1}{\vert U \vert} \sum_{i \in U} C_i > \frac{1}{m} \sum_{i \in E} \hat{c}_i. \quad (6)$$

We claim that for all $i \in E, j \in U, c_i \geq C_j$. Indeed, suppose $c_i < C_j$ for some $i$. But since $C_j > c_i$, $t_i$ should be allocated to $s_j$ because it can obtain greater utility on $s_j$ than its current server, which is a contradiction. Thus, $c_i \geq C_j$ for all $i \in E$. Using this and Equation 6 we have

$$\sum_{i \in E} c_i \geq \sum_{i \in E} \hat{c}_i \geq \frac{\vert E \vert}{m} \sum_{i \in E} \hat{c}_i.$$

However, this contradicts the assumption that $\sum_{i \in E} c_i < \frac{\vert E \vert}{m} \sum_{i \in E} \hat{c}_i$. Thus, the lemma follows. $\square$

Let $\gamma = \max_{i \in E} g_i(\hat{c}_i)$ be the maximum super-optimal
utility of any thread in \( E \). The following lemma says that all of the first \( m \) threads assigned by Algorithm 1 are given their super-optimal allocations and have utility at least \( \gamma \).

**Lemma V.8.** Let \( t_i \) be one of the first \( m \) threads assigned by Algorithm 1. Then \( i \in D \) and \( g_i(c_i) \geq \gamma \).

**Proof.** To show \( i \in D \), note that the \( m \) servers all had \( C \) resource at the start of Algorithm 1, and fewer than \( m \) threads were assigned before \( t_i \). So when \( t_i \) was assigned, there was at least one server with \( C \) resource. Then \( t_i \) can obtain \( \hat{c}_i \) resource on one of these servers, and so \( i \in D \).

To show \( g_i(c_i) \geq \gamma \), suppose the opposite, and let \( j \in E \) be such that \( g_j(\hat{c}_j) = \gamma \). Since \( \hat{c}_j \leq C \), and since in \( t_i \)’s iteration there was some server with \( C \) resource, then in that iteration Algorithm 1 would have obtained greater utility by assigning \( t_j \) instead of \( t_i \), which is a contradiction. Thus, \( g_i(c_i) \geq \gamma \). \( \square \)

**Lemma V.9.** \( \sum_{i \in D} g_i(c_i) \geq m \gamma \).

The next lemma shows that for the threads in \( E \), threads with higher slopes in the nonconstant portion of their utility functions are allocated more resources.

**Lemma V.10.** For any two threads \( i, j \in E \), if \( \frac{g_i(\hat{c}_i)}{\hat{c}_i} > \frac{g_j(\hat{c}_j)}{\hat{c}_j} \), then \( c_i \geq c_j \).

**Proof.** Suppose for contradiction \( c_i < c_j \), and suppose first that \( t_i \) was assigned before \( t_j \). Then when \( t_i \) was assigned, there was at least one server with \( c_j \) or more remaining resources. We have \( \hat{c}_i > c_j \), since otherwise \( t_i \) can be allocated \( \hat{c}_i \) resources, so that \( i \not\in E \). Now, since \( \hat{c}_i > c_j \), then \( t_i \) could obtain greater utility by being allocated \( \hat{c}_j \) instead of \( c_i \) amount of resources. This is a contradiction.

Next, suppose \( t_j \) was assigned before \( t_i \). Then when \( t_j \) was assigned, there was a server with at least \( c_j \) amount of resources. Again, we have \( \hat{c}_i > c_j \). Indeed, otherwise we have \( \hat{c}_i \leq c_j \), and \( \hat{c}_j > c_j \), since \( j \in E \), and so \( t_i \) can be allocated its super-optimal allocation while \( t_j \) cannot. But Algorithm 1 prefers in line 4 to assign threads that can receive their super-optimal allocations, and so it would assign \( t_i \) before \( t_j \), a contradiction. Thus, \( \hat{c}_i > c_j \). However, this means that in the iteration in which \( t_j \) was assigned, \( t_i \) can obtain greater utility than \( t_j \), since \( g_i(\hat{c}_j) = c_j \frac{g_i(\hat{c}_i)}{\hat{c}_i} > c_j \frac{g_i(\hat{c}_j)}{\hat{c}_j} = g_j(\hat{c}_j) \), where the first inequality follows because \( \hat{c}_i > c_j \), the inequality follows because \( \frac{g_i(\hat{c}_i)}{\hat{c}_i} > \frac{g_j(\hat{c}_j)}{\hat{c}_j} \), and the second equality follows because \( \hat{c}_j > c_j \). Thus, \( t_i \) would be assigned before \( t_j \), a contradiction. The lemma thus follows. \( \square \)

The following facts are used in later parts of the proof. Facts V.11 and V.12 follow by simple manipulation, while Fact V.13 follows from the Cauchy-Schwarz inequality, and Fact V.14 is Chebyshev’s sum inequality.

**Fact V.11.** Given \( a, a', b, c > 0 \) and \( a \geq a' \), \( b \leq c \), we have \( \frac{a+b}{a+c} \geq \frac{a'+b}{a'+c} \).

**Fact V.12.** Given \( a, a', b, b' > 0 \), if \( \frac{a}{a'} \leq \frac{b}{b'} \), then \( \frac{a+b}{a'+b'} \leq \frac{b}{b'} \).

**Fact V.13.** Given \( a_1, \ldots, a_n > 0 \), we have \( \left( \sum_{i=1}^{n} a_i \right) \left( \sum_{i=1}^{n} \frac{1}{a_i} \right) \geq n^2 \).

**Fact V.14.** Given \( a_1 \geq a_2 \geq \ldots \geq a_n \) and \( b_1 \geq b_2 \geq \ldots \geq b_n \), we have \( \sum_{i=1}^{n} a_i b_i \geq \left( \frac{1}{n} \sum_{i=1}^{n} a_i \right) \left( \frac{1}{n} \sum_{i=1}^{n} b_i \right) \).

We now state a lower bound on a certain function that will be used in later parts of the proof.

**Lemma V.15.** Let \( A, d > 0 \), and \( 0 < a_1 \leq a_2 \leq \ldots \leq a_n \). Also, let \( \beta = \frac{A + \sum_{i=1}^{n} a_i z_i}{A + \sum_{i=1}^{n} z_i} \), where each \( z_i \in [0, d] \). Then

\[
\beta \geq \min_{j=1,\ldots,n} \left( \frac{A + \sum_{j=1}^{j} a_i d_i}{A + j d} - 1 \right)
\]

**Proof.** If \( a_1 \geq 1 \), then Fact V.12 implies that \( \beta \geq 1 \), and the lemma holds. Otherwise, suppose \( a_1 < 1 \). Then differentiating \( \beta \) with respect to \( z_1 \), we get

\[
\beta' (z_1) = \frac{(a_1 - 1) A + \sum_{i=1}^{n} a_i z_i}{(A + \sum_{i=1}^{n} z_i)^2}
\]

Since \( a_1 \leq \ldots \leq a_n \) and \( a_1 < 1 \), we have \( \beta' (z_1) < 0 \). Thus, \( \beta (z_1) \) is minimized for \( z_1 = d \), and we have

\[
\beta \geq \frac{A + a_1 d + \sum_{i=2}^{n} a_i z_i}{A + d + \sum_{i=2}^{n} z_i}
\]

To simplify this expression, suppose first that \( A + a_1 d / A + d \geq a_2 \).

Then we have

\[
\beta \geq \frac{A + a_1 d + \sum_{i=2}^{n} a_i z_i}{A + d + \sum_{i=2}^{n} z_i} \geq \frac{A + a_1 d + a_2 d + \sum_{i=3}^{n} a_i z_i}{A + 2d + \sum_{i=3}^{n} z_i}
\]

The second inequality follows because \( a_2 \leq \ldots \leq a_n \) and by Fact V.12. Thus, the lemma is proved. Otherwise, \( (A + a_1 d) / (A + d) > a_2 \), and so

\[
\frac{A + a_1 d + \sum_{i=2}^{n} a_i z_i}{A + d + \sum_{i=2}^{n} z_i} \geq \frac{A + a_1 d + a_2 d + \sum_{i=3}^{n} a_i z_i}{A + 2d + \sum_{i=3}^{n} z_i}
\]

We can simplify the latter expression in a way similar to above, based on whether \( (A + a_1 d + a_2 d) / (A + 2d) \leq a_3 \). Continuing this way, if we stop at the \( j \)’th step, then \( \beta \geq (A + \sum_{i=1}^{j} a_i d) / (A + j d) \). Otherwise, after the \( n \)’th step, we have \( \beta \geq (A + \sum_{i=1}^{n} a_i d) / (A + n d) \). In either case, the lemma holds. \( \square \)

Since for \( i \in E \), \( \hat{c}_i > c_i \) and by definition of \( g \), we have \( g(c_i) = \frac{g_i(\hat{c}_i)}{\hat{c}_i} \hat{c}_i \) for \( i \in E \). Algorithm 1 produces an allocation \( c_1, \ldots, c_n \) with total utility \( G = \sum_{i \in D} g_i(\hat{c}_i) + \sum_{i \in E} g_i(c_i) \).

We now prove that this allocation is an \( \alpha \) approximation to the super-optimal utility \( \hat{F} = \sum_{i \in T} f_i(\hat{c}_i) \).

**Lemma V.16.** \( G \geq \alpha \hat{F} \), where \( \alpha = 2(\sqrt{2} - 1) > 0.828 \).

**Proof.** We have \( \hat{F} = \sum_{i \in T} f_i(\hat{c}_i) = \sum_{i \in T} g_i(\hat{c}_i) \) by the
definition of the $g_i$. Thus,

$$
\frac{G}{F} = \frac{\sum_{i \in D} g_i(\hat{\epsilon}_i) + \sum_{i \in E} g_i(\hat{\epsilon}_i) + \frac{g_i(\hat{\epsilon}_i)}{|E|}}{m\gamma + \sum_{i \in E} g_i(\hat{\epsilon}_i) + \frac{g_i(\hat{\epsilon}_i)}{|E|}} \\
\geq \frac{m\gamma + \sum_{i \in E} g_i(\hat{\epsilon}_i) + \frac{g_i(\hat{\epsilon}_i)}{|E|}}{m\gamma + \sum_{i \in E} g_i(\hat{\epsilon}_i) + \frac{g_i(\hat{\epsilon}_i)}{|E|}} \\
\geq \frac{m\gamma + \sum_{i \in E} g_i(\hat{\epsilon}_i)}{m\gamma + \sum_{i \in E} g_i(\hat{\epsilon}_i) + \frac{g_i(\hat{\epsilon}_i)}{|E|}} \\
\geq \frac{m\gamma + \sum_{i \in E} g_i(\hat{\epsilon}_i)}{m\gamma + \sum_{i \in E} g_i(\hat{\epsilon}_i) + \frac{g_i(\hat{\epsilon}_i)}{|E|}}
$$

Recall that $\gamma = \max_{\epsilon_i \in E} g_i(\hat{\epsilon}_i)$. To obtain the first inequality, we have $\sum_{i \in E} g_i(\hat{\epsilon}_i) \geq m\gamma$ by Corollary \ref{cor:upper_bound_gamma} and the fact that $\hat{\epsilon}_i = \epsilon_i$ for all $i \in D$. Also, we have $\sum_{i \in E} g_i(\hat{\epsilon}_i) \leq \sum_{i \in E} g_i(\hat{\epsilon}_i)$, since $\hat{\epsilon}_i \geq \epsilon_i$ for all $i \in E$. Thus, we can apply Fact \ref{fact:submodularity_square} to the first and second expressions above, letting $\sum_{i \in E} g_i(\hat{\epsilon}_i)$ play the role of $a$, $\sum_{i \in E} g_i(\hat{\epsilon}_i)$ play the role of $b$, $\sum_{i \in E} g_i(\hat{\epsilon}_i)$ play the role of $c$, $a_i$ play the role of $a'$, $b_i$ play the role of $b'$, the second inequality follows because by Lemma \ref{lem:submodularity_square} threads $i \in E$ with larger values of $g_i(\hat{\epsilon}_i)$ also have larger values of $\epsilon_i$. Thus, we can apply Fact \ref{fact:submodularity_square} to bring the term $\sum_{i \in E} |E|/|E|$ outside the sum $\sum_{i \in E} g_i(\hat{\epsilon}_i)$. The last inequality follows because of Lemma \ref{lem:monotonicity}. Now, assume WLOG that the elements in $E$ are ordered by nonincreasing value of $\hat{\epsilon}_i$, so that $\frac{1}{\epsilon_1}, \frac{1}{\epsilon_2}, \ldots, \frac{1}{\epsilon_m}$ are in nondecreasing order. Let $E_i$ denote the first $i$ elements of $E$ in this order. For any $i \in E$, we have $g_i(\hat{\epsilon}_i) \in [0, \gamma]$. Thus, applying Lemma \ref{lem:submodularity_square} to the last expression above, letting $g_i(\hat{\epsilon}_i)$ play the role of $z_i$ and $\sum_{i \in E}\frac{|E|}{m\epsilon_i}$ play the role of $a_i$, and noting $G/F \leq 1$, we have

$$
\frac{G}{F} \geq \min_{i=1, \ldots, |E|} \left( \frac{m\gamma + \sum_{j \in E_i} \frac{\epsilon_j}{m}}{m\gamma + \sum_{j \in E_i} \epsilon_j} \right) \\
\geq \min_{i=1, \ldots, |E|} \left( \frac{m + \sum_{j \in E_i} \frac{\epsilon_j}{m}}{m + \sum_{j \in E_i} \epsilon_j} \right) \\
\geq \min_{i=1, \ldots, |E|} \left( \frac{m + \frac{\epsilon_i^2}{m}}{m + \epsilon_i} \right)
$$

The second inequality follows by simplification and because $\sum_{j \in E_i} \epsilon_j \geq \sum_{j \in E_i} \epsilon_j$ for any $i$. The last inequality follows by Fact \ref{fact:submodularity_square}. It remains to lower bound the final expression. Recall $|E| \leq m - 1$ by Lemma \ref{lem:property_E}. Treating $i$ as a real value and taking the derivative with respect to $i$, we find the minimum value is obtained at $i = (\sqrt{2} - 1)m$, for which $G/F \geq 2(\sqrt{2} - 1) = \alpha$. Thus, the lemma is proved.

\section{D. Solving the concave problem}

To solve the original AA problem with concave utility functions $f_1, \ldots, f_n$, we run Algorithm 1 on the linearized problem to obtain an allocation $c_1, \ldots, c_n$, then simply output this as the solution to the concave problem. The total utility of this solution is $F = \sum_{i \in T} f_i(c_i)$. We now show this is an $\alpha$ approximation to the optimal utility $F^\ast$.

\begin{theorem}
$F \geq \alpha F^\ast$, and Algorithm 1 achieves an $\alpha$ approximation ratio.
\end{theorem}

\begin{proof}
We have $F = \sum_{i \in T} f_i(c_i) \geq \sum_{i \in T} g_i(c_i) \geq \alpha F \geq \alpha F^\ast$, where the first inequality follows because $f_i(c_i) \geq g_i(c_i)$ by Lemma \ref{lem:concave}, the second inequality follows by Lemma \ref{lem:upper_bound_gamma} and the last inequality follows by Lemma \ref{lem:approximation_ratio}. Hence, the theorem is proved.
\end{proof}

Next, we give an example that shows our analysis of Algorithm 1 is nearly tight.

\begin{theorem}
There exists an instance of AA where Algorithm 1 achieves $\frac{\alpha}{5} > 0.833$ times the optimal total utility.
\end{theorem}

\begin{proof}
Consider 3 threads, and 2 servers each with 10 units of resource. Let $f_1(x) = \begin{cases} 
\frac{1}{5}x & \text{if } x \in [0, 5] \\
1 & \text{if } x > 5.
\end{cases}$ Also, let $f_2(x) = \frac{1}{10}x$. Suppose the first two threads both have utility functions $f_1$, and the third thread has utility function $f_2$. The super-optimal allocation is $[c_1, c_2, c_3] = [5, 5, 10]$. Algorithm 1 may assign threads 1 and 2 to different servers, with 5 units of resource each, then assign thread 3 to server 1 with 5 units of resource. This has a total utility of $2 \frac{1}{2}$. On the other hand, the optimal assignment is to put threads 1 and 2 on server 1 and thread 3 on server 2. This has a utility of 3. Thus, the ratio between the total utility achieved by Algorithm 1 and the optimal utility is $\frac{5}{8} > 0.833$. Hence, the theorem is proved.
\end{proof}

Lastly, we analyze Algorithm 1’s time complexity.

\begin{theorem}
Algorithm 1 runs in $O(mn^2 + n(\log mC)^2)$ time.
\end{theorem}

\begin{proof}
Computing the super-optimal allocation takes $O(n(\log mC)^2)$ time using the algorithm in [12]. Then the algorithm runs $n$ loop iterations, where in each iteration it computes the set $U$ with $O(mn)$ elements. Thus, the theorem follows.
\end{proof}

\section{VI. A FASTER ALGORITHM FOR CONCAVE FUNCTIONS}

In this section, we present a faster approximation algorithm for concave utility functions that achieves the same approximation ratio as Algorithm 1 in $O(n(\log mC)^2)$ time.

\textbf{Remark:} Since the faster approximation algorithm has the same approximation ratio as Algorithm 1 and lower time complexity, one may wonder why not omit Algorithm 1 and present the faster algorithm directly. Indeed, Algorithm 1 is the starting point of the faster algorithm, and more natural to understand. By analyzing Algorithm 1, we find some important properties as shown in Section V-C that result in the approximation ratio. We propose the faster algorithm by improving Algorithm 1’s time complexity while keeping these properties.

\section{A. Algorithm description}

The pseudocode of the faster approximation algorithm is shown in Algorithm 2. The algorithm also takes as input a
super-optimal allocation $\hat{e}_1, \ldots, \hat{e}_n$, which we compute as in Section V-A. It sorts the threads in nonincreasing order of $g_i(\hat{e}_i)$. It then takes threads $m+1$ to $n$ in this ordering, and sorts them again, this time in nonincreasing order of $g_i(\hat{e}_i)/\hat{e}_i$. Next, it initializes $C_1, \ldots, C_m$ to $C$, and stores them in a max heap $H$. $C_j$ represents the amount of remaining resources on server $j$. The main loop of the algorithm iterates through the threads in order. Each time it chooses the server with the most remaining resources, allocates the minimum of the thread’s super-optimal allocation and the server’s remaining resources to it, and assigns the thread to the server. Then $H$ is updated accordingly.

Algorithm 2

**Input:** Super-optimal allocation $[\hat{e}_1, \ldots, \hat{e}_n]$, and $g_1, \ldots, g_n$ as defined in Equation 1

1: Sort threads in nonincreasing order of $g_i(\hat{e}_i)$ as $t_1, \ldots, t_n$
2: Sort $t_{m+1}, \ldots, t_n$ in nonincreasing order of $g_i(\hat{e}_i)/\hat{e}_i$
3: $C_j \leftarrow C$ for $j = 1, \ldots, m$
4: Store $C_1, \ldots, C_m$ in a max-heap $H$
5: for $i = 1, \ldots, n$ do
6: $j \leftarrow \arg\max_{1 \leq j \leq m} C_j$
7: $c_i \leftarrow \min(\hat{e}_i, C_j)$
8: $C_j \leftarrow C_j - c_i$, and update $H$
9: $r_i \leftarrow j$
10: end for
11: return $(r_1, c_1), \ldots, (r_n, c_n)$

1) An example execution: We show a simple example execution to illustrate Algorithm 2. We consider the same setup as the example for Algorithm 1, namely with $m = 2, n = 4, C = 7, \hat{e}_1 = 2, \hat{e}_2 = 3, \hat{e}_3 = 3, \hat{e}_4 = 6, g_1(\hat{e}_1) = 3, g_2(\hat{e}_2) = 6, g_3(\hat{e}_3) = 4, g_4(\hat{e}_4) = 7$. Table III shows how Algorithm 2 produces the assignment and allocation of the linearized problem. Initially, the threads are sorted in order $t_4,t_2,t_1,t_3$ in lines 1 and 2. For presentation purposes, we reorder the threads as $t_1,t_2,t_3,t_4$. In other words, the newly indexed threads have $\hat{e}_1 = 6, \hat{e}_2 = 3, \hat{e}_3 = 3, \hat{e}_4 = 3, g_1(\hat{e}_1) = 7, g_2(\hat{e}_2) = 6, g_3(\hat{e}_3) = 4, g_4(\hat{e}_4) = 7$. $C_1, C_2$ are initially 7, and the algorithm runs for 4 iterations. As shown in the table III, in each iteration, the column labeled $j$ shows the index of the server with the maximum remaining resource, as in line 6 of Algorithm 2, column Allocation shows the final allocation of thread $t_j$, and column Assignment shows the final assignment of thread $t_j$.

In the first iteration, $j = 1$ in line 6 since server $s_1$ has the maximum remaining resource. The algorithm assigns thread $t_1$ to server $s_1$ in line 9, and allocates 6 units of resource on server $s_1$ to it in line 7. $C_1 = 1$ in line 8, and $C_2$ remains unchanged. The algorithm then continues this way for three more iterations, producing the final allocation and assignment shown in Table III.

B. Algorithm analysis

We now show Algorithm 2 achieves an $\alpha = 2(\sqrt{2} - 1)$ approximation ratio, and runs in $O(n(\log mC)^2)$ time. The proof of the approximation ratio uses exactly the same set of lemmas as in Section V-A, V-B and V-C. The proofs for most of the lemmas are also similar. Rather than replicating them, we will go through the lemmas and point out any differences in the proofs. Please refer to Sections V-A, V-B and V-C for the definitions, lemma statements and original proofs.

- **Lemma V-5** The proof of this lemma depended on the fact that in Algorithm 1 if we assign a second $E$ thread to a server, then all the other servers have no remaining resources. This is also true in Algorithm 2, since in line 6 we assign a thread to a server with the most remaining resources, and so if we when we assign a second $E$ thread $t$ to a server $s$ and there was another server $s'$ with positive remaining resources, we would assign $t$ to $s'$ instead, a contradiction.

- **Lemma V-7** The only statement we need to check from the original proof is that for all $i \in E$ we have $c_i \geq C_j$. But this is true in Algorithm 2 because if there were any $c_i < C_j$, line 6 of Algorithm 2 would assign thread $i$ to server $j$ instead of $i$’s current server, a contradiction. All the other statements in the original proof then follow.

- **Lemma V-8** This follows because lines 1 and 2 of Algorithm 2 show that the first $m$ assigned threads have at least as much super-optimal utility as the remaining $n - m$ threads. Also, the first $m$ threads must be in $D$, since there is always a server with $C$ resources during the first $m$ iterations of Algorithm 2. Thus, all threads in $E$ are among the last $n - m$ assigned threads, and their maximum super-optimal utility is no more than the minimum utility of any $D$ thread.

- **Lemma V-10** As we stated above, all threads in $E$ must be among the last $n - m$ assigned by Algorithm 2. That is, they are among threads $t_{m+1}, \ldots, t_n$. In line 2 these threads are sorted in nondecreasing order of $g_i(\hat{e}_i)/\hat{e}_i$. Thus, the lemma follows.

Given the preceding lemmas, we can state the approximation ratio of Algorithm 2. The proof of the theorem is the same as the proof of Theorem V-17, and is omitted.

**Theorem VI.1.** Let $F$ be the total utility from the assignment produced by Algorithm 2, and let $F^*$ be the optimal total utility. Then $F \geq \alpha F^*$.

Lastly, we analyze Algorithm 2’s time complexity.

**Theorem VI.2.** Algorithm 2 runs in $O(n(\log mC)^2)$ time.

**Proof.** Finding the super-optimal allocation takes $O(n(\log mC)^2)$ time using the algorithm in [12]. Steps 1 and 2 take $O(n \log n)$ time. Since $C$ is usually large in practice, we can assume that $\log n = O(\log mC)^2$. Each iteration of the main for loop takes $O(\log m)$ time to extract the maximum element from $H$ and update $H$. Thus, the entire for loop takes $O(n \log m)$ time. Thus, the overall...
running time is dominated by the time to find a super-optimal allocation, and the theorem follows.

Comparing Theorem VII.2 and Theorem VII.19 we see that Algorithm 2 has lower time complexity than Algorithm 1.

VII. ALGORITHM FOR NONCONCAVE FUNCTIONS

In this section, we present an approximation algorithm for the AA problem for threads with nonconcave utility functions.2 One situation where such functions arise is in cache allocation, where increasing the cache allocated for a thread beyond a certain size allows its entire working set to fit into the fastest level of cache, and leads to a large nonconcave increase in performance. As nonconcave functions pose more challenges than concave ones, our algorithm achieves an approximation ratio of $\frac{1}{\alpha}$ instead of $2(\sqrt{2} - 1)$.

The basic model we consider is the same as in Section III. We additionally assume that each utility function consists of one or more concave or convex segments. That is, for each utility function $f: [0, C] \to \mathbb{R}^\geq$, there exist $0 = b_0 < b_1 < \ldots < b_k = C$, such that $f$ is either concave or convex in the interval $[b_{i-1}, b_i]$, for $1 \leq i \leq k$. Let $s$ be the maximum number of segments in any utility function. Note that in the case of $s = C$, $f$ can be an arbitrary nondecreasing function. However, it was observed in [10] that $s$ is typically a small value, e.g., 3 in practice. Moreover, a small value of $s$ enables our algorithm to run faster.

We present an approximation algorithm, which we call AANC, to solve the AA problem in this setting. AANC follows a similar structure as Algorithm 2, with the following difference. Recall that the input to Algorithm 2 is a super-optimal allocation, which for concave utility functions can be computed by the algorithm from [12]. In the nonconcave setting, we use the fast algorithm from [10], which computes an optimal allocation $[c_1, \ldots, c_n]$ for a single server with $mC$ resources in $O(sm\alpha C\log mC)$ time, where $\alpha$ denotes the inverse Ackermann function. Thus, we define AANC to be the same as Algorithm 2, but using $[c_1, \ldots, c_n]$ as the initial input.

AANC achieves an approximation ratio of $\frac{1}{2}$. The main reason for the reduced approximation ratio is that the property that $f_i \geq g_i$ for all $i \in T$ used in Lemma VII.4 does not hold for nonconcave utility functions. Thus, while Lemma VII.16 which states that the total utility using the linearized $g_i$ functions is at least $\alpha$ times the super-optimal utility, continues to hold in the nonconcave case, it does imply that the total utility under the original nonconcave utility functions $f_i$ is also $\geq \alpha$ times the super-optimal utility. Nevertheless, we can show that even in the nonconcave setting, the number of threads which do not get their super-optimal allocation is small, and their super-optimal utilities are smaller than those of threads which do get their super-optimal allocation. Thus, the threads still obtain a large fraction of the super-optimal utility, leading to a $\frac{1}{2}$ approximation ratio.

We now analyze the quality of the assignment produced by AANC. The proof uses several results, including Lemma VIII.2 and Corollary VIII.9 which were proven in Sections VIII-A and VIII-C. The proofs of these lemmas did not rely on the concavity of the $f_i$ functions, and thus continue to hold in the nonconcave setting. Please refer to Sections VIII-A, VIII-B, and VIII-C for the definitions, lemma statements and original proofs.

We first show that the super-optimal utility of the full threads in $D$ is no smaller than that of the unfull threads in $E$.

Lemma VII.1. $\sum_{i \in D} f_i(\hat{c}_i) \geq \sum_{i \in E} f_i(\hat{c}_i)$.

Proof. Recall the definition of $\gamma = \max_{i \in E} g_i(\hat{c}_i)$ in Section VIII-C. We first claim $\sum_{i \in D} f_i(\hat{c}_i) \geq m\gamma$. This is because by the definition of function $g_i$, we have $f_i(\hat{c}_i) = g_i(\hat{c}_i) = g_i(\hat{c}_i)$ for any thread $i \in D$. Thus, by Corollary VIII.9 we have $\sum_{i \in D} f_i(\hat{c}_i) \geq m\gamma$.

Next, we claim $\sum_{i \in E} f_i(\hat{c}_i) \leq m\gamma$. We have $\forall i \in E, g_i(\hat{c}_i) \leq \gamma$. Also, $|E| \leq m - 1$ by Lemma VIII.6. Thus, since $f_i(\hat{c}_i) = g_i(\hat{c}_i)$, we have $\sum_{i \in E} f_i(\hat{c}_i) = \sum_{i \in E} g_i(\hat{c}_i) \leq (m - 1)\gamma$.

Combining the above, we have $\sum_{i \in D} f_i(\hat{c}_i) \geq \sum_{i \in E} f_i(\hat{c}_i)$.

AANC produces an allocation $c_1, \ldots, c_n$ with total utility $F = \sum_{i \in D} f_i(\hat{c}_i) + \sum_{i \in E} f_i(\hat{c}_i)$. We now prove that this allocation is an $\frac{1}{2}$ approximation to the super-optimal utility $\hat{F} = \sum_{i \in D} f_i(\hat{c}_i)$. Lemma VII.1. $\sum_{i \in E} f_i(\hat{c}_i) \leq \sum_{i \in D} f_i(\hat{c}_i)$.

Proof. We have

$$\frac{F}{\hat{F}} = \frac{\sum_{i \in D} f_i(\hat{c}_i) + \sum_{i \in E} f_i(\hat{c}_i)}{\sum_{i \in D} f_i(\hat{c}_i) + \sum_{i \in E} f_i(\hat{c}_i)} \geq \frac{\sum_{i \in D} f_i(\hat{c}_i)}{\sum_{i \in D} f_i(\hat{c}_i) + \sum_{i \in E} f_i(\hat{c}_i)} \geq \frac{1}{2}.$$}

The last inequality follows since $\sum_{i \in D} f_i(\hat{c}_i) \geq \sum_{i \in E} f_i(\hat{c}_i)$ by Lemma VII.1. Thus, the lemma is proved.

Recall that $F^*$ is the optimal total utility. Also, $F^* \leq \hat{F}$, by Lemma VIII.9. Thus, combining these with Lemma VII.2 we have the following bound on the approximation ratio of AANC.

Theorem VII.3. $F \geq \frac{1}{2} F^*$.

Next, we give an instance of AA which shows our analysis of AANC is nearly tight.

Theorem VII.4. For any $\epsilon > 0$, there exists an instance of AA such that AANC achieves $\frac{1}{2} + \epsilon$ times the optimal total utility.

Proof. Consider $2m - 1$ threads, and $m$ servers each with $m$ units of resource. Let

$$f_1(x) = \begin{cases} x & \text{if } x \in [0, 1] \\ 1 & \text{if } x > 1, \end{cases}$$

and

$$f_2(x) = \begin{cases} 0 & \text{if } x \in [0, m - 1] \\ 1 & \text{if } x > m - 1. \end{cases}$$
Suppose the first \( m \) threads all have utility functions \( f_1 \), and the remaining \( m - 1 \) threads have utility functions \( f_2 \). The super-optimal allocation is \( \hat{c}_1 = \cdots = \hat{c}_m = 1, \hat{c}_{m+1} = \cdots = \hat{c}_{2m-1} = m \). AANC may assign threads 1, 2, \ldots, \( m \) all to different servers, with 1 unit of resource each, and then assign threads \( m+1, \ldots, 2m-1 \) also to different servers with \( m-1 \) units of resource each. This achieves a total utility of \( m \). On the other hand, the optimal assignment is to put threads 1, 2, \ldots, \( m \) on server 1 and threads \( m+1, \ldots, 2m-1 \) on servers 2, 3, \ldots, \( m \), respectively. This has a total utility of \( 2m - 1 \). Thus, AANC achieves an approximation ratio of 
\[
\frac{m}{2m-1} \in \left[ \frac{1}{2}, \frac{1}{2} + \epsilon \right]
\] for a sufficiently large \( m \).

Lastly, we analyze AANC’s time complexity.

**Theorem VII.5.** AANC runs in \( O(sm\text{Co}(mC)(\log mC)^2) \) time, where \( s \) is the maximum number of concave or convex segments in any utility function \( f_i \), and \( \alpha(mC) \) is the inverse Ackermann function.

**Proof.** Finding the super-optimal allocation takes \( O(sm\text{Co}(mC)(\log mC)^2) \) time using the algorithm in [10]. Note that \( \alpha(mC) \leq 4 \) for all realistic values of \( m \) and \( C \). Similar to the proof of Theorem VI.2, the overall running time of AANC is dominated by the time to find a super-optimal allocation, and the theorem follows.

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**VIII. Experimental Evaluation**

In this section we experimentally evaluate the performance of our algorithms using both synthetic and real-world utility functions. We compare the total utility our algorithms achieve with the super-optimal (SO) utility, which is at least as large as the optimal utility. We also compare the algorithm with several simple but practical heuristics we name UU, UR, RU and RR\( ^3 \). The UU (uniform-uniform) heuristic assigns threads in a round robin manner to the servers, and allocates the threads assigned to a server the same amount of resources. UR (uniform-random) assigns threads in a round robin manner, and allocates threads a random amount of resources on each server. RU (random-uniform) assigns threads to random servers, and equally allocates resources on each server. Finally, RR (random-random) randomly assigns threads and allocates them random amounts of resource.

Our simulation experiments use threads with synthetic random utility functions generated according to various probability distributions as described below.

To generate the random concave utility functions, we fix an amount of resource \( C \) on each server, and set the value of the utility function at 0 to be 0. We generate two values \( v \) and \( w \) according to the distribution \( H \), conditioned on \( w < v \), and set the value of the utility function at \( C \) to \( v \), and the value of \( C \) to \( v + w \). Then we apply the PCHIP interpolation function from Matlab to the three generated points to produce a concave utility function.

\( ^3 \)To the best of our knowledge, we are the first to study the thread assignment and resource allocation problems in a unified context, and we are unaware of other algorithms in the literature which can be directly compared to our algorithms. Thus, to evaluate our algorithms’ performance, we compare them to several simple but practically useful heuristics.

To generate the random nonconcave utility functions, we also set the function to be 0 at allocation 0. We generate a value \( b \in (0, C) \) according to a distribution \( \epsilon \), then divide \([0, C]\) into two segments \([0, b]\) and \([b, C]\). Then we generate two concave functions in these segments using a similar approach as for generating a concave utility function. In all the experiments on synthetic utility functions, we set the number of servers to be \( m = 8 \) and the resource size to be \( C = 100 \), and test the effects of varying different parameters. One parameter is \( \beta = \frac{n}{m} \), the average number of threads per server. The results in the following sections show the average performance from 1000 random trials.

**A. Concave Utility Functions**

We first look at the performance of Algorithm 2 with concave utility functions; we omit testing Algorithm 1, since it achieves the same approximation ratio as Algorithm 2. We note that for \( m = 8, n = 100, C = 100 \), Algorithm 2 terminated in 9 ms. This can be further improved by implementing the algorithm in C instead of Matlab and using better data structures.

1) **Uniform and normal distributions:** We first look the total utility obtained by Algorithm 2 compared to those obtained by the SO, UU, UR, RU and RR algorithms on threads with concave utility functions generated according to the uniform and normal distributions. We set the mean and standard deviation of the normal distribution to be 10 and 20, respectively.

Figures 2(a) and 2(b) show the average over 1000 random runs of the ratio of Algorithm 2’s total utility to the utilities of the other algorithms, for \( \beta \) varying between 1 and 15. The behaviors for both distributions are similar. Compared to SO, Algorithm 2’s utility ratio never drops below 0.99, showing...
that it always achieves at least 99% of the optimal utility. The ratios of Algorithm 2’s total utility compared to those of UU, UR, RU and RR are always above 1, showing that it always perform better than the simple heuristics. For small values of $\beta$, UU performs well. Indeed, for $\beta = 1$, UU achieves the optimal utility because it places one thread on each server and allocates it all the resources. UR does not achieve optimal utility even for $\beta = 1$, since it allocates threads random amounts of resources. RU and RR may allocate multiple threads per server, and also do not achieve the optimal utility. As $\beta$ grows, the performance of the heuristics gets worse relative to Algorithm 2. This is because as the number of threads grows, it becomes more likely that some threads have very high maximum utility. These threads need to be assigned and allocated carefully. For example, they should be assigned to different servers and allocated as much resources as possible. The heuristics likely fail to do this, and hence obtain low performance. The performance of UR and RR, as well as those of UU and RU converge as $\beta$ grows. This is because both random and uniform assignments assign the threads roughly evenly between the servers for large $\beta$. Also, the performance of UU and RU are substantially better than UR and RR, which indicates that the way in which resources are allocated has a larger effect on performance than how threads are assigned, and that uniform allocation is generally better than random allocation.

2) Power law distribution: We now look at the performance of Algorithm 2 using threads with utility functions generated according to the power law distribution. Here, each value $x$ has a probability $\lambda x^{-\mu}$ of occurring, for some $\mu > 1$ and normalization factor $\lambda$. Figure 3(a) shows the effect of varying $\beta$ while fixing $\mu = 2$. Here we see the same trends as those under the uniform and normal distributions, namely that Algorithm 2 always performs very close to optimal, while the performance of the heuristics gets worse with increasing $\beta$. However, the rate of performance degradation is faster than with the uniform and normal distributions. This is because the power law distribution with $\mu = 2$ is more likely to generate threads with very different maximum utilities. These threads must be carefully assigned and allocated, which the heuristics fail to do. For $\beta = 15$, Algorithm 2 is 3.9 times better than UU and RU, and 5.7 times better than UR and RR.

Figure 3(b) shows the effect of varying $\mu$, using a fixed $\beta = 5$. Algorithm 2’s performance is nearly optimal. In addition, the performance of the heuristics improves as $\mu$ increases. This is because for higher values of $\mu$, it is unlikely that there are threads with very high maximum utilities. So, since the maximum utilities of the threads are roughly the same, almost any even assignment of the threads works well. Despite this, we still observe that UU and RU perform better than UR and RR. This is because when the threads are roughly the same, the
Average performance ratio

Our algorithm achieves the lowest performance for

Figure 4(b) shows the effect of varying \( \gamma \) when \( \beta = 5 \) and \( \theta = 5 \). Our algorithm achieves the lowest performance for \( \gamma = 0.8 \), when we achieve 92% of the super-optimal utility. The four heuristics also perform worst for this value. For \( \gamma \) close to 0 or 1, all the heuristics perform well, since these correspond to instances where either \( h \) or \( \ell \) is very likely to occur, so that almost all the threads have the same maximum utility. Lastly, we consider the effect of varying \( \theta \). Here, as \( \theta \) increases, the difference between the high and low utilities becomes more evident, and the effects of poor thread assignments or misallocating resources become more serious. Hence, the performance of the heuristics decreases with \( \theta \). Meanwhile, Algorithm 2 always achieves over 92% of the optimal utility.

B. Nonconcave Utility Functions

In this section we evaluate the performance of AANC on threads with nonconcave utility functions. We note that for \( m = 8, n = 100, C = 100 \), AANC terminates in 9.47 seconds. Our implementation of AANC again used Matlab, and we believe the running time of the algorithm can be substantially improved using more optimized code. However, we leave a more efficient implementation as future work.

1) Uniform distribution: We first consider the total utility obtained by AANC compared to SO, UU, UR, RU and RR on threads with nonconcave utility functions generated according to the uniform distribution. Specifically, for each thread we generate a nonconcave utility function with two concave segments, where each segment is generated using the approach described earlier in this section. Figures 5 shows the average ratio of AANC’s total utility versus the utilities of the other algorithms, for \( \beta \) varying between 1 to 15. The behaviors are similar to that for Algorithm 2 using concave utility functions generated according to the uniform distribution. Compared to SO, our performance never drops below 0.98, which is slightly lower than the average ratio of 0.99 achieved by Algorithm 2. Likewise, AANC performs similarly to Algorithm 2 for other distributions. Due to space limitations we omit an in-depth discussion.

2) Real-world utility functions: We also use real-world utility functions derived from 10 SPEC CPU benchmarks: gap, parser, applu, twolf, mcf, fma3d, gzip, crafty, apsi, swim.
in the performance of AANC versus SO, UU, RU, and RR for different resource sizes using the first set of 8 real-world utility functions (i.e., gap, applu, twolf, mcf, mma3d, gzip, and crafty).

| Server resource size | SO | UU | RU | RR |
|----------------------|----|----|----|----|
| 4                    | 2.85 | 1.87 | 12.13 | 2.04 |
| 8                    | 1.53 | 1.32 | 1.40 | 1.32 |

In multicore processors with multiple levels of cache, the first level L1 cache is typically privately owned by each core, while the L2 cache is shared by different cores.

In our data, we do not show the benchmarks’ utility with zero resource. Since having sufficient cache is crucial to performance, we make the simplifying assumption that the utility of a benchmark allocated zero L2 cache is zero. While this assumption does not hold in practice, we use it to facilitate a more uniform comparison with synthetic utility functions.

In this section, we further generalize our algorithms to a setting where each server has multiple types of resources, and each thread’s utility function depends on all the resource types. An example of this situation is in cloud computing, where a virtual machine’s (i.e., thread’s) performance is affected by both the number of CPU cores and amount of memory it is allocated. We adapt our algorithms for the single resource setting to the multi-resource one, and demonstrate that the new algorithm achieves good empirical performance.

The model we use is similar to the one presented in Section III, and we only point out the differences. We assume there are $d$ types of resources, and refer to the $i$th type of resource as a type-$i$ resource. Each server has $C_i$ amount of type-$i$ resource, where $C_i$ is a positive integer. Each thread $t_i$ has a $d$-dimensional utility function $f_i(x_1, \ldots, x_d)$, where $x_j \in [0, C_j]$ is the amount of type-$j$ resource $t_i$ is allocated. We assume that each thread’s utility function is non-decreasing and concave. An assignment is given by a vector $[(r_1, c_{1,1}, \ldots, c_{1,d}), \ldots, (r_n, c_{n,1}, \ldots, c_{n,d})]$, indicating that each thread $t_i$ is allocated $c_{i,j}$ amount of type-$j$ resource for $j = 1, \ldots, d$ on server $s_{r_i}$.

We now present the algorithm for the AA problem with multiple resource types, which we call AAMR. Similar to Definition V.1, we first define the super-optimal utility and super-optimal allocation of the problem under multiple resource types.

Definition IX.1. Given an instance $A$ of the AA problem with $m$ servers each with $C_i$ amount of type-$i$ resource for $i = 1, \ldots, d$, and $n$ threads with utility functions $f_1, \ldots, f_n$, consider the quantity

$$F = \max_{c_{i,j} \in [1, C_i], j \in [1, d]} \frac{1}{n} \sum_{i=1}^{n} f(c_{i,1}, \ldots, c_{i,d})$$

subject to $\sum_{i=1}^{n} c_{i,j} \leq mC_j$ for $j = 1, \ldots, d$. Let $\hat{c}_{i,j}, i \in [1, n], j \in [1, d]$ be values for $c_{i,j}, i \in [1, n], j \in [1, d]$, respectively, which achieve the optimum $\hat{F}$. Then we call $\hat{F} = \sum_{i=1}^{n} f(\hat{c}_{i,1}, \ldots, \hat{c}_{i,d})$ the super-optimal utility of $A$, and $\hat{c}_{i,j}, i \in [1, n], j \in [1, d]$ the super-optimal allocation for $A$. AAMR first finds the super-optimal allocation of the problem with multiple resource types, and then uses the allocation as an indicator to assign threads and allocate resource to the assigned threads. Finding the super-optimal allocation is equivalent to finding an optimal allocation which maximizes the total utility of $n$ threads on a single server, given the threads’ utility functions $f_1, \ldots, f_n$, and a resource capacity utility functions from the second set of 8 CPU benchmarks (gap, applu, twolf, mcf, mma3d, gzip, apsi, swim). The first 4 benchmarks have concave utility functions and the last 4 ones have nonconcave utility functions. We set the number of threads to be $n = 8$, representing the following 8 benchmarks. Table V shows the ratio of AANC’s total utility compared to the utilities of SO, UU, RU, UR, and RR for different resource sizes $C$. The behaviors are similar with that for the first set of 8 benchmarks.

### Table IV

| Server resource size | SO | UU | RU | RR |
|----------------------|----|----|----|----|
| 4                    | 2.85 | 1.87 | 12.13 | 2.04 |
| 8                    | 1.53 | 1.32 | 1.40 | 1.32 |
| 16                   | 1.02 | 1.36 | 1.19 | 1.40 |
Note that \( \in_k \) each thread's utility function is \([0, C_i]\) for a type-\(i\) resource. We use a dynamic programming algorithm which we call DPSOPT to solve this problem. Given \( x_1 \in [0, \hat{C}_1], \ldots, x_n \in [0, \hat{C}_n] \) and \( k \in [1, n] \), let \( F_k(x_1, \ldots, x_d) \) be the maximum utility of the first \( k \) threads when they are allocated \( x_1 \) amount of type-1 resource, \( \ldots, x_d \) amount of type-\(d\) resource. Then we have

\[
F_k(x_1, \ldots, x_d) = \max_{i=1,\ldots,n} \{ f_k(z_1, \ldots, z_d) + F_{k-1}(x_1 - z_1, \ldots, x_d - z_d) \}.
\]

Algorithm 3 Pseudocode for AAMR

1. **Input:** Utility functions \( f_1, \ldots, f_n \), and super-optimal allocation \( \hat{c}_{i,j} \) for \( i = 1, \ldots, n \) and \( j = 1, \ldots, d \) returned by DPSOPT
2. \( C_{i,j} \leftarrow C_i \) for \( i = 1, \ldots, m \) and \( j = 1, \ldots, d \)
3. \( R \leftarrow \{1, \ldots, n\} \)
4. while \( R \neq 0 \) do
5. \( U \leftarrow \{ (i, j) \mid (i \in R) \land (1 \leq j \leq m) \land (\hat{c}_{i,j} \leq C_{j,1}) \land \ldots \land (\hat{c}_{i,d} \leq C_{j,d}) \} \)
6. if \( U = \emptyset \) then
7. \( (i, j) \leftarrow \arg\max_{i \in U} f_i(\hat{c}_{i,1}, \ldots, \hat{c}_{i,d}) \)
8. \( c_{i,k} \leftarrow \hat{c}_{i,k} \) for \( k = 1, \ldots, d \)
9. else
10. \( (i, j) \leftarrow \arg\max_{(i,j) \in U} f_i(\min(C_{j,1}, \hat{c}_{i,1}), \ldots, \min(C_{j,d}, \hat{c}_{i,d})) \)
11. \( c_{i,k} \leftarrow \min(C_{j,k}, \hat{c}_{i,k}) \) for \( k = 1, \ldots, d \)
12. end if
13. \( r_i \leftarrow j \)
14. \( R \leftarrow R - \{i\} \)
15. \( C_{i,k} \leftarrow C_{i,k} - c_{i,k} \) for \( k = 1, \ldots, d \)
16. end while
17. return \( (r_1, C_{1,1}, \ldots, C_{1,d}), \ldots, (r_n, C_{n,1}, \ldots, C_{n,d}) \)

Fig. 8. Average performance ratio of Algorithm AAMR versus SO, UU, RU, UR, RR as a function of \( \beta \) under two resource types.
and $O(n(\log mC)^2)$, respectively. In addition, we presented an algorithm with approximation ratio $\frac{1}{2}$ for threads with nonconcave utility functions, and an algorithm for concave utility functions with multiple resource types. Lastly, we tested our algorithms on multiple types of threads, and found that our algorithms always achieve at least 92% of the optimal utility, and typically over 98% of the optimal utility. Our utility is up to 9 times better than those of several heuristic methods.

In this work we considered homogeneous servers each with the same amount of resources. We are interested in extending this model to accommodate heterogeneous servers with different capacities. In addition, our algorithms are currently centralized, and all decisions are made by a single scheduler process. To scale the AA problem to larger system settings, we would like to consider distributed versions of our algorithms, where assignments and allocations are made concurrently by multiple schedulers.

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