Effects of size on the electrical and optical properties of InGaN-based red light-emitting diodes

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ABSTRACT

We investigated the effects of size on electrical and optical properties of InGaN-based red light-emitting diodes (LEDs) by designing rectangular chips with different mesa lengths. Larger chips exhibited lower forward voltages because of their lower series resistances. A larger chip helped to realize a longer emission wavelength, narrower full-width at half maximum, and higher external quantum efficiency. However, temperature-dependent electroluminescence measurements indicated that larger chips are detrimental to applications where high temperature tolerance is required. In contrast, a smaller red LED chip achieved a high characteristic temperature of 399 K and a small redshift tendency of 0.066 nm K\(^{-1}\), thus showing potential for temperature tolerant lighting applications.

InGaN-based blue/green light-emitting diodes (LEDs) have become increasingly prevalent in illumination and display applications, such as interior/exterior lighting and display backlights. However, their performance declines rapidly with the emission wavelength, especially in the red region. This decline is related to the content of In in InGaN quantum wells (QWs). InGaN layers with a high In content have a high density of defects and a strong quantum-confined Stark effect (QCSE).

Much effort has been devoted to improving InGaN-based red LEDs with approaches including InGaN quantum dots, a semipolar InGaN buffer layer, lattice-matched InGaN/ScAlMgO\(_4\)(0001) templates, and InGaN/GaN nanowires. Our high-temperature InGaN growth technique realized deep-red electroluminescence (EL) with 740-nm emission. We have also proposed several strain-compensation approaches to reduce defects, such as AlN/AlGaN barriers, hybrid QW structures, and different thicknesses of GaN templates. Recently, a very low forward voltage of 2.45 V at 20 mA was demonstrated in a 665-nm InGaN-based red LED on a \(\beta\)-Ga\(_2\)O\(_3\) substrate.

Device design and optimization are also critical for improving the performance of InGaN-based red LEDs. Previous works have demonstrated that chip size has a significant influence on the efficiency droop of InGaN-based blue LEDs. Different chip sizes result in different temperature tolerance. LEDs ranging from the micro-scale to large sizes have shown the current crowding effect and carrier recombination on the edge surfaces of the device. However, InGaN-based red LEDs have not been studied and optimized as much as blue ones, which have a long history.

This work examines the effect of size on device performance using different rectangular chips on the same InGaN-based red LED wafer. Current–voltage (I–V) curves were used to characterize the forward voltages and reverse leakage currents. We systematically investigated the EL characteristics at different currents, including the peak wavelength, full-width at half-maximum (FWHM), external quantum efficiency (EQE), and integrated intensity. The results demonstrate that the chip size can be adjusted to control the performance of InGaN-based red LEDs.

InGaN-based red LED epitaxial wafers were grown on c-plane patterned sapphire substrates by metalorganic vapor-phase epitaxy (MOVPE) in a single-wafer horizontal reactor at 100 kPa. The epitaxial structures of the InGaN-based red LEDs were reported in previous works. We fabricated red LEDs using our standard processes. Indium tin oxide (ITO) films were deposited as a transparent conductive layer and subjected to two-step annealing to form Ohmic contacts with p-GaN. LED mesas were obtained by standard photolithography and inductively coupled plasma etching to expose the n-type layer.
Finally, electrodes based on Cr/Ni/Au (50 nm/20 nm/200 nm) were deposited on both ITO and n-type layers as contact pads. Electrical pumping of all red LED chips was carried out at a probe station using a semiconductor parameter analyzer. The EL characteristics were measured at temperatures ranging from 295 K (room temperature, RT) to 373 K. The temperatures were measured from the sample-holding stage of the probe station.

Figure 1(a) shows the pattern of the rectangular LED chips. The chips had a constant mesa width of 250 μm, while the mesa lengths were 350 μm, 450 μm, 550 μm, and 650 μm. Thus, the active areas were calculated to be 0.0755 mm², 0.0985 mm², 0.1215 mm², and 0.1445 mm², respectively. The active area is the mesa area of each LED chip. Figures 1(b)–1(e) show EL images of the red LED chips with different sizes at 20 mA. All LEDs showed homogeneous EL emission, good current spreading, and no significant current crowding. However, many dark spots were observed in the emission area, which might be related to threading dislocations and trench defects.15

We studied the electrical properties of LEDs with different mesa lengths. Figure 2(a) shows a typical I–V curve of a red LED with a mesa length of 650 μm at RT. The curve at the forward bias displayed a sharp onset voltage and then exponentially increased with the injection current, exhibiting the standard behavior of a p–n junction diode. Figure 2(b) shows the forward voltages at 20 mA as a function of the inverse of the active area. The forward voltages obtained with different mesa lengths were found to scale linearly with the inverse of the active area. The forward voltages at 20 mA were calculated to be 0.0755 mm², 0.0985 mm², 0.1215 mm², and 0.1445 mm², respectively. The active area is the mesa area of each LED chip. Figures 1(b)–1(e) show EL images of the red LED chips with different sizes at 20 mA. All LEDs showed homogeneous EL emission, good current spreading, and no significant current crowding. However, many dark spots were observed in the emission area, which might be related to threading dislocations and trench defects.15

The EL spectra were measured at 5–100 mA at RT. The average peak wavelengths and FWHMs are shown in Figs. 3(a) and (b), respectively. Significant blueshifts of approximately 50 nm were observed between the peak wavelengths at 5 mA and 100 mA. The amount of blueshift for the red LEDs was much higher than that of the blue or green ones,26 demonstrating that red LEDs suffer from significant QCSE.

The peak wavelengths of smaller LEDs had blueshifts compared to those of larger ones at the same currents. This means that a higher current density accelerates the screening of QCSE by free carriers. The LED size is also crucial for the peak wavelength in high-In-content InGaN-based red LEDs, and so we must consider the In content in QWs and the chip size together. Generally, the fundamental transition energy of a QW could be reduced by increasing the In content or decreasing the screening of the internal electric field in a QW. When designing red QWs with a fixed thickness for a particular red emission wavelength at a specific operating current, larger chips need a lower In content in the active regions than those of smaller chips because larger chips exhibit less screening of the internal electric field (QCSE) due to a lower current density. Since the lower In content has a merit of maintaining the crystal qualities of the LED structures, a larger chip is preferable for better device performance.

Interestingly, the peak wavelengths were identical for different chip sizes at the same current density, which means that the amount of QCSE only depends on the current density. Therefore, our device fabrication did not influence the residual strain and localized states in the InGaN QWs. This situation differs from those of nanostructure-based LEDs with partially relaxed InGaN QWs.27

The FWHMs dropped quickly to the minimum values (approximately 60 nm) in the low current range in Fig. 3(b). All chip sizes indicated the same behavior, which was similar to that in previous reports.5,15,16 The FWHMs of smaller chips were narrower than those of larger ones at the same currents. This means that a higher current density accelerates the screening of QCSE by free carriers. The LED size is also crucial for the peak wavelength in high-In-content InGaN-based red LEDs, and so we must consider the In content in QWs and the chip size together. Generally, the fundamental transition energy of a QW could be reduced by increasing the In content or decreasing the screening of the internal electric field in a QW. When designing red QWs with a fixed thickness for a particular red emission wavelength at a specific operating current, larger chips need a lower In content in the active regions than those of smaller chips because larger chips exhibit less screening of the internal electric field (QCSE) due to a lower current density. Since the lower In content has a merit of maintaining the crystal qualities of the LED structures, a larger chip is preferable for better device performance.

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of larger chips at low current below 20 mA, which is attributed to the rapid saturation of the emission from deep localized states in the InGaN active region for the smaller chips. The minimum values depended on the LED chip size, and a larger chip had a smaller value. After the minimum values, each FWHM increased with the current. The size dependencies of the minimum FWHM and the FWHM increment originated from the effect of heat generation on the InGaN active region. The current densities of smaller chips are higher than those of larger chips, and so the temperatures of smaller ones will be higher, resulting in the continuous increment of FWHM after the minimum value. In contrast, heat generation in a larger LED can be suppressed. Thus, the FWHM of 650-μm chips had the smallest minimum around 60 nm and increased more slowly than those of smaller chips.

We operated a 650-μm chip under pulsed operation with a duty cycle of 20%. The results indicated a constant FWHM of approximately 59 nm at currents of 40–100 mA. Robin et al. reported that the FWHM is vital to achieving pure red emission. Therefore, fabricating larger chips is recommended because they are expected to have narrower FWHMs.

We also investigated the effects of the size on the EL integrated intensity of red LEDs. Figures 4(a)–4(c) show the EL integrated intensities and their peak wavelengths at currents of 20 mA, 30 mA, and 60 mA. At 20 mA [Fig. 4(a)], the LEDs with shorter mesa lengths exhibited higher EL intensities and shorter peak wavelengths. However, the LEDs with longer mesa lengths indicated higher EL intensities at 30 mA and 60 mA. These results are attributed to the current density and the efficiency droop, as shown in Fig. 4(d).

All the maximum EQE values occurred around 26 A/cm². The current density was just 26 A/cm² for the smallest chips with a mesa length of 350 μm at 20 mA, while those of the other chips were below 26 A/cm². Therefore, the smaller chips showed higher EL intensities due to the higher EQEs. At 30 mA, the current density of the chips with a mesa length of 550 μm was around 26 A/cm² at the EQE maxima. Thus, the EQEs of all chips were located around the maximum, leading to a similar EL intensity profile to the EQE profile.

At 60 mA, the current densities of all chips exceeded 26 A/cm². Therefore, the larger chips with lower current densities showed higher EL intensities. The inset also shows that the maximum EQE values were higher for the larger chips, indicating that a larger chip has a merit of realizing a higher EQE value.

Generally, the carrier loss due to surface recombination increases with the decreasing chip size down to micro-LEDs, which causes a significant decrease in EQE, even at the same current density. However, the EQE values in Fig. 4(d) were found to be independent of the chip sizes at the same current density except for the maximum EQE values. This indicates that the electron–hole recombination on the surface can be almost negligible with these relatively large chip sizes.

Figure 4(e) shows the temperature dependence of the normalized EL integrated intensity from 295 K (RT) to 373 K. The LED chips were operated at a constant current of 100 mA. The EL integrated intensities dropped down with the temperatures for all LEDs, which is usually called the thermal droop. An LED with a shorter mesa length exhibited a smaller temperature dependence of the EL intensity although the LED had the lowest EQEs at 100 mA. The thermal droop of the EL intensity is described by

$$ I = I_{T=295K} \exp \left( - \frac{T - 295 K}{T_0} \right), $$(1)

where $I$ is the normalized EL integrated intensity, $I_{T=295K}$ is the EL integrated intensity at 295 K, $T$ [K] is the stage temperature, and $T_0$ [K] is the characteristic temperature.

The characteristic temperature is a standard quantitative parameter to evaluate the temperature dependence. A larger characteristic temperature implies a weaker temperature dependence. From the fitting of the experimental results, the values of $T_0$ were measured to be 399 K, 367 K, 348 K, and 319 K for the chips with mesa lengths of 350 μm, 450 μm, 550 μm, and 650 μm, respectively. The reduction behavior of $T_0$ with the increasing mesa length is due to Shockley–Read–Hall (SRH) recombination. SRH recombination becomes effective at higher temperature but can be suppressed at higher current density.
Therefore, the EL integrated intensities dropped as the temperature increased, but less so at higher current densities. Consequently, the smallest chips, which worked at the highest current density, showed the highest characteristic temperature compared to other larger chips. The characteristic temperature was 399 K at 100 mA for the smallest chip, which is comparable to those reported for InGaN-based green LEDs and AlGaN-based red LEDs.3,33

Figure 4(f) shows the temperature dependence of the peak wavelength at a constant current of 100 mA. Redshifts were observed at the peak wavelengths for all chips as the temperature changed. The redshift coefficients from the linear fittings were 0.066 nm K\(^{-1}\), 0.071 nm K\(^{-1}\), 0.078 nm K\(^{-1}\), and 0.091 nm K\(^{-1}\) for the chips with mesa lengths of 350 μm, 450 μm, 550 μm, and 650 μm, respectively. These data indicate that smaller chips had smaller redshift coefficients. This tendency can be explained by the energy distribution of carriers.3,33

On the other hand, smaller chips had merits of a high characteristic current density. These characteristics are beneficial for InGaN-based red LEDs. Longer emission wavelength, narrower FWHM, and higher EQE at high current density (carrier density), carriers in smaller chips would make the peak wavelength show a larger blueshift, and the larger blueshift could compensate more for the redshift originated from the device area. Furthermore, a larger chip resulted in a higher transition probability of moving to higher energy levels. As a result, more percentages of carriers in smaller chips would move to higher energy levels, making the energy distribution of carriers higher than that in larger chips. The higher energy distribution of carriers in smaller chips would make the peak wavelength show a larger blueshift, and the larger blueshift could compensate more for the redshift due to the bandgap shrinking. Therefore, we finally observed the smallest redshift coefficient from the smallest chip. In the case of AlInGaN-based red LEDs, the redshift coefficients for the chip sizes of 300 × 300 μm\(^2\) and 500 × 500 μm\(^2\) were measured to be 0.142 nm K\(^{-1}\) and 0.148 nm K\(^{-1}\) at a pulsed current of 100 mA, respectively.31,32 These redshift coefficients were almost twice of that for our smallest chip, demonstrating that InGaN-based red LEDs are proper candidates for temperature tolerant lighting applications.

In summary, we investigated the effects of size on the performance of InGaN-based red LEDs by designing rectangular chips with different mesa lengths on the same LED wafer. Larger chips exhibited lower forward voltages at 20 mA due to their lower series resistance originated from the device area. Furthermore, a larger chip resulted in a longer emission wavelength, narrower FWHM, and higher EQE at high currents. These characteristics are beneficial for InGaN-based red LEDs. On the other hand, smaller chips had merits of a high characteristic temperature of 399 K and a small redshift coefficient of 0.066 nm K\(^{-1}\). These data suggest a potential for temperature tolerant lighting applications by reducing the size of InGaN-based red chips.

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