Truncated Microstrip Patch Antenna with DGS Based on Double Looped CSRR Arrays for Enhancement of Bandwidth

V.G. Ajay1, Thomaskutty Mathew2, N.V.Krishna Prasad3, M.S.S.R.K.N.Sarma4,
1School of Technology and Applied Sciences, M. G. University Regional Centre, Edappally, Kochi, Kerala, India
2,3,4Department of Physics, School of Technology, GITAM University, Bengaluru Campus, India

ABSTRACT
This paper presents a novel method for the design of truncated microstrip patch antenna with Defected Ground Structure (DGS) based on Double Looped Complementary Split Ring Resonator (CSRR) arrays for enhancement of bandwidth. The proposed antenna is suitable for WLAN/Wi-Fi, RFID applications. A truncated microstrip patch antenna without DGS was initially designed for a resonant frequency of 2.86GHz having a bandwidth of 95.6MHz. By integrating a new Double Looped CSRR array (DLCSRR) structure on the ground plane, resonant frequency of the antenna is shifted to 2.47GHz and also the bandwidth is enhanced to 202.5MHz. By implementing DLCSRR array structure on the ground plane size reduction and bandwidth enhancement of 52.79% is achieved. The performance of truncated microstrip patch antenna with DGS based on Double Looped CSRR array structure was also compared by replacing conventional proximity fed microstrip patch antenna with the same physical size. The conventional proximity coupled microstrip patch antenna is designed for a resonant frequency of 2.26GHz with a lower bandwidth of 23.3MHz. The antennas were designed using CST simulation software. The measurement results show a good agreement with simulated results

Keyword: Metamaterials, Double Looped Complementary Split Ring Resonator, Microstrip patch antennas

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1. INTRODUCTION
Microstrip patch antenna are commonly used in wireless communication due to its reduced size, low cost low profile and compatibility with integrated circuits. Some of the advantages of microstrip antennas are lower cost of fabrication, compact, and lower sensitivity towards manufacturing tolerance [1]. Microstrip patch antennas have some inherent limitations such as smaller impedance bandwidth, low gain and single band operation [2,3]. Microstrip patch antennas for practical applications usually demanded size reduction and enhancement of bandwidth. One method to improve the bandwidth is to provide thicker substrate in between patch and the ground plane so as to increase the separation between them[4]. Other miniaturization methods applied on Microstrip patch antennas include Defected Ground Structure(DGS) on the ground plane[5,6], providing slots on patch, or combination of both[7], usage of high permittivity dielectric substrate etc[8]. For wideband communication systems microstrip patch antennas with DGS have been used nowadays. Defected ground structure (DGS) was discovered initially by Park et al., in 1999. DGS is accomplished by etching off some simple shape from the ground plane [9]. The development of microstrip antennas with DGS is detailed in [10-13]. The defected shape has to be changed for better performance. DGS structures includes square, octagonal, rectangular, concentric ring and also combined structures have been used [14]. The circular geometry of Split Ring Resonator (SRR) was proposed by Pendry et.al.,in [15].
Afterwards a quantitative methodology for analyzing the effective permeability and permittivity of metamaterials was proposed by Smith et al. [16] in the year 2000 where the unit cell analyzed was square SRR. Later on the detailed study on design, fabrication and testing of DNG metamaterials of square SRR was conducted by Zilkowski in 2003 [17].

In this paper, truncated microstrip patch antenna with DGS based on Double Loop Complementary Split Ring Resonator (CSRR) arrays for enhancement of bandwidth is presented. The advantages of proximity coupled feeding method include higher bandwidth, better impedance matching and lower spurious radiation. In the proximity coupled feeding method, radiating patch is positioned on the top of the upper substrate and feed line is positioned in between two dielectric substrates [18]. Electromagnetic field coupling occurs between feed network and the patch. Higher bandwidth is the main advantage of this feeding method as compared to other feeding mechanisms. Bandwidth enhancement is obtained by using double looped structure in the ground plane. Bandwidth enhancement is achieved by etching this double looped CSRR array structure from the ground plane of the proposed truncated microstrip patch antenna. Some techniques used to enhance the bandwidth are summarized below.

The feed line is proximity-coupled with the patch antenna, which is printed on a superstrate will enhance the bandwidth [19]. Bandwidth enhancement is also achieved by Identical Dual Patch Microstrip Antenna with Air-Gap (IDMA) by increasing the overall thickness of microstrip patch anenna, and this method also provides air gap to reduce the effective permittivity [20]. Another method of bandwidth enhancement is achieved by varying the length of the dipole element [21]. The bandwidth of microstrip antenna is also increased by proximity fed method with stub loading at different locations [22]. Another method of increasing the bandwidth of rectangular microstrip patch antenna is to provide some slots in the non radiating area of the same patch [23]. Bandwidth enhancement is also achieved by cutting slots, so as to obtain the required hexagonal patch [24]. In the proposed work, proximity coupled feeding method is carried out with radiation patch which is placed on the top of the upper substrate and feed line is positioned in between the two substrates.

2. Antenna Design and Simulation Results

2.1. Conventional Proximity coupled microstrip patch antenna.

The proposed geometry of the conventional proximity coupled microstrip patch antenna is depicted in Figure 1. The antenna structure is designed using FR-4 substrate with thickness 1.6, εr = 4.4 and loss tangent is 0.02. Figure 2 shows the simulated variation of return loss with frequency characteristics of the proposed antenna. The resonant frequency of the antenna is 2.26 GHz. The optimized design parameters of the antenna using CST Microwave studio are WS1=80mm, WS2=40mm, LS2=40mm, LS1=80mm, feed length=40mm and width of the feed line = 2.4mm. Figure 3 represents the simulated surface current plot of the antenna at 2.26GHz. Figure 1 represents the simulated values of conventional proximity coupled microstrip patch antenna.

Bandwidth is calculated from the S11 plot (-10dB return loss bandwidth). The difference between upper cutoff frequency (2.2714GHz) and lower cutoff frequency (2.2481GHz) is 0.0233GHz so bandwidth is 23.3MHz. From Table 1, we can infer that bandwidth is narrow.

Figure 1. Geometry of the conventional proximity coupled microstrip patch antenna.
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Figure 5. Simulated return loss characteristics of the proposed antenna resonating at 2.86 GHz.

Figure 6. Surface current plot of the antenna (a) at 2.81GHz and (b) at 2.91GHz.

Table 2. Simulated values of proximity coupled truncated microstrip patch antenna without DGS.

| Sr.No | Parameters                   | Value    |
|-------|------------------------------|----------|
| 1     | Shape of ground              | Without DGS |
| 2     | Shape of patch area          | Truncated |
| 3     | Return loss(dB)              | -17.44   |
| 4     | Frequency(GHz)               | 2.86     |
| 5     | Bandwidth(MHz)               | 95.6     |
| 6     | Bandwidth(%)                 | 3.38     |
| 7     | Directivity(dBi)             | 7.21     |

Bandwidth is calculated from the S11 plot (-10dB return loss bandwidth). The difference between upper cutoff frequency (2.9115GHz) and lower cutoff frequency (2.8159GHz) is 0.0956GHz so bandwidth is 95.6MHz. By comparing Table 1 and Table 2, we can infer that and bandwidth is increased and also by comparing Figure 1 and 4, we can infer that the patch size is reduced.

2.3. Proximity coupled Truncated microstrip patch antenna with DGS based on Double Looped CSRR arrays for enhancement of Bandwidth.

The truncated microstrip patch antenna with DGS based on Double Looped CSRR array structure gives better results as compared to truncated microstrip patch antenna without DGS. The truncated microstrip patch antenna with DGS based on Double Looped CSRR arrays is also fabricated using two FR4 substrates with $\varepsilon_r = 4.4$ and thickness 1.6mm. Geometrical representation of the antenna with DLCSRR structure is depicted in Figure 7. The substrate and patch dimensions of the proposed antenna is same as that of the previous antenna structure without DGS. The dimensions of DLCSRR structure are as follows (unit: mm), $LS_1=LS_2=12.72$, $g=0.3$ and the width of both inner and outer loops are 0.5mm each. The return loss characteristics are depicted in Figure 8. The current distribution of the antenna at lower and upper resonant frequencies are depicted in Figure 9. Table 3 represents the simulated values of truncated microstrip patch antenna with DGS based on Double Looped CSRR arrays.
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Figure 7. Geometry of the truncated microstrip patch antenna with DGS based on Double Looped CSRR arrays.

Figure 8. Simulated variation of return loss with frequency of the truncated microstrip patch antenna with DGS based on Double Looped CSRR arrays resonating at 2.47 GHz.

Figure 9. Surface current distribution of the proposed truncated microstrip patch antenna with DGS based on Double Looped CSRR arrays (a) at lower resonant frequency and (b) at upper resonant frequency.

Bandwidth is calculated from the S11 plot (-10dB return loss bandwidth). The difference between upper cutoff frequency (2.6456GHz) and lower cutoff frequency (2.4431GHz) is 0.2025GHz, so bandwidth is ...
202.5MHz. By comparing Table 2 and Table 3, we can infer that 52.79% bandwidth enhancement is achieved with considerable reduction in antenna size.

Table 3. Simulated values of proximity coupled truncated microstrip patch antenna with DGS based on Double Looped CSRR arrays

| Sr.No | Parameters                   | Value   |
|-------|------------------------------|---------|
| 1     | Shape of ground              | DLCSRR  |
| 2     | Shape of patch area          | Truncated |
| 3     | Return loss(dB)              | -36.49  |
| 4     | Frequency(GHz)               | 2.47    |
| 5     | Bandwidth(MHz)               | 202.5   |
| 6     | Bandwidth(%)                 | 8.16    |
| 7     | Directivity(dBi)             | 6.67    |

3. RESULTS AND ANALYSIS

The truncated microstrip patch antenna without and with DGS based on Double Looped CSRR arrays was fabricated and measurements were taken in the laboratory. The fabricated prototypes of the proposed antennas are illustrated in Figure 10 and Figure 11. The radiation pattern of the proposed antennas are measured in anechoic chamber and the return loss characteristics are measured using Agilent PNA series Network analyzer N5230A(10MHz to 50 GHz). Figure 12 and Figure 13 represent the characteristics of simulated and measured values of return loss variation with frequency of the antenna without DGS and with DGS based on Double Looped CSRR arrays. A small variation arises between simulated and measured values because in the simulation, the antenna is excited using a waveguide port, but practically the antenna is excited using the SMA connector. The connector loss has an effect on the response of the antennaThe simulated and measurement results of the proposed truncated microstrip patch antenna with DGS based on Double Looped CSRR arrays shows that the bandwidth enhancement is achieved as compared with other two structures. Figure 14 represents the measured radiation pattern of truncated microstrip patch antenna with DGS based on Double Looped CSRR arrays at 2.6GHz. Comparison chart of the proximity coupled truncated microstrip patch antenna without and with DGS based on Double Looped CSRR arrays is shown in Table 4. The simulated and measured values of gain w.r.t. proximity coupled truncated microstrip patch antenna without DGS are 5.69dB and 4.21dB and truncated microstrip patch antenna with Double Looped CSRR arrays are 4.21dB and 4.03dB respectively.

Figure 10. Prototype of the proposed truncated microstrip patch antenna without DGS.

Figure 11. Prototype of the proposed truncated microstrip patch antenna with DGS based on Double Looped CSRR arrays.
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From Table 4, the measured results indicate 63.06% percentage increase in bandwidth along with size reduction is achieved with truncated microstrip patch antenna with DGS based on Double Looped CSRR arrays as compared to the truncated microstrip patch antenna without DGS.

Several simulation studies are conducted on truncated microstrip patch antenna at different positions and combinations of different CSRR structures in the ground plane. As per the comparison results, the proximity fed truncated microstrip patch antenna without DGS and with DGS based on three DLCSRRs provides optimized results. A comparison between the proposed work and published work is tabulated in Table 5. As per the comparison, the proposed Truncated microstrip patch antenna with DGS based on Double Looped CSRR arrays achieves bandwidth enhancement.

Table 5. Comparison chart of the proposed antenna and published papers

| Reference no.         | Bandwidth enhancement in percentage |
|-----------------------|-------------------------------------|
| Reference no. [21]    | 12 %                                |
| Reference no. [22]    | 23.6 %                              |
| Proposed Truncated Microstrip Patch Antenna with DGS Based on Double Looped CSRR Arrays for Enhancement of Bandwidth | 52.79 % |

4. CONCLUSION

Truncated microstrip patch antenna with DGS based on Double Looped CSRR arrays has been designed, fabricated and compared its performance with truncated microstrip patch antenna without DGS. From the simulated and measured results, it is observed that the truncated microstrip patch antenna with DGS based on Double Looped CSRR arrays achieved the percentage increase bandwidth of 52.79% and 63.06% respectively as compared to truncated proximity coupled microstrip patch antenna without DGS. The proposed antenna is suitable for WLAN/Wi-Fi and RFID applications.

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