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Power Scalable Bi-Directional DC-DC Conversion Solutions for Future Aircraft Applications

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Abstract: With the introduction of the more electric aircraft, there is growing emphasis on improving overall efficiency and thus gravimetric and volumetric power density, as well as smart functionalities and safety of an aircraft. In future on-board power distribution networks, so-called high voltage DC (HVDC, typically +/-270VDC) supplies will be introduced to facilitate distribution and reduce the associated mass and volume, including harness. Future aircraft power distribution systems will also very likely include energy storage devices (probably, batteries) for emergency back up and engine starting. Correspondingly, novel DC-DC conversion solutions are required, which can interface the traditional low voltage (28 V) DC bus with the new 270 V one. Such solutions presently need to cater for a significant degree of flexibility in their power ratings, power transfer capability and number of inputs/outputs. Specifically, multi-port power-scalable bi-directional converters are required. This paper presents the design and testing of such a solution, addressing the use of leading edge wide-band-gap (WBG) solid state technology, especially silicon carbide (SiC), for use as high-frequency switches within the bi-directional converter on the high-voltage side.

Keywords: DC-DC converters; multi-port dual-active bridge (DAB) converter; wide-band-gap (WBG) semiconductors; silicon carbide (SiC) MOSFETs; power converter

1. Introduction

The use of 115VAC 400 Hz and 28VDC power networks is a historical feature of avionic electrical power generation and distribution systems. The AC power is used directly for high power loads, such as starting, and is then rectified and conditioned to supply the bus power of +28VDC distributed to aircraft control systems, flight decks, and entertainment systems. However, there has been a pronounced movement within the aerospace industry to shift towards cleaner, more efficient and lower maintenance aircraft design as a result of the emergence of high fuel prices, the global warming problem, and high operating costs. The electrification of the aircraft to replace hydraulic or pneumatic functions with electrical ones is one of the prime movers in this field, as in the concepts of the More and All electric aircrafts [1–3]. This refers in particular to the replacement by electric actuators of complex aircraft hydraulic actuator systems, thereby dramatically reducing weight, maintenance costs, fuel consumption, footprint of carbon dioxide, and operating costs. Furthermore, the removal of pneumatic engine bleed systems makes it possible to run the engine more effectively and thus to save additional fuel.

The replacement of hydraulic and pneumatic systems with electric actuators and systems greatly increases the total electrical power requirements for an aircraft, requiring new approaches to the safe and intelligent delivery of aircraft power. To achieve the higher power ratings in a feasible way,
novel enhanced aircraft Electrical Power Distribution System (EPDS) architectures investigated over recent years include smart power management systems, characterized by the presence of at least one high-voltage dc bus (HVDC, +/-270 V) and a low-voltage DC bus (LVDC, typically, 28 V), as illustrated very summarily, for instance, in Figure 1. Such architectures require the presence of HVDC/LVDC bi-directional DC-DC converters to interconnect and manage power transfer between the two bus levels, also enabling the addition of energy storage devices. Due to the abundance and variety of loads and still partly undefined power ratings (e.g., batteries) here, the focus is on a solution which enables a high degree of flexibility in relation to power scaling. In particular, a solution is pursued, in which a basic DC-DC converter power cell can be paralleled a number of times with control and supervision functionalities carried out by a unique central board. The prime drivers of the design are of course efficiency and power density, with an eye also to solutions meeting the single-fault tolerance expectations typical of avionic solutions. The design and test results of both single power cell and parallel operation are presented.

![Figure 1](image)

**Figure 1.** Generic illustration of future aircraft electrical network, including an high voltage dc (HVDC) power distribution bus: dedicated bi-directional DC-DC converters interface the 270 and 28 V buses; inverters cater for the AC loads (e.g., motors in pumps, actuators). Additional ports in the DC-DC converters may also be used to interconnect storage devices, with different voltage levels.

2. 270-28 V Bi-Directional Converter Design

The dual-active-bridge (DAB) topology was chosen among different solutions, for the following main advantages in this context [4,5]:

- 1st order dynamics and current generator equivalent characteristics, which enable straightforward parallel-ability and thus: (a) overall power scalability; (b) in-built redundancy and guarantee of reduced power operation capability; (c) overall efficiency optimization taking into account maximum efficiency versus load values of individual converter cells;
- easy extension to multi-port realization to interconnect, for instance, to storage devices [6];
- Zero-Voltage-Switching (ZVS) turn on of the power devices over a broad load range;
- suitability for use of planar magnetics design with possibility of integrated magnetics solutions;
intrinsic current limitation in case of output short-circuit fault without the need for additional limiters or breakers.

DAB converter theory and operation is amply covered in literature already (see [2,3,6–8], for example) and so, here, the treatment of fundamental aspects is limited to some essential points of relevance to the subsequent discussion. For simplicity, we refer to the 270 V bus side as the input and the 28 V side as the output, but the discussion holds equal if the two are interchanged. The DAB consists of two full H-Bridges interconnected through a transformer, Figure 2a. The switches in diagonal pairs of either H-bridge are always turned on and off jointly and, within each bridge, each diagonal pair has 50% duty ratio. Control of output current delivery is achieved by introducing a phase-shift between the operation of the switches in the two H-bridges; with a series inductance all referred to the primary side, as in Figure 2a, the transformer secondary side gets directly connected to the output capacitor and so, the output voltage value is imposed back onto the primary scaled by the transformation ratio; the algebraic sum of input voltage and primary-reflected output voltage determines the voltage falling across the series inductor \(L_C\) during the phase-shift interval. Therefore, \(L_C\) can be effectively used as the current control element and its stored energy helps achieve turn-on soft switching of the power devices by means of resonance phenomena with the parasitic capacitance of the switches during the transitions. Both the power value and flow direction can be controlled by intervening on the value of the phase-shift \(\Phi\) between the driving signals of the input and output H-bridges.

Figure 2. Main features of Dual-Active-Bridge converter: circuit schematic of the converter topology based on SiC MOSFETs or the primary and Si MOSFETs for the secondary side (a) and summary of output power delivery as a function of input voltage and phase-shift (b).

According to the fundamental model and neglecting losses, the power flow can be expressed by:

\[
P_{DC} = \frac{V_{IN} \cdot V_O}{na_sL_C} \Phi \left(1 - \frac{\Phi}{\pi}\right) \tag{1}
\]

where \(V_{IN}, V_O\) are the values of the input and output DC voltages, \(n\) is the transformer turns ratio, \(\omega_s = 2\pi f_s\) (\(f_s\) switching frequency), and \(\Phi\) is the phase-shift (in radians). Equation (1) corresponds to a parabolic power delivery as a function of the phase-shift, with maximum power capability influenced by the value of input voltage, as summarized in Figure 2b considering a realistic variable input voltage range for the intended application.

2.1. Power Cell Design

Here, the intended nominal power rating of the brick power cell is 1.2 kW: more bricks can be connected in parallel to scale the power up. The primary (270 V) and secondary (28 V) side power cells are implemented using 650 V SiC MOSFETs (SCT2120AFC, ROHM Ltd., Kyoto, Japan) and 60 V
Opti-MOS (IPA032N06N3 G, Infineon Technologies, Munich, Germany), respectively. The choice of SiC devices for the primary (i.e., high voltage) side, as opposed to Si ones, is mainly motivated by the following reasons and advantages:

- Possibility to use a MOSFET type transistor as opposed to an IGBT, which allows for faster switching, removes the need for anti-parallel free-wheeling diode connection, and enables for more symmetrical operation in the forward and reverse power flow directions;
- Higher switching frequency capability with contained impact on efficiency, enabling the achievement of enhanced power density figures;
- Better temperature stability and higher temperature capability, which yield both more temperature independent efficiency levels over the intended operational range and favor long term reliability;
- Smaller overall intrinsic capacitances, which allow to achieve turn-on ZVS operation down to lower load values.

The gate drivers are bespoke designed, using non-symmetrical drive input voltages for the SiC transistors on the primary side of −4 to +20 V and −4 to +15 V for the low voltage Si MOSFETs on the secondary side. Whereas a non-symmetrical drive voltage is a strict functional requirement of SiC MOSFETs, the choice to also apply an unsymmetrical driving voltage for the secondary-side Si transistors merely responds to cost containment targets, by enabling the design and production of a single gate-drive circuit, which can be adapted for both transistor types simply by replacing two ICs and one resistor in the circuit, with identical footprint. It should be noted moreover, that applying a somewhat higher (i.e., closer to zero) off-state bias voltage to the Si MOSFETs also allows to achieve faster switching transitions, with slightly reduced losses. Whereas the impact on overall efficiency is contained, the electro-thermal stress reduction in the transistor is interesting. For reference, the gate driver circuit schematic is shown in Figure 3. Insulation at gate-driver level is achieved by means of optical couplers and signal transmission between control board and power cell is by means of optical fibers, ideal for avoiding electro-magnetic interference issues in reliability critical applications.

![Figure 3. Schematic of gate driver circuit, featuring a unified design for both primary and secondary side transistors.](image)

The magnetics are the other core components of the converter: the transformer provides galvanic isolation and voltage matching, the voltage across the series of its leakage inductance and the external inductor determines the current waveform flowing through the circuit. Since the transformer turns-ratio is relatively low, planar design of transformer and inductor are feasible and greatly interesting for increasing power density and improve thermal management; the components, shown in Figure 4, were custom designed and manufactured externally based on in-house specification. It is worth
noting that the inductor could be realized as leakage inductance of the transformer, in the form of integrated magnetics. However, since its value is a key parameter of the converter control and dynamic characteristics, as well as of the ZVS load range, keeping the two components separate enables easier design optimization and better transformer thermal management. The switching frequency was chosen to be 100 kHz to start with and the transformer turns ratio was 19:2. After fixing the switching frequency and the turn ratio values, the maximum power and ZVS load range depend on the series inductor and the controllable phase-shift. Hence, an inductance value capable to satisfy the power constraints needs to be selected.

**Figure 4.** Photograph of planar magnetic components: heat-sinked inductor, (a), and transformer, (b).

### 2.2. Control Design

The goal of the control strategy is to keep the DC bus voltage constant at the desired voltage reference (28 V) in both forward and backward power flow, adjusting the phase-shift between the primary and secondary bridge. Starting from the fundamental reduced model of the DAB converter shown in Figure 5 along with the representative voltage and current waveforms illustrating basic operation and control of the converter, a state-space-average (SSA) model of the system was developed.

**Figure 5.** Dual-active bridge (DAB) basic equivalent state-space average model (a), and characteristic voltage and current waveforms illustrating the control principle (b).

The open loop dynamic characteristics of the converter are shown in Figure 6, in terms of its Bode-plot gain and frequency Bode-plots. It is important to underline that the converter DC gain,
bandwidth and phase-margin are load-dependent, an aspect which needs to be duly taken into account when closing the loop.

\[
\Phi = 1 - \sqrt{1 - \frac{8nLcV_o}{V_lR_c}} = 1 - \sqrt{1 - \frac{8nLcI_c}{V_l}} \tag{2}
\]

Figure 6. Open-loop Bode-plots of gain and phase for the power converter cell.

The chosen control system block diagram is shown in Figure 7. It consists of DC bus voltage control loop with a feed-forward current loop. The measured voltage \( V_O \) is compared with the reference voltage (28 V) and the error is fed through a PI controller to generate the desired phase-shift between the primary and secondary voltage square-waves. A feed-forward phase-shift compensation is added to make the system response faster. Based on the analytical model of the converter, the PI parameters are chosen to obtain the desired bandwidth with a proper phase margin. The phase-shift \( \Phi \) depends non-linearly on load resistance \( R_c \) as:

![Diagram](image.png)

Figure 7. Converter control scheme with both a feedback and a feed-forward loop.
Since the switching frequency and the inductance are known, the output current and the input voltage are acquired by the sensors, it is possible to estimate the proper phase-shift during the working operations and let the PI controller manage only the small variations. Simulation results for the closed-loop response with the introduced control are summarized in Figure 8a, while in Figure 8b the converter step response for different start-up power levels is reported. It is clear that load dependence of the dynamics characteristics is significantly contained with the chosen approach.

![Figure 8. Dynamic response characterization of the converter (simulation results): closed-loop Bode plots (a) and system step response at different power levels (b).](image)

The design phase also addressed EMI filtering on the 270 V side, ensuring compliance of standard avionic requirements (e.g., MIL-STD-704F) with a standard and compact damped LC filter design.

### 2.3. Experimental Characterisation

Figure 9 shows the engineered TRL6 level DC-DC converter, including control platform and flight-like enclosure, with details of connectors and wiring. Figure 10a shows some representative voltage and current waveforms at about 50% load (600 W) and Figure 10b summarizes measured efficiency data, still at 600 W, as a function of the input voltage in the range 230–290 V. Efficiency over load in the forward and backward power flow direction is reported in Figure 11: as can be seen, in the design and engineering of the converter, attention was taken to ensure high performance at relatively low loads; such approach is dictated by the actual mission profile of the converter, which foresees low-load operation as the most frequent condition, with the requirement to deliver the full power capability limited only to very short time durations. Therefore, it is very important to consider energy (as opposed to power) efficiency; that is, the integral over time of the power losses at the various load conditions to maximize overall aircraft efficiency. Moreover, it should be noted that due to the intended parallel operation of the converters, more units can be made to work at an optimized efficiency level even when the full capability of one single converter is not exhausted: for instance, if the total power demand is 900 W, it is more efficient to run tow cells in parallel than a single cell. The above represent a novel approach to design efficient power conversion systems, which will become increasingly important as the electrification level increases. It is also worth noting here, that bi-directional operation is mainly requested for enabling the possibility to use batteries connected on the lower-voltage side to be used as power source during some abnormal operational regimes or to implement regenerative energy storage functionalities. Therefore, the forward power transfer direction is still to be regarded as the primary one for the characterization for the converter.
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Figure 9. Photographs of developed hardware: single DC-DC converter cell (a); view of cased converter and control board, with details of wiring, and connectors (b); external view of cased unit (c).

Figure 10. In (a), transformer primary and secondary voltage waveforms (top) and series inductor current (bottom); in (b), efficiency and detail of average voltage and current values.
It should be noted that, although some ringing due to parasitic inductance is visible on the secondary side voltage waveform, no snubbers were eventually used in the converter implementation due to the Si MOSFETs being fully avalanche rated by design and not giving signs of any degradation over extended test periods for the parasitic energies involved in the switching transitions here. Indeed, the transistors are never driven into avalanche up to full load.

Some examples of closed-loop dynamic response are reported in Figure 12. Tests on the prototype confirmed the anticipated benefits of the chosen control approach, with consistent performance for both directions of power flow: the presence of the additional feed-forward loop is profitable and enables the converter to promptly manage large and rapid changes of power demand without unacceptable rising or falling of the voltage on the 28 V bus.

![Figure 11](image1.png)

**Figure 11.** Measured converter efficiency in the forward and backward power transfer mode.

![Figure 12](image2.png)

**Figure 12.** Scope snapshots of output voltage variation in the presence of abrupt and large step changes in the load (±5 A in (a) and ±15 A in (b)), without (top) and with (bottom) feed-forward loop, respectively.

### 3. Converter Parallelising for Power Scalability

To ensure a modular system architecture, with all its associated benefits, the parallel operation of more converters was further considered. The basic straightforward parallel connection of two converter units directly linked to the load is illustrated with the help of Figure 13. Due to its current source
dynamic characteristics, paralleling of DAB converters is relatively straightforward and bi-directional converter (BDC) units are theoretically capable of working together without any additional control [9]. However, as soon as an unbalance arises, there is no way to equally split the power among the two modules without control. That is clearly shown in the results of Figure 14a,b. Moreover, a potential drawback is related to the bi-directional nature of the DAB converter: an issue could arise if the balance is established with one converter that works in regenerative configuration, as per experimental results of Figure 14c,d. In such a worst-case scenario, the paralleling of two units is not only pointless but even potentially destructive. For those reasons, a technique is needed for proper interconnections of BDC units.

Therefore, in this configuration, the aim of the control was still to regulate the load voltage, but also to ensure proper sharing of power delivery between the two modules. The corresponding equivalent control scheme in this case is illustrated in Figure 15. The methodology employed here consists in decoupling the control loops: one converter, acting as the master unit, ensures control of the output voltage to a constant value with the modality described in the previous section; the other, acting as a slave unit, is in charge of power sharing and operates in current control mode. The output current of each converter is sensed and the information is shared via a dedicated communication bus. The voltage controller in the slave unit is disabled and the master output current is fed-back as the set-point of the slave feedback-loop to determine the phase-shift. The feed-forward loop can also be added with a similar approach for both modules.
Figure 15. Structure of closed-loop system for parallel operation with current compensator.

Figure 16 shows the results obtained on the lab-prototypes for different power requests, after the implementation of this method. The tests confirm that the management of the energy is realized in good agreement with simulations and the output voltage is well regulated. The main advantage of the proposed solution is the fully controllable load sharing, which can be achieved with great accuracy in a simple and straightforward manner; the main limitation to its deployment is the need to exchange information between converters, which can become problematic in the case of multiple parallel modules if fast dynamic response is required. Loss of the communication-link or failure of the master unit would result in system shut-down.

Droop control is also a well-known practice used in power systems to share power among different generators. This concept has been recently proposed for other kinds of applications, such as speed control for integrated modular motor drives or modular DC/DC converter for smart transformers [10,11]. In this case, the droop control, illustrated in Figure 15, could be a suitable solution to enable parallel connection and to allow power sharing among the DABs; moreover, it does not require any communications between the BDC modules. The concept is to add a virtual resistor ($K_{\text{droop}}$) into the feedback loop, which drops the internal voltage set-point ($V_{o,\text{ref}}$) as a linear function of the output current ($I_o$)

$$V_{o,\text{ref}} = V_{o,\text{ref}}^* - K_{\text{droop}} I_o$$

Through this strategy, each converter exhibits a self-balancing characteristic, so it is not necessary for the single unit to be aware of the other elements in the system. The undesired effect of a steady-state error on the regulated voltage could be reduced or completely removed by another PI regulator, as also depicted in Figure 17.
The results in Figure 18 show each converter working independently with the proposed voltage droop method, which intrinsically equalizes the power flow between the two BDC units connected in parallel to a resistive load. As the ratio of the droop coefficients is directly related to the amount of current flowing in each module, an asymmetrical power distribution is also possible simply adopting different virtual resistors for the single units (Figure 18b,c). In the system under analysis, the outer loop cut-off frequency was around one-third that of the inner loop.

4. Conclusions

This paper has presented the design and development of a solution for implementing power-scalable bi-directional DC-DC conversion in future aircraft power networks [12,13]. The solution is based on the use of a dual-active bridge topology, which can easily be extended to multiple ports versions when the need to interface additional elements, such as batteries for storage, emerges [6,14]. In addition, the topology is suitable for the use of novel semiconductor technologies on the high-voltage side; in particular, silicon carbide (SiC) MOSFETs can yield important gains in switching frequency capability and converter performance optimization in terms of efficiency and thermal management requirements [14,15]. In the future, the possibility to develop bespoke modules will enhance the potential for disruptive progress in the integration level that can be realistically achieved [16].

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