Isolation Circuits Based on Metamaterial Transmission Lines for Multiplexers

*(Invited Paper)*

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Abstract

Multiplexers based on isolation circuits made of metamaterial lines are proposed and studied. The new approach provides unique advantageous features beneficial to system designer. For instance, there is no need to modify the filters used in multiplexers. Also, the design process is straightforward. In this paper, two types of multiplexers based on metamaterial isolation circuits are presented, and their operation concepts are explained. Also, theories and design process of isolation circuits are presented to help readers design and fabricate proposed multiplexers. For verifying the concepts, two types of triplexers and two types of quadruplexers are designed and fabricated. All filters used in the multiplexers are commercial surface acoustic wave filters. The measured results are well matched with the simulation results.

**Key Words:** Composite Right/Left Handed, Double-Lorentz, Isolation Circuit, Multiplexer, Transmission Line.

I . Introduction

Many modern communication systems are required to handle multiple frequency bands. If a base station for a mobile phone can deal with two or three communication standards, the price of its construction will be lowered with concomitant benefits for customers. Multiband is already intrinsic to cell phone designs for handling cellular, global positioning system (GPS), and WiFi bands. In order to support such multiband systems, a multiplexer is necessary if one antenna covers all the designated spectra and thus becomes an essential component. For multiplexers, many design concepts have been investigated and categorized by star junction type and manifold junction type [1-3]. The first star junction type multiplexer is based on synthesizing singly terminated channel filters [4]. After one decade, another method to synthesize doubly terminated filters for star junction type multiplexers was proposed [5]. In the recent approach [6], a general polynomial model is used for filters, and filters are connected with a complex load. The rapid development of manifold junction type multiplexers was caused by the advent of satellite communication systems [7]. In the initial stage, the analytic approach to design the filters, having proper interaction with other filters on the same manifold, was investigated [8]. However, recent manifold junction type multiplexers are mostly designed with optimization methods with the increased computer power [9]. In both star junction and manifold junction type multiplexers, modification of filters and a complex optimization process are generally required. However, the use of a commercial filter without modification, and a simple multiplexer design method are occasionally necessary for a system designer. The simplest multiplexer satisfying such demand is shown in Fig. 1(a). The diplexer is made of two isolation circuits, and each isolation circuit consists of a conventional right-handed transmission line (RH TL) connected to a filter. An isolation circuit isolates itself from the input port at the center frequency of the other channel. Hence the signal passes through one isolation circuit while it is rejected by the other. This procedure cannot be extended simply to a multiplexer containing three or more channels. Since a conventional TL can provide one phase response at a target frequency, an isolation circuit can be designed to have the isolation characteristic at only one frequency.

If a TL can have a multiple phase property, it can realize a multiband isolation circuit and result in a channel extension of the multiplexer based on isolation circuits.
This characteristic has been found in a metamaterial TL. A composite right/left-handed (CRLH) metamaterial TL has a dual-band characteristic [10]. A double-Lorentz (DL) TL has six parameters, and it satisfies six conditions for a tri-band characteristic [11].

In this paper, two types of multiplexers based on isolation circuits are presented. One consists of isolation circuits using same type of TLs, and it can be categorized as the star junction type. On the other hand, the other type uses both RH TLs and multiband TLs for making isolation circuits, and its junction shape is similar to a manifold junction. First, the concepts of multiplexers are explained, and the theory for designing an isolation circuit is investigated. The simulated and measured results of the proposed multiplexers are also provided.

II. Concept of Proposed Multiplexer

The diplexer shown in Fig. 1(a) is the starting point for a multiplexer based on isolation circuits. If it is necessary to increase one more channel, an additional isolation circuit can be connected to the junction point like shown in Fig. 1(b), and each circuit should have open-circuit input impedance at the passing frequencies of other circuits [12]. For satisfying this condition, a CRLH TL having dual band characteristic is necessary. In a similar manner, a four-channel multiplexer or a quadruplexer can be realized by connecting four tri-band isolation circuits composed of DL TLs having tri-band characteristic [13].

In the concept of a star junction multiplexer, all isolation circuits are composed with same type of TL. For example, a star junction triplexer is composed with three CRLH TLs followed by three filters. The multiplexer shown in Fig. 1(c) also consists of multiband TLs and filters for multiband isolation circuits. However, one special isolation circuit composed with RH TLs connecting with a filter exists in this type of multiplexers [14, 15]. This special isolation circuit also has multiband characteristic. The mechanism is as follows: 1) at frequency $f_1$, the first isolation circuit consisting of the $f_1$ filter allows signal path. Meanwhile, other isolation circuits composed with multiband TLs are disconnected from junctions, and the combined phase response of RH TLs (from $TL_1$ to $TL_{N-1}$) is already determined for the isolation circuits consisting of RH TLs (from $TL_1$ to $TL_3$) and the $f_3$ filter having infinite input impedance. 2) At frequency $f_{N-1}$, except the isolation circuit having the $f_{N-1}$ filter, all isolation circuits consisting of multiband TLs have infinite input impedance. Also the isolation circuit consisting of the RH $TL_{N-1}$ and the $f_N$ filter

![Diagram](image)

**Fig. 1.** Diagrams and operation concepts of multiplexers based on isolation circuit: (a) diplexer, (b) star-junction multiplexer, and (c) manifold-junction multiplexer. RH TL=right-handed transmission line.
is isolated from the junction. 3) At frequency $f_0$, all isolation circuits composed with multiband TLs are isolated from junctions, and the $f_0$ filter is directly connected with the input through RH TLs allowing signal path.

In the diagrams of Fig. 1, the frequencies from $f_i$ to $f_n$ do not need to be in ascending order.

III. Theory for Isolation Circuit Design

The method to determine a proper phase response of a TL for an isolation circuit and the theory of a multiband metamaterial TL are discussed in this section.

1. Required Phase Response for Isolation Circuit

Fig. 2(a) represents an isolation circuit comprising a TL connected to a filter. For simplicity, a case dealing with only one phase is considered. In Fig. 2(b) and (c), each triangle on the Smith chart shows an input impedance of a filter at target frequency to be isolated. In both cases, the real part of the impedance ($\alpha$) is assumed zero. Fig. 2(b) and (c) represent a positive imaginary impedance and a negative imaginary impedance, respectively. Since the input impedance is not infinite, a connected TL is required to rotate the triangle to the open position, represented by a hollow circle on the Smith chart, for an isolation characteristic. Two rotations are possible in both cases. Clockwise rotation represents a RH phase response or a phase delay, $\phi_1$, having a negative value. A left-handed (LH) phase response or a phase advance, $\phi_2$, having a positive value, is represented as a counter-clockwise rotation on the Smith chart. If the real part of the input impedance of a filter or a multiplexer (R) is very small or zero, the transmission line impedance equation can be used to solve the required phase response [16].

$$Z_m = Z_0 \frac{Z_l + jZ_0 \tan(\phi_2)}{Z_0 + jZ_l \tan(\phi_2)}$$  \hspace{1cm} (1)

Since the input impedance of the circuit $Z_m$ should be infinite, the denominator of (1) is zero, and the required phase response, $\phi_1$, is

$$\phi_1 = \begin{cases} -\arctan(Z_l/X), & X > 0 \\ -\pi - \arctan(Z_l/X), & X < 0 \end{cases}$$  \hspace{1cm} (2a)

$$\phi_2 = \begin{cases} \pi - \arctan(Z_0/X), & X > 0 \\ -\arctan(Z_0/X), & X < 0 \end{cases}$$  \hspace{1cm} (2b)

These equations can be simplified to a more general form:

$$\phi = n\pi - \arctan(Z_0/X) + \Psi$$  \hspace{1cm} (2c)

where $n$ is the integer and $Z_0$ is the characteristic impedance of the transmission line. Here, $\Psi$ is the adjusting constant to obtain a realistic value near open-circuit position [13]. A more rigorous equation considering the term $\alpha$ is also represented as

$$\phi = n\pi - \frac{\arctan\left[\left(\alpha^2 + X^2 - Z_0^2\right) + j2Z_0X\right]}{2} + \Psi.$$  \hspace{1cm} (2d)

2. Multiband Transmission Lines

The circuit parameters of a conventional RH TL are series inductance and shunt capacitance. This structure has a linear phase response, and only a RH phase shift or phase delay exists. However, the dual structure of a RH TL or LH TL, of which the circuit parameters are series capacitance and shunt inductance, has a nonlinear phase response, and only a LH phase response or phase advance is supported. Because of a parasitic RH TL, a pure LH TL does not exist in the real world, and a combination of a RH TL and a LH TL, a CRLH TL, has been actively investigated [17]. Fig. 3 shows the circuit model and phase response of a CRLH TL. A CRLH TL shows interesting characteristics in the balanced condition, where the series resonant and shunt resonant frequencies are equal. In the balanced condition of a CRLH TL, the phase response and characteristic impedance are simply represented as,
Fig. 3. (a) Circuit diagram of a composite right/left-handed transmission line (CRLH TL) unit cell and (b) phase response of a CRLH TL in the balanced case.

Fig. 4. (a) Circuit model of a double-Lorentz transmission line (DL TL) unit cell and (b) phase response of a DL TL in the balanced case. Source [13], reproduced by permission of © 2012 EuMA.

\[
\phi(\omega) = -N_D \left[ \omega \sqrt{L_p C_p} - \frac{1}{\omega \sqrt{L_c C_c}} \right]
\]  

(3)

\[
Z_C = \sqrt{\frac{L_p}{C_p}} = \frac{L_c}{C_c}
\]  

(4)

where \(N_C\) is the number of CRLH TL unit cells and \(\omega\) is the angular frequency. A CRLH TL has a dual-band characteristic: two desired phase responses, \(\phi_1\) and \(\phi_2\), can be obtained at two frequencies, \(\omega_1\) and \(\omega_2\), using a CRLH TL. Phase shift \(\phi_1\) at frequency \(\omega_1\) and phase shift \(\phi_2\) at frequency \(\omega_2\) bring two equations using (3). Because of the matching-to-termination impedance \(Z_0\) (4) makes two other equations. So, there are four equations with four unknowns: \(L_R, C_R, L_L,\) and \(C_L\). These four circuit parameters are presented with closed forms by modifying four equations which come from (3) and (4) [17].

A dual-CRLH (D-CRLH) TL has not only the dual characteristics of a CRLH TL but also has the dual structure of a CRLH TL, which has the parallel LC in series branch and series LC in shunt branch [18]. A DL TL is a combination of a D-CRLH TL and a parasitic RH TL, of which circuit parameters are series inductance \(L_p\) and shunt capacitance \(C_p\). Fig. 4(a) shows a circuit diagram of a DL TL. The phase response of a DL TL, shown in Fig. 4(b), is similar to that of a D-CRLH TL, but the existence of a RH phase shift region at a higher frequency band is a significant difference. A DL TL appears as a RH TL at low frequencies because components \(L_R, L_P, C_R,\) and \(C_p\) are dominant. On the contrary, if both impedance \(Z\) and admittance \(Y\) could have negative values at a certain frequency band, the DL TL would show CRLH-like behavior, because the effect of \(L_R\) and \(C_R\) in the structure is reduced at higher frequency. However, if both \(Z\) and \(Y\) do not support negative values in the frequency band, then the DL TL does not have a LH characteristic.

The phase response and characteristic impedance of a balanced DL TL are presented as (5) and (6), respectively [19].

\[
\phi(\omega) = -N_D \left[ \omega \sqrt{L_p C_p} + \frac{C_p}{\sqrt{L_p}} \frac{\omega L_R}{1 - \omega^2 L_R C_L} \right]
\]  

(5)

\[
Z_C = \sqrt{\frac{L_p}{C_p}} = \sqrt{\frac{L_c}{C_c}}
\]  

(6)

where \(N_D\) is the number of DL TL unit cells. Because a DL TL has a tri-band characteristic, three desired phase responses, \(\phi_1, \phi_2,\) and \(\phi_3\), can be obtained at three frequencies, \(\omega_1, \omega_2,\) and \(\omega_3\). This results in three equations using (5). Because of the matching-to-termination impedance \(Z_0\), (6) makes three other equations. Hence there are six equations with six unknowns, \(L_R, C_R, L_L, C_R, L_L,\) and \(C_L\). Solving these simultaneous equations
gives circuit parameters of a DL TL satisfying tri-band characteristic.

IV. Multiplexers

In order to verify the design concepts, star junction type multiplexers and manifold junction type multiplexers are designed and fabricated. A RT/Duroid 5870 substrate ( $\varepsilon_r=2.33$, height=0.787 mm) and commercial surface acoustic wave filters are used for multiplexers. Four filters (having 881.5, 942.5, 1,575, and 1,842.5 MHz center frequencies) are made by Panasonic while the manufacturers of other filters, with 2,140 and 2,450 MHz center frequencies, are EPCOS and Murata, respectively.

1. Star Junction Type Multiplexers

The triplexer based on dual-band isolation circuits is shown in Fig. 5(a). Because of using commercial filters, this triplexer has smaller size than the previous work [12]. The center frequencies of commercial filters are 881.5, 1,575, and 1,842.5 MHz, and each isolation circuit allows signal path at the center frequency of its filter. However, it rejects signal at two operating frequencies of other isolation circuits. For designing CRLH TLs used on dual-band isolation circuits, two target input impedances of each filter are necessary and they are represented in Table 1. After calculating required two phases from two input impedances with (2c) or (2d), the circuit parameters of each CRLH TL can be solved from (3) and (4). The results are summarized in Table 1. For a CRLH TL, lumped elements and microstrip lines are used. A LH part of a CRLH TL was realized using lumped elements whereas a microstrip line was used for a RH TL part. Fig. 6(a)–(c) show the simulated and measured results of the triplexer. Fig. 6(a) shows the insertion losses, which are less than 2.2 dB. Fig. 6(b) shows the return losses better than 15.1 dB, and the isolation is greater than 33.5 dB in Fig. 6(c).

Fig. 5(b) shows the fabricated quadruplexer based on tri-band isolation circuits [13]. The center frequencies are 942.5, 1,575, 2,140, and 2,450 MHz. In a manner similar to that of a dual-band isolation circuit, a DL TL for a tri-band isolation circuit can be designed by getting required phases from input impedances of a filter used on the isolation circuit. Then, circuit parameters of the DL TL are solved from (5) and (6) with the required phase responses and corresponding frequencies. Table 2 shows the input impedances of each filter, the required phase responses, and the circuit parameters of the DL TL satisfying tri-band isolation characteristic. A D-CRLH part of a DL TL was realized using lumped elements while a microstrip line was used for a RH TL part. The simulated and measured results of the quadruplexer are shown in Fig. 6(d)–(f). The worst case of insertion losses shown in Fig. 6(d) is 4.07 dB. This result can be improved by further adjusting the reactance of lumped elements. Insertion losses of other output ports are less than 2.9 dB. Fig. 6(e) shows the return losses larger than 11.3 dB, and the isolation is larger than 27.5 dB in Fig. 6(f).

2. Manifold Junction Type Multiplexers

Fig. 7(a) shows the picture of the manifold junction triplexer based on dual-band isolation circuits [14]. Its center frequencies are 881.5, 1,575, and 1,842.5 MHz. The design process of a dual-band isolation circuit based on a CRLH TL is same with that of the star junction triplexer shown in previous section. However, the special isolation circuit based on RH TLs has different ope-

![Fig. 5. Fabricated star-junction multiplexers based on isolation circuits: (a) triplexer and (b) quadruplexer. (b) is from [13], reproduced by permission of © 2012 EuMA.](image)
Fig. 6. The star-junction triplexer results: (a) insertion loss, (b) matching at port 1, and (c) isolation. The star-junction quadruplexer results: (d) insertion loss, (e) matching at port 1, and (f) isolation. (d), (e), and (f) are from [13], reproduced by permission of © 2012 EuMA.

The manifold junction quadruplexer based on tri-band isolation circuits is shown in Fig. 7(b) [15]. The design process is similar with the triplexer case, except that the quadruplexer uses DL TLs and three RH TLs instead of two RH TLs. Table 4 shows the design summary of the quadruplexer. Fig. 8(d)–(f) show the simulated and measured results of the quadruplexer, and simulation results are well matched with measured results. Fig. 8(d) shows the insertion losses, which are less than 4.1 dB.

ration concept and design method. At 1,575 MHz, the combined phase response of RH TL1 and RH TL2 should satisfy the condition that the input impedance of the special isolation circuit (consisting of two RH TLs and 881.5 MHz filter) is infinite. Similarly, the phase response of RH TL2 should be determined having open condition with the filter at 1,842.5 MHz. Table 3 shows the summary of designed isolation circuits. Fig. 8(a)–(c) show the simulated and measured results of the triplexer. The insertion losses shown in Fig. 8(a) are less than 2.1 dB. Fig. 8(b) shows the return losses better than 14.3 dB, and the isolation is greater than 32.4 dB in Fig. 8(c).

The manifold junction quadruplexer based on tri-band isolation circuits is shown in Fig. 7(b) [15]. The design process is similar with the triplexer case, except that the quadruplexer uses DL TLs and three RH TLs instead of two RH TLs. Table 4 shows the design summary of the quadruplexer. Fig. 8(d)–(f) show the simulated and measured results of the quadruplexer, and simulation results are well matched with measured results. Fig. 8(d) shows the insertion losses, which are less than 4.1 dB.
Fig. 8. The manifold junction triplexer results: (a) insertion loss, (b) matching at port 1, and (c) isolation. The manifold junction quadruplexer results: (d) insertion loss, (e) matching at port 1, and (f) isolation. (a), (b), and (c) are from [14], reproduced by permission of © 2012 IEEE. (d), (e), and (f) are from [15].

Table 1. Design summary of the star junction triplexer

| Impedance of filter (Ω) | Required phase delay/advance (deg.) | Circuit parameter of CRLH TL (L, nH; C, pF) |
|-------------------------|-----------------------------------|---------------------------------------------|
| 881.5 MHz Isolation circuit | 1,575 MHz 1,842.5 MHz $\phi_{11}$ $\phi_{12}$ | $L_R$ 2.2 $C_R$ 25.3 $L_L$ 10.1 |
| 1,575 MHz Isolation circuit | 881.5 MHz 1,842.5 MHz $\phi_{21}$ $\phi_{22}$ | 6.67 2.67 8.27 3.31 |
| 1,842.5 MHz Isolation circuit | 881.5 MHz 1,575 MHz $\phi_{31}$ $\phi_{32}$ | 8.86 3.54 6.77 2.71 |

CRLH TL=composite right/left-handed transmission line.

Fig. 8(e) shows that the return losses are better than 13.7 dB, and the isolation is greater than 27.1 dB as shown in Fig. 8(f).

V. Conclusions

Multiplexers based on isolation circuits are presented. The concepts, theories, and design processes have been explained to help the reader design and fabricate proposed multiplexers. These multiplexers have significant advantages. A straightforward and easy design process and the freedom to choose any filters without modification are attractive to a system designer. In this paper, two different types of triplexers and two types of quad-
Table 2. Design summary of the star junction quadruplexer

| Impedance of filter (Ω) | Required phase delay/advance (deg.) | Circuit parameter of DL TL (L; nH; C; pF) |
|-------------------------|-------------------------------------|-----------------------------------------|
| 942.5 MHz Isolation circuit | -j(27.75) -j(13.75) -j(8.10) | φ₁  φ₂  φ₃ | Lᵣ  Cᵣ  Lᵢ  Cᵢ  Lᵢ  Cᵢ |
| 1,575 MHz Isolation circuit | -j(54.25) -j(16.95) -j(11.30) | φ₁  φ₂  φ₃ | 2.75  1.10  20.43  8.17  7.31  2.92 |
| 2,140 MHz Isolation circuit | -j(90.60) -j(45.15) -j(27.15) | φ₁  φ₂  φ₃ | 6.23  2.49  6.01  2.4  9.17  3.67 |
| 2,450 MHz Isolation circuit | -j(18.30) -j(44.70) -j(332.65) | φ₁  φ₂  φ₃ | 3.92  1.57  8.24  3.29  3.12  1.25 |

Source [13], reproduced by permission of © 2012 EuMA. DL TL=double-Lorentz transmission line.

Table 3. Design summary of the manifold junction triplexer

| Impedance of filter (Ω) | Required phase delay/advance (deg.) | Circuit parameter of CRLH TL (L; nH; C; pF) |
|-------------------------|-------------------------------------|-----------------------------------------|
| 1,575 MHz Isolation circuit | -j(58.65) -j(25.55) | φ₁  φ₂ | Lᵣ  Cᵣ  Lᵢ  Cᵢ |
| 1,842.5 MHz Isolation circuit | -j(58.70) -j(23.75) | φ₁  φ₂ | 8.86  3.54  6.77  2.71 |
| 881.5 MHz Isolation circuit | -j(25.25) -j(18.20) | φ₁  φ₂ | - - - - |

Source [14], reproduced by permission of © 2012 IEEE. CRLH TL=composite right/left-handed transmission line.

Table 4. Design summary of the manifold junction quadruplexer

| Impedance of filter (Ω) | Required phase delay/advance (deg.) | Circuit parameter of DL TL (L; nH; C; pF) |
|-------------------------|-------------------------------------|-----------------------------------------|
| 942.5 MHz Isolation circuit | -j(27.75) -j(13.75) -j(8.10) | φ₁  φ₂  φ₃ | Lᵣ  Cᵣ  Lᵢ  Cᵢ |
| 2,140 MHz Isolation circuit | -j(90.60) -j(45.15) -j(27.15) | φ₁  φ₂  φ₃ | 6.23  2.49  6.01  2.4  9.17  3.67 |
| 2,450 MHz Isolation circuit | -j(18.30) -j(44.70) -j(332.65) | φ₁  φ₂  φ₃ | 3.92  1.57  8.24  3.29  3.12  1.25 |
| 1,575 MHz Isolation circuit | -j(54.25) -j(16.95) -j(11.30) | φ₁  φ₂  φ₃ | - - - - - |

Source [15]. DL TL=double-Lorentz transmission line.

These results are designed and fabricated. The measured results of each multiplexer show good agreement with the simulation results.

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