Generalizing Hybrid Integrator-Gain Systems Using Fractional Calculus

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Abstract—The Hybrid Integrator-Gain System (HIGS) has recently gained a lot of attention in control of precision motion systems. HIGS is a nonlinear low pass filter/integrator with a 52° phase advantage over its linear counterpart. This property allows us to avoid the limitations typically associated with linear controllers, like the waterbed effect and Bode’s gain-phase relation. In this paper, we generalize HIGS by replacing the involved integer-order integrator by a fractional-order one to adapt the phase lead from 0° (linear low pass filter) to 52° (HIGS). To analyze this filter in the frequency domain, the describing function of the proposed filter, i.e., the fractional-order HIGS, is obtained using the Fourier expansion of the output signal. In addition, this generalized HIGS is implemented in a PID structure controlling a double integrator system to validate the performance of the proposed filter in the time domain, in which by changing the fractional variable from zero to one, the output varies from the response of a linear control system to a nonlinear one.

I. INTRODUCTION

PID is one of the most popular controllers in industry [1] because it is easily implemented and tuned, and has a simple structure. Despite its popularity, it is a linear controller, which is limited due to the waterbed effect [2] and Bode’s gain-phase relation [3]. The solution to overcome the aforementioned limitations lies in the nonlinear control theory [4]. To this end, there have been several attempts, such as variable gain control [5], split-path nonlinear filters [6], and reset controllers [7],[9].

Among those, the reset controller has attracted a lot of attention since it can be easily implemented in a PID framework. J.C. Clegg was first to introduce the reset integrator in the late 1950s [8], which others named after him: the Clegg integrator. This integrator can reset its state and thus has a nonlinear behavior. The Clegg integrator has only −38.15° phase lag but similar gain behavior compared to a linear integrator. This property is interesting because it breaks Bode’s gain-phase relation. The phase lead reduces overshoot and improves the settling time of the system. In addition to the Clegg integrator, there have been several other reset controllers such as the first-order reset element (FORE) [9], generalized FORE [10],[11], and Constant in Gain Lead in Phase (CgLp) element [12]. Reset systems, despite their advantages, have a severe drawback, which is the harsh jump on its output. This discontinuity is undesired as it may cause practical issues like actuator saturation in the motion control systems [13].

The Hybrid Integrator-Gain System (HIGS) has recently been proposed to avoid the unwanted jumps in reset control systems. HIGS is presented as a piecewise linear system with no jump on its output [14]. In [15], a linear bandpass filter has been compared to a hybrid integrator-gain-based bandpass filter to control an active vibration isolation system. In [16], it has been shown that the overshoot inherent when using any stabilizing linear time-invariant feedback controller can be eliminated with a HIGS-based control strategy. In addition, the stability of HIGS-based control systems has been extensively discussed in [17],[18]. A frequency-domain analysis is preferred in the design of linear motion controllers since it allows intuitively ascertaining closed-loop performance measures. HIGS can also be analyzed in the frequency domain [19]. To this end, the describing function of HIGS is obtained from the Fourier expansion of its output signal. According to the describing function, HIGS acts as a linear low pass filter in gain but leads 52° in phase. Unlike generalized reset control systems [20], this phase lead is not controllable in HIGS. By changing the after reset value in reset control systems, the phase lead can be tuned, but doing the same for HIGS will cause discontinuity. Therefore, in this paper, we fill this gap in the state of the art by generalizing HIGS using fractional calculus. Fractional calculus has been used for control designs like fractional-order PID [21],[22] and CRONE control [24]. Recently, fractional-order elements have also been used within reset elements [25],[26], in order to regulate the higher-order harmonics in the reset control systems.

The principal contribution of this paper is to design a new HIGS where the integer-order integrator is replaced by a fractional-order one. By varying the order of the integrator from one to zero, the proposed fractional-order HIGS varies between nonlinear and linear behavior. The design of this filter enables the extension of the reset-based CgLp element to the HIGS-based CgLp element. In addition, the describing function is obtained as another contribution to analyze the controller in the frequency domain.

The remainder of this paper is organized as follows. In section II, the background information of HIGS will be given, the describing function of HIGS will be discussed, and finally, some fundamental definitions of the fractional-order derivative will be brought. In section III, the fractional-order HIGS will be introduced and formulated in state-space representation. Then, the describing function corresponding
to fractional-order HIGS will be derived. Finally, an approach to compensate the slope loss in the fractional integrator will be introduced and is named generalized HIGS. In section IV, an illustrative example including the controller design, implementation method, and results will be given. Section V summarizes the main conclusions of the article.

II. BACKGROUND

A. Hybrid integrator-gain system

The hybrid integrator-gain system (HIGS) is defined in [14] and its state-space representation is given by:

\[
H : \begin{cases}
    \dot{x}_h = \omega_h e, & \text{if } (e, \dot{e}, u) \in F_1, \\
x_h = k_h e, & \text{if } (e, \dot{e}, u) \in F_2, \\
u = x_h,
\end{cases}
\]  

(1)

where \( x_h \in \mathbb{R} \) is the state variable, \( u \in \mathbb{R} \) is the control output, \( e \in \mathbb{R} \) is the input, \( k_h \in \mathbb{R}_{>0} \) is the gain value, and \( \omega_h \in \mathbb{R}_{>0} \) is the integrator frequency. Furthermore, \( F_1 \) and \( F_2 \) denote the regions where the integrator- or gain mode is active, as defined by

\[
F_1 := \mathcal{F} \setminus F_2, \\
F_2 := \{(e, \dot{e}, u) \in \mathcal{F} \mid u = k_h e \wedge \omega_h e^2 > k_h \dot{e}^2\},
\]

(2a)

(2b)

where

\[
\mathcal{F} := \{(e, \dot{e}, u) \in \mathbb{R}^3 \mid eu \geq \frac{1}{k_h}u^2\}. 
\]

(3)

Regions \( F_1 \) and \( F_2 \) are set for three important reasons. First of all, the output stays continuous for all time. Second, the output is always bounded between \( k_h e \) and zero. Finally, the control output of HIGS will always be in the same direction as the input signal, as shown in Fig. 1.

B. Describing function of HIGS

To analyze the system in the frequency domain, a describing function analysis can be done. In [19] the describing function of HIGS for a sinusoidal input has been derived, as given by

\[
D(j\omega) = \frac{\omega_h}{j\omega} \left( \frac{\gamma}{\pi} + j \frac{e^{-j\gamma} - 1}{2\pi} - 4j \frac{e^{-j\gamma} - 1}{2\pi} \right) + k_h \left( \frac{\pi - \gamma}{\pi} + j \frac{e^{-j\gamma} - 1}{2\pi} \right),
\]

(4)

where \( j := \sqrt{-1} \) is the imaginary unit, and \( \gamma = 2 \arctan\left( \frac{\omega}{\omega_h} \right) \). In Fig. 2, the Bode plot of HIGS's describing function for different values of \( \omega_h \) and \( k_h \) is illustrated.

C. Fractional-order derivative

In this section, we define the Liouville-Caputo fractional-order derivative as an approach that we will use for fractional-order HIGS calculations [27]. Its definition is given by:

\[
^{\text{LC}}D^\alpha_t f(x) := \frac{1}{\Gamma(1-\alpha)} \int_{-\infty}^{x} dt(x-t)^{-\alpha} \frac{df(t)}{dt},
\]

(5)

Fig. 1: Time domain response of HIGS for multi-sine input \( e(t) = \sin(t) + 0.7 \sin(3t) \). The blue-colored area represents integrator-mode \((F_1)\), while the green-colored area represents gain-mode \((F_2)\).

Fig. 2: Bode plot for the describing function of HIGS for varying values of \( \omega_h \) \((k_h = 1)\) and \( k_h \) \((\omega_h = 1)\).

where \( 0 \leq \alpha \leq 1 \) is the derivative order, \( x \in \mathbb{R} \) is the upper-bound of the integral, and \( \Gamma(\cdot) \) is the Euler Gamma function [27]. For a sinusoidal function \( f(t) \) we have [28]:

\[
^{\text{LC}}D^\alpha_t [\sin(\omega t)] = \omega^\alpha \sin(\omega t + \frac{\pi \alpha}{2}).
\]

(6)

For convenience in the following, writing \( ^{\text{LC}}\) in \( ^{\text{LC}}D^\alpha_t \) will be refrained.

III. GENERALIZED HIGS

A. Fractional-order HIGS

HIGS is designed to dominantly operate as an integer-order integrator, except when it tends to leave sector \( \mathcal{F} \). Furthermore, HIGS has a continuous output signal. In this work, the integer-order integrator is extended to a fractional-order one. To guarantee sector-boundedness and continuity of the output signal, the gain-mode has to be revised. We define the fractional-order HIGS in state-space by

\[
\mathcal{H}_f : \begin{cases}
    ^{\text{LC}}D^\alpha_t x_h = \omega_h e, & \text{if } (e, \dot{e}, u) \in F_1, \\
x_h = k_h e, & \text{if } (e, \dot{e}, u) \in F_2, \\
x_h = 0, & \text{if } (e, \dot{e}, u) \in F_3, \\
u = x_h,
\end{cases}
\]

(7)
with integrator-region
\[ \mathcal{F}_1 = \mathcal{F} \setminus (\mathcal{F}_2 \cup \mathcal{F}_3), \] (8)
upper gain-region
\[ \mathcal{F}_2 := \{(e, \dot{e}, u) \in \mathcal{F} | u = k_h e \land (\omega_h D_t^{1-\alpha}(e)e > k_h \dot{e}e) \lor (e = 0 \land \omega_h D_t^{1-\alpha}(e)e > k_h e^2)\}, \] (9a)
and lower gain-region
\[ \mathcal{F}_3 = \{(e, \dot{e}, u) \in \mathcal{F} | u = 0 \land (D_t^{1-\alpha}(e)e < 0 \lor (e = 0 \land D_t^{1-\alpha}(e)e < 0))\}. \] (10a)

The upper gain-region contains an extended version of the condition in \( F_2 \), which prevents violation of the sector-boundary \( u = k_h e \) at \( e \neq 0 \) for the fractional-order integrator. The upper gain region also contains an additional condition to prevent violation of the sector-boundary at \( u = k_h e \) for \( e = 0 \). Namely, this can happen at that location if \( \dot{e} > 0 \) and \( \dot{u} > k_h \dot{e} \), or if \( \dot{e} < 0 \) and \( \dot{u} < k_h \dot{e} \). The lower gain-region contains several conditions which prevent the fractional-order HIGS from violating the sector-boundary at \( u = 0 \). This can happen in many different situations. For \( e > 0 \) it happens when \( \dot{u} < 0 \), and for \( e < 0 \) when \( \dot{u} > 0 \). If \( e = 0 \), it can happen when \( 1 \dot{\hat{e}} = 0 \) and \( \dot{\hat{u}} \neq 0 \), or \( 2 \dot{\hat{e}} > 0 \) and \( \dot{\hat{u}} < 0 \), or \( 3 \dot{\hat{e}} < 0 \) and \( \dot{\hat{u}} > 0 \). In all those cases, the fractional-order HIGS switches to its lower gain-mode, where its state \( x_h \) is kept at zero, guaranteeing continuity of its output signal.

The output of the fractional-order HIGS for a sinusoidal input \( e(t) = \sin(t) \) is depicted in Fig. 3a. It can be seen that by decreasing \( \alpha \) from 1 to 0 with fixed parameters \( \omega_h = 1 \) and \( k_h = 1 \), the output gradually goes from the behaviour of a HIGS to that of a proportional gain. Also, given the importance of HIGS being sector-bounded, Fig. 3b shows that the output of the fractional-order HIGS is bounded between 0 and \( k_h e \).

Remark 1: For the integer-order HIGS it is proven that the second condition of \( F_2 \) and all conditions of \( F_3 \) are never satisfied, such that they can be disregarded [14]. However, this does in general not hold for the fractional-order HIGS.

B. Describing function analysis for fractional-order HIGS

To derive the describing function of the fractional-order HIGS, first, the time domain output signal of the fractional-order HIGS with input \( e = \dot{e} \sin(\omega t) \) is obtained as:
\[
u(t) = \begin{cases} \frac{\omega \dot{e}}{2} \left( \sin(\tau - \frac{\pi \alpha}{2}) + \sin(\frac{\pi \alpha}{2}) \right), & 0 \leq \tau < \gamma, \\ \frac{\omega \dot{e}}{k_h \dot{e}} \sin(\tau), & \gamma \leq \tau < \pi, \\ \frac{\omega \dot{e}}{k_h \dot{e}} \left( \sin(\tau - \frac{\pi \alpha}{2}) - \sin(\frac{\pi \alpha}{2}) \right), & \pi \leq \tau < \gamma + \pi, \\ \frac{\omega \dot{e}}{k_h \dot{e}} \sin(\tau), & \gamma + \pi \leq \tau < 2\pi, \end{cases} \] (11)
where \( \tau = \omega t \). For the time intervals \( 0 \leq \tau < \gamma \) and \( \pi \leq \tau < \gamma + \pi \), when the HIGS is in integrator-mode, the output is derived using (6). Switching from integrator- to gain-mode happens at \( \tau = \gamma \). This time-instant is equal to the moment when the fractional-order HIGS reaches the sector-bound, as given by
\[
\frac{\omega \dot{e}}{\omega^\alpha} \left( \sin(\gamma - \frac{\pi \alpha}{2}) + \sin(\frac{\pi \alpha}{2}) \right) = k_h \dot{e} \sin(\gamma). \] (12)

After rewriting, we obtain
\[
\gamma = \arccos(X), \] (13)
where
\[
X = \frac{b - a}{b + a}, \quad a = (B \sqrt{1 - A^2 - C})^2, \quad b = B^2 A^2, \quad A = \sin(\frac{\pi \alpha}{2}), \quad B = \frac{\omega_h}{\omega^\alpha}, \quad C = k_h. \] (14)

The Fourier expansion of \( u(t) \), is given by
\[
u(t) = \frac{a_0}{2} + \sum_{n=1}^{\infty} (a_n \cos(n\omega t) + b_n \sin(n\omega t)), \] (15)
where Fourier coefficients \( a_n \) and \( b_n \) are described as
\[
a_n = \frac{1}{T} \int_0^T u(t) \cos(n\omega t) dt, \] (16a)
\[
b_n = \frac{1}{T} \int_0^T u(t) \sin(n\omega t) dt, \] (16b)
respectively. Here, \( n \in \mathbb{Z}^+ \) is the order of the harmonic and \( T \in \mathbb{R}^+ \) is the period of signal \( u(t) \). By substituting (11) in
(16) and setting \( n = 1 \) we have
\[
a_1 = \frac{\omega_h \hat{e}}{4 \pi \omega^\alpha} \left[ \cos\left(\frac{\pi \alpha}{2}\right) - \cos(2\gamma - \frac{\pi \alpha}{2}) - 2\gamma \sin\left(\frac{\pi \alpha}{2}\right) \right] \\
+ 2 \cos(\gamma - \frac{\pi \alpha}{2}) - 2 \cos(\gamma + \frac{\pi \alpha}{2}) \\
+ \frac{k_h \hat{e}}{4 \pi} \left[ \cos(2\gamma - 1) \right],
\]
(17a)
\[
b_1 = \frac{\omega_h \hat{e}}{4 \pi \omega^\alpha} \left[ 2\gamma \cos\left(\frac{\pi \alpha}{2}\right) - \sin(2\gamma - \frac{\pi \alpha}{2}) + 3 \sin\left(\frac{\pi \alpha}{2}\right) \right] \\
- 2 \sin(\gamma + \frac{\pi \alpha}{2}) + 2 \sin(\gamma - \frac{\pi \alpha}{2}) \\
+ \frac{k_h \hat{e}}{4 \pi} \left[ 2\pi - 2\gamma + \sin(2\gamma) \right].
\]
(17b)

According to [29], the describing function is given by
\[
\mathcal{D}(\omega, \hat{e}) = \frac{b_1}{\hat{e}} + ja_1.
\]
(18)

Fig. 4 shows the describing function of fractional-order HIGS obtained from (17) and (18). By substituting \( \alpha = 1 \) in (17) the describing function is equal to that of HIGS in (4). We can see that by changing parameter \( \alpha \), the output phase can vary between \(-38^\circ\) and \(0^\circ\). It represents the behavior of the system represented in (7). Also, it verifies that by setting \( \alpha \) to zero the system behaves as a proportional gain.

C. Architecture design for generalization of HIGS

In this subsection, we utilize the fractional-order HIGS to construct a generalized HIGS (\( \mathcal{H} \)) where the parameter \( \alpha \) can reproduce a linear low pass filter or HIGS if \( \alpha \) is chosen to be 0 or 1, respectively. The architecture of the proposed generalized HIGS is shown in Fig. 5a. Architecture \( a \) utilizes a fractional-order HIGS as described in (7) and a complementary linear filter to retrieve magnitude-characteristics similar to those of a linear low-pass filter. This complementary filter has the same cut-off frequency (\( \omega_r \)) as the \( \mathcal{H}_f \). Any \( \alpha \) between 0 and 1 generalizes the HIGS such that the phase lag of the filter can be varied between \(-90^\circ\) and \(-38^\circ\), as shown in Fig. 6. With this architecture, the gain of the describing function can be unchanged with respect to the variation of \( \alpha \) if each output has a same cut-off frequency (\( \omega_r \)). A similar analysis can be done for architecture \( b \), which is shown in Fig. 5b. Architecture \( b \) is inspired by the so-called PI+CI structure in reset control [30]. This structure consists of a linear low-pass filter (LPF) and a parallel HIGS element to construct the generalized HIGS. The value of \( \beta \) shows the percentage of utilization of each element. Setting this value to 0 represents a linear filter, while the value of 1 results in a HIGS. Any value between 0 and 1 adapts the phase lag of the filter from \(-90^\circ\) to \(-38^\circ\). Although both architectures in Fig. 5 generalize HIGS, architecture \( a \) is advantageous over architecture \( b \). Since it utilizes a linear low-pass filter after the nonlinear element, it attenuates the higher-order harmonics by \(1/n(1-\alpha)\). However, the higher-order harmonics are reduced by a constant value of \(1-\beta\) in architecture \( b \).

To visually illustrate this, we have compared the Fast Fourier transform of both architectures to achieve a generalized HIGS with a phase lag of \(-57^\circ\). To achieve this phase lag we set \( \alpha = 0.68 \) and \( \beta = 0.5 \) at \( \omega = 100 \text{ rad/sec} \). As can be seen from Fig. 7a, the magnitude of the higher-order harmonics for architecture \( a \) are smaller than those for architecture \( b \), and the magnitudes are decreasing for an increasing \( n \). Also, in Fig. 7b it is shown that by changing parameter \( \alpha \), the magnitude of the third harmonic of architecture \( a \) is always
Fig. 7: a) Higher-order harmonics for Architecture a and b with $\alpha = 0.68$ and $\beta = 0.5$, b) magnitude of third harmonic for both architectures, obtained by varying $\alpha$ and $\beta$.

less than with architecture b. A similar trend can be expected for the other higher-order harmonics.

IV. ILLUSTRATIVE EXAMPLE

In this section, in order to illustrate the time response of generalized HIGS, we control a single mass (double integrator) system with a proportional-integral-derivative (PID) controller. Here, we replace the linear integrator with an integrator made by fractional-order generalized HIGS.

A. Controller design

The PID controller ($C_{pid}$) consists of a lead-lag compensator in series connection to a PI and a proportional gain $K_p \in \mathbb{R}$, given by transfer function

$$C_{pid}(s) = K_p \left( \frac{1 + \frac{s}{\omega_d}}{1 + \frac{s}{\omega_l}} \right) \left( 1 + \omega_i I \right),$$

where $s \in \mathbb{C}$ denotes the Laplace variable, and $\omega_l \in \mathbb{R}$ and $\omega_d \in \mathbb{R}$ are the starting and ending frequency of the lead-action. Furthermore, $\omega_i \in \mathbb{R}$ represents the cut-off frequency of integrator $I$, which is given by

$$I = \mathcal{H} \times (1 + \frac{\omega_r}{s}).$$

(20)

As shown in section III.C, the term $\mathcal{H}$ in (20) can vary between linear and nonlinear behavior. Therefore, the non-linearity of integrator $I$ can be controlled by parameter $\alpha$. In the PID structure, setting $\alpha$ to zero yields linear integrator behaviour for $I$, and fully nonlinear behaviour when setting $\alpha$ to one. For $\alpha$ between 0 and 1, integrator $I$ yields a trade-off between both behaviours.

Without loss of generality, we have chosen the crossover frequency as $\omega_c = 200\pi (100Hz)$, and set parameters of the controller as $K_p = \omega_d^2/1.8$, $\omega_d = \omega_c/1.8$, $\omega_l = 1.8\omega_c$ and $\omega_i = \omega_c/10$. Furthermore, the parameters of $\mathcal{H}$ are set as $\alpha = [0, 0.1, 0.2, 0.4, 0.7, 0.9, 1]$, $\omega_h = [\omega_h, 0.874, 0.794, 0.716, 0.743, 0.874, 1]$, and $k_h = 1$ to have a same cut-off frequency ($\omega_{c,f} = 1$) as $H$.

B. Architecture design

The sequence of elements in reset controllers and HIGS controllers is very important. In [31], it has been shown that for a lead-lag compensator in series with a reset element, the optimal sequence for reducing the magnitude of higher-order harmonics is Lead-Reset-Lag. Therefore, the lead-lag compensator in (19), is divided into two elements as below:

$$\frac{1 + \frac{s}{\omega_d}}{1 + \frac{s}{\omega_l}} \implies \frac{1 + \frac{s}{\omega_d}}{\omega_l} \times \left( 1 + \frac{\beta}{\omega_i} \right)$$

(21)

According to the above decomposition, the block diagram of the controller is depicted in Fig. 8. Note that in an actual system, the PD term should be implemented in a proper form.

C. Results

In Fig. 9, the step response of the closed-loop system with controller $C_{pid}$ (19) is depicted. The figure confirms that by changing parameter $\alpha$, the output signal can vary between the responses of linear and nonlinear control systems. By looking at the step response of the system from Fig. 9, it is clear that by increasing the nonlinearity of the controller, overshoot and settling time are decreased. Therefore, the closed-loop system has better performance in transient response. In this example, the step response shows the improvement in transient response, where the conventional HIGS performs the best of all generalized HIGS elements. It is not the purpose of this paper to show the superiority of either of these controllers but rather to open a path for more flexible design in the future.

Fractional-order HIGS can be advantageous in improving steady-state response. As can be seen from Fig. 7b, the magnitude of the conventional HIGS’S third harmonic (the green dot) is the greatest of all possible generalized HIGS elements. It can be problematic, especially for more complex systems with high-frequency modes and control systems.

Fig. 8: Simplified schematic of the PID controller with integrator $I$, that can be variable between a linear and non-linear integrator.
Fig. 9: Step response of the closed-loop system with a PID controller made by generalized HIGS.

where the precision is of concern [26]. The generalized HIGS compromises between improving the transient response and reduction of higher-order harmonics for higher precision and reference tracking.

V. CONCLUSIONS

It is known that HIGS can overcome fundamental limitations of linear control, which leads to better performance without the harmful behavior of reset controllers. Unlike reset control systems, where the reset value can be tuned and its nonlinearity level (phase lag of the describing function) is controllable, the phase lag in HIGS cannot be tuned. In this paper, we proposed a novel fractional-order HIGS that overcomes the aforementioned limitation in HIGS and extends it for more general applications. The describing function was determined for the proposed filter. According to the describing function, it has been shown that the phase lag of generalized HIGS is variable between $-38.15^\circ$ and $-90^\circ$. Hence by using this system as an integrator, the output can vary between linear and nonlinear behaviors. In the end, the proposed new filter was used in form of a PID to control a double integrator (mass) system. The results validate the generality of generalized HIGS that can be utilized in the future to construct a CgLp element.

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