Modeling and Control Parameters Design for Grid-Connected Inverter System Considering the Effect of PLL and Grid Impedance

ZHIWEI XIE, YANDONG CHEN, WENHUA WU, YUANCAN XU, HAINING WANG, JIAN GUO, AND AN LUO

National Engineering Research Center for Power Conversion and Control, Hunan University, Changsha 410082, China

Corresponding author: Yandong Chen (yandong.chen@hnu.edu.cn)

This work was supported in part by the National Key Research and Development Program of China under Grant 2017YFB0902000, in part by the Hunan Provincial Innovation Foundation for Postgraduate under Grant CX20190297, and in part by the Postdoctoral Innovative Talent Support Program of China under Grant BX20190109.

ABSTRACT Small-signal stability problems often occur when the inverter for renewable energy generation is connected to weak grid. A small-signal transfer function integrated model reflecting the interaction of grid impedance, phase locked-loop (PLL), and current control loop is established in this paper. Based on the established model, the oscillation mechanism of the grid-connected inverter system is revealed: the inductance current flowing through the grid impedance can produce a voltage disturbance, which will eventually affect the inductance current through PLL and current control loop. Therefore, the loop composed of the grid impedance and PLL can easily lead to the oscillation of the grid-connected inverter system under weak grid condition. To suppress the oscillation, a control parameters design method of the grid-connected inverter is proposed. Without changing the control method, the proposed control parameters design method can ensure the stable operation of the grid-connected inverter system under the very weak grid condition when the short-circuit ratio (SCR) is 2. Finally, the experimental results verify the correctness of the stability analysis and the effectiveness of the control parameters design method proposed in this paper.

INDEX TERMS Small-signal transfer function integrated model, small-signal stability, grid-connected inverter, weak grid.

I. INTRODUCTION

Renewable energy generation has been developed rapidly in response to the crisis of energy shortage and increased environmental pollution [1]. However, the capacity of power electronic device is increasing with the development of renewable energy generation [2]. The grid-connected power electronic devices affect the operation characteristics of the grid and weaken the grid [3]. Thus the small-signal stability problems occur one after another when inverters are connected to the weak grid [4], [5], which seriously threatens the safe and stable operation of the grid.

It is well known that there are three main methods to analyze the small-signal stability of the grid-connected inverter: impedance-based method [6], state-space method [7], and traditional transfer function method [8]. The impedance-based method is to divide the interconnected systems into two independent subsystems and establish the impedance models of the two subsystems, which reduces the complexity of system modeling. Then, the system stability is analyzed based on the impedance stability criterion. However, because of the existence of the coupling impedance [9], [10], the stability analysis based on impedance model becomes complicated [11]. The state-space method establishes the state-space model of the system, and then judges the stability of the system by the characteristic equation [12]. This method enables to identify the influence of each component on the system stability. The traditional transfer function method is widely used in control parameters design and system stability analysis [13]. It is the most basic and important method in classical control theory because of its simplicity and the ability to clearly reveal the interaction between various components in the system [14].

In order to improve the stability and suppress oscillation of the grid-connected inverter system, there are also three main
methods: parameters design method [15], improved control strategy [16], and additional special equipment [17]. The parameters design method is the simplest and easiest oscillation suppression method to implement. By designing the parameters, the method can enhance the system stability without changing the control structure of grid-connected inverters or increasing additional equipment. At present, many parameters design methods are proposed. A tuning approach guided by the eigenvalue parametric sensitivities is proposed to enhance the system stability in [18]. Taking pulsedwidth modulation transport and controller sampling delays into account, an analytical method is presented to determine the best possible gains in [19]. To obtain the stable regions, the stability analysis methods can be repetitively applied for all the parameters [20]. What’s more, a novel parameters design method of dual-loop control strategy for grid-connected inverters is proposed in [21]. However, the above studies do not consider the impact of grid impedance, nor do they involve the interaction between weak grid, PLL, and internal loop control. It is pointed out in [22] that the grid impedance varies widely and the fluctuating grid impedance will have adverse effects on the system stability. The parameters design method which regards the grid impedance as zero is no longer applicable to grid-connected inverters under weak grid condition. At the same time, the larger the grid impedance is (the weaker the grid is), the more difficult the control parameters design of inverters is. Considering the influence of weak grid, an idea reducing PLL bandwidth to improve system stability is proposed [23]. In the further research, an adaptive control parameters adjustment method based on on-line measurement of the grid impedance is proposed [24]. But it is difficult to realize the real-time measurement of the grid impedance. The design method of current control loop parameters has not been given in the above studies [22]–[24]. These existing studies on the design of control parameters under weak grid condition is either simply optimized for current control loop [19]–[21], or only designed PLL parameters [23]–[25]. There is no research on the design method of control parameters including current control loop and PLL under weak grid condition.

To address the above issues, a control parameters design method of the grid-connected inverter considering the interaction between PLL and current loop under weak grid condition is proposed in this paper. By changing the control parameters of the grid-connected inverter, the stable operation of the system under weak grid condition can be guaranteed. Firstly, considering the influence of grid impedance and PLL, a small-signal transfer function integrated model of current control loop is established, and the interaction mechanism between PLL and current loop is analyzed in section II. In section III, a control parameters design method is proposed to determine the parameters of current control loop and PLL based on the established small-signal transfer function integrated model. Then, an experimental platform is built to verify the correctness of the proposed parameters optimization method in section IV. Finally, some conclusions are obtained in section V.

II. MODELING AND ANALYSIS OF THE GRID-CONNECTED INVERTER SYSTEM

A. THE GRID-CONNECTED INVERTER SYSTEM

Fig. 1 shows the topology and control of a typical grid-connected inverter system. \( C_{dc} \) is DC-side capacitor, \( v_{dc} \) is DC-side voltage of inverter, which can be considered as a constant. \( e_a, e_b \) and \( e_c \) are mid-point voltages of inverter bridge leg; \( v_a, v_b \) and \( v_c \) are point of common coupling (PCC) voltages. \( i_a, i_b \) and \( i_c \) are inductance currents of inverter; \( i_{ga}, i_{gb} \) and \( i_{gc} \) are grid-connected currents of inverter. \( i_{ca}, i_{cb} \) and \( i_{cc} \) are filter capacitor currents. \( L_g \) and \( R_g \) are the equivalent inductance and resistance of grid, respectively. \( G_f \) and \( R_f \) are filter capacitor and its damping resistance, respectively. \( L_f \) and \( R_f \) are filter inductance and its equivalent resistance, respectively. \( c_a, c_b \) and \( c_c \) are the modulation signals. \( \delta_d^* \) and \( \delta_q^* \) are the d-axis and q-axis components of the reference inductance current in the control frame, respectively. Subscripts “d” and “q” represent the d-axis components and the q-axis components, respectively, and the superscript “cf” represents the variable in the control frame.

The control equations are shown in (1). \( G_i(s) \) is the PI controller of current control loop. \( G_i(s) = k_{p,i} + k_{i,i}/s; \) \( K_d = \omega_1 L_f/K_{pwm}; \) \( K_f = 1/K_{pwm}; \) \( K_{pwm} = v_{dc}/2; \) \( \omega_1 \) is the fundamental angular frequency of the grid.

\[
\begin{align*}
\delta_d^* &= (\delta_d^* - \delta_d) G_i(s) - K_d \delta_d^* + K_y \delta_d^* \\
\delta_q^* &= (\delta_q^* - \delta_q) G_i(s) + K_d \delta_q^* + K_y \delta_q
\end{align*}
\]  

(1)

\( \theta_{PLL} \) is the output of PLL. Its expression [9] can be obtained from Fig. 1.

\[
\theta_{PLL} = v_q^* G_{PLL}(s)
\]  

(2)

where \( G_{PLL}(s) = (k_{p,PLL} + k_{i,PLL}/s)/s. \)
B. SMALL-SIGNAL TRANSFER FUNCTION INTEGRATED MODEL

As can be seen from Fig. 1, the mathematical model of the main circuit can be derived as

\[
\begin{align*}
sL_q i_{gd} & = -R_g i_{gd} + \omega_1 L_g i_{gq} + v_d - v_{gd} \\
sL_q i_{gq} & = -R_g i_{gq} - \omega_1 L_g i_{gd} + v_q - v_{gq} \\
sL_f q_i & = -R_f i_q + \omega_1 L_f i_{q} + e_d - v_d \\
sL_f q_i & = -R_f i_q - \omega_1 L_f i_{q} + e_d - v_q
\end{align*}
\]

(3)

According to Kirchhoff’s current law, we can obtain the equations

\[
\begin{align*}
i_{gd} &= i_d - i_{cd} \\
i_{gq} &= i_q - i_{cq}
\end{align*}
\]

(5)

where

\[
\begin{align*}
i_{cd} &= A_1 v_d - A_2 v_q \\
i_{cq} &= A_2 v_d + A_1 v_q \\
A_1 &= \frac{s C_l (s C_l R_d + 1) + C_f^2 R_d \omega_1^2}{(s C_l R_d + 1)^2 + (C_f R_d \omega_1)^2} \\
A_2 &= \frac{(s C_l R_d + 1)^2 + (C_f R_d \omega_1)^2}{(s C_l R_d + 1)^2 + (C_f R_d \omega_1)^2}
\end{align*}
\]

(6)

Because of the small-disturbance in the output phase angle of PLL, there exists a deviation \( \Delta \theta_{PLL} \) between the control frame and the system frame, as shown in Fig. 2 [9].

Therefore, the transformations between the control frame and the system frame are defined as

\[
\begin{align*}
\begin{bmatrix}
x_d \\
x_q
\end{bmatrix} & = \begin{bmatrix}
\cos \Delta \theta_{PLL} & -\sin \Delta \theta_{PLL} \\
\sin \Delta \theta_{PLL} & \cos \Delta \theta_{PLL}
\end{bmatrix}
\begin{bmatrix}
x^c_d \\
x^c_q
\end{bmatrix} \\
\begin{bmatrix}
x^d_d \\
x^d_q
\end{bmatrix} & = \begin{bmatrix}
\cos \Delta \theta_{PLL} & \sin \Delta \theta_{PLL} \\
-\sin \Delta \theta_{PLL} & \cos \Delta \theta_{PLL}
\end{bmatrix}
\begin{bmatrix}
x_d \\
x_q
\end{bmatrix}
\end{align*}
\]

(8)

where the variable \( x \) can represent the PCC voltage \( v \), the inductance current of inverter \( i \) and the modulation signal \( c \).

\( e_d \) and \( e_q \) can be expressed as

\[
\begin{align*}
e_d & \approx G_{\text{del}}(s)K_{\text{pwm}} \begin{bmatrix}
1 \\
\Delta \theta_{PLL}
\end{bmatrix}
\begin{bmatrix}
é^c_d \\
é^c_q
\end{bmatrix}
\end{align*}
\]

(10)

where the delay transfer function \( G_{\text{del}}(s) = 1/(1.5T_s s + 1) \), sampling period \( T_s = 5 \times 10^{-5} \) s.

The grid-connected inverter system is a nonlinear system. In order to study the stability of the grid-connected inverter system, it is necessary to linearize it. The prefix symbol “\( \Delta \)” denotes the small-disturbance of variables, and the subscript “0” denotes the steady state of variables. Combining (1), (4), (8) and (10), in addition, the variables are substituted by the form of \( x = x_0 + \Delta x \) and \( x_0 \) are eliminated, we can derive the linearized closed-loop expression of current control loop which considering the effect of PLL as

\[
\begin{align*}
\Delta i_d &= \left[ G_{\text{del}}(s)K_{\text{pwm}}G(s) \left( \Delta \hat{e}_{cd}^{\text{ef}} - \hat{e}_{cd}^{\text{qf}} \Delta \theta_{PLL} \right) \\
&\quad + (G_{\text{del}}(s) - 1) \times (\Delta v_d - \omega_1 L_f \Delta i_q) \right] (s L_d + R_f) \\
&\quad + G_{\text{del}}(s)K_{\text{pwm}}G(s) - 1 \\
\Delta i_q &= \left[ G_{\text{del}}(s)K_{\text{pwm}}G(s) \left( \Delta \hat{e}_{cq}^{\text{ef}} + \hat{e}_{cq}^{\text{qf}} \Delta \theta_{PLL} \right) \\
&\quad + (G_{\text{del}}(s) - 1) \times (\Delta v_q + \omega_1 L_f \Delta i_d) \right] (s L_d + R_f) \\
&\quad + G_{\text{del}}(s)K_{\text{pwm}}G(s) - 1
\end{align*}
\]

(11)

Since \( G_{\text{del}}(s) - 1 \approx 0 \), the term containing \( (G_{\text{del}}(s) - 1) \) can be ignored. Equation (11) can be simplified as

\[
\begin{align*}
\Delta i_d &= \frac{G_{\text{del}}(s)K_{\text{pwm}}G(s) \left( \Delta \hat{e}_{cd}^{\text{ef}} - \hat{e}_{cq}^{\text{qf}} \Delta \theta_{PLL} \right)}{s L_d + R_f + G_{\text{del}}(s)K_{\text{pwm}}G(s)} \\
\Delta i_q &= \frac{G_{\text{del}}(s)K_{\text{pwm}}G(s) \left( \Delta \hat{e}_{cq}^{\text{ef}} + \hat{e}_{cq}^{\text{qf}} \Delta \theta_{PLL} \right)}{s L_d + R_f + G_{\text{del}}(s)K_{\text{pwm}}G(s)}
\end{align*}
\]

(12)

Grid impedance and PLL can affect the stability of the grid-connected inverter system. In order to explore the interaction mechanism between grid impedance, PLL, and current control loop, a small-signal transfer function integrated model of current control loop considering the effect of grid impedance and PLL is established.

From (9), we can get the relationship in (13).

\[
\hat{v}_q^c = -\Delta \theta_{PLL}v_d + v_q
\]

(13)

In order to obtain the small-signal transfer function integrated model, the variables in (2), (3), (5), (6) and (13) ignored the quadratic term and eliminated \( x_0 \), are expressed in the form of \( x = x_0 + \Delta x \):

\[
\begin{align*}
\Delta v_d &= (s L_g + R_g) \Delta i_g - \omega_1 L_g \Delta i_{qg} \\
\Delta v_q &= (s L_g + R_g) \Delta i_{qg} + \omega_1 L_g \Delta i_{gd} \\
\Delta i_{gd} &= \Delta i_d - \Delta i_{cd} \\
\Delta i_{qg} &= \Delta i_q - \Delta i_{cq} \\
\Delta i_{cd} &= A_1 \Delta v_d - A_2 \Delta v_q \\
\Delta i_{cq} &= A_2 \Delta v_d + A_1 \Delta v_q \\
\Delta \theta_{PLL} &= \frac{G_{\text{PLL}}(s)}{1 + \hat{v}_q^c G_{\text{PLL}}(s)} \Delta v_q
\end{align*}
\]

(14)

(15)

(16)

(17)

Combining (14), (15) and (16), we can establish the relationships between the \( d \)-axis, \( q \)-axis small-disturbance components of PCC voltage \( \Delta v_d \), \( \Delta v_q \) and the \( d \)-axis, \( q \)-axis
small-disturbance components of inductance current $\Delta i_d$, $\Delta i_q$,

$$\begin{bmatrix} \Delta v_d \\ \Delta v_q \end{bmatrix} = \begin{bmatrix} A_3 & -A_4 \\ A_4 & A_3 \end{bmatrix} \begin{bmatrix} \Delta i_d \\ \Delta i_q \end{bmatrix} \tag{18}$$

where

$$A_3 = [(sL_g + R_g)(1 + A_1(sL_g + R_g)) + A_1(\omega_1 L_g)^2] \frac{1}{[1 + A_1 \times (sL_g + R_g) - A_2(\omega_1 L_g)^2]^2 + [A_2(sL_g + R_g) + A_1(\omega_1 L_g)^2]}$$

$$A_4 = [\omega_1 L_g(1 - A_2(\omega_1 L_g) - A_3(sL_g + R_g))^2] \frac{1}{[1 + A_1 \times (sL_g + R_g) - A_2(\omega_1 L_g)^2]^2 + [A_2(sL_g + R_g) + A_1(\omega_1 L_g)^2]}^{-1} \tag{19}$$

Substituting (18) into (17), the expression of the relationship between $\Delta \theta_{PLL}$, $\Delta i_d$, and $\Delta i_q$ can be obtained as

$$\Delta \theta_{PLL} = A_5 A_4 \Delta i_d + A_5 A_3 \Delta i_q \tag{21}$$

where

$$A_5 = \frac{G_{PLL}(s)}{1 + v_{d0} G_{PLL}(s)} \tag{22}$$

We can derive the small-signal transfer function integrated model of current control loop between the reference inductance current and inductance current as

$$\begin{align*}
\Delta i_d &= \frac{G_{dcl}(s)K_{pwm} G_i(s)}{Z (1 + A_6 - A_9)} \left[ (1 - A_9) \Delta i_d^{ref} - A_7 \Delta i_q^{ref} \right] \\
\Delta i_q &= \frac{G_{dcl}(s)K_{pwm} G_i(s)}{Z (1 + A_6 - A_9)} \left[ A_6 \Delta i_d^{ref} + (1 + A_6) \Delta i_q^{ref} \right] \tag{23}
\end{align*}$$

where

$$\begin{align*}
Z &= sL_g + R_g + G_{dcl}(s) K_{pwm} G_i(s) \\
A_6 &= i_{d0}^{ref} A_3 A_4 G_{dcl}(s) K_{pwm} G_i(s)/Z \\
A_7 &= i_{d0}^{ref} A_3 A_4 G_{dcl}(s) K_{pwm} G_i(s)/Z \\
A_8 &= i_{d0}^{ref} A_5 A_4 G_{dcl}(s) K_{pwm} G_i(s)/Z \\
A_9 &= i_{d0}^{ref} A_5 A_3 G_{dcl}(s) K_{pwm} G_i(s)/Z \tag{24}
\end{align*}$$

C. INTERACTION BETWEEN PLL AND CURRENT CONTROL LOOP UNDER WEAK GRID CONDITION

According to (12), (17), and (18), the small-signal transfer function integrated model of current control loop considering the effect of grid impedance and PLL can be obtained, as shown in Fig. 3.

It can be seen that the voltage disturbance will occur when the inductance current passes through the grid impedance. The voltage disturbance affects the output of the current control loop through PLL, and finally acts on the inductance current. The loop composed by grid impedance and PLL easily lead to the oscillation of the grid-connected inverter system under weak grid condition. When the grid can be regarded as stiff grid, $L_g \approx 0$ mH, $R_g \approx 0$ Ω. Therefore, $A_3 = A_4 = 0$. That is, $A_6 = A_7 = A_8 = A_9 = 0$. Equation (23) can be simplified as

$$\begin{align*}
\Delta i_d &= \Delta i_d^{ref} G_{dcl}(s) K_{pwm} G_i(s)/Z \\
\Delta i_q &= \Delta i_q^{ref} G_{dcl}(s) K_{pwm} G_i(s)/Z 
\end{align*} \tag{25}$$

Under this condition, the PLL does not affect the output of the current control loop. The d-axis component of the reference inductance current only affects active current control loop. The q-axis component of the reference inductance current only affects reactive current control loop. Therefore, the control parameters without considering the influence of grid impedance can meet the stability requirements of the system under stiff grid condition. When the grid is weak, the influence of $L_g$ and $R_g$ cannot be neglected. Grid impedance and PLL affect current control loop together, which will have a negative impact on the stability of the grid-connected inverter system. In general, the grid-connected inverter for renewable energy generation only generates active power. Therefore, this paper only discusses the case that the reactive power reference value of the inverters $i_{q0}^{ref}$ is 0 A. According to Fig. 3, it can be seen that the grid impedance, PLL, and $i_{d0}^{ref}$ have an impact on the reactive current control loop. The control parameters without considering
this impact are difficult to meet the grid-connected inverter system stability requirements under weak grid condition.

III. CONTROL PARAMETERS DESIGN METHOD OF THE GRID-CONNECTED INVERTER

A. PARAMETERS DESIGN OF CURRENT CONTROL LOOP

Because the active and reactive current control loops of the grid-connected inverters are symmetrical, the active current control loop and the reactive current control loop choose the same PI parameters. Because of $f_{q0} = 0$ A, grid impedance and PLL do not affect the active current control loop. The parameters of the active current control loop are easy to be designed. When designing the parameters of the grid-connected inverter, the PI parameters of active current control loop can be designed first.

From Fig. 3, the open-loop transfer function of the active current control loop before adding $G_i(s)$ can be obtained as

$$G_{OP}(s) = \frac{G_{az}(s)K_{pwm}}{sL_i + R_i}$$  \hspace{1cm} (26)

$G_{OP}(s)$ has two open-loop poles, and the corner frequencies of the amplitude-frequency characteristics are 0.5 Hz and 2122 Hz.

There is a relationship between $G_{OP}(s)$ and $G_i(s)$ in crossover frequency $\omega_c$:

$$|G_{OP}(j\omega_c)| = \frac{1}{|G_i(j\omega_c)|}$$  \hspace{1cm} (27)

Usually, the zero of the PI controller of the current control loop is designed at the dominant pole 0.5 Hz of $G_{OP}(s)$, thus

$$k_{p,i} = \frac{L_i}{R_i}$$  \hspace{1cm} (28)

Combining (27) and (28), $k_{i,j}$ can be obtained as

$$k_{i,j} = \{K_{pwm}(R_i - 1.5T_sl_{ocj}^2 + \omega_c^2(L_i + 1.5T_sl_{ocj}))^1/2\}
\times [1 + (L_{ocj}/R_i)^2]^{1/2} - 1$$  \hspace{1cm} (29)

The crossover frequency $\omega_c$ should be less than 1/10 of the inverter switching frequency. The PI parameters can be obtained under the parameters in Table 1.

$$\begin{cases} k_{p,i} = 0.0740 \\ k_{i,j} = 0.2467 \end{cases}$$  \hspace{1cm} (30)

The slope of $G_i(s)$ at the crossover frequency is 0 dB/dec. After adding $G_i(s)$, the amplitude-frequency characteristic curve of the open-loop transfer function of the active current control loop will pass through the 0 dB at $-20$ dB/dec. Thus, it can satisfy the phase margin requirement of the grid-connected inverter system.

Bode plot of the open-loop transfer function without PI controller is shown in Fig. 4 (a). It can be seen from the diagram that the gain margin (GM) of the system is infinite and the phase margin (PM) is 59°. Thus, it can satisfy the phase margin requirement of the grid-connected inverter system.

Bode plot of the open-loop transfer function with PI controller is shown in Fig. 4 (b). It can be seen that the PI controller is suitable.

B. PARAMETERS DESIGN OF PLL

Because the grid impedance affects the reactive current control loop through PLL, PLL parameters can be designed according to the stability of reactive current control loop.

FIGURE 4. Bode diagram of the open-loop transfer function. (a) without PI controller; (b) with PI controller.

TABLE 1. Parameters of main circuit and control.

| Symbol | Quantity | Values |
|--------|----------|--------|
| $L_i$  | filter inductance | 3 mH |
| $R_i$  | equivalent resistance of filter inductance | 0.01 Ω |
| $C_t$  | filter capacitor | 20 μF |
| $R_d$  | damping resistance of filter capacitor | 1.5 Ω |
| $V_o$  | DC-side voltage | 700 V |
| $P_n$  | rated capacity of the inverter | 10 kW |
| $f_s$  | switching frequency | 20 kHz |
| $\omega_c$ | crossover frequency | 6.28 krad/s |
| $i_{d}^{*}$ | the d-axis components of the reference inductance current | 22 A |
| $i_{q}^{*}$ | the q-axis components of the reference inductance current | 0 A |
| $V_k$  | grid voltage | 380 V |
| $f_i$  | fundamental frequency | 50 Hz |
1) INFLUENCE OF ACTIVE POWER

According to the above analysis, the $i_{d0}^{*}$ contained in the active power $P_s$, is closely related to PLL parameters design and system stability. The PLL parameters derived from [25], $k_{p,PLL} = 1.963$, $k_{i,PLL} = 299.1989$. While $L_g = 15.4$ mH, the stability of reactive current control loop when $P_s$ changes from 0.2 pu to 1 pu is discussed. The open-loop transfer function of the reactive current control loop $G_{OQ}(s)$ shown in (31) is obtained from Fig. 3. Under the parameters set in Table 1, $G_{OQ}(s)$ has no right half-plane poles.

$$G_{OQ}(s) = \frac{-G_{del}(s)K_{pwm}G_I(s)G_{PLL}(s)A3i_{d0}^{*}}{Z(1 + v_{d0}G_{PLL}(s))} \quad (31)$$

The Nyquist diagram of $G_{OQ}(s)$ with $P_s$ changing from 0.2 pu to 1 pu is shown in Fig. 5. It can be seen from Table 2 that the greater the $P_s$ is, the smaller the gain margin is, and the worse the stability of reactive current control loop has. When $P_s = 0.8$ pu and $P_s = 1$ pu, Nyquist curves surround the point $(-1, j0)$ and the gain margin is smaller than 1. Thus the inverter is unstable. In order to ensure the stable operation of the grid-connected inverter system under different active power output conditions, $P_s = 1$ pu is chosen when designing the PLL parameters.

| $P_s$ | Gain margin | Stability |
|-------|-------------|-----------|
| 0.2 pu | 3.04        | Stable    |
| 0.4 pu | 1.52        | Stable    |
| 0.6 pu | 1.02        | Stable    |
| 0.8 pu | 0.76        | Unstable  |
| 1 pu   | 0.61        | Unstable  |

2) INFLUENCE OF THE GRID IMPEDANCE

Taking the same PLL parameters as above, and $P_s = 1$ pu, the influence of grid impedance on the stability of the reactive current control loop is discussed. SCR is often used to analyze the relative strength of AC grid when power electronic device (such as photovoltaic) is fed into AC system. This paper considers stiff grid SCR $= 11.6$, weak grid SCR $= 6$, SCR $= 3$, SCR $= 2.5$, very weak grid SCR $= 2$ to analyze the stability of reactive current control loop.

The Nyquist diagram of $G_{OQ}(s)$ with the changing grid condition is shown in Fig. 6. It can be seen from Table 3 that the weaker the grid is, the smaller the gain margin is, and the worse the stability of reactive current control loop has. When SCR $= 3$, SCR $= 2.5$ or SCR $= 2$, Nyquist curves surround the point $(-1, j0)$ and the system gain margin is smaller than 1. Thus the inverter is unstable. The stability of reactive current control loop is the worst when SCR $= 2$. In order to ensure the stable operation of the grid-connected inverter system under different active power output conditions, $P_s = 1$ pu is chosen when designing the PLL parameters.

3) PARAMETERS DESIGN OF PLL CONSIDERING THE INFLUENCE OF $P_s$, SCR, AND CURRENT CONTROL LOOP

According to the analysis above, the PLL parameters are designed under the very weak grid condition with $P_s = 1$ pu. The parameters for current control loop are designed from (28) and (29): $k_{p,i} = 0.0740$, $k_{i,i} = 0.2467$. 
PLL is a typical second-order control system. According to the relationship between control parameters, PLL bandwidth $BW_{PLL}$ and damping ratio, the design equation of PLL parameters is obtained as

$$\begin{align*}
    k_{p,PLL} &= \frac{4\sqrt{3}\pi BW_{PLL}}{\sqrt{2V_g}\sqrt{1 + 2\xi^2 + (1 + 2\xi)^2 + 1}} \\
    k_{i,PLL} &= \frac{\sqrt{2V_g}k_{p,PLL}^2}{4\sqrt{3}\xi^2}
\end{align*}$$

The damping ratio $\xi = 0.707$. The Nyquist diagram of $G_{QQ}(s)$ with the changing $BW_{PLL}$ is shown in Fig. 7. In Table 4, it can be seen that the bigger the $BW_{PLL}$ is, the smaller the gain margin is, and the worse the stability of reactive current control loop has. Under the very weak grid condition, $BW_{PLL}$ is required smaller than 74 Hz to maintain the stability of the reactive current control loop. In order to keep certain stability margin, $BW_{PLL} = 70$ Hz.

In order to verify that the reactive current control loop can be stable in the parameter range shown in (33), taking a step of 0.1, a three-dimensional diagram with $P_s$ as the x-axis, SCR as the y-axis, and the real part of the rightmost pole of the closed-loop transfer function of reactive current control loop $GCQ(s)$ as the z-axis is drawn, as shown in Fig. 7. The expression of $GCQ(s)$ is shown in (34).

$$\begin{align*}
    \begin{cases}
        P_s \in [0\pu, 1\pu] \\
        SCR \in [2, 11.6] \\
        BW_{PLL} = 70\pu
    \end{cases}
\end{align*}$$

$$G_{COQ}(s) = \frac{G_{del}(s)K_{PWM}G_i(s)}{Z (1 - A_g)}$$

We define the tuned parameters as the parameters derived from the proposed method in this paper and define the quoted parameters as the parameters for current control loop derived from [19] and the PLL parameters derived from [25]. As can be seen from Fig. 8 (a), the real parts of the rightmost pole of $G_{COQ}(s)$ are less than zero. Therefore, the tuned parameters

| $BW_{PLL}$ | Gain margin | Stability |
|------------|-------------|-----------|
| 65 Hz      | 1.25        | Stable    |
| 74 Hz      | 1.01        | Stable    |
| 75 Hz      | 0.98        | Unstable  |
| 85 Hz      | 0.80        | Unstable  |
| 95 Hz      | 0.66        | Unstable  |
satisfies the stability requirement of different grid conditions and different active power output conditions. When the system operating with the quoted parameters, there have some real parts of the rightmost poles greater than zero. Therefore, the quoted parameters cannot meet the stability requirements of different grid conditions and different active power conditions. The simulation results verify the effectiveness of the proposed method.

In summary, the control parameters design method of the grid-connected inverter considering interaction between PLL and current control loop under weak grid condition proposed in this paper is presented in Fig. 9.

Firstly, the small-signal transfer function integrated model of current control loop considering the influence of grid impedance and PLL is established. The PI parameters of current control loop are designed according to the pole-zero cancellation method. Then, the interaction between weak grid, PLL, and current control loop is analyzed, and the worst condition is adopted to design the PLL parameters. Finally, the three-dimensional diagram of the real part of the rightmost pole of $G_{CQ}(s)$ of reactive current control loop verified that the grid-connected inverter system can be operated stably under different conditions with the designed parameters. Therefore, the proposed control parameters design method can ensure the stable operation of the grid-connected inverter system under different conditions without changing the control strategy. It is a very simple and effective method to suppress the oscillation, and can be readily used in engineering applications.

IV. EXPERIMENTS

In order to verify the correctness of the control parameters design method and conclusions of this paper, the grid-connected inverter experimental platform shown in Fig. 10 is built. The control method is implemented in the DSP and FPGA, and the experimental parameters are consistent with the previous analysis.

On the experimental platform, the following experiments Case 1~Case 8 are designed.

Case 1: The grid-connected inverter operates with the quoted parameters ($k_{p,i} = 0.0343$, $k_{i,i} = 4.5714$), $SCR = 2$, and $P_s$ increased from 0.2 pu to 0.4 pu. The grid-connected current of the inverter can be observed.

Case 2: The grid-connected inverter operates with the quoted parameters ($k_{p,i} = 0.0343$, $k_{i,i} = 4.5714$), $SCR = 2$, and $P_s$ increased from 0.4 pu to 0.8 pu. The grid-connected current of the inverter can be observed.

The experimental results of Case 1 and Case 2 are shown in Fig. 11. The experimental results show that the change of active power affects the stability of the system. Under the condition of $SCR = 2$, when $P_s = 0.2$ pu, the system can be operated stably. But with the increase of $P_s$, the system becomes unstable gradually: when $P_s = 0.4$ pu, the grid-connected current is seriously distorted; when $P_s = 0.8$ pu, the system oscillates and then over-current protection works. The critical value of over-current protection is 1.3 times of rated current.

FIGURE 9. Control parameters design method of the grid-connected inverter.

FIGURE 10. Experimental platform.

FIGURE 11. The grid-connected current of the inverter with the quoted parameters when $P_s$ changing. (a) $P_s$ increased from 0.2 pu to 0.4 pu; (b) $P_s$ increased from 0.4 pu to 0.8 pu.
Case 3: The grid-connected inverter operates with the quoted parameters \((k_{p,i} = 0.0343, k_{i,i} = 4.5714), P_s = 1 \text{ pu}\), and SCR decreased from 6 to 3. The grid-connected current of the inverter can be observed.

Case 4: The grid-connected inverter operates with the quoted parameters \((k_{p,i} = 0.0343, k_{i,i} = 4.5714), P_s = 1 \text{ pu}\), and SCR decreased from 3 to 2. The grid-connected current of the inverter can be observed.

The experimental results of Case 3 and Case 4 are shown in Fig. 12. It is obviously that when the SCR = 6 with the quoted parameters, the system can operate stable. However, when the SCR is reduced from 6 to 3, the grid-connected current is seriously distorted and the system oscillates. When the grid becomes very weak SCR = 2, the system oscillates and then over-current protection works.

Case 5: The grid-connected inverter operates with the tuned parameters \((k_{p,i} = 0.0740, k_{i,i} = 0.2467), P_s = 1 \text{ pu}\), and SCR decreased from 6 to 3. The grid-connected current of the inverter can be observed.

Case 6: The grid-connected inverter operates with the tuned parameters \((k_{p,i} = 0.0740, k_{i,i} = 0.2467), P_s = 1 \text{ pu}\), and SCR decreased from 3 to 2. The grid-connected current of the inverter can be observed.

The experimental results of Case 5 and Case 6 are shown in Fig. 13. From the experimental results, it can be concluded that the tuned parameters can keep the system stable under different active power output conditions: \(P_s = 0.8 \text{ pu, } 0.4 \text{ pu and } 0.2 \text{ pu}\). Compared with the experimental results in Fig. 11, it can be seen that the tuned parameters can keep the system stably under different \(P_s\) conditions, and have better stability than those quoted.

Case 7: The grid-connected inverter operates with the tuned parameters \((k_{p,i} = 0.0740, k_{i,i} = 0.2467), P_s = 1 \text{ pu}\), and SCR decreased from 6 to 3. The grid-connected current of the inverter can be observed.

Case 8: The grid-connected inverter operates with the tuned parameters \((k_{p,i} = 0.0740, k_{i,i} = 0.2467), P_s = 1 \text{ pu}\), and SCR decreased from 3 to 2. The grid-connected current of the inverter can be observed.

The experimental results of Case 7 and Case 8 are shown in Fig. 14. Under the tuned parameters, when the SCR is reduced from 6 to 3 and 2, the system can be operated steadily. Compared with the experimental results in Fig. 12, it can be seen that the tuned parameters can keep the system steadily in very weak grid, and have better adaptability to weak grid than those quoted. Experiments show that when designing control parameters of the grid-connected inverter, the influence of grid impedance on stability should be considered to prevent the system oscillation when the grid condition changes.

The above experimental results show that the grid-connected inverter system can be operated stably with the tuned parameters under different active power output conditions and different grid conditions.

![FIGURE 12. The grid-connected current of the inverter with the quoted parameters when SCR changing. (a) SCR decreased from 6 \((L_g = 7.7 \text{ mH})\) to 3 \((L_g = 15.4 \text{ mH})\); (b) SCR decreased from 3 \((L_g = 15.4 \text{ mH})\) to 2 \((L_g = 23.1 \text{ mH})\).](image)

![FIGURE 13. The grid-connected current of the inverter with the tuned parameters when \(P_s\) changing. (a) \(P_s\) increased from 0.2 pu to 0.4 pu; (b) \(P_s\) increased from 0.4 pu to 0.8 pu.](image)
The proposed control parameters design method can ensure the stable operation of the grid-connected inverter under the very weak grid condition of SCR = 2. It is a very simple and effective method to suppress the oscillation. It is easy to be used to engineering applications without changing the control strategy and hardware structure.

REFERENCES

[1] Y. Chen, Z. Xie, L. Zhou, Z. Wang, X. Zhou, W. Wu, L. Yang, and A. Luo, “Optimized design method for grid-current-feedback active damping to improve dynamic characteristic of LCL-type grid-connected inverter,” *Int. J. Electr. Power Energy Syst.*, vol. 100, pp. 19–28, Sep. 2018.

[2] W. Wu, L. Zhou, Y. Chen, A. Luo, Y. Dong, X. Zhou, Q. Xu, L. Yang, and J. M. Guerrero, “Sequence-impedance-based stability comparison between VSGs and traditional grid-connected inverters,” *IEEE Trans. Power Electron.*, vol. 34, no. 1, pp. 46–52, Jan. 2019.

[3] W. Wu, Y. Chen, A. Luo, L. Zhou, X. Zhou, L. Yang, and J. M. Guerrero, “A virtual inertia control strategy for DC microgrids analogized with virtual synchronous machines,” *IEEE Trans. Ind. Electron.*, vol. 64, no. 7, pp. 6005–6016, Jul. 2017.

[4] H. Liu, X. Xie, J. He, T. Xu, Z. Yu, C. Wang, and C. Zhang, “Sub-synchronous interaction between direct-drive PMSG based wind farms and weak AC networks,” *IEEE Trans. Power Syst.*, vol. 32, no. 6, pp. 4708–4720, Nov. 2017.

[5] M. Lu, A. Al-Durra, S. M. Muyeen, S. Leng, P. C. Loh, and F. Blaabjerg, “Benchmarking of stability and robustness against grid impedance variation for LCL-filtered grid-interfacing inverters,” *IEEE Trans. Power Electron.*, vol. 33, no. 10, pp. 9033–9046, Oct. 2018.

[6] J. Sun, “Impedance-based stability criterion for grid-connected inverters,” *IEEE Trans. Power Electron.*, vol. 26, no. 11, pp. 3075–3078, Nov. 2011.

[7] A. Adib, B. Mirafzal, X. Wang, and F. Blaabjerg, “On stability of voltage source inverters in weak grids,” *IEEE Access*, vol. 6, pp. 4427–4439, 2018.

[8] S. Jayalath and M. Hanif, “Generalized LCL-filter design algorithm for grid-connected voltage-source inverter,” *IEEE Trans. Ind. Electron.*, vol. 64, no. 3, pp. 1905–1915, Mar. 2017.

[9] B. Wen, D. Boroyevich, R. Burgos, P. Mattavelli, and Z. Shen, “Analysis of D-Q small-signal impedance of grid-tied inverters,” *IEEE Trans. Power Electron.*, vol. 31, no. 1, pp. 675–687, Jan. 2016.

[10] I. Vieto, X. Du, H. Nian, and J. Sun, “Frequency-domain coupling in two-level VSC small-signal dynamics,” in Proc. IEEE 18th Workshop Control Modeling Power Electron. (COMPEL), Jul. 2017, pp. 1–8.

[11] W. Wu, Y. Chen, L. Zhou, X. Zhou, L. Yang, Y. Dong, Z. Xie, and A. Luo, “A virtual phase-lead impedance stability control strategy for the maritime VSC–HVDC System,” *IEEE Trans. Ind. Informat.*, vol. 14, no. 12, pp. 5475–5486, Dec. 2018.

[12] C. Levisa, C. O’Loughlin, T. O’Donnell, and M. Hill, “A comprehensive state-space model of two-stage grid-connected PV systems in transient network analysis,” *Int. J. Electr. Power Energy Syst.*, vol. 100, pp. 441–453, Sep. 2019.

[13] L. Harnefors, “Modeling of three-phase dynamic systems using complex transfer functions and transfer matrices,” *IEEE Trans. Ind. Electron.*, vol. 54, no. 4, pp. 2239–2248, Aug. 2007.

[14] K. Ogata, “Control systems analysis and design by the frequency-response method,” in *Modern Control Engineering*, 5th ed. Upper Saddle River, NJ, USA: Prentice-Hall, 2010, pp. 454–477.

[15] W. Cao, Y. Ma, and F. Wang, “Sequence-impedance-based harmonic stability analysis and controller parameter design of three-phase inverter-based multibus AC power systems,” *IEEE Trans. Power Electron.*, vol. 32, no. 10, pp. 7674–7693, Oct. 2017.

[16] J. Wang, Y. Song, and A. Monti, “A study of feedforward control on stability of grid-parallel inverter with various grid impedance,” in Proc. IEEE 5th Int. Symp. Power Electron. Distrib. Gener. Syst. (PEDG), Jun. 2014, pp. 1–8.

[17] R. K. Varma and R. Salehi, “SSR mitigation with a new control of PV solar farm as STATCOM (PV-STATCOM),” *IEEE Trans. Sustain. Energy*, vol. 8, no. 4, pp. 1473–1483, Oct. 2017.

[18] S. D’Arco, J. A. Suul, and O. B. Fosso, “Automatic tuning of cascaded controllers for power converters using eigenvalue parametric sensitivities,” *IEEE Trans. Ind. Appl.*, vol. 51, no. 2, pp. 1743–1753, Mar./Apr. 2015.
[19] D. G. Holmes, T. A. Lipo, B. P. McGrath, and W. Y. Kong, “Optimized design of stationary frame three phase AC current regulators,” *IEEE Trans. Power Electron.*, vol. 24, no. 11, pp. 2417–2426, Nov. 2009.

[20] W. Cao, Y. Ma, L. Yang, F. Wang, and L. M. Tolbert, “D–Q impedance based stability analysis and parameter design of three-phase inverter-based AC power systems,” *IEEE Trans. Ind. Electron.*, vol. 64, no. 7, pp. 6017–6028, Jul. 2017.

[21] J. Yin, S. Duan, Y. Zhou, F. Liu, and C. Chen, “A novel parameter design method of dual-loop control strategy for grid-connected inverters with LCL filter,” in *Proc. IEEE 6th Int. Power Electron. Motion Control Conf.*, May 2009, pp. 712–715.

[22] X. Chen, Y. Zhang, S. Wang, J. Chen, and C. Gong, “Impedance-phased dynamic control method for grid-connected inverters in a weak grid,” *IEEE Trans. Power Electron.*, vol. 32, no. 1, pp. 274–283, Jan. 2017.

[23] M. Cespedes and J. Sun, “Impedance modeling and analysis of grid-connected voltage-source converters,” *IEEE Trans. Power Electron.*, vol. 29, no. 3, pp. 1254–1261, Mar. 2014.

[24] M. Cespedes and J. Sun, “Adaptive control of grid-connected inverters based on Online grid impedance measurements,” *IEEE Trans. Sustain. Energy*, vol. 5, no. 2, pp. 516–523, Apr. 2014.

[25] H. Zhang, Y. Su, D. Zhang, Z. Jun, R. Chi, J. Pei, and L. Wan, “The optimization of controller parameters for three phases PLL tracking system,” in *Proc. Chinese Automat. Congr. (CAC)*, Nov. 2015, pp. 1316–1321.

**ZHIWEI XIE** was born in Hunan, China, in 1994. She received the B.S. degree in electronic information engineering from the Changsha University of Science and Technology, Changsha, China, in 2016. She is currently pursuing the Ph.D. degree in electrical engineering with Hunan University, Changsha.

Her research interests include power electronics converter and distributed generation.

**YANDONG CHEN** (S’13–M’14–SM’18) was born in Hunan, China, in 1979. He received the B.S. and M.S. degrees in instrument science and technology, and the Ph.D. degree in electrical engineering from Hunan University, Changsha, China, in 2003, 2006, and 2014, respectively.

He has been an Associate Professor with the College of Electrical and Information Engineering, Hunan University. His research interests include power electronics for microgrid, distributed generation, power supply, and energy storage. He was a recipient of the 2014 National Technological Invention awards for China and the 2014 WIPO-SIPO Award for Chinese Outstanding Patented Invention. He is a Senior Member of the IEEE PES and PELS.

**WENHUA WU** was born in Hunan, China, 1991. He received the B.S. degree from the College of Electrical and Information Engineering, Hunan University, Changsha, China, in 2014, and the Ph.D. degree in electrical engineering from Hunan University, in 2019.

His research interests include power electronics and distributed power systems.

**HAINING WANG** was born in Henan, China, in 1992. He received the B.Sc. degree in electrical engineering from Henan Polytechnic University, Jiaozuo, China, in 2015. He is currently pursuing the Ph.D. degree in electrical engineering with Hunan University, Changsha, China.

His research interests include power electronics for microgrid, distributed generation, power quality, and energy storage.

**JIAN GUO** was born in Hubei, China, in 1995. He received the B.S. degree in electrical engineering from the China University of Mining and Technology, Xuzhou, China, in 2017. He is currently pursuing the Ph.D. degree in electrical engineering with Hunan University, Changsha, China.

His research interests include power electronics for microgrid, distributed generation, and power quality.

**AN LUO** (SM’09) was born in Changsha, China, in 1957. He received the B.S. and M.S. degrees in industrial automation from Hunan University, Changsha, in 1982 and 1986, respectively, and the Ph.D. degree in fluid power transmission and control from Zhejiang University, Hangzhou, China, in 1993.

From 1996 to 2002, he was a Professor with Central South University. Since 2003, he has been a Professor with the College of Electrical and Information Engineering, Hunan University, where he also serves as the Chief of the National Electric Power Conversion and the Control Engineering Technology Research Center. His research interests mainly include distributed generation, microgrid, and power quality. He was elected to the Chinese National Academy of Engineering (CNAE), in 2015, and the Highest Honor for scientists and engineers, and scientists in China. He was a recipient of the highly prestigious China National Science and Technology awards three times, in 2014, 2010, and 2006.

**YUANCAN XU** was born in Hunan, China, in 1996. She received the B.S degree in power system and automation from the Changsha University of Science and Technology, Changsha, China, in 2017. She is currently pursuing the Ph.D. degree in electrical engineering with Hunan University, Changsha.

Her research interests include power electronics for microgrid, distributed generation, power quality, and energy storage.