Justifying Equality

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Justifying Equality

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Abstract
We consider the problem of finding irredundant bases for inconsistent sets of equalities and disequalities. These are subsets of inconsistent sets which do not contain any literals which do not contribute to the unsatisfiability in an essential way, and can therefore be discarded. The approach we are pursuing here is to decorate derivations with proofs and to extract irredundant sets of assumptions from these proofs. This requires specialized operators on proofs, but the basic inference systems are otherwise left unchanged. In particular, we include justifying inference systems for union-find structures and abstract congruence closure, but our constructions can also be applied to other inference systems such as Gaussian elimination.

1 Introduction
Constraint solving has many applications, including the discovery of abstraction predicates in protocol and software verification \cite{4} and for lazy combinations for planning \cite{13} and formal verification \cite{13,2,6,5}. The effectiveness of these constraint solving problems depends on identifying “small” inconsistent subsets of constraints.

We therefore consider the problem of finding an irredundant basis for an inconsistent set $\Gamma$ of equalities and disequalities. These are subsets of $\Gamma$ which do not contain any redundant literal, that is, literals which do not contribute to the unsatisfiability of $\Gamma$ in an essential way, and can therefore be discarded.

This notion of irredundant basis is unrelated to the commonly used proof-theoretic measure which counts the number of inference steps. Also, irredundant bases are not necessarily minimal among all inconsistent subsets of $\Gamma$. For example, $\{x = z, y = z, x = y, x \neq y\}$ is inconsistent, $\{x = z, y = z, x \neq y\}$ is an irredundant basis for this inconsistency, but obviously it is not minimal as $\{x = y, x \neq y\}$ is also inconsistent. Indeed, the computation of minimally inconsistent
bases is usually harder than the problem of computing irredundant bases. For example, an irredundant basis for an inconsistent conjunction of variable equalities and disequalities can be computed in $O(n \log(n))$ time—with $n$ the number of variables—whereas standard algorithms based on Boolean matrix multiplication for producing shortest deduction paths between two variables take $O(n^3)$ time. Moreover, the problem of minimal bases for equality over uninterpreted functions is NP-hard.

Our starting point is the union-find structure used for deciding equality. A canonizer constructs canonical representatives for given terms with respect to the given equalities so that equality can be decided by syntactically comparing canonical forms. Such canonizer-based inference systems are attractive from an algorithmic point of view as equalities are applied in a directed way and term universes in combination procedures based on canonization are usually much smaller than the corresponding term universes in combination methods without such a canonizer. However, reduction to canonical forms accumulates many redundant literals into the assumptions of corresponding proofs.

Consider, for example, the inconsistent set $\{x = x', x' = z, y = x', u = f(x), u \neq v, v = f(y)\}$, where $f$ is an uninterpreted function symbol and all variables are existentially-quantified (constants). In processing these literals from left to right, abstract congruence closure (ACC) builds up a set of directed equalities $\{x \rightarrow z, x' \rightarrow z, y \rightarrow z\}$, with the left hand sides assumed to be larger than the right hand sides according to some given variable ordering. The variable arguments of the uninterpreted terms $f(x)$ and $f(y)$ are both replaced with their canonical representative $z$, and application of congruence yields the inconsistency. Since all input literals are used in this proof of unsatisfiability, simply tracking dependencies or collecting assumptions from an explicitly generated proof object is not sufficient for generating irredundant bases. The algorithm in [6] for computing irredundant bases by successively eliminating redundant literals has proven to be too costly in practice.

The approach we are pursuing here is to decorate derivations with proofs and to extract an irredundant set of assumptions from these proofs. This requires specialized operators on proofs, but the basic inference procedures are left unchanged. Our main contribution is a join operator on directed equality proofs for $x = z$ and $y = z$ with $x, y$ larger than $z$ according to some given variable ordering. Obviously, taking the union of the assumptions of these two proofs leads to imprecision as there might be a join $z'$ of $x, y$ which is greater than $z$. However, the symmetric difference of assumptions yields irredundant bases for these kinds of “valley” proofs. We extend this basic insight to produce proofs with small sets of justifications for congruence closure.

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1 But sub-cubic algorithms are possible for solving the related problem of finding successor vertices of shortest paths [7].
of identities on proofs such as (Definition 2.1) Let $\Gamma$ be a $\Sigma$-structure $M$ closed under isomorphism. A set of $\Sigma$-literals included in Figure 1. Judgements are of the form $\vdash \phi$. A set $\Gamma$ of $\Sigma$ literals is $U$-basis for $\Gamma$. Proofs built up from reflexivity ($1_t$), symmetry ($\rho^{-1}$), and transitivity ($\tau; \sigma : s = r$) are referred to as equality-chaining proofs. We also make use of a number of identities on proofs such as ($1_t; \rho = \rho$ and $cg_f(1_{t_1}, \ldots, 1_{t_n}) = 1_{f(t_1, \ldots, t_n)}$).

\[ \vdash e_{s,t} : s = t \quad \vdash d_{s,t} : s \neq t \]
\[ \vdash 1_t : t = t \quad \vdash \rho : s = t \]
\[ \vdash \rho^{-1} : t = s \quad \vdash \tau : s = t, \vdash \sigma : t = r \]
\[ \vdash \rho_1 : s_1 = t_1, \ldots, \rho_n : s_n = t_n \]
\[ \vdash cg_f(\rho_1, \ldots, \rho_n) : f(s_1, \ldots, s_n) = f(t_1, \ldots, t_n) \]
\[ \vdash \rho : t \neq t \]
\[ \vdash 0(\rho) : \bot \]

Fig. 1. Proof theory for $U$.

2 Background

Given a signature $\Sigma$, a $\Sigma$-structure $M$ maps each $n$-ary function symbol $f$ in $\Sigma$ to an $n$-ary map $M(f)$ over a suitable domain. We assume that $M$ also maps free variables $x$ to domain elements $M(x)$. The interpretation $M[t]$ of a $\Sigma$-term $t$ in a $\Sigma$-structure $M$ is defined so that $M[x] = M(x)$, and $M[f(t_1, \ldots, t_n)] = M(f)(M[t_1], \ldots, M[t_n])$. A $\Sigma$-literal is either a $\Sigma$-equality or a $\Sigma$-disequality, and a $\Sigma$-equality $s = t$ ($\Sigma$-disequality $s \neq t$) is satisfied in $\Sigma$-structure $M$ iff $M[s] = M[t]$ ($M[s] \neq M[t]$). The interpretation of the propositional connectives and quantifiers is standard [10]. When $\Sigma$-structure $M$ satisfies a $\Sigma$-formula $\phi$, we write $M \models \phi$. A $\Sigma$-theory $T$ is a class of $\Sigma$-structures—the models of the theory—closed under isomorphism. A set of $\Sigma$-literals $L$ is $T$-unsatisfiable if there is no $\Sigma$-structure $M$ in $T$ such that $M \models l$ for all $l \in L$. Literals $L$ are $T$-valid if for all $\Sigma$-structures $M$, $M \models l$ for all $l \in L$.

The proof theory for the theory $U$ of equality over uninterpreted functions is included in Figure 1. Judgements are of the form $\vdash p : \phi$ with $p$ a proof of literal $\phi$. The assumptions of proofs are obtained as follows.

\[ Ax(e_{s,t}) = \{e_{s,t}\} \quad Ax(d_{s,t}) = \{d_{s,t}\} \]
\[ Ax(1_s) = \emptyset \quad Ax(\rho^{-1}) = Ax(\rho) \]
\[ Ax(\tau; \sigma) = Ax(\tau) \cup Ax(\sigma) \quad Ax(cg_f(\rho_1, \ldots, \rho_n)) = Ax(\rho_1) \cup \ldots \cup Ax(\rho_n) \]

The theory of equality over variables is the $U$ theory for the empty signature $\Sigma$. A set $\Gamma$ of $\Sigma$ literals is $U$-unsatisfiable if, and only if, there is a proof $\rho$ of $\bot$ with $Ax(\rho) \subseteq \Gamma$. Proofs built up from reflexivity ($1_t$), symmetry ($\rho^{-1}$), and transitivity ($\tau; \sigma$) are referred to as equality-chaining proofs. We also make use of a number of identities on proofs such as ($1_t; \rho = \rho$ and $cg_f(1_{t_1}, \ldots, 1_{t_n}) = 1_{f(t_1, \ldots, t_n)}$).

**Definition 2.1** Let $\Gamma$ be a $T$-unsatisfiable set of $\Sigma$-literals.

(i) If $\Delta \subseteq \Gamma$ and $\Delta$ is $T$-unsatisfiable, then $\Delta$ is a $T$-basis for $\Gamma$.

(ii) A $T$-basis $\Delta$ of $\Gamma$ is minimal if there is no $T$-basis $\Delta'$ of $\Gamma$ such that $|\Delta'| < |\Delta|$, where $|.|$ denotes the set-theoretic cardinality.

(iii) A $T$-basis $\Delta$ of $\Gamma$ is irredundant if no strict subset of $\Delta$ is a $T$-basis for $\Gamma$. 

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3
Since $\Gamma \rightarrow \varphi$ is $T$-valid if, and only if, $\Gamma \land \neg \varphi$ is $T$-unsatisfiable, we also say that $\Theta \subseteq \Gamma$ is a $T$-basis for $\varphi$ if $\Theta \rightarrow \varphi$ if $T$-valid. Moreover, $\rho$ is an irredundant proof for $\varphi$ if $Ax(\rho)$ is an irredundant basis for $\varphi$.

3 Equality over Variables

A set $\Gamma$ of equalities and disequalities over variables is inconsistent if, and only if, there is a disequality $x \neq y$ in $\Gamma$ such that $x$ and $y$ are in the same equivalence class of the equivalence closure of the equalities in $\Gamma$. Using the union-find algorithm, a partitioning of the variables in $\Gamma$ is maintained in an incremental manner (see, for example, [3]). The operation $\text{union}(x = y)$ merges the two equivalence classes for $x$ and $y$, and $\text{find}(x)$ returns the canonical representative of the equivalence class containing the variable $x$. A sequence of $m$ union and find operations can be performed in worst-case time $O(m \alpha(n))$ with $n$ the number of variables in $\Gamma$ and $\alpha(n)$ the inverse of the Ackermann function [11].

We extend the union-find algorithm with an operator $\text{explain}(x = y)$ which returns an irredundant basis for the implied equality $x = y$, and analyze its complexity.

Definition 3.1 [Union-find-explain structure] Let $(\mathcal{V}, \prec)$ be a pair consisting of a nonempty, finite set $\mathcal{V}$ of variables with a total ordering $\prec$ on $\mathcal{V}$, and let $E$ be the set of equalities over $\mathcal{V}$. Then, a union-find-explain structure is a pair of functions $(\phi : \mathcal{V} \rightarrow \mathcal{V}, \pi : \mathcal{V} \rightarrow 2^E)$ such that, for all $x \in \mathcal{V}$, $\phi(x) \prec x$ or $\phi(x) \equiv x$, and $\pi(x)$ is a basis for $x = \phi(x)$.

For a union-find-explain structure $(\phi, \pi)$, $\phi^*(x)$ denotes the canonical representative of the equivalence class for $x$, and $\pi^*(x)$ is a basis for $x = \phi^*(x)$.

Definition 3.2 Let $(\phi, \pi)$ be a union-find-explain structure; then:

$$
\phi^*(x) := \begin{cases} 
  x, & \phi(x) \equiv x \\
  \phi^*(\phi(x)), & \text{otherwise}
\end{cases}
$$

$$
\pi^*(x) := \begin{cases} 
  \emptyset, & \phi(x) \equiv x \\
  \pi(x) \cup \pi^*(\phi(x)), & \text{otherwise}
\end{cases}
$$

where $s_1 \cup s_2 := (s_1 \cup s_2)\setminus(s_1 \cap s_2)$ denotes the symmetric difference of two sets.

Variable equalities are added incrementally to a union-find-explain structure by the union operation.

Definition 3.3 Let $(\phi, \pi)$ be a union-find-explain structure; then:

$$
\text{union}_{(\phi, \pi)}(\rho, x, y) := \begin{cases} 
  (\phi, \pi), & x' \equiv y' \\
  (\phi[x' := y'], \pi[x' := \Pi]), & y' \prec x' \\
  (\phi[y' := x'], \pi[y' := \Pi]), & x' \prec y'
\end{cases}
$$

with $x' \equiv \phi^*(x)$, $y' \equiv \phi^*(y)$, $\Pi = \pi^*(x) \cup \pi^*(y) \cup \{\rho\}$. 

4
We say that \((\phi, \pi)\) is a union-find-explain structure for a finite set \(E\) of variable equalities if \((\phi, \pi)\) is the result of processing the equalities in \(E\), starting with 
\((\lambda x. \; x, \; \lambda x. \; \emptyset)\). In this case, \(\phi^*(x) \equiv \phi^*(y)\) if, and only if, \(E \rightarrow x = y\) is valid. In other words, a disequality \(x \neq y\) is inconsistent with \(E\) if, and only if, \(\phi^*(x) \equiv \phi^*(y)\). We show that an irredundant basis for implied equalities \(x = y\) is obtained by computing the symmetric difference of \(\pi^*(x)\) and \(\pi^*(y)\).

**Theorem 3.4** Let \((\phi, \pi)\) be a union-find-explain structure for a finite set of variable equalities; then (for all \(x, y \in V\))

(i) \(\pi^*(x)\) is an irredundant basis for \(x = \phi^*(x)\), and

(ii) if \(\phi^*(x) \equiv \phi^*(y)\), then \(\pi^*(x) \cup \pi^*(y)\) is an irredundant basis for \(x = y\).

**Proof.** By induction on the number of unions. These properties hold initially, since all nodes are distinct and for all \(x, \phi(x) \equiv x\), and \(\pi(x)\) is empty. For the induction step from \(n\) to \(n + 1\), we assume that these properties hold for \((\phi_{n+1}, \pi_{n+1})\), \((\phi_{n+1}, \pi_{n+1}) = \text{union}(\phi_n, \pi_n)(\rho, x, y)\), and, without loss of generality, \(\phi^*(y) \prec \phi^*(x)\). Then, for any \(\hat{x}\) in the equivalence class of \(x\),
\[
\pi^*_{n+1}(\hat{x}) = \pi^*_{n}(\hat{x}) \cup \pi_{n+1}(\phi^*_n(x)) \\
\quad = \pi^*_{n}(\hat{x}) \cup (\pi^*_n(x) \cup \{\rho\} \cup \pi^*_n(y)) \\
\quad = (\pi^*_{n}(\hat{x}) \cup \pi^*_n(x)) \cup \{\rho\} \cup \pi^*_n(y),
\]
because the equivalence classes of \(x\) and \(y\) are disjoint, repeated assumptions can only occur within \(\pi^*_{n}(\hat{x}) \cup \pi^*_n(x)\), but we know by the induction hypothesis that this basis is irredundant.

For the second part, we need to show that for \(\hat{x}\) and \(\hat{y}\) from the equivalence classes of \(x\) and \(y\), respectively, \(\pi^*_{n+1}(\hat{x}) \cup \pi^*_{n+1}(\hat{y})\) is irredundant. As we have already seen,
\[
\pi^*_{n+1}(\hat{x}) = (\pi^*_{n}(\hat{x}) \cup \pi^*_n(x)) \cup \{\rho\} \cup \pi^*_n(y),
\]
and
\[
\pi^*_{n+1}(\hat{y}) = \pi^*_n(y).
\]
Hence \(\pi^*_{n+1}(\hat{x}) \cup \pi^*_{n+1}(\hat{y}) = (\pi^*_{n}(\hat{x}) \cup \pi^*_n(x)) \cup \{\rho\} \cup (\pi^*_n(\hat{y}) \cup \pi^*_n(y))\) which we know is irredundant by the induction hypothesis.

**Example 3.5** Let \(\Gamma := \{x \overset{\rho_1}{=} w, \; x \overset{\rho_2}{=} z, \; y \overset{\rho_3}{=} z\}\). Using the variable ordering \(w \prec z \prec x \prec y\), processing the equalities from left to right yields the following representation of a union-find-explain structure, where “find” edges are labeled with the elements of the corresponding basis.

\[
\begin{array}{c}
\text{W} \\
\rho_1, \rho_2, \rho_3 \\
\rho_1, \rho_2 \\
\rho_1 \\
\text{X} \\
\text{Z} \\
\text{Y}
\end{array}
\]

Clearly, the equality \(\Gamma \rightarrow x = y\) is valid, since \(\phi^*(x) \equiv \phi^*(y) \equiv w\). The irredundant basis \(\{\rho_2, \rho_3\}\) for \(x = y\) is obtained as \(\pi^*(x) \cup \pi^*(y) = \{\rho_1\} \cup \{\rho_1, \rho_2, \rho_3\}\).

The bases thus obtained are irredundant by Theorem 3.4 but they are not necessarily minimal.
Example 3.6 Let $\Gamma := \{ x \equiv z, \; y \equiv z, \; x \equiv y \}$, then the minimal basis for $y = x$ is $\{ \rho_3 \}$, but the method described above returns the basis $\{ \rho_1, \rho_2 \}$, which is irredundant but not minimal, since union discards the implied equality $x = y$.

Figure 3 includes an efficient implementation of union-find-explain as an extension of the usual union-find algorithm (see, for example, [3]). Sets of assumptions are represented using the constructors $\#$ (join), 1 (reflexivity), and $e[s = t]$ (assumption). The corresponding set of assumptions $Ax(\rho)$ for such a representation $\rho$ is obtained as follows.

$$
Ax(1) = \emptyset
$$

$$
Ax(e[s = t]) = \{ e[s = t] \}
$$

$$
Ax(\rho_1 \# \rho_2) = Ax(\rho_1) \cup Ax(\rho_2).
$$

The find structure is implemented using the function $p$ (parent) which is the identity on variables $(\lambda x.x)$ initially, whereas the proof structure $prf$ is initially 1 (reflexivity) on all inputs. $justify(x)$ is a straightforward implementation of $\pi^*$. Since $find(x)$ uses dynamic path compression, computations of $\pi^*$ are memoized and $\#$-nodes may be structure-shared. $union(x, y)$ uses the rank structure for selecting the new canonical variable. Because of possible path compression in the initial finds, $prf(z)$ and $justify(z)$ coincide for input variables of $union$. We implicitly use the identities $\rho \# 1 = 1 \# \rho = \rho$.

$explain(x, y)$ computes the set $Ax(\text{justify}(x) \# \text{justify}(y))$ as required by Theorem 3.4. Notice that an equality is in the assumptions $Ax(p)$ of such a $\#$-dag $p$ if, and only if, it occurs an odd number of times. We use reference counters $ref(\rho)$ for counting the number of occurrences of visited subdags. Subdags are visited in a breadth-first manner ignoring subdags with even reference counts. As a consequence, every 'node' in the $\#$-dag $\rho$ is visited at most once in collect. Finally, the breadth-first traversal in collect is obtained by a FIFO queue with operators enqueue, dequeue, and queue_is_empty, which ensures that a node is dequeued only after its immediate parent nodes have been dequeued.

Example 3.7 Processing $\{ x_1 \equiv x_2, \; x_3 \equiv x_2, \; y \equiv x_2, \; z \equiv x_4, \; x_4 \equiv x_5, \; x_2 \equiv x_4 \}$ yields a union-find-explain structure

Thus, $explain(y, x_3) = collect((\rho_3 \# \rho_1) \# \eta \# (\rho_2 \# \rho_1) \# \eta) = \{ \rho_2, \rho_3 \}$ without visiting $\eta$, since this subdag occurs twice.

If $h(n)$ denotes the maximum height of the find structure after $n$ union and find operations, then clearly find takes at most $O(h(n))$ time and creates at most $O(h(n))$ new $\#$-nodes, union takes at most $O(h(n))$ time and space. So,
\[ p \leftarrow (\lambda x. \, x); \text{prf} \leftarrow (\lambda p. \, 1); \text{rank} \leftarrow (\lambda x. \, 0); \text{result} \leftarrow \bot; \text{ref} \leftarrow (\lambda \rho. \, \bot) \]

\[
\text{find}(x) = \\
\quad \text{if not}(x \equiv p(x)) \text{ then} \\
\quad \quad p(x) \leftarrow \text{find}(p(x)); \text{prf}(x) \leftarrow (\text{prf}(p(x)) \# \text{prf}(x)); \\
\quad \text{return } p(x) \\
\]

\[
\text{union}(x, y) = \\
\quad x' \leftarrow \text{find}(x); \ y' \leftarrow \text{find}(y); \\
\quad \text{if rank}(x') > \text{rank}(y') \text{ then} \\
\quad \quad p(y') \leftarrow x'; \text{prf}(y') \leftarrow (\text{prf}(x) \# \text{prf}(y) \# e[x = y]) \\
\quad \text{else} \\
\quad \quad p(x') \leftarrow y'; \text{prf}(x') \leftarrow (\text{prf}(x) \# \text{prf}(y) \# e[x = y]); \\
\quad \quad \text{if rank}(x') \equiv \text{rank}(y') \text{ then rank}(y') \leftarrow \text{rank}(y') + 1 \\
\]

\[
\text{explain}(x, y) = \\
\quad \text{result} \leftarrow \emptyset; \text{ref} \leftarrow (\lambda \rho. \, 0); \\
\quad \text{collect}(\text{justify}(x) \# \text{justify}(y)); \\
\quad \text{return result} \\
\quad \text{justify}(x) = \\
\quad \quad \text{if } x \equiv p(x) \text{ then } 1 \text{ else } (\text{prf}(x) \# \text{justify}(p(x))) \\
\quad \text{collect}(\rho) = \\
\quad \quad \text{register}(\rho); \\
\quad \quad \text{while not(queue_is_empty()) do} \\
\quad \quad \quad \tau \leftarrow \text{dequeue}(); \\
\quad \quad \quad \text{if is_odd}(\text{ref}(\tau)) \text{ then} \\
\quad \quad \quad \quad \text{if } \tau \equiv \rho_1 \# \rho_2 \text{ then register}(\rho_1); \text{register}(\rho_2) \\
\quad \quad \quad \text{else if } \tau \equiv e[x = y] \text{ then result} \leftarrow \text{result} \cup \{e[x = y]\} \\
\]

\[
\text{register}(\rho) = \\
\quad \text{if ref}(\rho) \equiv 0 \text{ then enqueue}(\rho); \text{ref}(\rho) \leftarrow 1 \text{ else ref}(\rho) \leftarrow \text{ref}(\rho) + 1 \\
\]

Fig. 2. Implementation of union-find-explain.

A sequence of \( n \) union operations takes at most \( O(n \ h(n)) \) time and space. The explain is linear in the number of \#-nodes in justify(x) \# justify(y), so it takes at most \( O(n \ h(n)) \) time. Since the algorithm is using the weighted-union heuristic based on the rank structure, \( h(n) \) is bounded by \( \log(n) \). In fact, the run time of explain is bounded by \( O(n \ \alpha(n)) \), since the core union-find algorithm has this complexity and any (recursive) invocation of find and union produces only a constant number of \# nodes.
4 Equality over Uninterpreted Functions

We consider the problem of inferring small justifications for problems in the theory \( \mathcal{U} \) of equalities over uninterpreted functions. The starting point is abstract congruence closure (ACC) as defined by Kapur [8] and Bachmair and Tiwari [1]. ACC incrementally processes a finite set \( E \) of \( \mathcal{U} \)-equalities into an equivalent configuration \( V \) with \( \mathcal{U} \) a union-find-explain structure and \( U \) a set of directed, flat equalities of the form \( x = f(x_1, \ldots, x_n) \) with \( x, x_i \) variables. Irreducible configurations are congruence-closed in the sense that \( V \) implies \( x = y \) if \( U \) contains \( x = f(x_1, \ldots, x_n) \) and \( y = f(x_1, \ldots, x_n) \). The length of any maximal derivation using the inference rules for constructing an ACC is at most quadratic in the input size. In [9] we define a canonizer \( \text{can}_{(V,U)}(t) \) on irreducible configurations \( (V, U) \) and terms \( t \) for solving uniform word problems in \( \mathcal{U} \); that is, \( \text{can}_{(V,U)}(t_1) \equiv \text{can}_{(V,U)}(t_2) \) if, and only if, \( E \rightarrow t_1 = t_2 \) is \( \mathcal{U} \)-valid. In other words, a disequality \( t_1 \neq t_2 \) is \( \mathcal{U} \)-inconsistent with \( E \) if, and only if, \( \text{can}_{(V,U)}(t_1) \equiv \text{can}_{(V,U)}(t_2) \).

Justifying congruence closure is based on the ACC procedure in [9], and the results of Section 3 for generating irredundant bases using union-find-explain are reused below. In contrast to the developments in Section 3, however, justifications are given in terms of proof terms (see Figure 1) instead of sets of assumptions. This use of proof terms suggests various optimizations.

**Example 4.1** Consider the following equality chains.

\[
\begin{align*}
f_1(x_1) & \xRightarrow[\tau_1]{} x_1 \xRightarrow[\rho_1]{} x_2 \xRightarrow[\sigma_1]{} f_1(x_{n+1}) , \\
& \ldots , \\
f_n(x_1) & \xRightarrow[\tau_n]{} x_n \xRightarrow[\rho_n]{} x_{n+1} \xRightarrow[\sigma_n]{} f_n(x_{n+1})
\end{align*}
\]

With these equalities one obtains proofs \((i = 1, \ldots, n)\)

\[
\begin{align*}
\pi^i_1 := \tau_i ; \rho_i ; \sigma_i \\
\pi^i_2 := \text{cg}_{f_i} (\rho_1 ; \ldots ; \rho_n)
\end{align*}
\]

for \( f_i(x_1) = f_i(x_{n+1}) \). These two proofs are essentially different in that the set of assumptions \( A(x(\pi^i_1)) \) and \( A(x(\pi^i_2)) \) are incomparable with respect to set inclusion. There are \( 2^n \) different proofs for the equality

\[
g(f_1(x_1), \ldots, f_n(x_1)) = g(f_1(x_{n+1}), \ldots, f_n(x_{n+1}))
\]

depending on whether \( \pi^1_1 \) or \( \pi^2_2 \) is chosen for establishing equality between the \( i \)th argument terms, but the only irredundant proof is \( \text{cg}_g (\pi^1_2, \ldots, \pi^n_2) \).

This example shows that the generation of irredundant proofs for equalities in \( \mathcal{U} \) is expensive in general. Therefore, we introduce a weaker criterion that only requires irredundancy for equality chaining subproofs, that is, subproofs built up entirely from reflexivity, symmetry, and transitivity. The operator \( \text{unchain} \) transforms proofs to a set of non-equality-chaining subproofs. This operator is then used for defining a localized irredundancy criterion for proofs.
Definition 4.2 [Chaining]

\[
\text{unchain}(\rho_1; \rho_2) = \text{unchain}(\rho_1) \cup \text{unchain}(\rho_2)
\]

\[
\text{unchain}(\rho^{-1}) = \text{unchain}(\rho)
\]

\[
\text{unchain}(1_s) = \emptyset
\]

\[
\text{unchain}(e_{s,t}) = \{e_{s,t}\}
\]

\[
\text{unchain}(cg_f(\rho_1, \ldots, \rho_n)) = \{cg_f(\rho_1, \ldots, \rho_n)\}
\]

If \( \rho_1 \vdash t_1 = t_2 \) and \( \rho_2 \vdash t_2 = t_3 \), then \( \text{unchain}(\rho_1) \cup \text{unchain}(\rho_2) \) is an irredundant basis (Definition 2.1) for \( t_1 = t_3 \). The unchaining operator is used for defining the notion of local irredundancy for proofs. Intuitively, a proof is locally irredundant if all pure equality chaining subproofs are irredundant.

Definition 4.3 [Locally Irredundant Proofs]

A proof \( \rho \) for \( s = t \) is \textit{locally irredundant} if

(i) \( \rho \equiv 1_s \) or \( \rho \equiv e_{s,t} \) or

(ii) \( \rho \equiv cg_f(\rho_1, \ldots, \rho_n) \), and \( \rho_1, \ldots, \rho_n \) are locally irredundant, or

(iii) \( \rho \equiv \rho_1; \rho_2 \) or \( \rho \equiv \rho_1^{-1} \), and there is no proof \( \rho' \) for \( s = t \), such that \( \text{unchain}(\rho') \subset \text{unchain}(\rho) \), and for all \( \tau \in \text{unchain}(\rho) \), \( \tau \) is locally irredundant.

A locally irredundant proof is not necessarily irredundant.

Example 4.4 Reconsider Example 4.1. The proof \( cg_f(\pi_{1}, \pi_{2}, \ldots, \pi_{n}) \), for example, is locally irredundant but it is not irredundant, since the proof has a strictly smaller set of assumptions.

Example 4.5 Consider the judgement \( cg_f(e_{x,y}, e_{y,z}) \vdash f(x, y) = f(x, z) \). This proof is locally irredundant but it is not irredundant, since \( e_{y,z} \) already justifies \( f(x, y) = f(x, z) \) using the proof \( cg_f(1_{x}, e_{y,z}) \).

The operator \( \text{chain}_{s,t} \) simply converts a set of non-equality-chaining proofs for \( s = t \), as obtained from the unchaining operator, into an equality chaining proof for \( s = t \).

Definition 4.6 Let \( \Pi \) be a set of non-equality chaining proofs as obtained from \( \text{unchain}(\rho) \) for \( \rho \vdash s = t \); then:

\[
\text{chain}_{s,t}(\Pi) = \begin{cases} 
1_t & , \Pi = \emptyset \\
\rho; \text{chain}_{r,t}(\Pi - \{\rho\}) & , \rho \in \Pi \land \rho \vdash s = r \\
\rho^{-1}; \text{chain}_{r,t}(\Pi - \{\rho\}) & , \rho \in \Pi \land \rho \vdash r = s
\end{cases}
\]

Obviously, \( \text{chain}_{s,t}(\Pi) \vdash s = t \). Equality-chaining proofs are transformed by the proof transformer \( \rho_1 \downarrow \rho_2 \) in order to eliminate redundancies.

Definition 4.7 For \( \rho_1 \vdash s_1 = t \) and \( \rho_2 \vdash s_2 = t \), define the \textit{join} of \( \rho_1 \) and \( \rho_2 \) as

\[
\rho_1 \downarrow \rho_2 := \text{chain}_{s_1,s_2}(\text{unchain}(\rho_1) \cup \text{unchain}(\rho_2))
\]
a slight abuse of notation, we will just write $\rho$ and a proof of $x$ structure $(\phi, \pi)$ variables to equality proof terms. The $1$ is identical to the one in Definition 3.1, the $\pi$ for the literals in Example 3.5 one obtains the union-find-explain Example 4.10 Similar to Theorem 3.4.

**Proof.**

**Definition 4.8** A union-find-explain structure $(\phi, \pi)$ is *locally irredundant* if, and only if, for all $x$ and $y$ with $\phi^*(x) \equiv \phi^*(y)$, the proof $\pi^*(x) \downarrow \pi^*(y)$ is locally irredundant.

By replacing symmetric difference on sets of assumptions by the join operator, irredundant proofs for variable equalities are obtained as in Theorem 3.4.

**Lemma 4.9** Let $(\phi, \pi)$ be a union-find-explain structure with proofs for a set of equalities (see Figure 3), then:

(i) $\pi^*(x)$ is a locally irredundant proof for $x = \phi^*(x)$, and

(ii) $(\phi, \pi)$ is locally irredundant.

**Proof.** Similar to Theorem 3.4. \qed

**Example 4.10** For the literals in Example 3.3 one obtains the union-find-explain structure $(\phi, \pi)$ with

$$\phi := \{ x \mapsto w, z \mapsto w, y \mapsto w \}$$

$$\pi := \{ x \mapsto e_{x,w}, z \mapsto (e_{x,z}^{-1}; e_{x,w}), y \mapsto (e_{y,z}; e_{x,z}^{-1}; e_{x,w}) \}$$

and a proof of $x = y$ is $e_{x,z}; e_{y,z}^{-1}$ because

$$\downarrow_1 (e_{x,w}; e_{x,z}^{-1}; e_{x,w}) = \text{chain}_{x,y} (\text{unchain}(e_{x,w}) \cup \text{unchain}(e_{y,z}; e_{x,z}^{-1}; e_{x,w}))$$

$$= \text{chain}_{x,y} (\{e_{x,w}\} \cup \{e_{y,z}, e_{x,z}, e_{x,w}\})$$

$$= \text{chain}_{x,y} (\{e_{y,z}, e_{x,z}\})$$

$$= e_{x,z}; e_{y,z}^{-1}.$$
Configurations of justifying ACC include \((V, U)\) with \(V\) a union-find-explain structure and \(U\) a renaming context for representing a finite set of equalities \(u = f(x_1, \ldots, x_n)\).

**Definition 4.11** A renaming context is a finite map of bindings of the form \(u \mapsto f(x_1: \rho_1, \ldots, x_n: \rho_n)\) with \(u, x_1, \ldots, x_n \in V\) and \(\rho_i \vdash t_i = x_i\) for terms \(t_i\) \((i = 1, \ldots, n)\).

**Definition 4.12** A pair \((V, U)\) consisting of a union-find-explain structure and a renaming context is locally irredundant if

(i) \(V\) is a locally irredundant, and
(ii) for every \(u \mapsto f(x_1: \tau_1, \ldots, x_n: \tau_n)\) in \(U\), \(\rho_1, \ldots, \rho_n\) are locally irredundant proofs.

Abstract congruence closure (ACC) flattens input terms by introducing fresh renaming variables for nested flat subterms. The initial step in processing an equality or disequality in the ACC procedure compiles terms into variables by iteratively replacing flat subterms \(f(x_1, \ldots, x_n)\) with a renaming variable from a possibly extended renaming context. The following canonizer includes flattening and always returns a variable which is equal to the argument term in a possibly extended renaming context.

**Definition 4.13** For a union-find-explain structure \(V = (\phi, \pi)\) and a justifying renaming context \(U\), define:

\[
\text{can}_{(V, U)}(x) = (\phi^*(x) : \pi^*(x), U)
\]

\[
\text{can}_{(V, U)}(f(t_1, \ldots, t_n)) = \begin{cases} 
(\phi^*(u) : \text{cg} f(\rho_1 \downarrow \tau_1, \ldots, \rho_n \downarrow \tau_n); \pi^*(u), U_n) 
\text{ if } (u \mapsto f(x_1: \tau_1, \ldots, x_n: \tau_n)) \in U_n 
\end{cases}
\]

\[
(v : 1_v, \{v \mapsto f(x_1: \rho_1, \ldots, x_n: \rho_n\}) \cup U_n)
\]

otherwise (with \(v\) fresh)

where \((x_1: \rho_1, U_1) = \text{can}_{(V, U)}(t_1),\)

\((x_2: \rho_2, U_2) = \text{can}_{(V, U_1)}(t_2), \ldots,\)

\((x_n: \rho_n, U_n) = \text{can}_{(V, U_{n-1})}(t_n).\)

\(\text{can}_{(V, U)}(t)\) is a canonizer in the sense that the \(x \equiv y\) for \((x, \_ ) = \text{can}_{(V, U)}(t_1)\) and \((y, \_ ) = \text{can}_{(V, U)}(t_2)\) if, and only if, \(t_1 = t_2\) is validated by the equalities of \((V, U)\) \[9\]. Moreover, this canonizer returns locally irredundant proofs for the equality of its source and target term.

**Lemma 4.14** If \((V, U)\) is locally irredundant, and \(\text{can}_{(V, U)}(t) = (x: \rho, U')\), then

(i) \((V, U')\) is also locally irredundant, and
(ii) \(\rho\) is a locally irredundant proof for \(t = x\).

**Proof.** By induction on the structure of the term \(t\), and Lemma \[4.9\]. \[4.10\]
A justifying ACC inference system for processing equalities and disequalities over uninterpreted functions is described in Figure 4. Configurations \((\Gamma, D, V, U)\) of this inference system consist of a finite set \(\Gamma\) of assumptions of the form \(e_{s,t}\) and \(d_{s,t}\), a finite set \(D\) of variable disequalities \(d_{s,t} \mapsto (x : \rho_1) \neq (y : \rho_2)\) with \(\rho_1 \vdash s = x\) and \(\rho_2 \vdash t = y\), a union-find-explain structure \(V = (\phi, \pi)\), and a renaming context \(U\).

**Definition 4.15** A configuration \((\Gamma, D, V, U)\) is locally irredundant if \(V, U\) is locally irredundant according to Definition 4.12, and for every \(d_{s,t} \mapsto (x : \rho_1) \neq (y : \rho_2)\) in \(D\), \(\rho_1\) and \(\rho_2\) are locally irredundant.

The \(\text{eq}\) rule in Figure 4 processes input equalities \(s = t\) by merging the corresponding decorated variables in \(V\), and, similarly, \(\text{diseq}\) processes disequalities \(s \neq t\). Disequalities \(x \neq x\) are reduced to the unsatisfiable \(\bot\) using the \(\text{bot}\) rule, whereas \(\text{cong}\) deduces variable equalities \(u = v\) from \(u = t\) and \(v = t\) with \(t\) a flat term, and \(\text{uprop}\) and \(\text{dprop}\) propagate variable equalities into renaming contexts \(U\) and disequalities \(D\), respectively. Here, \(\text{dprop}\) is applied symmetrically.

For termination, soundness and completion of undecorated versions of this abstract congruence closure procedure see, for example, [1, 2].

**Theorem 4.16** Let \(\Gamma\) be an unsatisfiable, finite set of \(U\)-equalities and \(U\)-disequalities, and let \(\text{unsat}(d_{s,t}, \rho)\) be an irreducible configuration with respect to the justifying ACC inference system in Figure 4 and starting configuration \((\Gamma, (\lambda x.x, \lambda x.1_x), \emptyset, \emptyset)\), then \(\rho\) is a locally irredundant proof.

**Proof.** Clearly the initial configuration is locally irredundant. By Lemmas 4.3 and 4.14, all rules but \(\text{bot}\) preserve local irredundancy. The proof \(\rho \equiv \tau \downarrow \sigma\) obtained by the application of the \(\text{bot}\) rule is locally irredundant, because \(\tau\) and \(\sigma\) are locally irredundant proofs for \(s = x\) and \(t = x\) (Lemma 4.3).

**Example 4.17** Processing \(\{x_1 \overset{\rho_1}{=} f(x_2), x_3 \overset{\rho_2}{=} f(x_4), x_5 \overset{\rho_3}{=} x_6, x_2 \overset{\rho_4}{=} x_5, x_4 \overset{\rho_5}{=} x_3\}\) from the left to right yields the final configuration

\[
(\emptyset, \left\{ \begin{array}{lll}
    x_1 \overset{\rho_1}{=} u_1, & x_3 \overset{\rho_2}{=} u_2, & x_5 \overset{\rho_3}{=} x_6 \\
    x_2 \overset{\rho_4; \rho_3}{=} x_6, & x_4 \overset{\rho_5; \rho_3}{=} x_6, & u_1 \overset{\text{eq}(\rho_4; \rho_5^{-1})}{\rightarrow} u_2
\end{array} \right\}, \emptyset, \{u_2 \mapsto f(x_6 : \rho_5; \rho_3)\})
\]

First, canonization of \(x_1\) yields \((x_1 : 1_{x_1})\) and canonization of \(f(x_2)\) yields \((u_1 : 1_{u_1})\), where \(u_1\) is a fresh variable and \(u_1 \mapsto f(x_2 : 1_{x_2})\) is the corresponding renaming. Using rule \(\text{eq}\), variables \(x_1\) and \(u_1\) are merged with proof \((1_{x_1}^{-1}; \rho_1; 1_{u_1}) = \rho_1\). Second, when processing \(x_3 = f(x_4)\), \(x_3\) canonizes to \((x_3 : 1_{x_3})\) and \(f(x_4)\) canonizes to \((u_2 : 1_{u_2})\), where \(u_2\) is a fresh variable and \(u_2 \mapsto f(x_4 : 1_{x_4})\) is the corresponding renaming. Thus, \(x_3\) and \(u_2\) are merged with proof \(\rho_2\) (rule \(\text{eq}\)). Third, \(x_5\) and \(x_6\) are canonized to \((x_5 : 1_{x_5})\) and \((x_6 : 1_{x_6})\), respectively. Using rule \(\text{eq}\), variables \(x_5\) and \(x_6\) are merged with
\[
\text{eq} \quad \frac{\{(e_{s,t}) \cup \Gamma, V, D, U\}}{(\Gamma, \text{union}_V(\{(\tau^{-1}; e_{s,t}; \sigma), x, y\}, D, U'))}
\]

where \((x : \tau, U') = \text{can}_V(s)\) and \((y : \sigma, U') = \text{can}_{V,U'}(t)\)

\[
\text{diseq} \quad \frac{\{(d_{s,t}) \cup \Gamma, V, D, U\}}{(\Gamma, V, \{d_{s,t} \mapsto (x : \tau) \neq (y : \sigma)\} \cup D, U')}
\]

where \((x : \tau, U') = \text{can}_V(s)\) and \((y : \sigma, U') = \text{can}_{V,U'}(t)\)

\[
\text{bot} \quad \frac{(\Gamma, \{d_{s,t} \mapsto (x : \tau) \neq (x : \sigma)\} \cup D, U)}{\text{unsat}(d_{s,t}, \tau \downarrow \sigma)}
\]

\[
\text{uprop} \quad \frac{(\Gamma, \{d_{s,t} \mapsto (x : \tau) \neq (x : \sigma)\} \cup D, U)}{(\Gamma, (\phi, \pi), D, \{u \mapsto f(x_1 : \rho_1, \ldots, x_i : \rho_i, \ldots, x_n : \rho_n\}) \cup U)}
\]

where \(\phi^*(x_i) \equiv y\) and \(\pi^*(x_i) = \tau\)

\[
\text{dprop} \quad \frac{(\Gamma, (\phi, \pi), \{d_{s,t} \mapsto (x : \sigma_1) \neq (z : \sigma_2)\} \cup D, U)}{(\Gamma, (\phi, \pi), \{d_{s,t} \mapsto (y : \tau) \downarrow \sigma_1) \neq (z : \sigma_2)\} \cup D, U)}
\]

where \(\phi^*(x) \equiv y\) and \(\pi^*(x) = \tau\)

\[
\text{cong} \quad \frac{(\Gamma, V, D, \{u \mapsto f(x_1 : \rho_1, \ldots, x_n : \rho_n)\}) \cup U)}{(\Gamma, V', D, \{u \mapsto f(x_1 : \tau_1, \ldots, x_n : \tau_n)\}) \cup U)}
\]

where \(V' = \text{union}_V(cg_f(\rho_1 \downarrow \tau_1, \ldots, \rho_n \downarrow \tau_n), u, v)\)

Fig. 4. Abstract congruence closure with proofs.

proof \(\rho_3\). Forth, \(x_2\) and \(x_5\) are canonized to \((x_2 : 1_{x_2})\) and \((x_6 : \rho_3)\), respectively. Using rule eq, variables \(x_2\) and \(x_6\) are merged with proof \(\rho_4 ; \rho_3\). This variable equality is propagated (rule uprop) to obtain the instantiated renaming \(u_1 \mapsto f(x_6 : \rho_4 ; \rho_3)\). Finally, \(x_4\) and \(x_5\) are canonized to \((x_4 : 1_{x_4})\) and \((x_6 : \rho_3)\), respectively. Using rule eq, variables \(x_4\) and \(x_6\) are merged with proof \(\rho_5 ; \rho_3\). This variable equality is propagated (rule uprop) to obtain the instantiated renaming \(u_2 \mapsto f(x_6 : \rho_5 ; \rho_3)\). Using rule cong, variables \(u_1\) and \(u_2\) are merged with proof \(\text{cg}_f(\rho_4 ; \rho_3) \downarrow (\rho_5 ; \rho_3) = \text{cg}_f(\rho_4 ; \rho_5^{-1})\), and the renaming \(u_1 \mapsto f(x_6 : \rho_4 ; \rho_3)\) is removed from \(U\).

Now, consider the implied equality \(f(x_2) = f(x_4)\). Its left hand side \(f(x_2)\) canonizes to \((u_2 : \text{cg}_f(\rho_4 ; \rho_5^{-1}))\), whereas \(f(x_4)\) canonizes to \((u_2 : \text{cg}_f(\rho_5 ; \rho_3)) = (u_2 : 1_{x_4})\). Thus \(\text{cg}_f(\rho_4 ; \rho_5^{-1}) \downarrow 1_{x_4} = \text{cg}_f(\rho_4 ; \rho_5^{-1})\) is a locally irredundant proof for \(f(x_2) = f(x_4)\). This proof also happens to be minimal. Now, consider the implied equality \(f(x_2) = f(x_6)\). The right hand side \(f(x_6)\) canonizes to \((u_2 : \text{cg}_f(\rho_5 ; \rho_3))\). Thus \(\text{cg}_f(\rho_4 ; \rho_5^{-1}); \text{cg}_f(\rho_5 ; \rho_3)\) is a locally
irredundant proof for \( f(x_2) = f(x_6) \), but it is not irredundant. However, this proof can be simplified using the following identity on proofs.

\[
( cg_f(\tau_1, \ldots, \tau_n); cg_f(\sigma_1, \ldots, \sigma_n) ) = cg_f(\tau_1 \downarrow \sigma_1, \ldots, \tau_n \downarrow \sigma_n )
\]

Thus we obtain the irredundant proof \( cg_f((\rho_4; \rho_5^{-1}) \downarrow (\rho_5; \rho_3)) = cg_f(\rho_4; \rho_3) \) for \( f(x_2) = f(x_6) \).

**Lemma 4.18** \( Ax(cg_f(\tau_1 \downarrow \sigma_1, \ldots, \tau_n \downarrow \sigma_n)) \subseteq Ax(cg_f(\tau_1, \ldots, \tau_n); cg_f(\sigma_1, \ldots, \sigma_n)) \)

**Proof.** Using \( Ax(\rho_1 \downarrow \rho_2) \subseteq (Ax(\rho_1) \cup Ax(\rho_2)) \). \( \square \)

In general justifying ACC does not yield irredundant proofs or even minimal proofs (Example 4.1), but irredundant proofs may be obtained by replacing equality-chaining subproofs \( \sigma \) with congruence subproofs \( \tau \) whenever \( Ax(\tau) \subseteq Ax(\sigma) \). For example, the proof \( cg_f(\sigma); cg_f(\tau) \) might be replaced by \( cg_f(\sigma \downarrow \tau) \). In cases \( Ax(\sigma) \) and \( Ax(\tau) \) are incomparable with respect to set inclusion (as is the case in Example 4.1), however, it is unclear which proof should be used, since the proof transformation may now depend on the structure of the complete proof. Therefore, it seems too expensive, in practice, to produce (globally) irredundant proofs, and, arguably, maintaining locally irredundant proofs is a good compromise between the conflicting goals of conciseness of justifications and associated computational costs.

## 5 Conclusions

We have presented systems for proving irredundant proofs for variable equality and extended this proof-producing system to obtain “small”, that is, locally irredundant proofs, for abstract congruence closure. The main characteristics of our proof-producing extensions is that they do not change the underlying algorithms and therefore the algorithmic advantage of canonization is maintained. Although it is possible to maintain fully irredundant proofs for congruence closure and other theories, this may be prohibitively expensive in practice as search is involved.

Our approach can be extended to also work for other inference systems such as Gaussian elimination for the linear arithmetic equality theory. Polynomials are decorated with proofs to obtain \( (q_0 + q_1 x_1 : \sigma_1 + \ldots + q_n x_n : \sigma_n) : \rho \). Such a decoration represents a proof \( (\rho; cg(\sigma_1, \ldots, \sigma_n)) \) for \( p' = q_0 + q_1 x_1 + \ldots + q_n x_n \), for some source polynomial \( p' \). Our justifying equality framework can also be applied to obtain small proofs for a Shostak combination \( \exists \).

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