COMPLEXITY CLASSES AND COMPLETENESS IN ALGEBRAIC
GEOMETRY

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Abstract. We study the computational complexity of sequences of projective
varieties. We define analogues of the complexity classes P and NP for these and prove
the NP-completeness of a sequence called the universal circuit resultant. This is the
first family of compact spaces shown to be NP-complete in a geometric setting.

Valiant’s theory of algebraic/arithmetic complexity classes is an algebraic analogue
of Boolean complexity theory, where Boolean functions are replaced by polynomials
over any ring, and Boolean circuits are replaced by arithmetic circuits that use the
×, + operations of the ring instead of the Boolean operations. An interesting point
about Valiant’s theory is how sequences in VNP are made. Keeping with the Boolean
procedure of taking an efficiently computable ‘verifier’ \( g_n(x_1, \ldots, x_n) \) and searching
through all ‘proofs’ \( e \) it might accept by taking

\[
f_n(\cdot) = \bigvee_{e \in \{0,1\}^{m(n)}} g_{n+m(n)}(\cdot, e),
\]

Valiant’s theory defines the sequences in VNP as those of the form

\[
f_n(\cdot) = \sum_{e \in \{0,1\}^{m(n)}} g_{n+m(n)}(\cdot, e),
\]

where \( g_n(x_1, \ldots, x_n) \in F[x_1, \ldots, x_n] \) form an efficiently computable sequence. In this
sense, Valiant’s theory, while a source of interesting geometry problems, is a purely
algebraic analogue of Boolean complexity.

What would a geometric analogue look like? If we wanted to make a naive, geometric
version of Boolean complexity, we would do the following. Take sequences of \( X_n \subset F_2^n \),
and say they are in P if they are the zero-set of polynomials \( F_2[x_1, \ldots, x_n] \) which can
be computed by arithmetic circuits of polynomially bounded size. Then, define NP by
taking sequences \( Y_n \subset F_2^n \) in P and projecting them down to the first \( n \) coordinates:
\( X_n = \pi(Y_{n+m(n)}) \). It is easy to see that we can make arithmetic circuits over \( F_2 \)
efficiently simulate Boolean circuits and vice-versa; so the these two theories are the
same.

The issue arises when we generalize from a finite field to an arbitrary field \( F \). For
general fields, the projections of affine varieties are not necessarily closed subsets. One
solution is to generalize from algebraic subsets to semi-algebraic subsets, which would
give a non-uniform version of the Blum-Shub-Smale (BSS) theory, which is a geometric
analogue of the Turing theory. Such an approach would be a more basic version of the
complexity of constructible functions and sheaves studied in [Bas15].

The alternative, if one wants to stay within algebraic geometry, is to work with
projective varieties. This is the approach we take in this paper. The basic objects we
consider are sequences \((X_n)_{n=1}^\infty\) of projective (and bi-projective) varieties. A sequence
\((X_n)\) is in \(\text{P}_{\text{proj}}\) if, for each \(n\), there are homogeneous polynomials whose zero-set
is \(X_n\), which are the outputs of a homogeneous arithmetic circuit of polynomially
bounded size, and which have polynomially bounded degree. Sequences in \(\text{NP}_{\text{proj}}\) are
the projections of sequences of \(X_n \subset \mathbb{P}^{n_1(n)} \times \mathbb{P}^{n_2(n)}\) in \(\text{P}_{\text{proj}}\), onto the first component
\(\mathbb{P}^{n_1(n)}\). This is similar to the compact projective complexity classes studied in the BSS
setting in [Bas12]; the main difference is that we are in the arithmetic circuit model of
computation.

Key concepts in computational complexity are those of reduction and completeness.
A sequence \((X_n \subset \mathbb{P}^{n(n)})\) reduces to \((Y_n \subset \mathbb{P}^{m(n)})\) if there are linear maps
\(\rho_n : \mathbb{P}^{n(n)} \to \mathbb{P}^{m(p(n))}\), where \(p(n)\) is a polynomially bounded function, such that \(\rho^{-1}(Y_n) = X_n\). Our
main result is that there is a sequence, which we call the universal circuit resultant
which is \(\text{NP}_{\text{proj}}\)-complete (or, more precisely, the Segre embedding of the universal
circuit resultant, c.f. Theorems 1.18 and 1.19). A key tool in the construction of the
universal circuit resultant is a modified version of the universal circuits of [Raz08].

The universal circuit resultant is the first example of a compact family proven to
be \(\text{NP}\)-complete in a geometric complexity setting. The \(\text{NP}\)-completeness of the usual
resultant (the homogeneous Nullstellensatz problem) is a long-standing open problem
in BSS theory [Shu14].

We also discuss similar definitions for affine varieties and projective schemes. In the
affine case, we define \(\text{NP}_{\text{aff}}\) by taking closures of projections of families in \(\text{P}_{\text{aff}}\); this
way, the image under a projection stays affine. However, it is not known whether \(\text{NP}_{\text{aff}}\)
is closed under reductions. In the scheme case, the separation of \(\text{P}_{\text{sch}}\) and \(\text{NP}_{\text{sch}}\) should
be an easier problem than in the projective variety case.

Why study a complexity theory of algebraic varieties? One motivation is to make
an analogue of the P vs \(\text{NP}\) problem, in algebraic geometry, that might possibly be
easier to make progress on. The second aim is to lay the ground-work for notions and
questions of computational complexity theory to be used as tools in algebraic geometry.
Many complexity-related results on geometry questions involve starting with finite field,
or rational coefficients so that complexity can be discussed with the Turing machine
or Boolean models. While this makes perfect sense for finite field questions and for
concrete applications; from an algebraic geometry point of view, it adds the additional
difficulty of arithmetic geometry on top of the difficulty of algebraic geometry. The
language of this paper would be useful for expressing results on the complexity of
de-singularization, minimal-model program, projective duality and other interesting
topological and geometric operations without assuming rational coefficients or any bit reduction.

While we discuss a geometric theory of complexity, there is no direct relation between
this work and the Geometric Complexity Theory program of Mulmuley and Sohoni
(\cite{MS01}, \cite{Mul11} and references therein). GCT is an approach to proving lower bounds
in algebraic complexity using deep statements in representation theory. As such, it can
also prove lower bounds on the complexity of hypersurfaces, but the theory discussed
here is not advanced enough to ask such questions at this point.

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1. **Complexity of Projective Varieties**

We refer to the books \cite{BCS13, B"ur13} for background on algebraic/arithmetic com-

1.1. **Arithmetic circuits.** We use arithmetic circuits as our model of computation.
Let $F$ be any field. An arithmetic circuit over $F$ with input variables $x_1, \ldots, x_n$ is a
labeled directed acyclic multigraph (two or more distinct edges from the same source
to the same target are allowed). Every node with in-degree 0 is called an input-gate
and is labeled by an input variable or a constant in $F$. Every other node is labeled by
$\times$ or $+$, making it a product-gate or sum gate. Every edge is labeled by an element of
$F$ called the weight of the edge. Nodes with no outgoing edges are called output gates.

Each gate computes a polynomial in $F[x_1, \ldots, x_n]$ as follows. Input gates calculate
the variable or constant they are labeled with. If $v$ is a sum-gate, and its incoming
edges are with weights $\lambda_1, \ldots, \lambda_d$ from gates computing $f_1, \ldots, f_d$, then $v$ computes
the polynomial $\sum_{i=1}^d \lambda_i f_i$. If $v$ is a product gate, it computes $\prod_{i=1}^d \lambda_i f_i$. The size of a
circuit is the number of edges in the circuit.

An equivalent model is that of a straight-line program. These are sequences of
instructions that start with a list of the input variables, and each subsequent instruction
is weighted sum or product of earlier instructions.

1.2. **Basic definitions.** The basic objects we want to consider are sequences of pro-
jective varieties $(X_n) = (X_n)_{n=1}^\infty$. But, since we will be taking projections, we want
to also consider sequences of bi-projective varieties, i.e. sequences $X_n \subset \mathbb{P}^{n_1(n)} \times \mathbb{P}^{n_2(n)}$
of varieties, where $n_i : \mathbb{N} \to \mathbb{N}$ are any polynomially bounded functions. By abuse of
notation, we will write $\mathcal{N}(n) = (n_1(n), n_2(n))$ and

$$X_n \subset \mathbb{P}^{\mathcal{N}(n)} = \mathbb{P}^{n_1(n)} \times \mathbb{P}^{n_2(n)}.$$
Sequences of projective varieties are the special case where \( n_2(n) = 0 \) identically.

In order to prevent our language from becoming too cumbersome, we may drop the ‘bi’ in bi-projective varieties and bi-homogeneous polynomials; referring to them as projective and homogeneous when the meaning is clear from the context.

**Definition 1.1.** A homogeneous algebraic circuit (or straight-line program) \( C \) computes a projective variety \( X \subset \mathbb{P}^{n_1} \times \mathbb{P}^{n_2} \) if its output polynomials \( f_1, \ldots, f_m \) are homogeneous polynomials, and \( X \) is the zero-set of \( f_1, \ldots, f_m \) in \( \mathbb{P}^{n_1} \times \mathbb{P}^{n_2} \).

In other words, \( C \) computes \( X \) if it can solve the problem of determining whether a point in \( \mathbb{P}^{n_1} \times \mathbb{P}^{n_2} \) is in \( X \). The cost of the computation is the size of the algebraic circuit.

**Definition 1.2.** The complexity \( C(X) \) of \( X \subset \mathbb{P}^{n_1} \times \mathbb{P}^{n_2} \) is the size of the smallest homogeneous algebraic circuit that computes \( X \). For a sequence \( (X_n) \), the complexity is a function \( \mathbb{N} \to \mathbb{N} \) that takes each \( X_n \) to its complexity. This function is also denoted by \( C(X_n) \) by abuse of notation.

**Example 1.3.** As a basic example, let \( X_n = \{[1 : 0 : \ldots : 0]\} \subset \mathbb{P}^n \). Let \( x_0, \ldots, x_n \) be the homogeneous variables for \( \mathbb{P}^n \). Then, a circuit of size \( n \), which directly outputs \( x_1, \ldots, x_n \) computes \( X_n \) so \( C(X_n) \leq n \). Each of the variables \( x_1, \ldots, x_n \) must be involved in any computation of \( \{[1 : 0 : \ldots : 0]\} \) because otherwise the zero-set would be a cone. So the above is the most efficient computation of \( X_n \) and \( C(X_n) = n \).

**Example 1.4.** Consider the zero-set \( X \subset \mathbb{P}^n \) of a polynomial \( f \) in \( \mathbb{F}[x_0, \ldots, x_n] \). Then the complexity of \( X \) is bounded above by the algebraic complexity of \( f \); but the two may differ for two reasons. First, since we are looking at the complexity of the zero-set rather than scheme-theoretic complexity, we could be looking at a non-reduced polynomial, \( f = g^r \). Second, if our field is not algebraically closed, we wouldn’t have Nullstellensatz, so we could have, for example, \( \mathbb{F} = \mathbb{R} \) and \( f = x_0^2 + \cdots + x_n^2 \), and the zero-set would be empty, making the arithmetic complexity of \( f \) different from the complexity of its zero-set.

**Example 1.5.** [Universal Quadric] Consider the universal quadratic equation

\[
q_n(x_0, \ldots, x_n, a_1, \ldots, a_N) = a_0x_0^2 + a_1x_0x_1 + \cdots + a_Nx_n^2
\]

in variables \( x_0, \ldots, x_n \). Here, \( q_n \) has a term for each degree 2 monomial, so \( N = \binom{n+2}{2} - 1 \). The zero-set \( Q_n \subset \mathbb{P}^n \times \mathbb{P}^N \) is called the universal quadric. By making a circuit that computes each term and adds the result, we see that the universal quadric has complexity in \( O(n^2) \). Similarly, the solution to the universal degree \( d \) polynomial has complexity in \( O(n^d) \).

1.3. Complexity classes, reduction and completeness.

**Definition 1.6.** A sequence \( (X_n \subset \mathbb{P}^n) \) is in \( \mathbb{P}^{\text{proj}} \) if there is a sequence of homogeneous circuits, of size polynomially bounded in \( n \), that compute polynomials \( f_1, \ldots, f_{m(n)} \) of degree and number polynomially bounded in \( n \), whose zero-set is \( X_n \).
To define $\mathrm{NP}_{\text{proj}}$, we consider the sequences of projections
\[ \pi_{n_1,n_2}^{n_1,n_2} : \mathbb{P}^{n_1} \times \mathbb{P}^{n_2} \to \mathbb{P}^{n_1}, \]
where $n_1$ and $n_2$ are functions polynomially bounded in $n$.

**Definition 1.7.** A sequence $(X_n \subset \mathbb{P}^{n_1})$ is in $\mathrm{NP}_{\text{proj}}$ if there is a sequence $(Y_n \subset \mathbb{P}^{n_1} \times \mathbb{P}^{n_2})$ such that $(Y_n)$ is in $\mathrm{P}_{\text{proj}}$ and $X_n = \pi_{n_1,n_2}(Y_n)$.

We now discuss a sequence which is in $\mathrm{NP}_{\text{proj}}$.

**Example 1.8 (Resultant).** Let $V_n = \mathbb{P}^{n+1}$ be a vector space of dimension $n + 1$. Fix a degree $d$ and consider
\[ \mathbb{P}^{n(n)} = \mathbb{P}(S^d V_n \times \cdots \times S^d V_n) \]
where there are $n + 1$ terms in the product, making $n(n) = \binom{n+d}{d}(n+1) - 1$. Every point in $\mathbb{P}^{n(n)}$ corresponds to a tuple of $n + 1$ equations of degree $d$ in $n + 1$ variables.

Define $\text{Res}_{d,n} \subset \mathbb{P}^{n(n)}$ to be the subvariety consisting of the points that correspond to tuples of equations, homogeneous of degree $d$, that have common non-zero solutions; i.e., $\text{Res}_{d,n}$ is the resultant. In other words, letting $Z_{d,n}$ be the projectivization of $\text{ev}^{-1}(0)$, where $\text{ev} : \mathbb{A}^{n(n)+1} \times \mathbb{A}^{n+1} \to \mathbb{A}^{n+1}$ is the evaluation map; $\text{Res}_{d,n}$ is the image $\text{Res}_{d,n} = \pi_1(Z_{d,n})$. Since $d$ is fixed, $Z_{d,n}$ is in $\mathrm{P}_{\text{proj}}$, and therefore we have that $\text{Res}_{d,n}$ is in $\mathrm{NP}_{\text{proj}}$. There is also the related example of the discriminant variety which we do not discuss here.

We can now discuss reductions and completeness. In Boolean complexity and in Valiant complexity [Val79a], reductions are usually taken to be substitutions of variables and constants. Asymptotically, there is no significant difference between considering substitution reductions and affine-linear reductions, c.f. [Kay12].

For families of projective varieties, we take reductions to be linear maps; and this is all we need for Theorem 1.19. But when we consider products of projective spaces, we must be more careful. To this end, let us define a projective-linear map $\mathbb{P}^{n_1} \times \cdots \times \mathbb{P}^{n_s} \to \mathbb{P}^{m_1} \times \cdots \times \mathbb{P}^{m_r}$ to be a map that is mapping, to each component, as either a linear map depending on only one of the $\mathbb{P}^{n_i}$ or a constant map. An example of a projective-linear map $\mathbb{P}^1 \times \mathbb{P}^1 \to \mathbb{P}^1 \times \mathbb{P}^1 \times \mathbb{P}^1$ is the map $([x_0 : x_1], [y_0 : y_1]) \to ([y_0 + 2y_1 : y_1], [3 : 5], [4x_0 : x_0 + x_1])$. Note that the target spaces we will consider will only have two components. Of course, other reductions, such as reductions linear in each set of variables but higher in total degree could also be considered, c.f. Remark 1.22.

**Definition 1.9.** We say that a sequence $(X_n \subset \mathbb{P}^{m})$ reduces to a sequence $(Y_n \subset \mathbb{P}^{m})$ if there is a polynomially bounded function $p : \mathbb{N} \to \mathbb{N}$ and a sequence of projective-linear maps $\rho_n : \mathbb{P}^{m(n)} \to \mathbb{P}^{m(p(n))}$ such that, for each $n$, $X_n$ is the pullback of $Y_{m(p(n))}$ via $\rho_n$, i.e. $\rho_n^{-1}(Y_{p(n)}) = X_n$.

Reductions are sometimes called $p$-projections in the literature. We reserve the word projection for taking the image under geometric projections $\pi : A \times B \to A$. 

**Remark 1.10.** Unlike the affine case (c.f. section 2.1), \( P_{\text{proj}} \) and \( \text{NP}_{\text{proj}} \) are automatically closed under polynomial reductions as the image of a projective variety is always closed.

**Definition 1.11.** A sequence \((X_n)\) is said to be \( \mathcal{C} \)-hard for a complexity class \( \mathcal{C} \) if every sequence in \( \mathcal{C} \) can be reduced to \((X_n)\). If \((X_n)\) is \( \mathcal{C} \)-hard and is in \( \mathcal{C} \), then \((X_n)\) is said to be \( \mathcal{C} \)-complete.

### 1.4. Universal circuits.

We now describe a modified version of the construction, in [Raz08], of a VP-complete sequence constructed via *universal circuits* (see also [DMM+14, MS16] for other VP-complete sequences). The difference is that we want to handle the bi-homogeneous case. We will later use this sequence of circuits to make \( \text{NP}_{\text{proj}} \)-complete and \( \text{P}_{\text{proj}} \)-complete sequences. It should be noted that this section could have been based also on the ‘genetic computations’ studied in [B"ur13] Chapter 5.

The universal circuits of [Raz08] are based on the ability to put any circuit into normal homogeneous form.

**Definition 1.12 (Normal bi-homogeneous form).** A bi-homogeneous arithmetic circuit is in normal bi-homogeneous form if it satisfies the following:

(i) All leaves are labeled by input variables.
(ii) All edges from the leaves go to sum gates.
(iii) All output gates are sum gates.
(iv) Gates are alternating in the sense that if \((u, v)\) is an edge and if \(v\) is a sum gate, then \(u\) is a product gate and vice versa.
(v) The fan-in of every product gate is 2.
(vi) The fan-out of every sum gate is 1.

**Proposition 1.13.** For every bi-homogeneous circuit \(C\) of size \(s\), there is a circuit \(C'\) in normal bi-homogeneous form, of size \(O(s)\), that has the same output.

**Proof.** We refer to [Raz08, Proposition 2.3] for the proof of the fact that every circuit of size \(s\) can be turned into circuit in normal homogeneous form. We simply observe that all the steps in the proof in *loc. cit.* preserve the bi-homogeneity of the circuit. One difference between the statement in *loc. cit.* and this one is that size \(O(s)\) is enough for us; whereas, *loc. cit.* has the size as \(O(sr^2)\), where \(r\) is the maximum degree of the output polynomials. The difference is that *loc. cit.* starts with non-homogeneous circuits, and the \(r^2\) factor is the cost of homogenizing the circuit. So, as we already start with bi-homogeneous circuits, the normal form circuit has size \(O(s)\). \(\square\)
We say that a circuit $\Phi$ is *universal* for a set $S$ of circuits if, for every $C \in S$, there is an assignment of multipliers for the edges of $\Phi$ that makes $\Phi$ compute the same polynomials as $C$.

The proposition [Raz08, Proposition 2.8] proves the existence of universal circuits; we adapted it to the bi-homogeneous case we are considering as follows.

**Theorem 1.14.** For every $r_1, r_2, n, m$ and $s \in \mathbb{N}$, with $s \geq n + m$, there exists a circuit $\Phi$ that is universal for the set of circuits of size $s$ that are bi-homogeneous in $n$ and $m$ inputs, and that output $n$ polynomials of bi-degree $(r_1, r_2)$. Moreover, $\Phi$ is in normal bi-homogeneous form and has $O(r_1^2 r_2^3 s)$ gates.

**Proof.** Replace $s$ by a constant factor of $s$ to account for the cost of normalization of a circuit of size $s$ (Proposition 1.13).

We describe the gates of the universal circuit $\Phi$ in groups which are the inputs, the sum-levels and the product-levels. There are:

- $n + m$ input gates: $n$ inputs for variables of bi-degree $(1, 0)$ and $m$ inputs for variables of bi-degree $(0, 1)$.
- $r_1 r_2$ sum-levels, indexed by pairs of natural numbers $(l_1, l_2)$. Each sum-level contains $r_1 r_2 s$ sum-gates and no product gates.
- $r_1 r_2$ product-levels, again indexed by pairs of natural numbers $(l_1, l_2)$, each containing $r_1 r_2 s$ product gates and no sum gates.

A gate in sum-level $(l_1, l_2)$ (respectively, product-level $(l_1, l_2)$), will compute a bi-homogeneous polynomial of bi-degree $(l_1, l_2)$. Let us now describe the edges of $\Phi$:

- The inputs of each sum gate at sum-level $(l_1, l_2)$ are all the product gates in product-level $(l_1, l_2)$. So, we imagine the sum-level $(l_1, l_2)$ as being above the product level $(l_1, l_2)$. There are no product-levels $(1, 0)$ and $(0, 1)$. Each sumgate at sum-level $(1, 0)$ is connected to every input corresponding to the variables of degree $(1, 0)$; similarly for $(0, 1)$. Each sum gate has fan-out at most 1.
- In product-level $(l_1, l_2)$, each gate has fan-in equal to 2. For each pair $(j_1, j_2)$ with $1 \leq j_1 \leq l_1$, $1 \leq j_2 \leq l_2$, there are $s$ product gates which take their inputs from sum gates in sum-level $(j_1, j_2)$ and $(l_1 - j_1, l_2 - j_2)$. These gates are called product-level $(l_1, l_2)$ gates of type $(j_1, j_2)$. This is why we took $r_1 r_2 s$ gates in each level; each sum gate has fan-out only one, so there need to be enough sum gates for the product gates to connect to. Since all sum gates in a sum-level $(j_1, j_2)$ are connected to all product gates in the product-level $(j_1, j_2)$, it does not matter which specific sum gate is connected to a product gate in product-level $(l_1, l_2)$.

This completes the description of $\Phi$. Note that, according to the above description, there are some gates which are left with no outgoing connections. These can be removed without effecting the result. Let $C$ be a bi-homogeneous circuit of size $s$, computing
n polynomials of bi-degree \((r_1, r_2)\) on \(n + m\) variables. By Proposition 1.13 above, \(C\) can be replaced by a circuit \(C'\) in normal bi-homogeneous form. Being in normal bi-homogeneous form, the gates of \(C'\) can be grouped into the same levels and types as above. So we can embed \(C'\) into \(\Phi\) and set the unused edges in \(\Phi\) to 0, provided there are enough gates, which is true since we made \(\Phi\) have \(s\) product gates for each product-level and type, and more than \(s\) gates for each sum-level. \(\square\)

We can relax the degree requirement by putting together \(r^2\) versions of \(\Phi\); one for each pair of degrees.

**Corollary 1.15.** For every \(r, n, m\) and \(s \in \mathbb{N}\), with \(s \geq r(n+m)\), there exists a circuit \(\Phi_{n,m,r,s}\) with \(O(r^6s)\) gates that is universal for the set of bi-homogeneous circuits of size \(s\) that have \(n + m\) inputs, and that output, for each \(r_1, r_2 \leq r\), at most \(n\) polynomials which are bi-homogeneous of bi-degrees \((r_1, r_2)\).

**Remark 1.16.** We focused in this section on the bi-homogeneous case, but all the results of this section have obvious generalizations to the multi-homogeneous case with fixed number of components. The \(l\)-homogeneous \(\Phi\) and \(\Phi'\) would have \(n_1 + \cdots + n_l\) inputs, and there would be \(r^l\) sum-levels and \(r^l\) product levels. But, \(l\) would have to be fixed in sequences because otherwise the possible multi-degrees would become exponentially many.

### 1.5. The Universal Circuit Resultant.

The aim of this section is to make \(P_{\text{proj}}\)-complete and \(\text{NP}_{\text{proj}}\)-complete sequences.

Let \(\Phi_{n,m,r,s}\) be a universal circuit produced in Corollary 1.15 above. We want to modify \(\Phi_{n,m,r,s}\) so that the outputs of the circuits that we embedded in \(\Phi_{n,m,r,s}\) become reductions of the outputs of the modified circuit \(\Phi'_{n,m,r,s}\). For this, we add, as input to the circuit, a control-variable \(t_e\) for each edge \(e\) in \(\Phi_{n,m,r,s}\), break the edge into two and put a multiplication gate to multiply with \(t_e\) in the middle. All the edges of \(\Phi'_{n,m,r,s}\) have multiplier one, but the computed polynomials are now controlled by the \(t\)-variables.

A few observations:

- Since each gate in \(\Phi_{n,m,r,s}\) has fan-in at most \(r^2s\), \(\Phi'_{n,m,r,s}\) has \(O(r^8s^2)\) control variables \(t_e\). Let \(N = N(n,m,r,s)\) denote the number of control variables.
- \(\Phi'_{n,m,r,s}\) has \(O(r^8s^2)\) gates.
- The outputs of \(\Phi'_{n,m,r,s}\) are tri-homogeneous polynomials of degree at most \(r\) (component-wise) in the original variables and degree \(2r\) in the new control variables \(t_e\).

As the outputs of \(\Phi'_{n,m,r,s}\) are tri-homogeneous, they have a well-defined zero-set \(Z_{n,m,r,s} \subset \mathbb{P}^{n-1} \times \mathbb{P}^{m-1} \times \mathbb{P}^{N-1}\). We project it using the projection map

\[
\pi_{1,3} : \mathbb{P}^{n-1} \times \mathbb{P}^{m-1} \times \mathbb{P}^{N-1} \to \mathbb{P}^{n-1} \times \mathbb{P}^{N-1}.
\]
\textbf{Definition 1.17.} The universal circuit resultant, $R_{n,m,r,s} \subseteq \mathbb{P}^n$, is the image $\pi_{1,3}(Z_{n,m,r,s})$ of the zero-set of the outputs of $\Phi'_{n,m,r,s}$.

To get a sequence, we need to specialize the circuit size and the degree, as will be apparent later, it is better to not specialize $m$. We set $r = n$ and $s = n^2 + n + m$. Here, the choice of $n^2 + n + m$ is arbitrary, in the sense that any $s$ whose difference to $n^2$, the maximum number of outputs of $\Phi'_{n,m,r,s}$ when $r = n$, is a non-constant polynomial in $n$ would have sufficed. We added $m$ here just to make sure that $s \geq n + m$. For simplicity, let us abuse notation and write

$$R_{n,m} := R_{n,m,n^2+n+m} \subseteq \mathbb{P}^{n-1} \times \mathbb{P}^{N(n)-1},$$

and $\Phi'_{n,m} := \Phi'_{n,m,n^2+n+m}$, and and its zero-set, $Z_{n,m} = Z_{n,m,n^2+n+m}$ as necessary.

The universal circuit resultant $R_{n,m}$ is the family that will be shown to the NP$_\text{proj}$-complete; but it has two indices and we defined completeness for only single-indexed families. But, $(R_{n,m})$ can easily be re-indexed to have only one index that enumerates all pairs, with only quadratic loss.

\textbf{Theorem 1.18.} $(R_{n,m})$ is NP$_\text{proj}$-hard (in the sense that its single-index re-indexing is NP$_\text{proj}$-hard).

\textit{Proof.} Let $X_n = \pi_1(Y_n)$, be a sequence in NP$_\text{proj}$; with $Y_n \subseteq \mathbb{P}^{n_1(n)-1} \times \mathbb{P}^{n_2(n)-1}$ a sequence in P$_\text{proj}$.

We want to show that $(X_n)$ reduces to $(R_{n,m})$. Let $C_n$ be a bi-homogeneous circuit of polynomially bounded size $s(n)$ which computes $Y_n \subseteq \mathbb{P}^{n_1} \times \mathbb{P}^{n_2}$. Let the maximum component of the multi-degree of the outputs of $C_n$ be $d(n)$. For each $n$, let $q(n)$ be the maximum of $n_1(n) + n_2(n) + 1$, $d(n)$ and $s(n)$. Then, by Corollary 1.15 and the discussion above, the control variables $t_i$ or each edge in $\Phi'_{q,n_2,q^2+q+n_2}$ can be set to appropriate values $t_i$ so that the non-zero outputs of $C_n$ and $\Phi'_{q,n_2,q^2+q+n_2}$ are identical. Therefore, for all $x_1, \ldots, x_{n_1}, y_1, \ldots, y_{n_2}$, we have, after omitting extra zero-outputs,

$$C_n(x_1, \ldots, x_{n_1}, y_1, \ldots, y_{n_2}) = \Phi'_{q,n_2,q^2+q+n_2}(x_1, \ldots, x_{n_1}, 0, \ldots, 0, y_1, \ldots, y_{n_2}, \tau_1, \ldots, \tau_N).$$

Hence, for $[x_1 : \ldots : x_{n_1}] \in \mathbb{P}^{n_1}$, there exists $[y_1 : \ldots : y_{n_2}] \in \mathbb{P}^{n_2}$ such that

$$C_n(x_1, \ldots, x_{n_1}, y_1, \ldots, y_{n_2}) = (0, \ldots, 0),$$

if and only if there exists $[y_1 : \ldots : y_{n_2}] \in \mathbb{P}^{n_2}$ such that

$$\Phi'_{q,n_2,q^2+q+n_2}(x_1, \ldots, x_{n_1}, 0, \ldots, 0, y_1, \ldots, y_{n_2}, \tau_1, \ldots, \tau_N) = (0, \ldots, 0).$$

So, if make the reduction $\rho_n : \mathbb{P}^{n_1-1} \rightarrow \mathbb{P}^{q-1} \times \mathbb{P}^{N(n,m)-1}$ by mapping

$$\rho([x_1, \ldots, x_{n_1}] = ([x_1, \ldots, x_{n_1}, 0, 0, \ldots, 0], [\tau_1, \ldots, \tau_N]),$$

then, we have $\rho^{-1} R_{n,m} = X_n$. Thus, $(X_n)$ reduces to $(R_{n,m})$. \qed
While \((R_{n,m} \subset \mathbb{P}^{n-1} \times \mathbb{P}^{N(n,m)-1})\) is \(\text{NP}_{\text{proj}}\)-hard, it is not \(\text{NP}_{\text{proj}}\)-complete because sequences in \(\text{NP}_{\text{proj}}\) were defined to be sequences of varieties in projective spaces, not products of projective spaces. While this is not a fundamental issue and could be remedied by slightly changing the definitions, c.f. Remark \[1.22\], it is also possible to use the Segre embedding to make, directly, a complete sequence of projective varieties as follows.

Consider the incidence variety \((\mathbb{P}^{nN(n,m)-1})\) inside \(\mathbb{P}^{n-1} \times \mathbb{P}^{m-1} \times \mathbb{P}^{N(n,m)-1}\) whose projection \(\pi_{1,3}(Z_{n,m}) = R_{n,m}\). The equations for \(Z_{n,m}\) are computed by the universal circuit \(\Phi'_{n,m}\). If we consider the induced map, 

\[
\sigma_{n,m} : \mathbb{P}^{n-1} \times \mathbb{P}^{m-1} \to \mathbb{P}^{nN(n,m)-1},
\]

then we have \(\pi_1(\sigma_{n,m}(Z_{n,m})) = \sigma_{n,m}(R_{n,m})\). So, to see that \(\sigma_{n,m} R_{n,m}\) is in \(\text{NP}_{\text{proj}}\), it suffices to show that \((\sigma_{n,m}(Z_{n,m}))\) is in \(\text{P}_{\text{proj}}\).

To compute \(\sigma_{n,m}(Z_{n,m})\), consider the equations \(f_1, \ldots, f_M\) of \(Z_{n,m}\), which are the outputs of \(\Phi'_{n,m}\). We have \(M = n^4\), since the number of top level sum-gates in \(\Phi'_{n,m}\). These are tri-homogeneous in the \(n\)-many \(x\)-variable inputs, \(m\)-many \(y\)-variable inputs of \(\Phi'_{n,m}\) and the control variables \(t_i\). Consider the homogeneous coordinates of \(\mathbb{P}^{nN-1} \times \mathbb{P}^{m-1}\) as the variables \(\{z_{ij}\}\) and \(\{y_i\}\).

We make the equations for \(\sigma_{n,m}(Z_{n,m})\) in two sets:

- \(O(n^2)\) quadratic equations in the \(z\)-variables for the image of the Segre embedding,
- \(O(n^3)\) equations to cut out \(\sigma_{n,m}(Z_{n,m})\) inside \(\sigma_{n,m}(\mathbb{P}^{n-1} \times \mathbb{P}^{N-1})\).

To make the second set of equations, we will turn \(f_1, \ldots, f_{n^2}\) into equations in the \(z\)-variables. Since \(z_{ij} = x_i t_j\) under the Segre map, we want to ensure that the \(x\)-degree and the \(t\)-degree of each \(f_i\) are equal. But since the \(t\)-variables are control variables for the edges in \(\Phi_{n,m}\), each \(x\)-variable is already multiplied by a \(t\)-variable in the first set of edges of the circuit. This means that the variable \(z_{ij}\) can already be substituted in the circuit for \(x_i t_j\) to remove all \(x\) variables from the circuit. After this process, by the construction of \(\Phi'_{n,m}\), there are still \(t\)-variables in each \(f_i\). These can also be replaced by \(z\)-variables by taking, \(n\) copies of the circuit, and replacing, in the \(j\)th copy, every occurrence of every \(t_i\) by \(z_{ij} = t_i x_j\). This adds, for each equation \(f_i\), equations of the form \(x_0^i f_i, x_1^i f_i, \ldots, x_{n^2}^i f_i\). All-together, these equations cut out \(\sigma_{n,m}(Z_{n,m})\), and
the final circuit produced still has polynomial size. This completes the proof that 
\((\sigma_{n,m} R_{n,m})\) is in NP\(_{\text{proj}}\).

To see that \((\sigma_{n,m} R_{n,m})\) is also NP\(_{\text{proj}}\)-hard. Recall that, in the proof of the NP\(_{\text{proj}}\)-hardness of \(R_{n,m}\) above (Theorem 1.18), the reduction maps 

\[ \rho_n : \mathbb{P}^{n_1 - 1} \rightarrow \mathbb{P}^{q-1} \times \mathbb{P}^{N-1} \]

were of the form:

\[ \rho([x_1, \ldots, x_{n_1}] = ([x_1, \ldots, x_{n_1}, 0, 0, \ldots, 0], [\tau_1, \ldots, \tau_N]) \]

where \(\tau_1, \ldots, \tau_N\) were constants. For the reduction to \(\sigma_{n,m} R_{n,m}\), we take the reduction 

\[ \rho'_{n,m}(x_1, \ldots, x_{n_1}) = (\tau_j x_i)_{i,j} \]

with \(x_i = 0\) for \(i > n_1\). \(\square\)

**Remark 1.20.** The above proof gives the following general fact. If 

\[ (X_n \subset \mathbb{P}^{n_1(n)} \times \mathbb{P}^{n_2(n)}) \]

is NP\(_{\text{proj}}\)-complete, with projective-linear reductions which are non-constant on only one of the components, then the Segre embedding of \((X_n)\) is also NP\(_{\text{proj}}\)-complete.

There is also a P\(_{\text{proj}}\)-complete family. Recall that the universal circuit \(\Phi'_{n,0,n,n^2+n}\) is a homogeneous circuit, with \(n\) so-called \(x\)-variables, no \(y\)-variables and \(N = N(n,0)\) control variables \(t_i\).

**Proposition 1.21.** The sequence of zero-sets of \(\Phi'_{n,0,n,n^2+n}\) in \(\mathbb{P}^{n-1} \times \mathbb{P}^{N-1}\) is P\(_{\text{proj}}\)-complete.

**Proof.** Since \(\Phi'_{n,0,n,n^2+n}\) can simulate any homogeneous circuit of size less than or equal to \(n^2 + n\) (Corollary 1.13), we immediately have that sequences of projective varieties in P\(_{\text{proj}}\) reduce to the sequence of zero-sets of \(\Phi'_{n,0,n,n^2+n}\). To get the reductions for bi-projective varieties, we consider Segre embeddings as we did above. \(\square\)

**Remark 1.22.** We could also have chosen to work with multi-homogeneous varieties \((X_n \subset \mathbb{P}^{n_1(n)} \times \cdots \times \mathbb{P}^{n_l(n)}(n))\), defined complexity in the same way using multi-homogeneous circuits and multi-homogeneous varieties. If \(l(n)\) was required to be bounded by a constant for all \(n\), then the above theorems would still hold with \(r^l\) levels in the proof of Theorem 1.14 and larger, but polynomially growing circuits everywhere, with \(l\) in the exponent. But, for Theorem 1.18 we would need to change the reductions to multi-linear reductions; this is so that the Segre map itself can be considered a reduction. If \(l(n)\) was not required to be constant, but required to be polynomially bounded, then the proof Theorem 1.14 and therefore of subsequent theorems would not work as the number of ‘levels’ and therefore the size of the universal circuits would increase exponentially in \(n\).
2. Alternative versions

2.1. Affine Varieties. There is an alternative to going directly to projective varieties. We can consider, as basic objects, sequences \((X_n \subset \mathbb{A}^{n_1(n)})\). Such sequences are in \(P_{\text{aff}}\) if there are polynomials computed by circuits of polynomially bounded size that cut out \(X_n\).

We define the class \(\text{NP}_{\text{aff}}\) as those sequences of the form \(\pi(Y_{n+m(n)})\) for sequences \((Y_n)\) in \(P_{\text{aff}}\), where \(\pi : \mathbb{A}^{n+m(n)} \to \mathbb{A}^n\) is the projection onto the first \(n\) coordinates and \(m\) is a polynomially bounded function. The closure ensures that the projection stays affine. This is consistent with algebraic elimination as well, since if we take an ideal \(I \subset k[x_1, \ldots, x_n, y_1, \ldots, y_m]\), then the elimination ideal \(I \cap k[x_1, \ldots, x_n]\) is the ideal of the closure of the projection of \(V(I) \subset \mathbb{A}^{n+m}\).

The problem is that it is not clear whether this class \(\text{NP}_{\text{aff}}\) is closed under reductions. More precisely, we are asking the following question:

**Question 2.1.** Given \(f_1, \ldots, f_k\), in variables \(x_1, \ldots, x_n\), computed by an arithmetic circuit of size \(s\), can one construct polynomials \(g_1, \ldots, g_s\) in variables \(x_1, \ldots, x_n\), and \(z_1, \ldots, z_N, t\), computed by an arithmetic circuit of size polynomially bounded in \(s\), such that the image of the zero-set of \(g_1, \ldots, g_s\) under a projection map is the cone of the projective closure of the zero-set of \(f_1, \ldots, f_k\)?

In other words, does the operation of taking projective closure ‘reduce’ to geometric elimination?

This question is also the main technical challenge in proving that the resultant (of quadratics) is \(\text{NP}_{\text{proj}}\)-complete (or also \(\text{NP}\)-complete in BSS theory).

2.2. Scheme-Theoretic Complexity. Now consider, as basic objects sequences of closed subschemes \((X_n \to \mathbb{P}^{n_1(n)})\) of projective space and closed subschemes \((X_n \to \mathbb{P}^{n_1(n)} \times \mathbb{P}^{n_2(n)})\) of bi-projective space. We say that a sequence \((C_n)\) of circuits computes \((X_n)\) if for each \(n\), \(X_n\) is the zero-scheme of the outputs of \(C_n\). This gives a definition of \(P_{\text{sch}}\).

We define \(\text{NP}_{\text{sch}}\) just we defined \(\text{NP}_{\text{proj}}\), but using the scheme-theoretic image under the projection. A sequence \((X_n \subset \mathbb{P}^{n_1(n)})\) reduces to \((Y_n \subset \mathbb{P}^{m(n)})\) if there are linear maps \(\rho_n : \mathbb{P}^{n_1(n)} \to \mathbb{P}^{m(p(n))}\), where \(p(n)\) is a polynomially bounded function, such that the fiber product

\[
Y_n \times \mathbb{P}^{m(n)} \mathbb{P}^{n_1(n)} = X_n.
\]

The proof of Theorem 1.18 carries over exactly to the scheme case; but the proof of Theorem 1.19 does not.
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