Deterministic multi-mode gates on a scalable photonic quantum computing platform

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Quantum computing can be realized with numerous different hardware platforms and computational protocols. A highly promising approach to foster scalability is to apply a photonic platform combined with a measurement-induced quantum information processing protocol where gate operations are realized through optical measurements on a multipartite entangled quantum state—a so-called cluster state. Hereofore, a few quantum gates on non-universal or non-scalable cluster states have been realized, but a full set of gates for universal scalable quantum computing has not been realized. We propose and demonstrate the deterministic implementation of a multi-mode set of measurement-induced quantum gates in a large two-dimensional (2D) optical cluster state using phase-controlled continuous variable quadrature measurements. Each gate is simply programmed into the phases of the high-efficiency quadrature measurements which execute the transformations by teleportation through the cluster state. Using these programmable gates, we demonstrate a small quantum circuit consisting of 10 single-mode gates and 2 two-mode gates on a three-mode input state. On this platform, fault-tolerant universal quantum computing is possible if the cluster state entanglement is improved and a supply of Gottesman-Kitaev-Preskill qubits is available. Moreover, it operates at the telecom wavelength and is therefore network connectable without quantum transducers.

Recent remarkable advances in developing fully programmable quantum computing platforms have led to a plethora of groundbreaking results in quantum information science including the demonstration of fault-tolerant operations on an error-corrected logical ion-trap qubit and the demonstration of quantum sampling at a super-classical rate in a 53-qubit superconducting quantum computer. Albeit marked progress, the currently realized qubit-based platforms for quantum computing are still strongly limited in size while the proposed methods for up-scaling are stymied by significant technical challenges.

An alternative is the continuous variable (CV) photonic platform which has recently gained interest due to its proven scalability potential for measurement-based quantum computation (MBQC) as exemplified by the generation of 2D cluster states with thousands of modes and the sequential operation of one hundred single-mode gates. In CV quantum computing, information is encoded and processed in bosonic harmonic oscillators—e.g., the optical field—that are described by states in infinite-dimensional Hilbert spaces. Although the idea of using CVs for quantum computing dates back more than 20 years, it is only within the last few years that feasible models for fault-tolerant large-scale CV MBQC were conceived. Our demonstration represents a critical step towards these CV computing models. It constitutes the first realization of a fully deterministic and programmable multi-mode computation platform for MBQC.

Architecture and hardware

In CV MBQC, quantum information processing is realized by teleporting the constituent gates through a computationally universal cluster state, comprising quadrature entangled modes in a 2D grid. For Gaussian gates, these teleportation protocols are effectuated by quadrature measurements of the cluster state modes, where the determined gate or sequence of gates is fully programmed into the phases of the quadrature measurements, i.e., the measurement bases. Such a cluster state architecture including solely Gaussian transformations allows for full computational universality in the sub-space of Gottesman-Kitaev-Preskill (GKP) qubits provided that a supply of these qubits are available—inline non-Gaussian gates are not needed. Moreover, by using GKP encoding, fault-tolerance is attainable via quantum error correction involving solely Gaussian transformations in the cluster.

The reconfigurable and programmable nature of the cluster state quantum computer is illustrated in Fig. 1a by the different layers of operation from software to hardware. First, the quantum algorithm is specified and subsequently resolved into a certain sequence of single- and two-mode gates, such as the rotation, shear, squeezing and the controlled-Z gate. This sequence of gates is then converted into a sequence of phases that finally controls the consecutive quadrature measurements to effect the quantum algorithm on the cluster state.

At the hardware level, the processor is based on the generation of a time-encoded one-dimensional (1D) cluster state as illustrated in Fig. 1b. The cluster state is created in two spatial modes, A and B, using squeezed states of light (produced by optical parametric oscillators, OPO\textsubscript{A} and OPO\textsubscript{B}, at the wavelength of 1550nm) that are interfered in an imbalanced interferometer and subsequently coiled up by the \textit{N}\tau-delay into a cylindrical 2D cluster state with a structure as shown in Fig. 1c.
This coiled-up 1D cluster state represents the logic level, and it is at this point the computational logic takes place via projective quadrature measurements of the cluster state. Each node of the graph corresponds to spatial modes $A$ and $B$ in different temporal modes, $k$, with duration $\tau$ set by the $\tau$-delay in the 1D cluster state generator, and the number, $N$, of temporal modes on the cylinder circumference is determined by the $N\tau$-delay. We have chosen the delays of 50 m and 600 m leading to a temporal mode duration of $\tau \approx 250$ ns and $N = 12$. The projective quadrature measurements of each node are performed with high-efficiency homodyne detectors with variable and fully controllable basis settings. It is precisely this full control of projective quadrature measurements that enables the execution of an arbitrary sequence of Gaussian gates and thus allows for universal quantum computation when using GKP states.

**Computation scheme**

Input quantum states for computation may be encoded on the circumference of the cluster state, and for the implementation of the desired quantum gates, they are teleported along the cylinder by projective measurement of each mode. The actually implemented gate depends on the measurement bases used for the teleportation. Note that the measurements are performed chronologically, swirling round the cylinder. This is exactly the right measurement order for universal computation on GKP-qubits—no additional optical storage is required since the measurement order follows the propagation of information along the cluster state cylinder. In the following, we summarize the computation scheme with details described in the supplementary information (SI) section 2.

At the logic level in Fig. 1b and 1c, a joint measurement is performed on spatial modes $A$ and $B$ of every temporal mode $k$ (examples of the mode indexing is shown in Fig. 1d,e). The two-mode measurement device consists of a beam-splitter (BS$_3$) followed by two homodyne detectors each measuring in a basis determined by the local oscillator phase $\theta$—i.e., measuring the quadrature $\hat{x}(\theta) = \hat{x}\cos\theta + \hat{p}\sin\theta$ where $\hat{x}$ and $\hat{p}$ are the electric field amplitude and phase (or position and momentum) quadratures, respectively. Temporal modes of odd $k$ are used as control modes: Measuring these in basis $\theta_c = (-1)^{(k-1)/2}\pi/4$ of $A$ and $B$, the cluster state is projected into $N/2$ wires along the length of the cylinder as illustrated in Fig. 1c$^{31}$. These wires, which at the logic level consist of segments of two-mode entangled states, can be used for single-mode computation by gate teleportation$^{32}$. By performing a joint measurement on mode $B,k$ containing an input state and mode $A,k$ of the neighbouring wire segment, the input state is teleported through a gate, $U$, to mode $B,k+N$ of the same wire. The operation $\hat{U}$ depends on the measurement bases setting, $(\theta_A,k,\theta_B,k)$ (see Fig. 1d). To implement two-mode gates, modes of neighboring wires must be coupled which can be done by changing the measurement basis, $\theta_c$, of some of the control modes$^{27,33}$. Depending on the basis
setting on the wire modes (indices \(k, k + N\) for one wire and \(k + 2, k + N + 2\) for the other) and the coupling control mode (index \(k + N + 1\)), a two-mode gate operation \(\hat{V}\) is implemented as illustrated in Fig. 1e.

In the Heisenberg picture, an implemented Gaussian \(n\)-mode gate operation transforms the quadratures as

\[
\hat{q}' = G \hat{q} + N \hat{p}_i + D m ,
\]

(1)

where \(\hat{q} = (\hat{x}_1, \ldots, \hat{x}_n, \hat{p}_1, \ldots, \hat{p}_n)^T\) and \(\hat{q}' = (\hat{x}'_1, \ldots, \hat{x}'_n, \hat{p}'_1, \ldots, \hat{p}'_n)^T\) are \(2n\) vectors of quadratures of the gate input and output modes, respectively. For single- and two-mode gates, \(n = 1\) and \(2\). In Eq. (1), the first term represents the Gaussian gate with \(G\) being the corresponding symplectic matrix that depends on the measurement basis setting. The last term, \(D m\), represents a teleportation by-product of displacements with \(m\) being a vector of measurement outcomes, transformed by the basis setting dependent matrix \(D\). Finally, the middle term, \(N \hat{p}_i\), represents noise occurring in the gate with \(\hat{p}_i\) being a vector of initial momentum squeezed quadratures of the cluster state modes, transformed by the gate noise matrix \(N\). \(G\), \(N\) and \(D\) are given in the SI section 2 for different basis settings.

An ideal gate transformation is performed when the gate noise and displacement terms are zero. Since the measurement outcomes, \(m\), are known, the displacement by-product can be compensated for by adding \(-D m\) to the measurement outcomes of output state quadratures, \(\hat{q}'\). The gate noise, however, is only negligible for cluster states generated from infinitely squeezed vacuum states, i.e. \(\text{Var}(\hat{p}_i) = 0\). Such states are however nonphysical and in practice, non-zero additive Gaussian noise will inevitably occur and must eventually be accounted for by quantum error-correction.

Our proposed computational scheme of Gaussian gates offer full universality and fault-tolerance when provided with a supply of GKP qubits. Moreover, the scheme requires limited active feedforward: The displacement by-products, \(D m\), as well as the displacements needed for the noise correction, can be handled by post-processing of the measurement outcomes. The only active feedforward required is simple binary updating of the basis settings in later measurements to implement either a single-mode identity or a shear gate for each non-Clifford \(T\) gate.\(^\text{12}\)

Quantum gates

For single-mode gates, the implemented operation corresponding to \(G\) in Eq. (1) is

\[
(-1)^w \hat{R}(\theta_+/2) \hat{S}(\tan \theta_-/2) \hat{R}(\theta_+/2) ,
\]

(2)

where \(w = (k \mod N)/2\) is the wire number, which in our realization runs from 0 to 5, \(\hat{R}(\theta) = e^{i\theta(\hat{x}_+^2 + \hat{p}_+^2)/2}\) and \(\hat{S}(s) = e^{i \ln(s)(\hat{x} \hat{p} + \hat{p} \hat{x})}\) are rotation and squeezing operations, and \(\theta_{\pm}\) depends on the basis settings as \(\theta_\pm = \pm \theta_{A,k} + \theta_{B,k}\). We experimentally implemented the rotation gate, \(\hat{R}(\theta)\), a modified shear gate, \(\hat{F}^3 \hat{P}(\sigma) = \hat{F}^3 e^{i \sigma \hat{q}^2}/2\), and the squeezing gate, \(\hat{S}(\epsilon')\), by appropriately choosing the basis settings, as detailed in the SI section 2.1. Note, this modified version of the shear gate, with \(\hat{F}^3 = R(j\pi/2)\) where \(j = (-1)^w\), makes it possible to implement shear in a single computation step of Eq. (2), while \(\hat{F}^3\) can be compensated for in a second computation step if necessary. \(\{\hat{R}(\theta), \hat{S}(\epsilon')\}\) constitutes a universal single-mode Gaussian gate-set\(^\text{34}\), while \(\{\hat{R}(\pi/2), \hat{F}^3 \hat{P}(1)\}\) constitutes a single-mode Clifford gate-set on GKP-encoded qubits.\(^\text{12}\) Note that in CV MBQC, similar to feedforward of displacement by-products and GKP noise correction, displacement gates for universal computation are implemented in the post-processing of the measurement outcomes\(^\text{2,11}\).

We characterize the implemented gates with gate tomography by letting the input mode be entangled to a reference mode and measuring the quadrature correlations between the reference and the gate-teleported output modes.\(^\text{6}\) The resulting symplectic matrices are shown in Fig. 2a with the corresponding circuit summarized in Fig. 2b—for details, see SI section 3.1. The symplectic matrix elements are seen to agree well with the theoretical values, certifying the accuracy of the gates. The same measurements reveal the added gate noise, the other pertinent parameter characterizing the gates’ performance. For GKP qubit computation, quadrature gate noise is the most relevant figure of merit, since this is what eventually causes qubit errors. Fault-tolerance thresholds are therefore traditionally presented in terms of quadrature noise, i.e. quadrature squeezing\(^\text{24,25,27,35}\). The gate noise is shown in Fig. 2c. For our computation scheme, we expect a gate noise of \(\text{Var}(N \hat{p}_i) = 4 e^{-2\gamma V_0}\) (SI section 2.1) which is four times larger than the initially squeezed state variance of \(e^{-2\gamma V_0}\) (where \(V_0\) is the vacuum variance). Therefore, by compensating the measured gate noise by the four vacuum units, \(1/4 \approx -6\text{ dB}\), we expect to regain the initially measured squeezing variance of 4.4 dB. The compensated noise level is illustrated in Fig. 2c and is seen to agree well with the expected value except for the squeezing gate where the squeezing level, \(\epsilon' = \tan \theta_- /2\), becomes highly sensitive to phase fluctuations in \(\theta_- = -\theta_{A,k} + \theta_{B,k}\) for large \(|r|\).

To further demonstrate the impact of the cluster state entanglement, the measured gate noise is plotted in Fig. 2d as a function of the OPO pump power that controls the squeezing process. For vanishing squeezing (zero pump power) where the cluster state is simply a vacuum state, gate noise of 6 dB is measured, corresponding to the classical limit of our scheme. When increasing the OPO pump power, the gate noise reduces below this limit due to the increasing cluster state entanglement. The measured gate noise agrees well with that estimated from the experimental parameters of the setup.\(^\text{18}\) Obviously, for fault-tolerant computation, much lower gate noise is required. Gate noise with potential improvements of the setup are estimated and plotted in Fig. 2d as well. It is clear that higher optical efficiencies and larger squeezing bandwidth significantly decrease the gate noise and
FIG. 2. Single-mode gates. a, Symplectic matrices corresponding to $G$ in Eq. (1) for the implemented rotation, shear (modified by $F$), and squeezing gates, measured by gate tomography as described in SI section 3 and summarized in b, c. Measured gate noise variance in each output quadrature for each implemented gate (solid points). Also, gate noise compensated by $-6$ dB to account for the effect of the gate noise matrix $N$ in Eq. (1) is shown (hollow points), in which case it should agree with the initial momentum squeezing variance, which is independently measured to be $-4.4$ dB (dashed line). Estimation of error bars are described in Fig. 2. d, Single-mode gate noise as a function of pump power, where zero pump power corresponds to no squeezing in which case the cluster state is replaced by vacuum. Here the measured gate noise (solid points) is for $R(\theta)$, averaged over $\theta$, $x$, and $p$, and error bars are estimated as standard deviation hereof. Black-solid line represents the estimated gate noise using the setup parameters, while the dashed line corresponds to the case with no correlations. Green and purple lines show the expected gate noise for more optimal parameters with $\geq 90\%$ optical efficiencies and for 7.7 MHz squeezing bandwidth as in the experimental setup (green), together with a broader squeezing bandwidth of 100 MHz (purple)—see SI section 4 for a derivation and further discussion.

FIG. 3. Two-mode gate. a, Symplectic matrix corresponding to $G$ in Eq. (1) for the implemented controlled-Z gate (modified by $F \otimes F$) as a function of the coupling coefficient $g$, measured by gate tomography (described in SI section 3) with the corresponding circuit shown in b. c, Measured gate noise in the four output quadratures for each implemented controlled-Z gate (solid points), together with gate noise compensated for the effect of the gate noise matrix $N$ in Eq. (1) (hollow points). The values for compensation are given in the SI Fig. S6 for each value of $g$. The compensated gate noise can be compared with the initial squeezing variance for cluster state generation, measured to be $-4.4$ dB (dashed line). Estimation of error bars are described in Fig. 2.
can bring the system towards fault-tolerant computation. See SI section 4 for a more comprehensive discussion on gate noise.

To complete the universal Gaussian gate set, we implemented a two-mode gate—a modified version of the controlled-Z gate, \((\hat{F} \otimes \hat{F}) \hat{C}_Z(g) = (\hat{F} \otimes \hat{F}) e^{igZ} \hat{F} \hat{C}_z(g) \hat{F} \otimes \hat{F} \). Here \(w \) in \( j = (\hat{1})^w \) is the lower wire number of the two wires on which the gate is implemented. The required basis setting and resulting gate noise is derived in SI section 2.2. To shorten the notation, in the following we denote \((\hat{F} \otimes \hat{F}) \) simply as \( \hat{FF} \). Note that the transformation \( \hat{FF} \) can be easily reversed in subsequent transformations to realize a pure \( \hat{C}_Z(g) \) gate\(^{27} \). Together with the implemented single-mode gates, \( \{ \hat{F} \hat{F} \hat{C}_Z(g), \hat{R}(\theta), \hat{S}(\epsilon^r) \} \) constitutes a universal multi-mode Gaussian gate-set\(^{20} \), while \( \{ \hat{F} \hat{F} \hat{C}_Z(1), \hat{R}(\pi/2), \hat{F} \hat{F} \hat{P}(1) \} \) constitutes a multi-mode Clifford gate-set on GKP-encoded qubits\(^{12} \). For gate tomography of the implemented \( \hat{F} \hat{F} \hat{C}_Z(g) \)-gate, quadrature correlations of the output state and reference states entangled to the input states are measured (see SI section 3.2). The resulting symplectic matrix, together with the corresponding gate tomography circuit, is shown in Figs. 3a and 3b, and the measured symplectic matrix elements are seen to agree well with the expected values. The gate noise, shown in Fig. 3c, is larger than for single-mode gates since two-mode gates are implemented in two computation steps and depend on \( g \). By compensating for the effect of \( N \), we again retrieve the expected squeezing variance of \(-4.4 \text{ dB with good agreement.}\)

**Quantum circuit**

To demonstrate the flexibility of combining gates into a programmable quantum circuit, here we implement as an example a three-mode circuit, which for three physical GKP qubits as input states encodes a logical qubit in the three-qubit bit-flip error correction code (see Fig. 4a). The implementation of the circuit on the cluster state is illustrated in Fig. 4b. It includes two \( \hat{F} \hat{F} \hat{C}_Z(1) \) gates corresponding to qubit controlled-Z and Hadamard gates, two \( F^{±1} = R(±\pi/2) \) gates corresponding to qubit Hadamard gates, and eight identity gates which can be thought of as qubit memory and may be unnecessary depending on the surrounding circuit. The details of the implementation and associated gate noise are discussed in SI section 2.3.

To characterize the performance of the implemented circuit, we perform circuit tomography similar to the strategy applied for gate transformations (see SI section 3.3). The resulting gate symplectic matrix, shown in Fig. 4c, is seen to resemble the desired matrix. In Fig. 4d, the measured gate noise is shown and compared to the expected gate noise for a cluster state with no entanglement. It is clear that the entanglement of the cluster state leads to a reduction of the gate noise. To verify the measured gate noise values, we back-propagate the combined gate noise through the circuit by compensating for \( N \) (as presented in Fig. S7 of SI) with the result shown in Fig. 4d, and the estimated values agree well with the initially measured degree of squeezing of 4.4 dB. Note that the large total circuit noise stems from the accumulation of gate noise associated with multiple concatenated gates and the lack of error correction. To prevent gate noise accumulation as required for fault-tolerant computation, GKP quadrature error correction should be performed as often as possible, preferably in between each implemented gate\(^{27} \).

**Outlook**

We have demonstrated the machinery for performing MBQC on our cluster state architecture, which relies on comparatively low-tech photonic technology at room temperature, and illustrated its computational flexibility by combining 12 gates into a programmable quantum circuit. The single- and two-mode gates can be organized in any order on the six input modes of the cluster state, thereby allowing for the implementation of an arbitrary six-mode circuit transformation of, in principle, infinite depth. The demonstrated platform is currently restricted to a six-mode circuit, but due to its inherent deterministic nature, the platform can be efficiently up-scaled to allow for large-scale computation. This can be attained by increasing the bandwidth of the optical squeezing process and complement it with broadband homodyne detectors. Bandwidths of several GHz are possible\(^{36,37} \), so the number of input modes can be increased to several thousands, bringing the platform well into NISQ (noisy intermediate-scale quantum technology) territory\(^{38,39} \). Furthermore, with the platform’s telecom compatibility, multiple processing units may straight-forwardly be combined and scaled up without the need of complex quantum transduction. More generally, instead of the all-temporal encoding used here for constructing and scaling the optical cluster state, it is also possible to use spectral\(^{40,41} \) and spatial\(^{42} \) degrees of freedom.

To attain fault-tolerant universal quantum computing on our platform, the gate noise must be significantly decreased and the quantum information must be encoded as qubits, such as GKP qubits. Fault-tolerance is attained by increasing the amount of squeezing of the cluster state to lower the gate noise and by using GKP ancilla states for quantum error-correction to prevent the accumulation of noise. Such error-correction can be done directly in our computation scheme (with homodyne detection and without active feedforward) simply by replacing certain squeezed states of the 1D cluster with GKP states\(^{29} \). The squeezing threshold for fault-tolerance of similar MBQC schemes has been estimated to be in the range of 10–17 dB\(^{25,35,43} \) which should be compared to the state-of-the-art of squeezing of 15 dB\(^{44} \). GKP states have been generated in vibrational modes of trapped ions\(^{45} \) and in microwave cavity fields\(^{46} \) but it remains a challenge to produce them in the optical spectrum although proposals exist\(^{47–52} \). Once this challenge has been solved, all basic ingredients for fault-tolerant, universal, scalable quantum computing are available.
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Data availability: Datasets generated during this study, and code for data analysis, are available at https://doi.org/10.11583/DTU.14673570

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Supplementary Information for
Deterministic multi-mode gates on a scalable photonic quantum computing platform

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1 Experimental setup

The experimental setup in the main text Fig. 1b is shown here in Fig. S1 with more details of the measurement device including control of the basis settings for different temporal modes. The cluster state generation scheme is similar to the scheme presented in Ref. [1], where a detailed description of the cluster state generation setup can be found in the corresponding supplementary information. To summarize: Two-mode squeezing in spatial mode $A$ and $B$ is generated by interfering two single mode squeezed states in a beam splitter denoted BS$_1$. By delaying one mode of the two-mode squeezed states by $\tau$ and subsequently interfering the two modes ($A$ and $B$) at beam-splitter BS$_2$, a one-dimensional (1D) cluster state, namely a dual-rail wire [3, 4] with temporal mode duration of $\tau$, is formed. The 1D cluster state is coiled up by a long delay of $N\tau$ to form a cylinder with $N$ temporal modes in the circumference. Locally on the cylinder surface the coiled-up cluster state can be pictured as a 2-dimensional (2D) cluster state of parallel 1D cluster states. In [1] the coiled-up 1D cluster state is then interfered with itself by beam-splitter BS$_3$ to form a double bilayer square lattice [1, 5]. However, for cluster state computation, here we will consider the coiled-up cluster state just before BS$_3$, while BS$_3$ is part of a joint measurement device for implementing quantum gates by projective measurements. As such, in the experimental setup, the modes in computation are located just before BS$_3$, and is marked as the logic level in Fig. S1. A computation scheme in this logic level is presented in [5], while here we use a slightly modified scheme as presented in section 2.

In Ref. [1], the generated coiled up 1D cluster state is in practice a $\mathcal{H}$-graph state [6]. Here, we rotate the phases of every mode in the logic level by $-\pi/4$ to transform this $\mathcal{H}$-graph state into a cluster state with real edges. The $-\pi/4$ phase-rotations on both spatial mode $A$ and $B$ commutes with BS$_3$, and thus for simplicity, in the experimental realization we apply the $-\pi/4$ phase-rotations directly onto the the local oscillators, LO$_A$ and LO$_B$, of the two homodyne detectors HD$_A$ and HD$_B$. Thus, in the following, the generated state is considered as a cluster state, and not a $\mathcal{H}$-graph state.

In the measurement device, the phase relation of the input modes of BS$_3$ is actively locked by tapping off and detecting about 1% of the power in spatial mode $A$, and the DC measurement value is fed back via a PID to a fiber phase controller—a so-called DC-lock. The LO phases of the homodyne detectors are phase-locked in similar fashion using DC-locks, thereby enabling stable measurements of the $\hat{p}$- and $\hat{x}$-quadrature of the spatial modes $A$ and $B$. Finally, using an electro-optical phase modulator (EOM) in each local oscillator, the homodyne detector bases can be individually and dynamically controlled in each spatial mode for different temporal modes. To prevent technical noise from the probe beams used for phase-locking of BS$_{1-3}$ and the homodyne detectors, a sample/hold locking scheme is used, where a probe beam is chopped on and off as described in Ref. [1]. All phase locks are activated when the probe is on, while when the probe is off, the

![Fig. S1: Schematics of the experimental setup showing the details of the measurement device for gate implementation by projective measurements, while details on the setup for cluster state generation can be found in the supplementary information of [1]. Red and blue represents free-space and single-mode fiber optics respectively. In the measurement device P indicates polarization control while $\theta$ indicates active phase control using a fiber stretcher [2]. The required basis setting sequence in the homodyne detectors (HD) to implement a desired gate is controlled using electro-optical modulators (EOM) in the local oscillators (LO). The EOM is driven using an amplified two-channel arbitrary waveform generator (AWG), and the measured quadrature time traces are acquired on a digital storage oscilloscope (DSO). The AWG, scope and sample/hold locking scheme is triggered from a function generator (FG).](image-url)
phase-lock feedback is kept constant while acquiring data with basis settings controlled for each mode by the EOMs to implement a desired gate. Quadrature time traces from the homodyne detectors are acquired on a digital storage oscilloscope (DSO), and for each temporal mode \( k \) the measurement outcome is extracted using the corresponding temporal mode function

\[
f_k(t) = \begin{cases} 
N(t - k\tau)e^{-\kappa^2 t^2}, & |t - k\tau| < \tau/2, \\
0, & \text{otherwise}
\end{cases}
\]  

(1)

where \( N \) is a normalization factor of units \( s^{-1} \). Here, \( \kappa \) is optimized to be \( 2\pi \times 2.0 \text{MHz} \) to minimize the gate noise. In the data acquisition we measure 228 temporal modes, consecutively, corresponding to 19 turnarounds of the cylindrical cluster state for \( N = 12 \). As shown in section 3, this allows us to implemented and characterize multiple gates at once in parallel.

The experimental setup is operated at the telecom wavelength of 1550 nm to minimize propagation losses in optical fibers (blue lines in Fig. S1). For the short delay line a 50 m fiber is used leading to \( \tau \approx 250 \text{ ns} \) temporal mode duration, while for the long delay line 600 m is used leading to \( N = 12 \). For phase locks, fiber stretchers of negligible optical losses are used \[2\]. The EOMs for setting the homodyne detection bases are of model MPZ-LN-10 from iXblue with 10 GHz bandwidth. To control the EOMs, a two channel arbitrary waveform generator (AWG) of model M4i.6631-x8 from Spectrum Instruments with 1 GS/s sampling rate and 400 MHz bandwidth is used to generate the required waveforms to measure in a basis setting sequence implementing a desired gate. The waveform signals from the AWG driving the EOMs are amplified using THS3491 operational amplifiers (op-amps) from Texas Instrument with a 6000 V slew rate. To compensate for electrical responses in the AWG, op-amps and EOMs, before the experiment is carried out the AWG waveforms are optimized by inserting each EOM in a Mach–Zehnder interferometer, and feeding back the resulting applied phase shift from the EOMs to the AWG in order to update the waveform targeting a desired phase shift sequence. As a result, we are able to switch the homodyne detection basis from \(-\pi/2\) to \( \pi/2 \) (both stable within 1% of the value) within 8 ns, followed by a constant phase of the desired value after switching.

2 Computation scheme

In the experimental setup shown in the main text Fig. 1b, and explained in detail in section 1, a dual-rail 1D cluster state \[3, 4\] is generated and coiled up into a cylinder with \( N \) temporal modes in the circumference to form a cluster state with a local 2D topology. The 1D cluster state can be used for computation along the cluster state \[7, 8\], but here, with the cluster state coiled up, the goal is instead to perform computation with information flowing along the cylinder, i.e. across the 1D cluster state. The scheme for doing so is explained in the following, and the required basis settings for implementing single-mode and two-mode gates are described in section 2.1 and 2.2, while in section 2.3 we combine single- and two-mode gates to implement a circuit.

In the language of graphical calculus of Gaussian states \[6\], the generated 1D cluster state has edge weights of \( \pm t = \pm \tanh(2r)/2 \) and self-loops of \( \text{sech}(2r) \) \[3\], where \( r \) is the squeezing parameter of the initial momentum squeezed states. This is an approximate cluster state with the variance of the nullifiers,

\[
\hat{n}_{A,i} = \hat{p}_{A,i} - t (\hat{x}_{A,i-1} - \hat{x}_{A,i+1} - \hat{x}_{B,i+N-1} + \hat{x}_{B,i+N+1}) \\
\hat{n}_{B,i} = \hat{p}_{B,i} - t (\hat{x}_{A,i-N-1} - \hat{x}_{A,i-N+1} + \hat{x}_{B,i-1} + \hat{x}_{B,i+1}),
\]

(2)

vanishing in the limit of infinite squeezing:

\[
\text{Var}\{\hat{n}_{A,i}\} = \text{Var}\{\hat{n}_{B,i}\} = V_0 \text{sech}(2r) \rightarrow 0 \quad , \quad \text{for} \quad r \rightarrow \infty,
\]

(3)

where \( V_0 = 1/2 \) is the variance of vacuum for \( h = 1 \), and the subscript indicates the mode numbering in Fig. S2. Since the edge weight, \( t \), depends on the squeezing parameter, \( r \), the required basis setting for implementing a desired quantum gate will in general depend on \( r \). Such scheme is described in detail in Ref. [5] for the cluster state considered here, namely the double bilayer square lattice (DBSL). However, in practice, it is inconvenient to have the basis settings depend on \( r \), as the exact initial squeezing most often is
unknown at the point the experiment is carried out and may vary slightly from time to time. Instead, here we redefine the generated state to have the same graph but with edge weights $\pm t = \pm 1/2$, i.e. we drop the squeezing dependent $\tanh(2r)$ in the edge weights. The resulting 1D graph state is by the definition in Ref. [9] a cluster-type state\(^1\) with the variance of the nullifiers in Eq. (2) equal

$$\text{Var}\{\hat{n}_{A,i}\} = \text{Var}\{\hat{n}_{B,i}\} = 2V_0 e^{-2r} \to 0 \quad \text{for } r \to \infty,$$

which as well vanish in the limit of infinite squeezing. Note that at high squeezing levels where $\text{sech}(2r) \approx 2e^{-2r}$, the nullifier variances for the approximate cluster state in Eq. (3) and the cluster-type state in Eq. (4) are equal, while for vanishing squeezing, (3) approaches the vacuum variance, $V_0$, while (4) approaches $2V_0$. Thus, for finite squeezing, the redefined cluster-type state with $t = 1/2$ is more noisy than the more traditional approximate cluster state with $t = \tanh(2r)/2$. Fortunately, we find that the noise produced by gates implemented on the generated graph state considered as a cluster-type state is at the same level or, in some cases, even lower than if we use the graph state as an approximate cluster state. In the following, unless it may lead to confusion, we will use the term ‘cluster state’ for both cluster-type states and approximate cluster states.

The rules for graphical calculus of Ref. [6] applies to approximate cluster states and are therefore, to our knowledge, not necessarily valid for the more general cluster-type states. As a result, here we will derive the necessary graph transformation. A section of the coiled-up 1D cluster state is shown in Fig. S2a. To perform computation, we need to project the cluster state into wires along the cylinder, on which gates can be implemented [5]. To do so, control modes (in the grey shaded areas of Fig. S2a) are measured in alternating bases of $(-1)^{(k-1)/2}/\sqrt{4}$ where odd $k$ is the mode number of the temporal control modes in Fig. S2a, i.e. measuring $\hat{x}(\pm \pi/4) = (\hat{x} \pm \hat{p})/\sqrt{2}$ of control modes by homodyne detection. From standard graphical calculus of Ref. [6], we expect this measurement to form new edges of weight $\pm 2t$ as shown in Fig. S2b. To see that this is indeed the case for the cluster-type state with $t = 1/2$, we consider the 1D cluster state generation scheme in Fig. S2c: After measuring the control modes, the generation scheme can be simplified to separate the generation of two-mode entangled states as shown in Fig. S2d (to derive this, we have used the method of calculating quadrature transformations outlined in appendix A of Ref. [5]). Here $D(\alpha)$ symbolises a displacement in phase-space, $D(\alpha)$, by $\text{Re}\{\alpha\}$ and $\text{Im}\{\alpha\}$ in $\hat{x}$- and $\hat{p}$-quadrature, respectively, depending on the control mode measurement outcomes as

$$\begin{aligned}
\hat{D}_{A,k}(\alpha), \quad \alpha &= \frac{1}{2\sqrt{2}} \left[ (j-i)m_{A,k-1} + (-j-i)m_{A,k+1} + (j-i)m_{B,k+N-1} + (j+i)m_{B,k+N+1} \right], \\
\hat{D}_{B,k+N}(\beta), \quad \beta &= \frac{1}{2\sqrt{2}} \left[ (-j+i)m_{A,k-1} + (-j-i)m_{A,k+1} + (-j+i)m_{B,k+N-1} + (j+i)m_{B,k+N+1} \right],
\end{aligned}$$

where $j = (-1)^{w}$ with $w = \{k \bmod N\}/2$ being the wire number, while $i$ is the imaginary unit, $i^2 = -1$. Since the measurement outcomes, $m_{A(B),k}$, are known, this displacement can be compensated for by feeding the measurement results forward to displacement operations displacing the wire modes back. Or, we can simply keep track of the displacements and compensate for them in the final measurement outcomes since all operations implemented here are Gaussian operations [10, 11]. $\hat{N}$ symbolises a quadrature symmetric noise operator, and originate from finite squeezing in the 1D cluster state generation. In the Heisenberg picture, this is represented by adding initial finite squeezed momentum quadratures, $\hat{\xi}^i_{A,B,k}$, to the the wire cluster state quadratures $\hat{x}_{A(B),k}$ and $\hat{p}_{A(B),k}$. Here $(\hat{\xi}^i_{A(B),k}, \hat{\xi}_{A(B),k}, \hat{p}_{A(B),k})$ and $(\hat{x}_{A(B),k}, \hat{p}_{A(B),k})$ are marked on Fig. S2c,d. The resulting quadrature evolution of $\hat{N}$ on the wire mode quadratures is

$$\begin{pmatrix}
\hat{x}_{A,k} \\
\hat{x}_{B,k+N} \\
\hat{p}_{A,k} \\
\hat{p}_{B,k+N}
\end{pmatrix} \xrightarrow{\hat{N}} \begin{pmatrix}
\hat{x}_{A,k} \\
\hat{x}_{B,k+N} \\
\hat{p}_{A,k} \\
\hat{p}_{B,k+N}
\end{pmatrix} + \frac{1}{2\sqrt{2}} \begin{pmatrix}
-1 & -1 \\
-1 & 1 \\
1 & -1 \\
1 & 1
\end{pmatrix} \begin{pmatrix}
\hat{\xi}_{A,B,k} \\
\hat{\xi}_{A,B,k+N} \\
\hat{\xi}_{A,B,k+N} \\
\hat{\xi}_{A,B,k+N}
\end{pmatrix}.$$

\(^1\)A cluster-type state, with graph vertices $\alpha \in G$ and connected nodes $b \in N_{\alpha}$ to $\alpha$, is defined in [9] as a multi-mode Gaussian state where the variance of $\mu_b - \sum_{b \in N_{\alpha}} t_b \hat{x}_b$ (here the non-zero variable $t_b$ is added by us to generalize the cluster-type states to have variable edge weights) vanishes in the limit of infinite squeezing. Thus, cluster-type states are a more general group of states allowing cluster state approximations not covered by graphical calculus for Gaussian pure states [6].
Fig. S2: (a) Section of the coiled-up dual-rail 1D cluster state in the logic level of the main text Fig. 1b, forming a local 2D cluster state of parallel vertical 1D cluster states. Here the bright and dark modes represent spatial mode A and B, respectively, while temporal mode k = 0 is marked as a red area. Modes in the gray shaded area are control modes, which are measured in basis $x(\pm \pi/4)$ to project the 2D cluster state into wires. The red arrow in each temporal mode indicates the beam-splitter of the measurement device, BS3, and can for the control modes be compensated for as shown in Eq. (8). (b) After measuring control modes, the cluster state is projected into wires with wire number $w = (k \mod N)/2$. (c) Shows the 1D cluster state generation and wire projection, while (d) shows the corresponding simplified circuit after measurements. Here $D$ and $N$ corresponds to displacements and noise as described in Eq. (5) and (6,7).

for even wires ($w = (k \mod N)/2 = \text{even}$), and

$$
\begin{pmatrix}
\hat{x}_{A,k} \\
\hat{x}_{B,k+N} \\
\hat{p}_{A,k} \\
\hat{p}_{B,k+N}
\end{pmatrix}
\rightarrow
\begin{pmatrix}
\hat{x}_{A,k} \\
\hat{x}_{B,k+N} \\
\hat{p}_{A,k} \\
\hat{p}_{B,k+N}
\end{pmatrix}
+ \frac{1}{2\sqrt{2}}
\begin{pmatrix}
1 & -1 \\
-1 & -1 \\
-1 & 1 \\
1 & -1
\end{pmatrix}
\begin{pmatrix}
\hat{x}_{A,k-1} \\
\hat{x}_{B,k+N+1}
\end{pmatrix}
$$

(7)

for odd wires ($w = (k \mod N)/2 = \text{odd}$). Since the initial momentum squeezed quadratures, $\hat{p}_{A(B),k}^i$, squeezed by $e^{-r}$, has a Gaussian quadrature distribution with variance $\text{Var}\{\hat{p}_{A(B),k}^i\} = e^{-2r}V_0$ and mean $\langle \hat{p}_{A(B),k}^i \rangle = 0$, the $N$-operation adds noise to the wire mode quadratures depending on the initial momentum squeezing, which vanish in the limit of infinite squeezing. From Eq. (6) and (7) the noise added by $N$ is seen to be correlated in the quadratures between two connected wire modes. This is exactly in such a way, that $N$ can be brought to the left side of the beam-splitter and phase shifts in Fig. S2d, in which case $N$ adds uncorrelated noise in the two wire-modes before the beam-splitter. Fortunately, for the same reason, the quadrature noise added by $N$ cancels out in the wire cluster state nullifiers and when implementing gates, as we will see later. Finally, note the $\sqrt{2}$ anti-squeezing on the initial momentum squeezed states in Fig. S2d. This anti-squeezing leads to a degradation of the “effective” initial momentum squeezed variance which becomes $2e^{-2r}V_0$ instead of $e^{-2r}V_0$, and is the cost of projecting the cluster state into another cluster state useful for computation [5].

Similar to the 1D cluster state with edge weights $\pm t = \pm 1/2$, the projected two-mode entangled states in
Single-mode gates are implemented on each wire, and a single computation step is shown in Fig. S3. The input mode may be switched into the spatial mode $B$ at the logic level using an optical switch, or is, most often, be the output of the previous computation step. The red arrow indicates the beam-splitter BS$_3$ of the measurement device.

During the projection of the wires in Fig. S2c,d we ignored the beam splitter of the measurement device, BS$_3$, marked by red arrows in Fig. S2a,b (here we define a balanced beam-splitter operation as $B = e^{-i\pi(\hat{x} \otimes \hat{p} - \hat{p} \otimes \hat{x})/4}$ similarly as in Ref. [5, 1], and its direction-dependency is marked below by an arrow pointing from the first to the second mode of the tensor products). This is possible when the two spatial modes $A$ and $B$ of the same temporal mode, $k$, are measured in the same basis, as is the case for the control modes, since

$$
\bar{x}(\theta) = m_{A,k} \quad \text{and} \quad \bar{x}(\theta) = m_{B,k}
$$

Thus, the hypothetical measurement outcomes of the control modes before BS$_3$, $m_{A(B),k}$, are extracted from the homodyne measurement outcomes of the measurement device after BS$_3$ as $m_{A,k} = (m_{+,k} + m_{-,k})/\sqrt{2}$ and $m_{B,k} = (m_{+,k} - m_{-,k})/\sqrt{2}$. Note that, since we consider the computation from the logic-level point of view, the notation used here is opposite to that used in the language of macro-nodes in [7, 12, 13]. Here ‘$A$’ and ‘$B$’ refer to spatial mode $A$ and $B$ before BS$_3$, while ‘$-$’ and ‘$+$’ refer to spatial mode $A$ and $B$ after BS$_3$, respectively.

The wire modes in Fig. S2b, projected into two-mode cluster states, are now suitable for the implementation of gates. With $N = 12$ temporal modes in the circumference of the cluster state cylinder generated in this work, the coiled-up 1D cluster state is projected into $N/2 = 6$ wires, numbered as $w \in \{0, 1, 2, 3, 4, 5\}$ with edge weight $-(-1)^w$. As a result, the generated cluster state may hold 6 modes in computation, while this can be scaled up simply by increasing $N$ corresponding to the ratio between the long and short delay lines in the experimental setup.

### 2.1 Single mode gates

Single-mode gates are implemented on each wire, and a single computation step is shown in Fig. S3. The input mode may be switched into the spatial mode $B$ at the logic level using an optical switch, or is, most...
often, the output mode of the previous computation step. With a joint measurement of the input mode and one mode of the two-mode wire cluster state, using the measurement device consisting of BS3 and two homodyne detectors, the input mode is teleported to the second mode of the wire cluster state. Depending on the basis setting the input mode undergoes a Gaussian operation, \( \hat{U} \), while teleported:

\[
|\psi_{\text{out}}\rangle_{B,k+N} = \hat{U} |\psi_{\text{out}}\rangle_{B,k} .
\]

Here, \( \hat{U} \) consists partly of a desired gate, \( \hat{G} \), a displacement by-product, and a noise operation due to finite squeezing. In the Heisenberg picture, \( \hat{U} \) transforms the quadratures of the input mode \( B,k \) to the output mode \( B,k+N \) as

\[
\begin{pmatrix} \hat{x}_{B,k+N} \\ \hat{p}_{B,k+N} \end{pmatrix} = \mathbf{G} \begin{pmatrix} \hat{x}_{B,k} \\ \hat{p}_{B,k} \end{pmatrix} + \mathbf{N} \begin{pmatrix} \hat{p}_{A,k}^\dagger \\ \hat{p}_{B,k+N}^\dagger \end{pmatrix} + \mathbf{D} \begin{pmatrix} \hat{m}_{-,k} \\ \hat{m}_{+,k} \end{pmatrix} ,
\]

(9)

where the prime denotes the output mode quadratures after gate implementation. In the following we discuss each of the three terms on the right-hand-side. \( \mathbf{G} \) is the symplectic matrix corresponding to the implemented gate and can be derived to be

\[
\hat{G} = (-1)^w \hat{R} \left( \frac{\theta_{+,k}}{2} \right) \hat{S} \left( \tan \frac{\theta_{-,k}}{2} \right) \hat{R} \left( \frac{\theta_{-,k}}{2} \right) , \quad \theta_{\pm,k} = \pm \theta_{A,k} + \theta_{B,k} ,
\]

(10)

where \( \hat{R}(\theta) = e^{-i\theta(\hat{x}^2+\hat{p}^2)/2} \) and \( \hat{S}(s) = e^{i\ln(s)(\hat{x}\hat{p}+\hat{p}\hat{x})/2} \) are the rotation and squeezing operations, respectively, with the squeezing parameter \( r = \ln(s) \) (leading to squeezing in the \( \hat{x} \)-quadrature for positive \( r \)). Note, here \( \theta_{A,k} \) and \( \theta_{B,k} \) are basis settings of the homodyne detectors in spatial mode \( A \) and \( B \) after BS3 and should not be confused with the hypothetical measurement outcomes \( m_{A,k} \) and \( m_{B,k} \) before BS3. The subscripts '+' and '-' of \( \theta_{\pm,k} \) simply notes the addition and difference of \( \theta_{A,k} \) and \( \theta_{B,k} \). The term \( \mathbf{N}(\hat{p}_{A,k}^\dagger,\hat{p}_{B,k+N}^\dagger)^T \) represents the gate noise due to finite squeezing in the cluster state generation. Here \( \hat{p}_{A,k}^\dagger \) and \( \hat{p}_{B,k+N}^\dagger \) are the “effective” initial momentum quadratures for the two-mode wire cluster state including the \( \sqrt{2} \) anti-squeezing contribution from the wire projection shown in Fig. S2d, and so, when including the wire projection, the second term in Eq. (9) is

\[
\mathbf{N} \begin{pmatrix} \hat{p}_{A,k}^\dagger \\ \hat{p}_{B,k+N}^\dagger \end{pmatrix} = \begin{cases}
\sqrt{2} \begin{pmatrix} -1 & -1 \\ 1 & -1 \end{pmatrix} \begin{pmatrix} \hat{p}_{A,k-1} \\ \hat{p}_{B,k+N+1} \end{pmatrix} , & \text{for } w = \text{even} \\
\sqrt{2} \begin{pmatrix} -1 & 1 \\ 1 & 1 \end{pmatrix} \begin{pmatrix} \hat{p}_{A,k} \\ \hat{p}_{B,k+N} \end{pmatrix} , & \text{for } w = \text{odd}
\end{cases}
\]

(11)

Note, as mentioned previously, the noise added by \( \mathcal{N} \) does not appear in the implementation of gates as the added noise is correlated between two connected wire-modes. As a result, with \( \hat{p}_{A(B),k}^\dagger \) being squeezed by \( e^{-r} \), symmetric quadrature noise of variance \( 4V_0 e^{-2r} \) is added as gate noise in each computation step, independent on the basis setting and wire number. Here, \( \sum_i N_{qi}^2 = 4 \), with \( N_{qi} \) being the elements of the gate noise matrix \( \mathcal{N} \) and \( q = 1,2 \) for single-mode gates, are gate noise factors. Assuming identical squeezing in all cluster states, the gate noise variances of each quadrature can in general be written as the initial momentum squeezing variance scaled by the gate noise factors of each corresponding quadrature. Finally, \( \mathbf{D}(\hat{m}_{+,k},\hat{m}_{-,k})^T \) is a displacement by-product depending on the measurement outcomes. Here, the tilde indicates that the displacements in the wire projection \( \mathbf{D} \) in Fig. S2 are included. Writing this out, the third term in Eq. (9) is

\[
\mathbf{D} \begin{pmatrix} \hat{m}_{-,k} \\ \hat{m}_{+,k} \end{pmatrix} = \frac{j\sqrt{2}}{\sin \theta_{-,k}} \begin{pmatrix} -\cos \theta_{A,k} & \cos \theta_{B,k} \\ \sin \theta_{A,k} & -\sin \theta_{B,k} \end{pmatrix} \begin{pmatrix} m_{+,k} \\ m_{-,k} \end{pmatrix} + \frac{1}{\sqrt{2}} \begin{pmatrix} -j & -j & -j & j \\ 1 & -1 & 1 & 1 \end{pmatrix} \begin{pmatrix} m_{A,k-1} \\ m_{A,k+1} \\ m_{B,k+N-1} \\ m_{B,k+N+1} \end{pmatrix}
\]

(12)

where \( j = (-1)^w \). Here, the first term is the displacement by-product from the gate implemented by teleportation, while the second term includes displacements from the wire projection. Note that \( m_{\pm,k} \) are the measurement outcomes in Fig. S3 after BS3, while \( m_{A(B),k} \) are measurement outcomes of control modes before BS3 extracted using Eq. (8).
We implemented three single-mode gates, namely the rotation gate, $\hat{R}(\theta) = e^{-i\theta(\hat{x}^2+\hat{p}^2)/2}$, the shearing gate $\hat{P}(\sigma) = e^{i\sigma \hat{x}}$, and the squeezing gate $\hat{S}(\epsilon) = e^{i\epsilon(\hat{x}^2+\hat{p}^2)/2}$, each with symplectic matrices

$$
\begin{bmatrix}
\cos \theta & \sin \theta \\
-\sin \theta & \cos \theta
\end{bmatrix}, \quad
\begin{bmatrix}
1 & 0 \\
\sigma & 1
\end{bmatrix}, \quad
\begin{bmatrix}
1 & 0 \\
e^{-\epsilon r} & e^{\epsilon r}
\end{bmatrix}
$$

transforming a quadrature vector $(\hat{x}, \hat{p})^T$. $\{\hat{R}(\theta), \hat{S}(\epsilon)\}$ constitute a universal single-mode Gaussian gate set together with displacements in phase-space [14], while for GKP-encoded qubits on square grids in phase-space $\{\hat{R}(\pi/2), \hat{P}(1)\}$, together with $\sqrt{\pi}$ displacements in phase-space, leads to a single-mode Clifford gate set in the encoded qubit subspace [15]. Note, phase-space displacements are ubiquitous in measurement-based quantum computation, but they are simply absorbed into the measurement results.

From Eq. (10), $\hat{R}(\theta)$ is implemented from mode $B, k$ to $B, k + N$ with basis setting

$$
\begin{bmatrix}
\theta_{A,k} \\
\theta_{B,k}
\end{bmatrix}_R = \frac{1}{2} \begin{bmatrix} \theta - (-1)^w \pi/2 \\ \theta + (-1)^w \pi/2 \end{bmatrix},
$$

such that $(\theta_{+,k}, \theta_{-,k}) = (\theta, (-1)^w \pi/2)$. Note that $(-1)^w$ in $\theta_{-,k}$ compensates for $(-1)^w$ in Eq. (10). From Eq. (10), $\hat{P}(\sigma)$ cannot be implemented in a single computation step. However, $\hat{F}^j \hat{P}(\sigma) = \hat{R}(j\pi/2) \hat{P}(\sigma)$ can be implemented in a single computation step, in which $\hat{F}^j$ keeps a GKP-encoded qubit within the qubit subspace for $\sigma = 1$, and may be compensated for in a following computation step if necessary. Here, with $j = (-1)^w$, $\hat{F}^0 = \hat{F}$ for even wires and $\hat{F}^j = \hat{F}^{-1} = \hat{F}^1$ for uneven wires. $\hat{F}^j \hat{P}(\sigma)$ is implemented from mode $B, k$ to $B, k + N$ with the basis setting

$$
\begin{bmatrix}
\theta_{A,k} \\
\theta_{B,k}
\end{bmatrix}_P = \begin{bmatrix} \pi/2 - \arctan(\sigma/2) \\
0
\end{bmatrix},
$$

such that $\theta_{+,k} = \theta_{-,k} = \pi/2 - \arctan(\sigma/2)$. Finally, $\hat{S}(\epsilon)$ is implemented from mode $B, k$ to $B, k + N$ with basis setting

$$
\begin{bmatrix}
\theta_{A,k} \\
\theta_{B,k}
\end{bmatrix}_S = (-1)^w \arctan \epsilon^r \begin{bmatrix} -1 \\ 0 \end{bmatrix},
$$

such that $(\theta_{+,k}, \theta_{-,k}) = (0, (-1)^w 2 \arctan \epsilon^r)$. For each of the implemented gates, the displacement byproduct is compensated for in the measurement outcomes using Eq. (12), while with gate noise factors of 4 in each quadrature, we expect gate noise variance of $4V_0 e^{-2\epsilon^r}$ (with $r$ here being the initial squeezing parameter in the cluster state generation) added to each quadrature, corresponding to 6 dB relative to the initial momentum squeezed quadrature variance.

### 2.2 Two-mode gate

If, instead of measuring the spatial modes $A$ and $B$ in the same basis for every temporal control mode, we measure them in different bases (such that Eq. (8) does not apply), two neighboring wires will be coupled [5]. This can then be used to implement two-mode gates as illustrated in Fig. S4.

In Fig. S4, the projected cluster state corresponds to the cluster-type state discussed for wire projection in Fig. S2. However, instead of measuring all control modes in the $\pm \pi/4$-basis, one temporal control mode is only phase-rotated by $(-1)^{(k-1)/2} \pi/4$ but is left unmeasured. The result is a 10-mode projected cluster state, which is then connected to two input modes, $B, k$ and $B, k + 2$, via the joint measurement of the measurement device. Measuring all modes, except the output modes $B, k + 2N$ and $B, k + 2N + 2$, the states of the input modes are teleported to the output modes with a two-mode Gaussian operation, $\hat{V}$, applied depending on the basis settings,

$$
|\psi_{\text{out}}\rangle_{(B,k+2N),(B,k+2N+2)} = \hat{V} |\psi_{\text{in}}\rangle_{(B,k),(B,k+2)}.
$$

In general, $\hat{V}$ transforms the quadratures of the input to the output modes as

$$
\begin{bmatrix}
\hat{x}_{B,k+2N} \\
\hat{x}_{B,k+2N+2} \\
\hat{p}_{B,k+2N} \\
\hat{p}_{B,k+2N+2}
\end{bmatrix} = G_2
\begin{bmatrix}
\hat{x}_{B,k} \\
\hat{x}_{B,k+2} \\
\hat{p}_{B,k} \\
\hat{p}_{B,k+2}
\end{bmatrix} + \hat{N}_t + \hat{D}_m,
$$

(13)
The basis setting to implement $\hat{G}$ within the encoded qubit subspace for the quadrature transformation directly from the initial momentum squeezed quadratures and measurement operation, $\hat{D}$, being a coupling constant, constitutes, together with the single-mode gate set $\{\hat{R}(\theta), \hat{S}(\epsilon^r)\}$ in section 2.1, a universal multi-mode Gaussian gate set. Furthermore, for GKP-encoded qubits on square grids in phase-space, $\hat{C}_Z(1)$ leads, together with $\{\hat{F} = \hat{R}(\pi/2), \hat{F}(1)\}$, to a multi-mode Clifford gate set in the encoded qubit subspace [15]. However, on the cluster state architecture considered here, $\hat{C}_Z(g)$ cannot be implemented directly. Instead, we implement $(\hat{F} \otimes \hat{F}^j)\hat{C}_Z(g)$, with short notation $\hat{F}\hat{F}^j\hat{C}_Z(g)$, where $j = (-1)^w$ with $w$ being the wire number of the input mode $B, k$ (again $\hat{F}^j = \hat{F}$ and $\hat{F}^{-1} = \hat{F}^j$). Here, $\hat{F}$ and $\hat{F}^j$ on the two output modes keep a GKP-encoded qubit within the encoded qubit subspace for $g = 1$, and may be compensated in a subsequent computation step if necessary. The basis setting to implement $\hat{F}\hat{F}^j\hat{C}_Z(g)$ from modes $B, k$ and $B, k + 2$ to modes $B, k + 2N$ and $B, k + 2N + 2$ is

$$
\left( \begin{array}{c} \theta_{A,k} \\ \theta_{B,k} \\ \theta_{A,k+2} \\ \theta_{B,k+2} \\ \theta_{A,k+N} \\ \theta_{B,k+N} \\ \theta_{A,k+N+1} \\ \theta_{B,k+N+1} \\ \theta_{A,k+N+2} \\ \theta_{B,k+N+2} \end{array} \right) =
\left( \begin{array}{c} \pi/4 \\ -\pi/4 \\ (-1)^w \pi/4 \\ -(-1)^w \pi/4 \\ (-1)^w [\pi/2 - \arctan(g/2)] \\ 0 \\ (-1)^w \pi/4 \\ (-1)^w [\pi/4 + 2 \arctan(g/2)] \\ (-1)^w [\pi/2 - \arctan(g/2)] \\ 0 \end{array} \right) .
$$

(14)

This basis setting includes the settings for the temporal control mode $k + N + 1$ coupling the two wires $w$ and $w + 1$ for the desired two-mode gates, while all other control modes surrounding the two wires are measured.
in the regular $\pm \pi/4$ control mode basis as described at the beginning of section 2.

The displacement matrix, $D$, and corresponding vector of measurement outcomes, $m$, is shown in Fig. S5 for the different coupling constants $g$ implemented. For the measurement outcomes in $m$ corresponding to the modes in Eq. (14), the direct homodyne measurement outcomes, $m_{\pm,k}$, are used, while for control modes used to form the projected cluster state in Fig. S4, the measurement outcomes before BS$_2$, $m_{A(B),k}$, are used, extracted using Eq. (8). In the characterization of the gate, the displacement is compensated for in the measurement outcomes of the output modes $B,k + 2N$ and $B,k + 2N + 2$.

Finally, the gate noise matrix, $N$, and corresponding vector of initial momentum squeezed quadratures, $\hat{p}_i$, is shown in Fig. S6. Assuming all initial momentum squeezed quadratures, $\hat{p}_{A(B),k}^i$, are equally squeezed by $e^{-r_i}$ such that the variances of the momentum quadrature distributions are $e^{-2r_i}$, gate noise of variance $e^{-2r_i}V_0 \sum_q N_q^2$ is added in each output quadrature. Here, again, $\sum_q N_q^2$ are gate noise factors scaling the initial momentum squeezing variance into gate noise variance with $q = 1, 2, 3, 4$ for the output quadratures $\hat{x}_{B,k+2N}$, $\hat{x}_{B,k+2N+2}$, $\hat{p}_{B,k+2N}$, and $\hat{p}_{B,k+2N+2}$, respectively. The resulting gate noise factors are shown in Fig. S6.

### 2.3 Circuit

Besides implementing individual gates of a multi-mode universal Gaussian gate set, the cluster state computation architecture in this work allows quantum gates to be combined universally, with single-mode gates on each wire by teleportation as described in section 2.1, and two-mode gates between neighboring wires coupled by control modes as described in section 2.2. To demonstrate this, we implement a quantum circuit consisting of 10 single-mode gates and 2 two-mode gates distributed on 3 modes as shown in the main text Fig. 4b. For GKP-encoded qubits encoded on square grids in phase-space, the implemented circuit corresponds to the encoding scheme of a logical qubit in the 3-qubit bit-flip error correction code as shown in the main text Fig. 4a.

The circuit is implemented with the three input states on mode $(B,k)$, $(B,k+2)$ and $(B,k+4)$, and output states on mode $(B,k+6N)$, $(B,k+6N+2)$ and $(B,k+6N+4)$. Since in this work the displacement by-product is compensated for in the measurement outcomes, the displacement of each gate will accumulate throughout the computation. By keeping track of each measurement outcome, and how they propagate through the subsequent gates towards the output modes, the displacements are compensated for in the measurements of the circuit’s three output states. Here, using the expressions for displacements in single- and two-mode gates in Eq. (5) and in Fig. S5 for $g = 1$, an accumulated displacement term, $Dm$, on the circuit output modes is derived, and the resulting displacement matrix, $D$, and vector of measurement outcomes, $m$, are shown in Fig. S7.

Each gate of the implemented circuit adds gate noise to the quadratures of the modes in computation proportional to the initial momentum squeezing in the cluster state generation. To avoid gate noise accumulation, the noise must be removed after each gate implementation. This can be done by using GKP-encoded qubits for computation and applying GKP error correction of the computation quadratures [15] as often as possible, preferably after each implemented gate depending on the magnitude of the gate noise. In the work here, however, GKP-encoding and error correction is not implemented and as a result, the gate noise accumulates during the computation and thus adds to the circuit output modes. This noise is measured and compared to the theoretically expected accumulated gate noise which depends on the initial momentum squeezing. Using the expression for single- and two-mode gate noise in Eq. (11) and in Fig. S6 for $g = 1$, and keeping track of the gate noise propagation through each gate of the implemented circuit, a combined circuit noise term of accumulated gate noise, $N\hat{p}_i$, is derived with the noise matrix, $N$, and the vector of initial momentum squeezed quadrature, $\hat{p}_i$, presented in Fig. S7. The resulting noise factors, $\sum_q N_q^2$, of the output quadratures $\hat{x}_{B,k+6N}$, $\hat{x}_{B,k+6N+2}$, $\hat{x}_{B,k+6N+4}$, $\hat{p}_{B,k+6N}$, $\hat{p}_{B,k+6N+2}$, and $\hat{p}_{B,k+6N+4}$ for $q = 1, 2, 3, 4, 5, 6$ are also shown. Note that the large circuit noise is due to the lack of error correction. In fault-tolerant quantum computation the purpose of error correction is to prevent gate noise to accumulate into such large values.
Fig. S5: Displacement matrix $D$ in Eq. (13) for implementing the $(\hat{F} \otimes \hat{F}^j)\hat{C}_Z(g)$-gate. Here the left and right column of matrices show $D$ for input mode $B,k$ being on an even and odd wire respectively, while each row represents a different value of coupling constant $g$. Indices without numbers are zero. On top of each column, the vector $m$ of measurement outcomes are shown.
Fig. S6: Gate noise matrix $N$ in Eq. (13) for implementing the $(\hat{F} \otimes \hat{F})\hat{C}_2(g)$-gate. Here the left and right column of matrices show $N$ for input mode $B, k$ being on an even and odd wire, respectively, while each row represents a different value of coupling constant $g$. Indices without numbers are zero. On the very right, the total gate noise factor is shown for each output quadrature, and is equal for the left and right column of gate noise matrices. Above the matrices, the vector $p_i$ of initial momentum squeezed quadratures is shown.
Fig. S7: Upper part shows the displacement matrix, \( \mathbf{D} \), and vector of measurement outcomes, \( \mathbf{m} \), of the accumulated displacement by-products on the circuit’s output quadratures. The matrix rows correspond to the output mode quadratures \( \hat{x}_{B,k+6N} \), \( \hat{x}_{B,k+6N+2} \), \( \hat{x}_{B,k+6N+4} \), \( \hat{p}_{B,k+6N} \), \( \hat{p}_{B,k+6N+2} \), and \( \hat{p}_{B,k+6N+4} \). Similarly, the lower part shows the gate/circuit noise matrix, \( \mathbf{N} \), and vector of initial momentum squeezed quadratures, \( \mathbf{p}_i \), of accumulated gate noise on the circuit’s output quadratures.
3 Gate tomography

In section 2 we described how quantum gates and circuits are implemented by projective measurements on the generated cluster state presented in section 1. Here, we describe the method of gate and circuit characterization which is based on gate tomography. The procedure is based on probing the gates using entangled states as input and subsequently determine the quadrature correlations that remain after the gate operation. This method was also used in Ref. [8] but here we generalize it to multi-mode gates and circuits.

The implemented n-mode Gaussian gate/circuit, or operation, can be represented by a \(2^n \times 2^n\) symplectic matrix \(\mathbf{S}\) (referred to in section 2.1 and 2.2 as \(\mathbf{G}\) and \(\mathbf{G}_2\)) plus gate noise. Here, we consider the case where the displacement by-products have been compensated for, either by feed-forward or, as in this work, in the measurement results. Thus, the goal is to estimate \(\mathbf{S}\) and the variance of the gate noise in each quadrature.

To do so, each of the \(n\) input modes to the implemented operation is entangled to one of \(n\) reference modes, and from measuring correlations of the reference modes and the operation’s output modes we can extract \(\mathbf{S}\), while the gate noise variance can be estimated from measured variances of the input and output modes together with the estimated \(\mathbf{S}\). The corresponding circuit is presented in Fig. S8.

The entanglement probes in Fig. S8 are prepared by the wire projections shown in Fig. S2, and the resulting two-mode entangled states are formed as two-mode wire cluster states. One mode of each of the \(n\) two-mode cluster state wires serves as an input mode to the implemented \(n\)-mode operation, while the other mode serves as the corresponding reference. The projected wires are two-mode squeezed states obtained by interfering two squeezed states, as shown in Fig. S2d, with the corresponding quadrature transformations

\[
\begin{align*}
\left( \hat{\mathbf{x}}_{\text{ref}}^j, \hat{\mathbf{p}}_{\text{ref}}^j \right) & \rightarrow \left( \hat{\mathbf{x}}_{\text{ref}}^j, \hat{\mathbf{p}}_{\text{ref}}^j \right) + \frac{\pi}{4}, \\
\left( \hat{\mathbf{x}}_{\text{in}}^j, \hat{\mathbf{p}}_{\text{in}}^j \right) & \rightarrow \left( \hat{\mathbf{x}}_{\text{in}}^j, \hat{\mathbf{p}}_{\text{in}}^j \right) - \frac{\pi}{4},
\end{align*}
\]

for a \(w\) = odd wire, with a similar transformation for an even wire. Here, the displacement, \(\delta\), is compensated for, while the \(\sqrt{2}\) momentum anti-squeezing contribution and the effect of \(N\) (after moving \(N\) to the left of the beam-splitter and phase shifts in Fig. S2d where it adds uncorrelated noise in connected wire modes as discussed in section 2) are combined in an effective squeezing of \(s > e^{-r}\) of the initial input and reference mode momentum quadratures such that

\[
\begin{align*}
\langle (\hat{\mathbf{x}}_{\text{ref}}^j)^2 \rangle & = \langle (\hat{\mathbf{x}}_{\text{in}}^j)^2 \rangle = aV_0 / s^2, \\
\langle (\hat{\mathbf{p}}_{\text{ref}}^j)^2 \rangle & = \langle (\hat{\mathbf{p}}_{\text{in}}^j)^2 \rangle = s^2 V_0,
\end{align*}
\]

\(V_0 = 1/2\) is the variance of vacuum for \(\hbar = 1\), and \(a = 1\) for pure states while excess anti-squeezing is...
described by \( a > 1 \). Note that here we assume the reference and input modes to be equally squeezed, while the exact values of \( s \) and \( a \) are unimportant for the purpose here. The resulting quadrature correlations between the input and reference modes are

\[
\begin{align*}
\langle \hat{x}_i^{\text{in}} \hat{x}_j^{\text{ref.}} \rangle &= \langle \hat{p}_i^{\text{int}} \hat{p}_j^{\text{ref.}} \rangle = \langle \hat{x}_i^{\text{in}} \hat{p}_j^{\text{ref.}} \rangle = \langle \hat{p}_i^{\text{int}} \hat{x}_j^{\text{ref.}} \rangle = 0 \quad , \quad i \neq j \\
\langle \hat{x}_j^{\text{ref.}} \hat{x}_j^{\text{ref.}} \rangle &= \langle \hat{p}_j^{\text{int}} \hat{p}_j^{\text{ref.}} \rangle = 0 \\
\langle \hat{p}_j^{\text{int}} \hat{p}_j^{\text{ref.}} \rangle &= \langle \hat{x}_j^{\text{in}} \hat{x}_j^{\text{ref.}} \rangle = \frac{1}{2} \left( \frac{a}{s} - s^2 \right) \nu_0 \equiv \varepsilon_j > 0
\end{align*}
\]

for odd wires with positive edge weight, while similar for even wires with opposite sign of \( \varepsilon_j \). Here the first line shows quadrature correlations between input and reference modes of different two-mode wire states which are naturally zero.

After the gate/circuit operation the input mode quadratures are transformed by the symplectic matrix \( \mathbf{S} \) with entries \( s_{i,j} \) while gate noise is added to the output quadratures as

\[
\begin{pmatrix}
\hat{x}_1^{\text{out}} \\
\vdots \\
\hat{p}_n^{\text{out}} \\
\hat{p}_1^{\text{out}} \\
\vdots \\
\hat{p}_n^{\text{out}}
\end{pmatrix} =
\begin{pmatrix}
\hat{x}_1^{\text{ref.}} \\
\vdots \\
\hat{x}_n^{\text{ref.}} \\
\hat{p}_1^{\text{ref.}} \\
\vdots \\
\hat{p}_n^{\text{ref.}}
\end{pmatrix} + \mathbf{S} \mathbf{N} \hat{p}_i .
\]

Here the gate noise vector of the initial momentum squeezed quadrature vector, \( \hat{p}_i \), transformed by the gate noise matrix, \( \mathbf{N} \), is described in section 2. Using Eq. (15) the quadrature correlations between the input and reference modes can then be used to extract \( s_{i,j} \) since

\[
\begin{align*}
\langle \hat{x}_i^{\text{out}} \hat{p}_j^{\text{ref.}} \rangle &= \langle (s_{1,1} \hat{x}_1^{\text{in}} + \cdots + s_{i,n} \hat{x}_n^{\text{in}} + s_{i+1,1} \hat{p}_1^{\text{in}} + \cdots + s_{i+2n,1} \hat{p}_n^{\text{in}} ) \hat{p}_j^{\text{ref.}} \rangle \\
&= s_{i,j} \langle \hat{x}_j^{\text{in}} \hat{p}_j^{\text{ref.}} \rangle + s_{i+1,j} \langle \hat{p}_j^{\text{in}} \hat{x}_j^{\text{ref.}} \rangle \\
&= s_{i,j} \varepsilon_j \\
\langle \hat{p}_i^{\text{out}} \hat{x}_j^{\text{ref.}} \rangle &= \langle (s_{n+1,1} \hat{x}_1^{\text{in}} + \cdots + s_{n+i,n} \hat{x}_n^{\text{in}} + s_{n+i+1,1} \hat{p}_1^{\text{in}} + \cdots + s_{n+i+2n,1} \hat{p}_n^{\text{in}} ) \hat{x}_j^{\text{ref.}} \rangle \\
&= s_{n+i,j} \langle \hat{x}_j^{\text{in}} \hat{p}_j^{\text{ref.}} \rangle + s_{n+i+1,j} \langle \hat{p}_j^{\text{in}} \hat{x}_j^{\text{ref.}} \rangle \\
&= s_{n+i+1,j} \varepsilon_j \\
\langle \hat{p}_i^{\text{out}} \hat{x}_j^{\text{ref.}} \rangle &= \langle (s_{n+i+1,1} \hat{x}_1^{\text{in}} + \cdots + s_{n+i+n} \hat{x}_n^{\text{in}} + s_{n+i+n+1,1} \hat{p}_1^{\text{in}} + \cdots + s_{n+i+2n,1} \hat{p}_n^{\text{in}} ) \hat{x}_j^{\text{ref.}} \rangle \\
&= s_{n+i,j} \langle \hat{x}_j^{\text{in}} \hat{p}_j^{\text{ref.}} \rangle + s_{n+i+n+1,j} \langle \hat{p}_j^{\text{in}} \hat{x}_j^{\text{ref.}} \rangle \\
&= s_{n+i+n+1,j} \varepsilon_j
\end{align*}
\]

for \( i, j \in \{1, \cdots, n\} \). Thus, finally, the symplectic matrix of the implemented operation can be estimated from quadrature correlations of output and reference modes as

\[
\mathbf{S} =
\begin{pmatrix}
\vdots & \vdots & \vdots \\
\langle \hat{x}_1^{\text{out}} \hat{p}_j^{\text{ref.}} \rangle / \varepsilon_j & \langle \hat{x}_i^{\text{out}} \hat{x}_j^{\text{ref.}} \rangle / \varepsilon_j & \vdots \\
\vdots & \vdots & \vdots \\
\langle \hat{p}_i^{\text{out}} \hat{p}_j^{\text{ref.}} \rangle / \varepsilon_j & \langle \hat{p}_i^{\text{out}} \hat{x}_j^{\text{ref.}} \rangle / \varepsilon_j & \vdots \\
\end{pmatrix}
\]
Here, $\varepsilon_j$ is estimated from the measurements of the quadrature correlations between input and reference modes on even and odd wire states individually side by side with the implemented operations. As such, within even and odd wires, we assume $\varepsilon_j$ to be identical for different $j$. This is a valid assumption, as the gate tomography of each implemented gate (including estimating $\varepsilon_j$ for even and odd wires) in this work is carried out within $19N = 228$ temporal modes, i.e. within a time period of $228 \times 247 \text{ ns} = 56.3 \text{ ms}$, while the setup stability allows for a stable setup without changes for much longer time periods.

Using the estimated symplectic matrix in Eq. (17), the variance of the gate noise in Eq. (16) can be estimated as the difference of the quadrature variance on the output modes and the expected quadrature variance from the input modes as

$$
\text{Var}\{N\hat{p}_i\} = \text{Var}\left\{\begin{pmatrix}
\hat{x}_{1}^{\text{out}} \\
\vdots \\
\hat{x}_n^{\text{out}} \\
\hat{p}_1^{\text{out}} \\
\vdots \\
\hat{p}_n^{\text{out}} 
\end{pmatrix}
\right\} - \text{Var}\left\{S\begin{pmatrix}
\hat{x}_{1}^{\text{in}} \\
\vdots \\
\hat{x}_n^{\text{in}} \\
\hat{p}_1^{\text{in}} \\
\vdots \\
\hat{p}_n^{\text{in}} 
\end{pmatrix}\right\}. \tag{18}
$$

Here the reference mode is traced out. Similar to $\varepsilon_j$, $\text{Var}\{\hat{x}_j^{\text{in}}\}$ and $\text{Var}\{\hat{p}_j^{\text{in}}\}$ are estimated by measurements on wire cluster state individually for even and odd wires, both before and after the gate tomography, but within $19N = 228$ temporal modes.

### 3.1 Single-mode gates

In Fig. S9 the basis setting layout on the generated cluster state for single-mode gate implementation and tomography is shown. Since $S$ is a $2 \times 2$ matrix for single-mode gates, 4 different measurements of quadrature correlations of the output-reference mode are required to estimate $S$. Thus, with a section of $19N = 228$ temporal modes, 8 different single-mode gates can be implemented and characterized at once in parallel on 4 wires. The remaining 2 of the 6 wires are used to estimate the correlations between input and reference modes, $\varepsilon_j$, of the two-mode wire states in both odd and even wires, together with the input mode variance when tracing out the reference modes. To build up statistics, measurements with the layout in Fig. S9 are repeated 10\,000 times. In the following we will refer to such 10\,000 repeated measurements of the same layout as a set of measurements.

Each of the implemented single-mode gates, $\hat{R}(\theta)$, $\hat{P}(\sigma)$ and $\hat{S}(e^{-\tau})$, are implemented in different sets of measurements with 7 different values of $\theta$, $\sigma$ and $\tau$. To fill out one set, which can implement and characterize 8 different gates, the value $\theta$, $\sigma$, $\tau$ and $r$ are repeated twice in each set. From wire $w = 4$ in each set the input-reference quadrature correlations are estimated to be $\varepsilon_j = -2.14 \pm 0.03$, $-2.15 \pm 0.03$ and $-2.14 \pm 0.03$ for the sets with $\hat{R}(\theta)$, $\hat{P}(\sigma)$ and $\hat{S}(e^{-\tau})$, respectively, and are used for gate tomography on $w = \text{even}$ wires with negative weight. Similarly, from wire $w = 5$, $\varepsilon_j = 2.11 \pm 0.04$, $2.12 \pm 0.03$ and $2.11 \pm 0.02$ are estimated and used for gate tomography on $w = \text{odd}$ wires with positive weight. The uncertainties on $\varepsilon_j$ are estimated as the standard deviation of the 16 measured quadrature correlations in each wire $w = 4$ and 5 for each measurement set. The resulting symplectic matrix for each implemented single-mode gates, extracted using Eq. (17), are shown in the main text Fig. 2a as a function of $\theta$, $\sigma$ and $r$.

Also, using wires $w = 4$ and 5, the quadrature variances of the input modes when tracing out the reference mode (i.e. ignoring the reference mode measurement outcome) are estimated to be $(\text{Var}\{x_j^{\text{in}}\}, \text{Var}\{p_j^{\text{in}}\}) = (2.28 \pm 0.04, 2.67 \pm 0.05)$, $(2.33 \pm 0.03, 2.64 \pm 0.03)$ and $(2.31 \pm 0.02, 2.66 \pm 0.02)$ on wire $w = 4$ for measurement sets with $\hat{R}(\theta)$, $\hat{P}(\sigma)$ and $\hat{S}(e^{-\tau})$ respectively, and similarly $(\text{Var}\{x_j^{\text{in}}\}, \text{Var}\{p_j^{\text{in}}\}) = (2.30 \pm 0.03, 2.78 \pm 0.04)$, $(2.34 \pm 0.03, 2.75 \pm 0.03)$ and $(2.31 \pm 0.03, 2.75 \pm 0.03)$, respectively, on wire $w = 5$. The uncertainties in the variances of the $\hat{x}$ and $\hat{p}$ quadratures in each measurement set are estimated as standard deviation of the 8 measurements of input modes in each quadrature on wires $w = 4$ and 5. The resulting gate noise variances, extracted using Eq. (18) with the variance of the output modes measured in each gate tomography, are shown in the main text Fig. 2c. Also, the gate noise variance compensated by the theoretical gate noise factor of 6\,dB is shown, in which case it agrees with the initial momentum squeezing of $4.4 \text{ dB}$ below the vacuum variance as discussed at the end of section 2.1 (a more general discussion of gate noise is presented in section 4).
Fig. S9: Basis setting layout with $19N = 228$ temporal modes for implementation and characterization by gate tomography of 8 different single-mode gates on wire $w = 0–3$, each in the regions marked by (1–8). In each region the gate is implemented 4 times in order to measure $pp$, $xx$, $xp$- and $pp$-correlations between the reference and output modes to estimate the indices $s_{1,1}$, $s_{1,2}$, $s_{2,2}$ and $s_{2,1}$, respectively, of the implemented gate symplectic matrix $S$ by Eq. (17). Here, modes marked by red and blue are measured in $\hat{x}$- and $\hat{p}$-basis, respectively, while faded modes are control modes measured in the $\pm \pi/4$ basis to form wires. The faded edges to control modes are only shown to indicate the initial coiled up 1D cluster state before wire projection, and has now physical meaning here. Even though present in all temporal modes, the beam splitter $BS_3$ is only shown between spatial modes measured in different basis in order to implement the desired gates. The measurement outcomes before $BS_3$ on all other modes are extracted using Eq. (8). Wire $w = 4$ and $5$ are used to estimate the quadrature correlations between input and reference modes, $\varepsilon_j$, together with the quadrature variance of input modes in order to estimate the gate noise variance by Eq. (18). To the right of the cluster state basis layout, the corresponding single-mode gate tomography circuit of input, output, and reference modes is shown.
To show how the gate noise behaves as a function of squeezing, in the main text Fig. 2d the gate noise of \( \hat{R}(\theta) \), averaged over \( \hat{x} \) and \( \hat{p} \) and over the 7 different implemented values of \( \theta \), is shown as a function of pump power relative to the OPO thresholds. The measured gate noise is seen to agree well with the gate noise expected for the estimated properties of the experimental setup and is discussed further in section 4.

### 3.2 Two-mode gate

When performing gate tomography on the two-mode controlled-Z gate, \( \hat{C}_Z(g) \), the 4×4 = 16 entries \( s_{i,j} \) of the corresponding symplectic matrix \( \mathbf{S} \) can be estimated by executing the \( \hat{C}_Z(g) \) gate four times, since 4 different input-reference mode quadrature correlations can be estimated simultaneously using the two reference and output modes of each implementation. The basis setting layout of 228 temporal modes is shown in Fig. S10, in which \( \hat{C}_Z(g) \) can be implemented with three different values of \( g \). In total, \( \hat{C}_Z(g) \) is implemented with 5 different values of \( g \) distributed on two sets of measurements with \( g = 0 \) repeated twice to fill out the sets.

Similar to the gate tomography of single-mode gates in section 3.1, the input-reference quadrature correlations, \( \varepsilon_j \), averaged on the two measurement sets, are estimated from wires \( w = 4 \) and 5 to be \( \varepsilon_j = -2.15 \pm 0.04 \) and \( 2.13 \pm 0.03 \), respectively, with uncertainties estimated as the standard deviation of 16 measured quadrature correlations in each wire. From the gate tomography, the resulting symplectic matrix estimated using Eq. (17) is shown in the main text Fig. 3a as a function of \( g \).

From wires \( w = 4 \) and 5 the input mode variances, averaged over the two measurement sets, are estimated to be \( \text{Var} \{ \hat{x} \text{in}^w_j \}, \text{Var} \{ \hat{p} \text{in}^w_j \} \) = (2.36 ± 0.06, 2.72 ± 0.04) and (2.34 ± 0.03, 2.65 ± 0.04) on even and odd wires respectively, with uncertainties estimated as the standard deviation of the 8 measured input modes per \( \hat{x} \) and \( \hat{p} \)-quadrature of each wire. Using this, and the estimated \( \mathbf{S} \), the gate noise variance is estimated using Eq. (18), and shown in the main text Fig. 3c as a function of \( g \). Here, the gate noise variance is also compensated for by the theoretical gate noise factor presented in Fig. S6, in which case the gate noise agrees well with the initial momentum squeezed variance of 4.4 dB below the vacuum variance.
Fig. S11: Basis setting layout for implementation and characterization of the circuit described in section 2.3, here implemented on wire $w = 1$, 2 and 3, while wire $w = 0$, 4 and 5 are not shown here. The notation is the same as in Fig. S9 for single-mode gates. For full tomography the circuit is implemented 4 times, distributed on two sets of measurements. Here, the first set is shown from which the first 6 columns of the symplectic matrix, $S$, is estimated. In the second set for estimating the last 6 columns of $S$, the $\hat{x}$- and $\hat{p}$-basis settings on all output and reference modes are swapped. Below the basis setting layout, the corresponding circuit of the multi-mode circuit tomography with input, output, and reference modes is shown.

3.3 Circuit

The quantum circuit, described in section 2.3, is implemented on wires $w = 1$, 2 and 3 as shown in the basis setting layout in Fig. S11. Similar to the $C_Z(g)$-gate in section 3.2, all $6 \times 6 = 36$ entries of the corresponding symplectic matrix $S$ can be extracted from implementing the circuit 4 times, as $3 \times 3 = 9$ indices can be estimated from the three reference and output modes of one implementation. Two measurement sets are required for full gate tomography of the circuit implemented on wires $w = 1$, 2 and 3: One as in Fig. S11 where the first 6 columns of $S$ are estimated, and a similar set where the $\hat{x}$- and $\hat{p}$-basis settings are swapped on the reference and output modes in order to estimate the last 6 columns of $S$.

The first and last two time steps on all wires, $w = 0$–5 (though only wire $w = 1$, 2 and 3 are shown in Fig. S11), are used to estimate the input-reference mode quadrature correlations, $\varepsilon_j$, and are estimated to be $\varepsilon_j = -2.14 \pm 0.03$ and $2.12 \pm 0.06$ on even and odd wires, respectively, averaged over the two measurement sets. Uncertainties are estimated as the standard deviation of 12 measured quadrature correlations each for even and odd wires. Using this, and the estimated $S$, the gate noise variance is estimated using Eq. (18), and shown in the main text Fig. 4d.

Similar to $\varepsilon_j$, the quadrature variances of input modes are estimated from the first and last two time steps in each wire when tracing out reference modes by ignoring the reference mode measurement outcomes. The input mode quadrature variances are estimated to be $(\text{Var}\{x_{\text{in}}\}^\prime, \text{Var}\{p_{\text{in}}\}^\prime) = (2.34 \pm 0.05, 2.72 \pm 0.03)$ and $(2.34 \pm 0.04, 2.68 \pm 0.08)$ on even and odd wires respectively. Uncertainties are estimated as the standard deviation from 6 measured input modes per $\hat{x}$- and $\hat{p}$-quadrature each in even and odd wires. Using this, and the estimated $S$, the gate noise variance is estimated using Eq. (18), and shown in the main text Fig. 4d. The gate noise is also shown when compensating for the theoretical gate noise factors presented in Fig. S7, and the resulting gate noise agrees well with the initial momentum squeezed variance of 4.4 dB below the vacuum variance.
4 Gate noise

In this section, we discuss the performance with respect to measured gate noise in the cluster state computation scheme of this work. Ideal cluster states have nullifiers of zero variance. However, such ideal cluster states, generated from infinite squeezed states, are non-physical. Instead, as discussed in section 2, we generate approximate cluster states, or cluster-type states, from finitely squeezed states with nullifier variances that vanish in the limit of infinite squeezing but are otherwise non-zero. When implementing gates on the cluster state by projective measurement, the inherent noise in the cluster state from finite squeezing leads to gate noise proportional to the initial squeezing in the cluster state generation as discussed in section 2.1 and 2.2—the gate noise variance in each quadrature is the product of the initial squeezing variance and the corresponding gate noise factor.

To prevent gate noise from accumulating on the states in computation, error correction is required. This can be achieved by encoding the computational states as qubits using Gottesman-Kitaev-Preskill (GKP) encoding, where qubits are encoded in the bosonic mode quadratures [15]. The gate noise, which appears as noise in the quadratures, can then be corrected with GKP quadrature error correction with a success probability depending on the amount of squeezing. In the event of an error in the quadrature error correction, a bit-flip and/or phase-flip error is induced on the encoded qubit, which can then be corrected with a concatenated qubit error correction code. A detailed review of GKP encoding and error correction can be found in [15, 16, 17], while in [5, 18] GKP error correction and resulting qubit error probability is reviewed in terms of gate noise. To achieve fault-tolerant quantum computation, the probability of inducing a qubit error on the GKP-encoded qubits should be lower than the error probability threshold allowed by the concatenated qubit error correction code. Since this qubit error probability depends on the gate noise and thereby the initial squeezing, the amount of required squeezing for fault-tolerance depends on the error probability that the concatenated qubit error correction code can tolerate. Different squeezing levels have been estimated in the settings of cluster state computation, ranging from at highest 20 dB below the vacuum variance [18], to 17 dB in [19] where excess anti-squeezing is shown to not affect the squeezing threshold, and further to 10 dB in [20] using topological qubit error correction codes (though this requires a 3-dimensional cluster state instead of the 2-dimensional cluster state used in this work).

In conclusion, even though the gate noise measured in this work is larger than what can be achieved with different architectures, it is less than what can be achieved with the computation scheme used here without the presence of a cluster state [5]. In the cluster state computation scheme here, the function of the cluster state can be thought of as to decrease the gate noise depending on the cluster state quality in terms of initial squeezing. In the main text Fig. 2d, the measured single-mode gate noise is shown as a function of pump power. At zero pump power where the cluster state is simply vacuum, we measured 6 dB gate noise as expected, while increasing the pump power, generating a cluster state, decreases the gate noise. At some point the gate noise saturates due to optical losses and phase fluctuations, limiting the cluster state quality achievable in the given setup. This is not a fundamental limit but merely a technical challenge. For fault-tolerant computation, optical losses and phase control need to be optimized to improve the cluster state quality, while the computation scheme will remain the same as demonstrated here. Even though some cluster
state architectures, like the quad-rail lattice [3, 12], can offer an improvement in the gate noise by 3 dB [5], to achieve fault-tolerant computation it is not enough to simply measure gate noise less than the vacuum variance, \( V_0 = 0 \) dB; optical losses and phase control need to be optimized for any architecture. As such, although the gate noise is a good figure of merit, a gate noise below \( V_0 \) is not the target—it is gate noise below some fault-tolerant squeezing threshold which is the target. Below we estimate how the gate noise would scale in more optimal settings of the computation scheme in this work.

### 4.1 Gate noise in optimal settings

To estimate the gate noise, it is sufficient to estimate the initial quadrature squeezing in the cluster state generation and multiply with the gate noise factors \( \sum_i N_i^2 \) with \( N_i \) being entries of the gate noise matrix \( \mathbf{N} \) as discussed in section 2. For single-mode gates, the gate noise factor is \( \sum_i N_i^2 = \sum_i N_i^2 = 4 \) in both \( \hat{x} \)- and \( \hat{p} \)-quadrature, corresponding to adding 6 dB to the initial squeezing, while the gate noise factor for the \( C_Z(g) \)-gate is given in Fig. S6. Thus, we need to first estimate the initial momentum squeezing in the cluster state temporal modes.

The temporal modes are defined by the temporal mode function, \( f_k(t) \), in Eq. (1). For the initial momentum squeezed states in the cluster state generation \( \hat{x} = \langle \hat{p} \rangle = 0 \), and the quadrature variance of a temporal mode \( k \) becomes

\[
\text{Var}\{\hat{q}_k\} = \langle \hat{q}^2 \rangle = \left\langle \int f_k(t)\hat{q}(t)\,dt \int f_k(t')\hat{q}(t')\,dt' \right\rangle = \iint f_k(t)f_k(t') \langle \hat{q}(t)\hat{q}(t') \rangle \,dt\,dt' , \quad \hat{q} = \hat{x}, \hat{p} ,
\]

where \( \langle \hat{x}(t)\hat{x}(t') \rangle \) and \( \langle \hat{p}(t)\hat{p}(t') \rangle \) are the quadrature auto-covariance functions. For momentum squeezed states generated as output from an optical parametric oscillator (OPO) pumped below threshold, the quadrature auto-covariance functions are

\[
\langle \hat{x}(t)\hat{x}(t') \rangle = \frac{1}{2} \delta(t-t') + \frac{\eta\gamma\varepsilon}{\gamma - \varepsilon} e^{-(\gamma-\varepsilon)(t-t')} ,
\]

\[
\langle \hat{p}(t)\hat{p}(t') \rangle = \frac{1}{2} \delta(t-t') - \frac{\eta\gamma\varepsilon}{\gamma + \varepsilon} e^{-(\gamma+\varepsilon)(t-t')} ,
\]

where \( \eta, \gamma, \) and \( \varepsilon \) are the overall efficiency, the OPO decay rate (or bandwidth), and pump rate, respectively [22]. In the experimental setup \( \gamma = 2\pi \times 7.7 \text{ MHz} \) on average for the two OPO squeezing sources [1].

Inserting Eq. (1) and (20) into Eq. (19), and using that \( \varepsilon = \gamma \sqrt{\frac{P}{P_{\text{thr}}}} \), where \( \frac{P}{P_{\text{thr}}} \) is the pump power relative to the OPO threshold, we estimate the quadrature squeezing and anti-squeezing as a function of pump power for different efficiencies. Adding 6 dB to the noise variance, we estimate the resulting single-mode gate noise, which is presented in the main text Fig. 2d for some optimal settings with high efficiencies of \( \eta \geq 90\% \). Here, the gate noise is presented with two different OPO bandwidths, namely \( \gamma = 2\pi \times 7.7 \text{ MHz} \) as in the experimental setup, and \( \gamma = 2\pi \times 100 \text{ MHz} \) for more optimal settings. Predicted gate noise for the current experimental efficiency of \( \eta = 77.7\% \) (an average of 78.9\% and 76.4\% of two fitted efficiencies in the supplementary information of [1]), together with an estimated phase fluctuation of \( \sigma = 4^\circ \), is shown as well and is seen to agree well with the experimentally measured gate noise. The same analysis is possible for the two-mode \( C_Z(g) \)-gate using the gate noise factors in Fig. S6. In general, the gate noise is seen to decrease with increasing efficiencies and pump power. Furthermore, the gate noise is also seen to decrease with increasing squeezing bandwidth. This is explained by a better coverage of the temporal mode frequency response by the wider squeezing bandwidth of 100 MHz.

In conclusion, for high enough efficiency and phase control where the gate noise decrease persistently with increasing pump power instead of saturating, the gate noise in the cluster state computation scheme here can be brought down and eventually allow fault-tolerant computation, depending on the squeezing threshold set by the concatenated qubit error correction code. Besides this, the squeezing source bandwidth should be broad enough to cover the temporal modes in the frequency domain well. This latter condition is especially important to keep in mind when scaling up the number of encoded modes by reducing the short delay line, \( \tau \), shortening the temporal modes in time domain and thereby broadening them in frequency domain.
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