Dual-mode operation of 2D material-base hot electron transistors

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Vertical hot electron transistors incorporating atomically-thin 2D materials, such as graphene or MoS2, in the base region have been proposed and demonstrated in the development of electronic and optoelectronic applications. To the best of our knowledge, all previous 2D material-base hot electron transistors only considered applying a positive collector-base potential (V_{CB} > 0) as is necessary for the typical unipolar hot-electron transistor behavior. Here we demonstrate a novel functionality, specifically a dual-mode operation, in our 2D material-base hot electron transistors (e.g. with either graphene or MoS2 in the base region) with the application of a negative collector-base potential (V_{CB} < 0). That is, our 2D material-base hot electron transistors can operate in either a hot-electron or a reverse-current dominating mode depending upon the particular polarity of V_{CB}. Furthermore, these devices operate at room temperature and their current gains can be dynamically tuned by varying V_{CB}. We anticipate our multi-functional dual-mode transistors will pave the way towards the realization of novel flexible 2D material-based high-density and low-energy hot-carrier electronic applications.

Since 1960, ballistic hot electron transistors (HETs) have been vigorously researched and implemented in diverse material systems (e.g. cold cathode transistor exploiting a thin metal base2,3, planar doped barrier transistor incorporating III-V compound semiconductors3, two-dimensional electron gas (2DEG)-based HETs4–6, etc.) for their potential in high-speed applications. Analogous in design to a bipolar transistor, HETs are comprised of an emitter, base, and collector. However, various properties of the injected ballistic hot electrons, such as their initial velocity, higher kinetic energy, and quasi-mono-energetic distribution upon injection via quantum tunneling, differ from the diffusive transport in bipolar transistors2,7. In HETs, the ballistic hot electrons are injected through a thin tunnel barrier separating the emitter from the base, and a portion of these hot electrons are collected upon traversing a filter barrier at the base-collector junction (e.g. contribute towards the on-state collector current). Furthermore, the cutoff frequency of HETs is primarily governed by the base thickness and the resistances and capacitances of the emitter and collector regions. To this end, various bulk semiconductor heterostructures, such as InGaAs/InP and AlGaAs/GaAs, have been precisely engineered with undoped and narrow (<100 nm) base regions since the 1970s with the introduction of advanced epitaxial technologies, such as molecular beam epitaxy (MBE) and metal-organic chemical vapor deposition (MOCVD)7. However, several issues including inelastic electron scattering in the finite-width base region, finite base transit time, and quantum-mechanical reflections (e.g. impedance-mismatching) at the collector-base junction typically resulted in subpar current gains at or below room temperature8–5. In addition, these epitaxial techniques add to the complexity in the time and cost of fabricating such structures.

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The advent of 2D van der Waals materials, such as graphene and the transition metal dichalcogenides (TMDs), has sparked a paradigm shift in the design and engineering of atomic-scale systems. Their strong in-plane mechanical stability in addition to their weak out-of-plane van der Waals forces allow us to amalgamate atomic-scale heterostructures exhibiting novel optoelectronic phenomena and functionalities. Recently, ballistic hot electron transistors incorporating either monolayer graphene or monolayer MoS2 in the base region have achieved high current modulation ($I_{ON}/I_{OFF} \approx 10^{4}–10^{5}$) and high-current gain ($\alpha \approx 0.95$) at room temperature, respectively. This unique class of 2D material-base hot electron transistors (2D-HETs) shows great potential for 2D material-based high-frequency logic applications upon further device optimization.

The 2D-HETs rely upon the vertical (e.g. out-of-plane) emission of hot electrons through an atomic-scale base region and the subsequent filtering of these hot electrons by a built-in potential energy barrier near the base-collector junction. However, in spite of these accomplishments, there is a dearth of insight into the actual out-of-plane transport (e.g. the dominant scattering mechanisms and the actual potential energy landscape) experienced by the hot electrons in these 2D-HETs. As far as we know, all previous 2D-HETs operated under the application of a positive collector-base potential ($V_{CB} > 0$) and thus were limited to a single functionality, namely the typical unipolar hot electron transistor behavior. To augment the functionality of electronics such as multi-level cells for low footprint vertical transport-based memory applications, here we introduce an alternative and peculiar conduction mode of operation which we refer to as a dual-mode operation in our 2D-HETs upon application of either a positive collector-base potential ($V_{CB} > 0$) or a negative collector-base potential ($V_{CB} < 0$). Thus, our 2D-HETs can operate in either a hot-electron or a reverse-current dominating mode depending upon the particular bias configuration. The 2D-HETs operate at room temperature and their current gains can be dynamically tuned by varying $V_{CB}$. Furthermore, we surmise that the current saturation-like behavior in the transfer characteristics of the MoS2-HETs when operating in the reverse-current dominating mode ($V_{CB} < 0$) could serve as a multi-level cell (e.g. data storage) in future multi-functional 2D material-based high-density and low-energy hot-carrier electronic (e.g. vertical transport based logic and memory) applications.

Results

We demonstrate vertical transport 2D-HETs which exhibit a novel dual-mode operation by incorporating either monolayer MoS2 (MoS2-HET) or monolayer graphene (G-HET) in the base region. The device structure of the 2D-HET is presented in Fig. 1a and a top-view optical micrograph of an actual MoS2-HET is shown in Fig. 1b. The three-terminal device consists of a degenerately doped n++ silicon substrate ($N_D \sim 10^{18}$ cm$^{-3}$) as the emitter (E), a monolayer of chemical vapor deposition (CVD) grown 2D material (e.g. either MoS2 or graphene) as the base (B), and sputtered (~45 nm) ITO as the collector (C). A thermally grown thin (~3 nm) SiO2 tunnel barrier separates the emitter and base terminals, whereas an atomic-layer deposited (~55 nm) HfO2 separates the base and collector and serves as the filtering barrier. The detailed fabrication process is described in the Methods and in our previous work. In this particular study, a common-base configuration was employed during the electrical measurements. Note that both of the base contacts are grounded during the electrical measurements in order to achieve a uniform potential distribution across the MoS2 base region.

We first focus on describing the two modes of operation for the 2D-HETs using energy band diagrams in order to clearly understand the physics governing the device transport. The 2D-HET with monolayer MoS2 as the base (MoS2-HET) will serve as an example. Figure 1c shows the energy band diagram for the off-state and the on-state conditions of the MoS2-HETs. In the absence of an applied $V_{CB}$, most of the hot-electrons injected through the tunnel oxide have insufficient kinetic energy to overcome the filter barrier at the collector-base junction and do not reach the collector. Instead, they back-scatter and thermalize into the MoS2 base region. However, the situation drastically changes with the application of a large $V_{CB}$. There are two possible cases for the on-state condition of the MoS2-HETs, depending upon the polarity of the applied $V_{CB}$. The first case describes the typical hot-electron injection behavior and occurs for $V_{CB} > 0$. In this scenario, hot-electrons tunneling through the emitter-base tunnel oxide have sufficient kinetic energy to overcome the filter barrier, reach the collector, and contribute to the collector current ($I_C$). The second case describes a reverse-current behavior, which is a novel feature and mode of operation enabled by our 2D-HETs, and occurs for $V_{CB} < 0$. In this scenario, the injected hot-electrons tunneling from the emitter do not have sufficient kinetic energy to surpass the raised filter barrier. Subsequently, these electrons are back-scattered and accumulate within the 2D material-base region which serves to suppress the base-collector reverse-current ($I_C$). Interestingly, $\Delta I_C$, which denotes the amount of change in the base-collector current due to the hot electron injection from the emitter, can be tuned with the applied $V_{CB}$ in this mode of operation. Figure 1d shows the common-base output characteristics of one of our MoS2-HETs.
of the MoS$_2$-HETs. The input characteristics ($I_E$-$V_{BE}$) correspond to how the emitter current depends on $V_{BE}$, whereas the transfer characteristics ($I_C$-$V_{BE}$) correspond to the manner in which the collector current varies...
with $V_{BE}$ Figure 2b shows the input and transfer characteristics for one of MoS$_2$-HETs. The emitter current ($I_{E}$) and the collector current ($I_{C}$) are shown as a function of $V_{BE}$ ($V_{BE}$ was swept from 0 to +3 V) at a $V_{CB}$ of +2 V. Both currents rapidly increase at larger $V_{BE}$, as is typical for HETs. From the input and transfer characteristics, the common-base current gain ($\alpha$) of this device can be determined, which is a figure of merit for HETs and is defined as $\alpha = I_{C}/I_{E}$. For this particular device and biasing condition of $V_{CB}$ = +2 V and $V_{BE}$ = +3 V, $\alpha$ is about 0.81, which implies that at least 80% of the injected hot-electrons ballistically traverse the single-layer MoS$_2$ base region at room temperature. Further details concerning the hot-electron dominating mode of operation in the MoS$_2$-HETs is mentioned in our previous work$^{22}$.

Reverse-current mode of operation in the MoS$_2$-HETs. Shifting from the hot-electron dominating mode of the MoS$_2$-HET, we next investigate the device characteristics operating under the reverse-current dominating condition. Figure 3a shows the energy band diagram depicting the reverse-current mode of operation for the MoS$_2$-HET. Specifically, Fig. 3a shows the conduction and valence band edges at the collector-base junction with a negative $V_{CB}$ applied. In this mode of operation, the increasingly negative $V_{CB}$ drives more and more electrons to flow from the degenerately n-doped ITO conduction band, past the filter barrier and into the base region, thus forming the reverse-current. With the injection of hot-electrons from the emitter, the continuously increasing filter barrier ($V_{CB} < 0$) causes these hot-electrons to have insufficient kinetic energy to reach the collector and thus they back-scatter into the MoS$_2$ base region. Consequently, these back-scattered electrons build up in the MoS$_2$ base region which cause a deficiency in the available density of states in the MoS$_2$ and thereby decrease or suppress the reverse-current flowing into the base region from the degenerately n-doped ITO conduction band. The change or modulation in the reverse base-collector current ($I_{C}$) as a function of $V_{BE}$ is shown for $V_{BE}$ from 0 to −10 V with step of −1 V. (d) $\alpha^*$ as a function of $V_{BE}$ at $V_{CB}$ = −10 V. The inset shows $\alpha^*$ as a function of $V_{BE}$ at $V_{CB}$ = −4, −6, −8, −9, and −10 V.
current in the MoS$_2$ base region, where $\Delta I_C$ is defined as the suppressed reverse base-collector current arising from the build-up of the injected hot-electrons in the MoS$_2$ base region. Thus, our MoS$_2$-HETs, when biased in the reverse-current dominating mode, enable the dynamic control of the available density of states in the 2D base region by varying $V_{BE}$. This qualitative behavior for the reverse base-collector current mode of operation is exhibited in the input and transfer characteristics of the MoS$_2$-HETs. Figure 3b shows the input and transfer characteristics for one of the MoS$_2$-HETs biased at three different $V_{CB}$ ($V_{CB} = 0$, $-8$, and $-10$ V). It is evident that both the emitter and collector currents increase with larger negative $V_{CB}$. Similarly, Fig. 3c shows a family of transfer characteristics, with the suppressed reverse base-collector current ($\Delta I_C$) as a function of $V_{BE}$ shown for various negative $V_{CB}$. The transfer characteristics of the MoS$_2$-HET when operated in the reverse-current mode ($V_{CB} < 0$) are peculiar in that the reverse base-collector current tends to saturate with increasing $V_{BE}$. Additionally, the reverse-current magnitude increases with larger negative $V_{CB}$ bias. We speculate that this novel current saturation-like behavior could serve as a multi-level cell for low footprint vertical transport-based memory applications in the future. As an example, consider biasing the 2D-HET at $V_{BE} = +3$ V (e.g. the highest hot electron injection current to avoid dielectric breakdown of the tunnel barrier). We can vary the steady-state reverse-current ($\Delta I_C$) by setting $V_{CB} < 0$ to various values. Based on Fig. 3c, we can address distinguishable ($\Delta I_C$) charge states for $V_{CB}$ from $-6$ V to $-10$ V and thus encode at least 4 states for a minimum of a 2-bit memory cell. Multi-level cells are memory units capable of storing more than one bit of information and thus can result in lower cost per unit of storage and higher data storage density. Furthermore, it was recently shown that cheaper multi-level cell flash drives used in practice are just as reliable as more expensive single-level cells. Thus, our dual-mode 2D-HETs may find opportunities as ultra-dense multi-functional logic/memory units. From the input and transfer characteristics, we can next ascertain the effective current gain ($\alpha^*$) of this device for the reverse-current dominating mode of operation. Figure 3d shows $\alpha^*$ for this MoS$_2$-HET as a function of $V_{BE}$ at $V_{CB} = -10$ V. Such a large negative $V_{CB}$ significantly raises the filter barrier height for the injected hot-electrons originating from the emitter, which causes them to back-scatter into the MoS$_2$ base region and build up, leading to the effective suppression of the reverse base-collector current. Similar to the hot-electron dominating mode of operation in our previous paper, it is evident that $\alpha^*$ exhibits a nearly constant characteristic at all $V_{BE}$ with a value of at least 90% for this particular MoS$_2$-HET biased at $V_{CB} = -10$ V. The inset of Fig. 3d shows a family of $\alpha^*$ characteristics as a function of $V_{BE}$ at several negative $V_{CB}$ ($V_{CB} = -4$, $-6$, $-8$, $-9$, and $-10$ V). The effective current gain, $\alpha^*$, increases with negative $V_{CB}$ and exhibits a nearly constant characteristic throughout the entire $V_{BE}$ range with a magnitude of about 94% at $V_{CB} = -10$ V.

Output characteristics and tunable current gain in the MoS$_2$-HETs. With the analysis of the input and transfer characteristics complete, we now investigate the common-base output characteristics of the MoS$_2$-HETs, which correspond to how the output collector current depends on $V_{CB}$. In order to clearly present the dual-mode operation of our MoS$_2$-HETs, the base-collector leakage current when $V_{BE} = 0$ was subtracted from the measured collector current. Figure 4a shows the common-base output characteristics for one of the MoS$_2$-HETs. The collector current is shown as a function of $V_{CB}$ at three positive $V_{CB}$ biases. The dual-mode operation is evident as the device is biased in either the hot-electron ($V_{CB} > 0$) or the reverse-current ($V_{CB} < 0$) dominating mode of operation. Above a critical electric field across the HfO$_2$, the collector current is quite sensitive to modulation and rapidly increases with a further increase in $V_{CB}$ for both cases of $V_{CB} > 0$ and $V_{CB} < 0$. Based on Fig. 4a, the on-off current ratio ($I_{OFF}/I_{ON}$) is about 140 when $V_{CB} = -10$ V and $V_{BE} = +3$ V, whereas $I_{OFF}/I_{ON}$ is $\sim 125$ when $V_{CB} = +10$ V and $V_{BE} = +3$ V. In order to convey the robust and dual-mode operation of our MoS$_2$-HETs, Fig. 4b shows a semi-log plot of the current gain as a function of $V_{CB}$ at positive $V_{BE}$ = +3 V, which is biased in both the hot-electron ($\alpha$; $V_{CB} > 0$) and the reverse-current ($\alpha^*$; $V_{CB} < 0$) dominating modes of operation. The effective current gain, $\alpha^*$, increases with larger negative $V_{CB}$ as a result of a suppression in the reverse-current and reaches a very high-current gain with a value of about 90% at $V_{CB} = -9$ V. It is evident that $\alpha^*$ can be tuned around two orders of magnitude by varying $V_{CB}$. A similar dependence of $\alpha$ on $V_{CB}$ for the hot-electron dominating case.
uniformity of the tunnel barrier, implementation of bilayer insulator tunnel barrier as well as lowering the suitable level for practical applications. This can be achieved via fine tuning of the thickness, barrier height, and performance of the 2D-HETs will be directed towards increasing the injected tunneling current density to a more dielectrics will greatly benefit future device optimization. In the meantime, further improvement in the device investigations into the out-of-plane transport among different 2D materials and their contact with bulk materials40).

Output characteristics and tunable current gain in our new class of 2D material-base hot electron transistors. Nevertheless, the profiles of both the collector current and the current gain in the output characteristics of the MoS2-HET and G-HET are quite different. At this time, not much is known of the actual out-of-plane transport (e.g. the dominant scattering mechanisms and the actual potential energy landscape) experienced by the hot electrons in these 2D-HETs27–29. What we do know is that these are two very different materials (e.g. feature different conductance band offsets, etc.). Monolayer graphene lacks a bandgap and features a linear dispersion relation, whereas monolayer MoS2 has a direct bandgap and features a parabolic dispersion relation at the K and K' points in the Brillouin zone. Furthermore, the effective mass of the electrons travelling perpendicular to the graphene was predicted to be ~25–30 m0 in a seminal paper37. A few reasons for the particularly low current gain in the G-HET compared to the MoS2-HET, may be due to the fact that the graphene-HfO2 interface features a much higher filter barrier height (e.g. 2.05 eV) compared to that of the MoS2-HfO2 interface (e.g. 1.52 eV) as well as the possibility of more prevalent acoustic phonon scattering near the base-collector junction for graphene than for MoS2. Clearly, further investigations into the out-of-plane transport among different 2D materials and their contact with bulk dielectrics will greatly benefit future device optimization. In the mean time, further improvement in the device performance of the 2D-HETs will be directed towards increasing the injected tunneling current density to a more suitable level for practical applications. This can be achieved via fine tuning of the thickness, barrier height, and uniformity of the tunnel barrier, implementation of bilayer insulator tunnel barrier38, as well as lowering the contact resistance between the 2D material and the metallic contact leads (e.g. via chemical doping39 or 1D edge contact to 2D materials40).

Summary
In conclusion, we have demonstrated a novel vertical dual-mode 2D material-base hot-electron transistor (2D-HET) incorporating either monolayer MoS2 (MoS2-HET) or monolayer graphene (G-HET) in the base region. This new class of 2D-HETs can operate in either a hot-electron or a reverse-current dominating mode depending upon the particular bias configuration. For the hot-electron dominating mode of operation (V_{CB} > 0), once the hot-electrons tunneling through the emitter-base tunnel barrier have sufficient kinetic energy, they can vertically transport through the 2D material base region, surpass the filter barrier at the collector-base junction, and reach the collector. For the reverse base-collector current dominating mode of operation (V_{CB} < 0), the continuously increasing filter barrier precludes the injected hot-electrons from having sufficient kinetic energy to reach the collector, hence they back-scatter into the 2D material base region. Consequently, these back-scattered electrons build up in the 2D material base region, which induces a deficiency in the available density of states
in the 2D material, thereby reducing the reverse base-collector current. Furthermore, these 2D-HETs operate at room temperature and their current gains can be dynamically tuned by varying $V_{CB}$. This dual functionality is enabled by incorporating 2D materials in the base region of the HET structure and by varying the polarity of $V_{CB}$. We anticipate our transistors will pave the way towards the realization of novel flexible 2D material-based high-density and low-energy hot-carrier electronic applications.

**Methods**

In this work, we commenced the fabrication process with a 100 nm degenerately-doped $n^+$ ($N_D \sim 1 \times 10^{19}$ cm$^{-3}$) silicon wafer and performed a standard LOCOS Oxidation of Silicon (LOCOS) procedure in order to define arrays of active areas for the 2D material-HETs, which were isolated from each other by a 300 nm thick SiO$_2$ field oxide. With the silicon surface of the active areas exposed, we then thermally grew a thin ~3 nm SiO$_2$ tunnel oxide. Afterwards, we transferred either a large-area CVD monolayer MoS$_2$ or graphene on top of the substrate (e.g. SiO$_2$ field oxide) so that the particular 2D material covered several arrays of active areas of the 2D material-HETs. The large-area monolayers of MoS$_2$ and graphene were grown using CVD methods$^{41,42}$ and transferred onto the substrates using PMMA transfer methods$^{43,44}$. Subsequently, a photolithography step was performed to mask circular regions of the 2D material covering the active areas. The 2D material outside of the active regions was etched in order to isolate the various devices. The 2D material area of each device is about $8 \times 10^5 \mu$m$^2$. A second photolithography step was performed in order to pattern and deposit the side contacts (20 nm thick Ti/100 nm thick Au for MoS$_2$-HETs or 20 nm thick Cr/100 nm thick Au for G-HETs). A 1 nm thick Ti seed layer was evaporated on top of the 2D material and naturally oxidized in air, followed by atomic layer deposition (ALD) of a 55 nm thick HfO$_2$ as the filtering barrier. A third photolithography step was performed in order to define a central circular top-gate (e.g. collector) region which encompasses the entire active area of the 2D material-HETs. We then RF sputtered 45 nm of ITO at room temperature into this circular region followed by lift-off. Finally, a fourth photolithography step was performed in order to pattern and deposit the side electrodes (150 nm thick Al/50 nm thick Cr) on top of the filtering barrier dielectric. These metallic side electrodes intimately contact the central ITO collector region and allow for easy probing and biasing of the 2D material-HETs. Electrical measurements were performed with a Keithley 4200 Semiconductor Characterization System. All measurements were performed in air and at 300 K. The leakage current was subtracted for all of the data presented in the main text. Specifically, the base-collector leakage current when $I_E = 0$ was subtracted from the measured collector current when biased in the common-base configuration.

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**Author Contributions**

K.L.W., Y.-W.L. and C.M.T. Jr. conceived the idea and designed the experiments; J.R.A. and M.B.L. provided the CVD graphene; Y.S. and L.-J.L. provided the MoS₂ samples; C.M.T. Jr., X.Z. and S.-H.T. fabricated the devices; Y.-W.L. and C.M.T. Jr. performed the electrical measurements; Y.-W.L., C.M.T. Jr., X.Z., W.-K.Y., H.Q. and K.L.W. analyzed the data. All of the authors discussed the results and wrote the paper together.

**Additional Information**

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