ABSTRACT

Neuromorphic neural network processors, in the form of compute-in-memory cross-bar arrays of memristors, or in the form of subthreshold analog and mixed-signal ASICs, promise enormous advantages in compute density and energy efficiency for NN-based ML tasks. However, these technologies are prone to computational non-idealities, due to process variation and intrinsic device physics. This degrades the task performance of networks deployed to the processor, by introducing parameter noise into the deployed model. While it is possible to calibrate each device, or train networks individually for each processor, these approaches are expensive and impractical for commercial deployment. Alternative methods are therefore needed to train networks that are inherently robust against parameter variation, as a consequence of network architecture and parameters. We present a new network training algorithm that attacks network parameters during training, and promotes robust performance during inference in the face of random parameter variation. Our approach introduces a loss regularization term that penalizes the susceptibility of a network to weight perturbation. We compare against previous approaches for producing parameter insensitivity such as dropout, weight smoothing and introducing parameter noise during training. We show that our approach produces models that are more robust to random mismatch-induced parameter variation as well as to targeted parameter variation. Our approach finds minima in flatter locations in the weight-loss landscape compared with other approaches, highlighting that the networks found by our technique are less sensitive to parameter perturbation. Our work provides an approach to deploy neural network architectures to inference devices that suffer from computational non-idealities, with minimal loss of performance. This method will enable deployment at scale to novel energy-efficient computational substrates, promoting cheaper and more prevalent edge inference.

1 INTRODUCTION

There is increasing interest in NN and ML inference on IoT and embedded devices, which imposes energy constraints due to small battery capacity and untethered operation. Existing edge inference solutions based on CPUs or vector processing engines such as GPUs or TPUs are improving in energy efficiency, but still entail considerable energy cost (Huang et al., 2009). Alternative compute architectures such as memristor crossbar arrays and mixed-signal event-driven neural network accelerators promise significantly reduced energy consumption for edge inference tasks. Novel non-volatile memory technologies such as resistive RAM and phase-change materials (Chen, 2016; Yu & Chen, 2016) promise increased memory density with multiple bits per memory cell, as well as compact compute-in-memory for NN inference tasks (Sebastian et al., 2020). Analog implementations of neurons and synapses, coupled with asynchronous digital routing fabrics, permit high sparsity in both network architecture and activity, thereby reducing energy costs associated with computation.

However, both of these novel compute fabrics introduce complexity in the form of computational non-idealities, which do not exist for pure synchronous digital solutions. Some novel memory technologies support several bits per memory cell, but with uncertainty about the precise value stored on each cycle (Le Gallo et al., 2018b; Wu et al., 2019). Others exhibit significant drift in stored
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Inference processors based on analog and mixed-signal devices (Neckar et al., 2019; Moradi et al., 2018; Cassidy et al., 2016; Schemmel et al., 2010; Khaddam-Aljameh et al., 2022) exhibit parameter variation across the surface of a chip, and between chips, due to manufacturing process non-idealities. Collectively these processes known as “device mismatch” manifest as frozen parameter noise in weights and neuron parameters.

In all cases the mismatch between configured and implemented network parameters degrades the task performance by modifying the resulting mapping between input and output. Existing solutions for deploying networks to inference devices that exhibit mismatch mostly focus on per-device calibration or re-training (Ambrogio et al., 2018; Bauer et al., 2019; Nandakumar et al., 2020a). However, this, and other approaches such as few-shot learning or meta learning entail significant per-device handling costs, making them unfit for commercial deployment.

We consider a network to be “robust” if the output of a network to a given input does not change in the face of parameter perturbation. With this goal, network architectures that are intrinsically robust against device mismatch can be investigated (Thakur et al., 2018; Büchel et al., 2021). Another approach is to introduce parameter perturbations during training that promote robustness during inference, for example via random pruning (dropout) (Srivastava et al., 2014) or by injecting noise (Murray & Edwards, 1994).

In this paper we introduce a novel solution, by applying adversarial training approaches to parameter mismatch. Most existing adversarial training methods attack the input space. Here we describe an adversarial attack during training that seeks the parameter perturbation that causes the maximum degradation in network response. In summary, we make the following contributions:

- We propose a novel algorithm for gradient-based supervised training of networks that are robust against parameter mismatch, by performing adversarial training in the weight space.
- We demonstrate that our algorithm flattens the weight-loss landscape and therefore leads to models that are inherently more robust to parameter noise.
- We show that our approach outperforms existing methods in terms of robustness.
- We validate our algorithm on a highly accurate Phase Change Memory (PCM)-based Compute-in-Memory (CiM) simulator and achieve new state-of-the-art results in terms of performance and performance retention over time.

2 RELATED WORK

Research to date has focused mainly on adversarial attacks in the input space. With an increasing number of adversarial attacks, an increasing number of schemes defending against those attacks have been proposed (Wang et al., 2020; Zhang et al., 2019; Madry et al., 2019; Moosavi-Dezfooli et al., 2018). In contrast, adversarial attacks in parameter space have received little attention. Where parameter-space adversaries have been examined, it has been to enhance performance in semi-supervised learning (Cicek & Soatto, 2019), to improve robustness to input-space adversarial attacks (Wu et al., 2020), or to improve generalisation capability (Zheng et al., 2020).

We define “robustness” to mean that the network output should change only minimally in the face of a parameter perturbation — in other words, the weight-loss landscape should be as flat as possible at a loss minimum. Other algorithms that promote flat loss landscapes may therefore also be useful to promote robustness to parameter perturbations.

Dropout (Srivastava et al., 2014) is a widely used method to reduce overfitting. During training, a random subset of units are chosen with some probability, and these units are pruned from the network for a single trial or batch. This results in the network learning to distribute its computation across many units, and acts as a regularization against overfitting.

Entropy-SGD (Chaudhari et al., 2019) is a network optimisation method that minimises the local entropy around a solution in parameter space. This results in a smoothed parameter-loss landscape that should penalize sharp minima.

Adversarial Block Coordinate Descent (ABCD) (Cicek & Soatto, 2019) was proposed in order to complement input-space smoothing with weight-space smoothing in semi-supervised learning.
ABCD repeatedly picks half of the network weights and performs one step of gradient ascent on them, followed by applying gradient descent on the other half.

**Adversarial Weight Perturbation (AWP)** (Wu et al., 2020) was designed to improve the robustness of a network to adversarial attacks in the input space. The authors use Projected Gradient Ascent (PGA) on the network parameters to approximate a worst case perturbation of the weights \( \Theta' \). PGA repeatedly computes the gradient of a loss function and updates the parameters in the direction of the (positive) gradient. After each update, the parameters are projected back onto a ball (e.g. in \( l_p \)) around the original parameters to ensure that a maximum distance is kept. Having identified an adversarial perturbation in the weight-space, an adversarial perturbation in the input-space is also found using PGA. Finally, the original weights \( \Theta \) are updated using the gradient of the loss evaluated at the adversarial perturbation \( \Theta' \).

**Adversarial Model Perturbation (AMP)** (Zheng et al., 2020) improves the generalisation of conventional neural networks by optimizing a standard loss evaluated using parameters that were perturbed adversarially using PGA. Unlike our method, (Zheng et al., 2020) did not formulate the loss function as a trade-off between performance and robustness. Furthermore, the presented algorithm, unlike our method, treats the perturbation \( \Delta \Theta \) to the parameters \( \Theta \) as a constant during backpropagation.

**TRadeoff-inspired Adversarial DEfense via Surrogate-loss minimization (TRADES)** (Zhang et al., 2019) is a method for training networks that are robust against adversarial examples in the input space. The method consists of adding a boundary loss term to the loss function that measures how the network performance changes when the input is attacked. The boundary loss does not take the labels into account, so scaling it by a factor \( \beta_{rob} \) allows for a principled trade-off between the robustness and the accuracy of the network. We refer to this method as “Forward Noise”.

A recent paper proposed a method for improving the resilience to random and targeted bit errors in SRAM cells on digital Deep Neural Network (DNN) accelerators (Stutz et al., 2021). By employing adversarial or random bit flips during training, the authors significantly improved the robustness to bit perturbations, enabling the accelerators to be operated below the conventional supply voltage.

### 3 Methods

We use \( \Theta \) to denote the set of parameters of a neural network \( f(x, \Theta) \) that are trainable and susceptible to mismatch. The adversarial weights are denoted \( \Theta^* \), where \( \Theta^*_t \) are the adversarial weights at the \( t \)-th iteration of PGA. We denote the PGA-adversary as a function \( A \) that maps parameters \( \Theta \) to attacking parameters \( \Theta^* \). We denote a mini-batch of training examples as \( X \) with \( y \) being the corresponding ground-truth labels. \( \prod_{E(\zeta)}(m) \) denotes the projection operator on the \( \zeta \)-ellipsoid in \( l^p \) space. The operator \( \odot \) denotes elementwise multiplication.

The effect of component mismatch on a network parameter can be modelled using a Gaussian distribution where the standard deviation depends on the parameter magnitude (Joshi et al., 2020; Büchel et al., 2021). In this paper we restrict ourselves to mismatch-driven perturbations in the network weights. For complex Spiking Neural Networks (SNNs), “network parameters” can refer to additional quantities such as neuronal and synaptic time constants or spiking thresholds. Our training approach described here can be equally applied to these additional parameters.

We define the value of an individual parameter when deployed on a neuromorphic chip as

\[
\Theta_{\text{mismatch}} \sim \mathcal{N}(\Theta, \text{diag}(\zeta|\Theta|)) \tag{1}
\]

where \( \zeta \) governs the perturbation magnitude, referred to as the “mismatch level”. The physics underlying the neuronal- and synaptic circuits lead to a model where the amount of noise introduced into the system depends linearly on the magnitude of the parameters. If mismatch-induced perturbations had constant standard deviation independent of weight values, one could use the weight-scale invariance...
of neural networks as a means to achieve robustness, by simply scaling up all network weights (see Figure S4). The linear dependence of weight magnitude and mismatch noise precludes this approach.

In contrast to adversarial attacks in the input space (Carlini & Wagner, 2016; Moosavi-Dezfooli et al., 2015; Madry et al., 2019; Goodfellow et al., 2015), our method relies on adversarial attacks in parameter space. During training, we approximate the worst case perturbation of the network parameters using PGA and update the network parameters in order to mitigate these attacks. To trade-off robustness and performance, we use a surrogate loss (Zhang et al., 2019) to capture the difference in output between the normal and attacked network. Algorithm 1 illustrates the training procedure in more detail.

```
\begin{algorithm}
\caption{Algorithm 1:}
\begin{algorithmic}
\State $\Theta^* \leftarrow \Theta + |\Theta|\epsilon \odot R ; R \sim \mathcal{N}(0, 1)$
\For {$t = 1$ to $N_{\text{steps}}$}
\State $g \leftarrow \nabla_{\Theta^*} \mathcal{L}_{\text{rob}}(\Theta, \Theta^*_{t-1}, X)$
\State $v \leftarrow \underset{v}{\arg \max} \; v^T g$
\State $\Theta^*_t \leftarrow \prod_{\mathcal{V}_{\text{stack}}} (\Theta^*_{t-1} + \alpha \odot v)$
\EndFor
\State $\Theta \leftarrow \Theta - \eta \nabla_{\Theta} \mathcal{L}_{\text{nat}}((\Theta, X), y) + \beta_{\text{rob}} \mathcal{L}_{\text{rob}}(\Theta, \Theta^*_{N_{\text{steps}}}, X)$
\end{algorithmic}
\end{algorithm}
```

Algorithm 1: In $l_{\infty}$, $v$ corresponds to $\text{sign}(g)$ and the step size $\alpha$ is $\frac{|\Theta| \odot \epsilon \odot \zeta_{\text{attack}}}{N_{\text{steps}}}$. $\prod_{\mathcal{V}_{\text{stack}}} (m)$ denotes the projection operator on the $\zeta_{\text{attack}}$-ellipsoid in $l^p$ space. In $l_{\infty}$ this corresponds to $\min(\max(m, \Theta - \epsilon), \Theta + \epsilon)$ with $\epsilon = \zeta_{\text{attack}} \odot |\Theta|$. $\zeta_{\text{attack}}$ and $\beta_{\text{rob}}$ are hyperparameters of our model.

Unlike adversarial training in the input space, where adversarial inputs can be seen as a form of data augmentation, adversarial training in the parameter space poses the following challenge: Because the parameters that are attacked are the same parameters being optimized, performing gradient descent using the same loss that was used for PGA would simply revert the previous updates and no learning would occur. ABCD circumvents this problem by masking one half of the parameters in the adversarial loop and masking the other half during the gradient descent step. However, this limits the adversary in its power, and requires multiple iterations to be performed in order to update all parameters at least once. AWP approached this problem by assuming that the gradient of the loss with respect to the attacking parameters can be used in order to update the original parameters to favor minima in flatter locations in weight-space. However, it is not clear whether this assumption always holds since the gradient of the loss with respect to the attacking parameters is not necessarily the same direction that would lead to a flatter region in the weight-loss-landscape.

We approach this problem slightly differently: Similar to the TRADES algorithm (Zhang et al., 2019), our algorithm optimizes a natural (task) loss and a separate robustness loss.

$$\mathcal{L}_{\text{gen}}(\Theta, X, y) = \mathcal{L}_{\text{nat}}(\Theta, X, y) + \beta_{\text{rob}} \mathcal{L}_{\text{rob}}(\Theta, A(\Theta), X)$$

Using a different loss for capturing the susceptibility of the network to adversarial attacks enables us to simultaneously optimise for performance and robustness, without PGA interfering with the gradient descent step. In our experiments, $\mathcal{L}_{\text{rob}}$ is defined as

$$\mathcal{L}_{\text{rob}}(\Theta, \Theta^*, X) = \text{KL} (f(\Theta, X), f(\Theta^*, X))$$

(2)

This formulation comes with a large computational overhead since it requires computing the Jacobian $J_{\Theta^*}(\Theta)$ of a complex recurrent relation between $\Theta$ and $\Theta^*$. To make our algorithm more efficient we assume that the Jacobian is diagonal, meaning that $\Theta^* = \Theta + \Delta \Theta$ for some $\Delta \Theta$ given by the adversary. In $l_{\infty}$, the Jacobian can then be calculated efficiently using (see suppl. material for details):

$$J_{\Theta^*}(\Theta) = I + \text{diag} \left[ \frac{\text{sign}(\Theta) \odot (\zeta_{\text{attack}} + \epsilon \cdot R_1) \odot \sum_{t=1}^{N_{\text{steps}}} \text{sign} (\nabla_{\Theta^*_t} \mathcal{L}_{\text{rob}}(\Theta, \Theta^*_t, X))}{N_{\text{steps}}} \right]$$

By making this assumption, our algorithm effectively multiplies the original training time by the number of PGA steps, similar to (Wu et al., 2020; Cicek & Soatto, 2019; Zheng et al., 2020).
Because component mismatch is independently proportional to the magnitude of each parameter, one has to model the space in which the adversary can search for a perturbation using an axis-aligned ellipsoid in $l_2$ and an axis-aligned box in $l_\infty$. Using an $\epsilon$-ball where the radius depends linearly on the individual parameter sets (Li et al., 2018; Cicek & Soatto, 2019; Wu et al., 2020) would either give the adversary too little or too much attack space. Projecting onto an axis-aligned ellipsoid in $l_2$ corresponds to solving the following optimization problem (Gabay & Mercier, 1976; Dai, 2006), which does not have a closed-form solution:

$$x^* = \arg\min_x \frac{1}{2} \|m - x\|_2$$

s.t. $(x - c)^TW^{-2}(x - c) \leq 1$

where $W = \text{diag}(|\Theta| \odot \zeta) + I \cdot \zeta_{\text{const}}, c = \Theta$ and $m = \Theta^* \odot v$. Because of the computational overhead this would incur, we only consider the $l_\infty$ case in our experiments.

4 RESULTS

The ultra-low power consumption of mixed-signal neuromorphic chips make them suitable for edge-applications, such as always-on voice detection (Cho et al., 2019), vibration monitoring (Gies et al., 2021) or always-on face recognition (Liu et al., 2019). For this reason, we consider two compact network architectures in our experiments: A Long Short-term spiking recurrent Neural Network (LSNN) with roughly 65k trainable parameters; a conventional CNN with roughly 500k trainable parameters; and a Resnet32 architecture (He et al., 2015) (see Supplementary Material S1 for more information). We trained models to perform four different tasks:

- Speech command detection of 6 classes (Warden, 2018);
- ECG-anomaly detection on 4 classes (Bauer et al., 2019);
- Fashion-MNIST (F-MNIST): clothing-image classification on 10 classes (Xiao et al., 2017);
- The Cifar10 colour image classification task (Krizhevsky, 2009).

We compared several training and attack methods, beginning with a standard Stochastic Gradient Descent (SGD) approach using the Adam optimizer (Kingma & Ba, 2015) (“Standard”). Learning rate varied by architecture, but was kept constant when comparing training methods on an architecture. We examined networks trained with dropout (Srivastava et al., 2014), AWP (Wu et al., 2020), AMP (Zheng et al., 2020), ABCD (Cicek & Soatto, 2019), and Entropy-SGD (Chaudhari et al., 2019). The adversarial perturbations used in AWP and ABCD were adapted to our mismatch model (i.e. magnitude-dependent in $l_\infty$) unless stated otherwise. AMP was not adapted.

A dropout probability of 0.3 was used in the dropout models and $\gamma$ in AWP was set to 0.1. When Gaussian noise was applied to the weights during the forward pass (Murray & Edwards, 1994) a relative standard deviation of 0.3 times the weight magnitude was used ($\eta_{\text{train}} = 0.3$). For Entropy-SGD, we set the number of inner iterations to 10 with a Langevin learning rate of 0.1. Because Entropy-SGD and ABCD have inner loops, the number of total epochs were reduced accordingly. All other models were trained for the same number of epochs (no early stopping) and the model with the highest validation accuracy was selected.

**Effectiveness of adversarial weight attack** We examined the strength of our adversarial weight attack during inference and training. Standard networks trained using gradient descent alone with no additional regularization (Fig. S5a, “Standard”) were disrupted badly by our adversarial attack during inference ($\zeta = 0.1$; final mean test accuracy 91.40% $\rightarrow$ 17.50%), and this was not ameliorated by further training. When our adversarial attack was implemented during training (Fig. S5a, $\beta_{\text{rob}} = 0.1$), the trained network was protected from disruption both during training and during inference (final test accuracy 91.97% $\rightarrow$ 78.41%).

Our adversarial attack degrades network performance significantly more than a random perturbation. Because our adversary uses PGA during the attack, it approximates a worst-case perturbation of the network within an ellipsoid around the nominal weights $\Theta$. We compared the effect of our attack against a random weight perturbation (random point on $\zeta$–ellipsoid) of equal magnitude.
We examined only cross-entropy loss over the test set, and not the adversarial attack loss component \( \zeta \) was repeated 5 times for (KL divergence loss; see Eq. 2). For each trained network, we chose a random vector visualisation over several random seeds.

Under our definition of robustness, the network output adversary used in our training method, which attacks the boundary loss (Eq. 2). The AWP method performance, by performing PGA on the cross-entropy loss. We further evaluated the robustness of our trained networks against a parameter adversary that directly attacks task performance (F-MNIST and Speech tasks) at flatter locations in the cross-entropy test loss landscape. See text for further details, and Fig. S2 for visualisation over several random seeds.

For increasing perturbation size during inference (Fig. S5c; \( \zeta \)), our adversarial attack disrupted the performance of the standard network significantly more than a random perturbation (test acc. 91.40% \( \rightarrow \) 17.50% (attack) vs. 91.40% \( \rightarrow \) 90.63% (random) for \( \zeta = 0.1 \)). When our adversarial attack was applied during training (Fig. S5d; \( \beta_{\text{rob}} = 0.1 \)), the network was protected against both random and adversarial attacks for magnitudes up to \( \zeta = 0.7 \) and \( \zeta = 0.1 \), respectively.

**Flatness of the weight-loss landscape** Under our definition of robustness, the network output should change only minimally when the network parameters are perturbed. This corresponds to a loss surface that is close to flat in weight space. We measured the test weight-loss landscape for trained networks, compared over alternative training methods and for several architectures (Fig. 1). We examined only cross-entropy loss over the test set, and not the adversarial attack loss component (KL divergence loss; see Eq. 2). For each trained network, we chose a random vector \( \mathbf{v} \sim \mathcal{N}(0, \zeta|\Theta|) \) and calculated \( L_{\text{cce}}(f(X_{\text{test}}, \Theta + \alpha \cdot \mathbf{v}), y_{\text{test}}) \) for many evenly-spaced \( \alpha \in [-2, 2] \). This process was repeated 5 times for \( \zeta = 0.2 \), and the means plotted in Fig. 1. Weight-loss landscapes for the individual trials are shown in Fig. S2.

Our adversarial training approach found minima of trained parameters \( \Theta \) in flatter areas of the weight-loss landscape, compared with all other approaches examined (flatter curves in Fig. 1). In most cases our training approach also found deeper minima at lower categorical cross-entropy loss \( L_{\text{cce}} \), reflecting better task performance. These results are reflected in the better generalization performance of our approach (see Table 1). Not surprisingly, dropout and AWP also lead to flatter minima than the Standard network with no regularization. ABCD and Entropy-SGD were not included in Fig. 1 because they did not outperform the Standard model.

**Network robustness against parameter mismatch** We evaluated the ability of our training method to protect against simulated device mismatch. We introduced frozen parameter noise into models trained with adversarial attack, with noise modelled on that observed in neuromorphic processors (Joshi et al., 2020; Büchel et al., 2021). In these devices, uncertainty associated with each weight parameter is approximately normally distributed around the nominal value, with a standard deviation that scales with the weight magnitude (Eq 1). We measured test accuracy under simulated random mismatch for 100 samples across two model instances. A comparison of our method against standard training is shown in Fig. 2. For mismatch levels up to 70% \( (\zeta = 0.7) \), our approach protected significantly against simulated mismatch for all three tasks examined \( (p < 2 \times 10^{-8} \text{ in all cases; } U \text{ test}) \). A more detailed comparison between the different models is given in Table 1.

**Network robustness against direct adversarial attack on task performance** Our training approach improves network robustness against mismatch parameter noise. We further evaluated the robustness of our trained networks against a parameter adversary that directly attacks task performance, by performing PGA on the cross-entropy loss \( L_{\text{cce}} \). Note that this is separate from the adversary used in our training method, which attacks the boundary loss (Eq. 2). The AWP method uses the cross-entropy loss to find adversarial parameters during training. Nevertheless, we found that...
Figure 2:  **Adversarial attack during training protects networks against random mismatch-induced parameter noise.** Networks were evaluated for the Speech (a), ECG (b) and F-MNIST tasks (c), under increasing levels of simulated mismatch ($\zeta$). Networks trained using standard SGD were disrupted by mismatch levels $\zeta > 0.1$. At all mismatch levels our adversarial training approach performed significantly better in the presence of mismatch (higher test accuracy). Red boxes highlight misclassified examples.

Figure 3: **Our training method protects against task-adversarial attacks in parameter space.** Networks trained under several methods were attacked using a PGA adversary that directly attacked the task performance $\mathcal{L}_{\text{cce}}$. Our adversarial training approach (bold; dashed) outperformed all other methods against this attack.

our method consistently outperforms all other compared methods for increasing attack magnitude $\zeta$ (Fig. 3). In networks trained with our method, the adversary needed to perform a considerably larger attack to significantly reduce performance (test accuracy < 70%). ABCD and Entropy-SGD were not included in the comparison because they did not outperform the standard network.

**Robustness against parameter drift for PCM based CiM**  CiM devices based on memristor technologies such as PCM promise to deliver energy- and space-efficient accelerators. With the increasing interest in CiM devices for accelerated inference and energy-efficient edge computing (Sebastian et al., 2020), the problem of deploying a model that is robust to noise originating from the device physics of PCM cells has gained in significance. We investigated the effect of our training method on the robustness of networks that are simulated to run on PCM-based CiM hardware (for details on the simulator, see SM 5).

Networks trained with our method outperform state-of-the-art networks deployed on PCM-based CiM. Currently, the method that has proven to yield the best performance on CiM hardware is training with noise on the network parameters during the forward pass. We adapted this method by adding our algorithm and show that we consistently outperform the conventional method (see Figure S14) for a wide range of hyperparameters, and even surpass the FP baseline (a model trained without noise injection and evaluated on a standard PC) for some configurations (see Figure 4). Following
Verifiable robustness for LSNNs We investigated the provable robustness of LSNNs trained using our method using abstract interpretation (Cousot & Cousot, 1977; Gehr et al., 2018; Mirman et al., 2018). In this analysis a function $f(x, \Theta)$ (in our case, a neural network with input $x$ and parameters $\Theta$) is overapproximated using an abstract domain. We specify the weights $\Theta$ in our network as an interval parameterised by the attack size $\zeta$, spanning $[\Theta - \zeta|\Theta|, \Theta + \zeta|\Theta|]$. We examined the proportion of provably correctly classified test samples for the Speech and ECG tasks, under a range of attack sizes $\zeta$, and comparing our approach against standard gradient descent and against training

with forward-pass noise only (Fig. S9). We found that our approach is provably more correct over increasing attack size $\zeta$ (higher verified test accuracy).

5 DISCUSSION

We proposed a new training approach that includes adversarial attacks on the parameter space during training. Our proposed adversarial attack was significantly stronger than random weight perturbations at disrupting the performance of a trained network. Including the adversarial attack during training significantly protected the trained network from weight perturbations during inference. Our approach found minima in the weight-loss landscape that usually corresponded to lower loss values, and were always in flatter regions of the loss landscape. This indicates that our approach found network solutions that are less sensitive to parameter variation, and therefore more robust. Our approach was more robust than several other methods for inducing robustness and good generalisation. To the best of our knowledge, our work represents the first example of interval bound propagation applied to SNNs, and the first application of parameter-space adversarial attacks to promote network robustness against device mismatch for mixed-signal compute. Our experiments only considered the impact of weight perturbations, and did not examine the influence of uncertainty in other parameters of mixed-signal neuromorphic processors such as time constants or spiking thresholds. Our approach can be adapted to include adversarial attacks in the full network parameter space, increasing the robustness of spiking networks. The technique of interval bound propagation can also be applied to these additional network parameters. We did not quantize network parameters either during or after training, in this work. On some platforms (Moradi et al., 2018) it is necessary to deploy quantized weights and it is unclear how our adversarial attacks would interact with quantization during the training process. However, most PCM-based CiM hardware does not require quantization during training in order to get good performance (Joshi et al., 2020). Per-device training for a device with known calibrated parameter noise is likely to achieve the highest possible deployed performance on that single device. However, this approach has significant drawbacks. Firstly, each device must be either measured / calibrated accurately — not a trivial requirement — or trained with the device in the forward inference pass of the training loop. Secondly, training must be performed individually for each device, entailing significant logistical problems if the training is conducted in the factory or inside a consumer product. Thirdly, this approach will retain full sensitivity to parameter variation on the device. Our method improves the performance of neural networks deployed to inference hardware that include computational non-idealities. For example, NN processors with crossbar architectures based on novel memory devices such as RRAM and PCM (Sebastian et al., 2020) display uncertainty in stored memory values as well as conductance drift over time (Le Gallo et al., 2018b; Wu et al., 2019; Joshi et al., 2020). Our method could also address in-memory computing-based NN processors based on SRAM and switched capacitors (Verna et al., 2019). Analog neurons and synapses in mixed-signal NN processors, for example SNN inference processors (Moradi et al., 2018), exhibit variation in weights and neuron parameters across a processor. We showed that our training approach finds network solutions that are insensitive to mismatch-induced parameter variation. Our networks can therefore be deployed to inference devices with computational non-idealities with only minimal reduction in task performance, and without requiring per-device calibration or model training. This reduction in per-device handling implies a considerable reduction in expense when deploying at commercial scale. Our method therefore brings low-power neuromorphic inference processors closer to commercial viability.

Ethics statement The authors declare no conflicts of interest.

Reproducibility statement Code for reproduce all experiments described in this work are provided at https://github.com/jubueche/BPTT-Lipschitzness and https://github.com/jubueche/Resnet32-ICLR

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Table 1: Results of training multiple networks using several methods over three tasks. Networks were evaluated using different levels of mismatch (ζ).

| Method          | Standard Forward Noise | Standard Forward Noise_rob | Standard Forward Noise_rob_0.01 | Standard Forward Noise_rob_0.02 | Standard Forward Noise_rob_0.05 | Standard Forward Noise_rob_0.1 | Standard Forward Noise_rob_0.2 |
|-----------------|-------------------------|---------------------------|---------------------------------|---------------------------------|---------------------------------|-------------------------------|-------------------------------|
| CNN             | 91.88 0.35 0.45         | 96.49 0.19 0.21           | 99.09 0.19 0.21                 | 98.06 0.21 0.23                 | 97.03 0.23 0.25                 | 96.02 0.24 0.26               | 95.01 0.26 0.28               |
| Mismatch 0.0    | 91.88 0.35 0.45         | 96.49 0.19 0.21           | 99.09 0.19 0.21                 | 98.06 0.21 0.23                 | 97.03 0.23 0.25                 | 96.02 0.24 0.26               | 95.01 0.26 0.28               |
| Mismatch 0.2    | 91.88 0.35 0.45         | 96.49 0.19 0.21           | 99.09 0.19 0.21                 | 98.06 0.21 0.23                 | 97.03 0.23 0.25                 | 96.02 0.24 0.26               | 95.01 0.26 0.28               |
| Mismatch 0.5    | 91.88 0.35 0.45         | 96.49 0.19 0.21           | 99.09 0.19 0.21                 | 98.06 0.21 0.23                 | 97.03 0.23 0.25                 | 96.02 0.24 0.26               | 95.01 0.26 0.28               |

REFERENCES

Stefano Ambrogio, Prithis Narayanan, Hsiyung Tsai, Robert M. Shelby, Irem Boybat, Carmelo di Nolfo, Severin Sidler, Massimo Giordano, Martina Bodini, Nathan C. P. Farinha, Benjamin Killeen, Christina Cheng, Yassine Jaoudi, and Geoffrey W. Burr. Equivalent-accuracy accelerated neural-network training using analogue memory. *Nature*, 558(7708):60–67, June 2018. ISSN 1476-4687. doi: 10.1038/s41586-018-0180-5. URL https://doi.org/10.1038/s41586-018-0180-5.

F. C. Bauer, D. R. Muir, and G. Indiveri. Real-time ultra-low power eeg anomaly detection using an event-driven neuromorphic processor. *IEEE Transactions on Biomedical Circuits and Systems*, 13(6):1575–1582, 2019. doi: 10.1109/TBCAS.2019.2935001.

Guillaume Bellec, Darjan Salaj, Anand Subramoney, Robert A. Legenstein, and Wolfgang Maass. Long short-term memory and learning-to-learn in networks of spiking neurons. *CoRR*, abs/1803.09574, 2018. URL http://arxiv.org/abs/1803.09574.

Irem Boybat, Manuel Le Gallo, SR Nandakumar, Timoleon Moraitis, Thomas Parnell, Tomas Tuma, Bipin Rajendran, Yusuf Leblebici, Abu Sebastian, and Evangelos Eleftheriou. Neuromorphic computing with multi-memristive synapses. *Nature communications*, 9(1):2514, 2018.

Julian Büchel, Dmitrii Zendrikov, Sergio Solinas, Giacomo Indiveri, and Dylan R. Muir. Supervised training of spiking neural networks for robust deployment on mixed-signal neuromorphic processors, 2021.

Nicholas Carlini and David A. Wagner. Towards evaluating the robustness of neural networks. *CoRR*, abs/1608.04644, 2016. URL http://arxiv.org/abs/1608.04644.

Andrew S. Cassidy, Jun Sawada, Paul Merolla, John V. Arthur, Rodrigo Alvarez-Icaza, Filipp Akopyan, Bryan L. Jackson, and Dharmendra S. Modha. Truenorth: A high-performance, low-power neurosynaptic processor for multi-sensory perception, action, and cognition. 2016.

Pratik Chaudhari, Anna Choromanska, Stefano Soatto, Yan LeCun, Carlo Baldassi, Christian Borgs, Jennifer Chayes, Levent Sagun, and Riccardo Zecchina. Entropy-SGD: biasing gradient descent into wide valleys. *Journal of Statistical Mechanics: Theory and Experiment*, 2019(12):124018, dec 2019. doi: 10.1088/1742-5468/ab39d9. URL https://doi.org/10.1088%2F1742-5468%2Fab39d9.

An Chen. A review of emerging non-volatile memory (nvm) technologies and applications. *Solid-State Electronics*, 125:25–38, 2016. ISSN 0038-1101. doi: https://doi.org/10.1016/j.sse.2016.07.006. URL https://www.sciencedirect.com/science/article/pii/S0038110116300867. Extended papers selected from ESSDERC 2015.

Minchang Cho, Sechang Oh, Zhan Shi, Jongyup Lim, Yejoong Kim, Seokhyeon Jeong, Yu Chen, David Blaauw, Hun-Seok Kim, and Densionsstille. 17.2 a 142w voice and acoustic activity
detection chip for mm-scale sensor nodes using time-interleaved mixer-based frequency scanning. In 2019 IEEE International Solid-State Circuits Conference - (ISSCC), pp. 278–280, 2019. doi: 10.1109/ISSCC.2019.8662540.

Safa Cicek and Stefano Soatto. Input and weight space smoothing for semi-supervised learning. In 2019 IEEE/CVF International Conference on Computer Vision Workshop (ICCVW), pp. 1344–1353, 2019. doi: 10.1109/ICCVW.2019.00170.

Patrick Cousot and Radhia Cousot. Abstract interpretation: A unified lattice model for static analysis of programs by construction or approximation of fixpoints. In Proceedings of the 4th ACM SIGACT-SIGPLAN Symposium on Principles of Programming Languages, POPL ’77, pp. 238–252, New York, NY, USA, 1977. Association for Computing Machinery. ISBN 9781450373500. doi: 10.1145/512950.512973. URL https://doi.org/10.1145/512950.512973.

Yu-Hong Dai. Fast algorithms for projection on an ellipsoid. SIAM Journal on Optimization, 16(4):986–1006, 2006. doi: 10.1137/040613305. URL https://doi.org/10.1137/040613305.

Daniel Gabay and Bertrand Mercier. A dual algorithm for the solution of nonlinear variational problems via finite element approximation. Computers & Mathematics with Applications, 2(1): 17–40, 1976. ISSN 0898-1221. doi: https://doi.org/10.1016/0898-1221(76)90003-1. URL https://www.sciencedirect.com/science/article/pii/0898122176900031.

Timon Gehr, Matthew Mirman, Dana Drachsler-Cohen, Petar Tsankov, Swarat Chaudhuri, and Martin Vechev. A2l: Safety and robustness certification of neural networks with abstract interpretation. In 2018 IEEE Symposium on Security and Privacy (SP), pp. 3–18, 2018. doi: 10.1109/SP.2018.00058.

Valentin Gies, Sebastián Marzetti, Valentin Barchasz, Hervé Barthélemy, and Hervé Glotin. Ultra-low power embedded unsupervised learning smart sensor for industrial fault classification. In 2020 IEEE International Conference on Internet of Things and Intelligence System (IoTaIS), pp. 181–187, 2021. doi: 10.1109/IoTaIS50849.2021.9359716.

Xavier Glorot and Yoshua Bengio. Understanding the difficulty of training deep feedforward neural networks. In Yee Whye Teh and Mike Titterington (eds.), Proceedings of the Thirteenth International Conference on Artificial Intelligence and Statistics, volume 9 of Proceedings of Machine Learning Research, pp. 249–256, Chia Laguna Resort, Sardinia, Italy, 13–15 May 2010. JMLR Workshop and Conference Proceedings. URL http://proceedings.mlr.press/v9/glorot10a.html.

Ian J. Goodfellow, Jonathon Shlens, and Christian Szegedy. Explaining and harnessing adversarial examples, 2015.

Kaiming He, Xiangyu Zhang, Shaoqing Ren, and Jian Sun. Deep residual learning for image recognition. CoRR, abs/1512.03385, 2015. URL http://arxiv.org/abs/1512.03385.

S. Huang, S. Xiao, and W. Feng. On the energy efficiency of graphics processing units for scientific computing. In 2009 IEEE International Symposium on Parallel Distributed Processing, pp. 1–8, 2009. doi: 10.1109/IPDPS.2009.5160980.

Vinay Joshi, Manuel Le Gallo, Simon Haefeli, Irem Boybat, S. R. Nandakumar, Christophe Piveteau, Martino Dazzi, Bipin Rajendran, Abu Sebastian, and Evangelos Eleftheriou. Accurate deep neural network inference using computational phase-change memory. Nature Communications, 11(1):2473, May 2020. ISSN 2041-1723. doi: 10.1038/s41467-020-16108-9. URL https://doi.org/10.1038/s41467-020-16108-9.

Riduan Khaddam-Aljameh, Milos Stanisavljevic, Jordi Fornt Mas, Geethan Karunaratne, Matthias Braendl, Feng Liu, Abhairaj Singh, Silvia M Müller, Urs Egger, Anastasios Petropoulos, et al. HERMES-core—a 1.59-TOPS/mm² PCM on 14-nm CMOS in-memory compute core using 300-ps/LSB linearized CCO-based ADCs. IEEE Journal of Solid-State Circuits, 2022.

Diederik P. Kingma and Jimmy Ba. Adam: A method for stochastic optimization. In Yoshua Bengio and Yann LeCun (eds.), 3rd International Conference on Learning Representations, ICLR 2015, San Diego, CA, USA, May 7-9, 2015, Conference Track Proceedings, 2015. URL http://arxiv.org/abs/1412.6980.
Alex Krizhevsky. Learning multiple layers of features from tiny images. 2009.

Manuel Le Gallo, Daniel Krebs, Federico Zipoli, Martin Salinga, and Abu Sebastian. Collective structural relaxation in phase-change memory devices. Advanced Electronic Materials, 4(9):1700627, 2018a. doi: https://doi.org/10.1002/aelm.201700627. URL https://onlinelibrary.wiley.com/doi/abs/10.1002/aelm.201700627.

Manuel Le Gallo, Abu Sebastian, Roland Mathis, Matteo Manica, Heiner Giefer, Tomas Tuma, Costas Bekas, Alessandro Curioni, and Evangelos Eleftheriou. Mixed-precision in-memory computing. Nature Electronics, 1(4):246–253, April 2018b. ISSN 2520-1131. doi: 10.1038/s41928-018-0054-8. URL https://doi.org/10.1038/s41928-018-0054-8.

Hao Li, Zheng Xu, Gavin Taylor, Christoph Studer, and Tom Goldstein. Visualizing the loss landscape of neural nets. In S. Bengio, H. Wallach, H. Larochelle, K. Grauman, N. Cesa-Bianchi, and R. Garnett (eds.), Advances in Neural Information Processing Systems, volume 31. Curran Associates, Inc., 2018. URL https://proceedings.neurips.cc/paper/2018/file/a41b3bb3e6b050b6c9067c67f663b915-Paper.pdf.

Qian Liu, Ole Richter, Carsten Nielsen, Sadique Sheik, Giacomo Indiveri, and Ning Qiao. Live demonstration: Face recognition on an ultra-low power event-driven convolutional neural network asic. In 2019 IEEE/CVF Conference on Computer Vision and Pattern Recognition Workshops (CVPRW), pp. 1680–1681, 2019. doi: 10.1109/CVPRW.2019.00213.

Aleksander Madry, Aleksandar Makelov, Ludwig Schmidt, Dimitris Tsipras, and Adrian Vladu. Towards deep learning models resistant to adversarial attacks, 2019.

Matthew Mirman, Timon Gehr, and Martin T. Vechev. Differentiable abstract interpretation for provably robust neural networks. In Jennifer G. Dy and Andreas Krause (eds.), Proceedings of the 35th International Conference on Machine Learning, ICML 2018, Stockholmsmässan, Stockholm, Sweden, July 10-15, 2018, volume 80 of Proceedings of Machine Learning Research, pp. 3575–3583. PMLR, 2018. URL http://proceedings.mlr.press/v80/mirman18b.html.

Seyed-Mohsen Moosavi-Dezfooli, Alhussein Fawzi, and Pascal Frossard. Deepfool: a simple and accurate method to fool deep neural networks. CoRR, abs/1511.04599, 2015. URL http://arxiv.org/abs/1511.04599.

Seyed-Mohsen Moosavi-Dezfooli, Alhussein Fawzi, Jonathan Uesato, and Pascal Frossard. Robustness via curvature regularization, and vice versa. CoRR, abs/1811.09716, 2018. URL http://arxiv.org/abs/1811.09716.

S. Moradi, N. Qiao, F. Stefanini, and G. Indiveri. A scalable multicore architecture with heterogeneous memory structures for dynamic neuromorphic asynchronous processors (dynaps). IEEE Transactions on Biomedical Circuits and Systems, 12(1):106–122, 2018. doi: 10.1109/TBCAS.2017.2759700.

A.F. Murray and P.J. Edwards. Enhanced mlp performance and fault tolerance resulting from synaptic weight noise during training. IEEE Transactions on Neural Networks, 5(5):792–802, 1994. doi: 10.1109/72.317730.

S. R. Nandakumar, I. Boybat, V. Joshi, C. Piveteau, M. Le Gallo, B. Rajendran, A. Sebastian, and E. Eleftheriou. Phase-change memory models for deep learning training and inference. In 26th IEEE International Conference on Electronics, Circuits and Systems (ICECS), pp. 727–730, 2019. doi: 10.1109/ICECS46596.2019.8964852.

S. R. Nandakumar, Manuel Le Gallo, Christophe Piveteau, Vinay Joshi, Giovanni Mariani, Irem Boybat, Geethan Karunaratne, Riduan Khaddam-Aljameh, Urs Egger, Anastasios Petropoulos, Theodore Antonakopoulos, Bipin Rajendran, Abu Sebastian, and Evangelos Eleftheriou. Mixed-precision deep learning based on computational memory. Frontiers in Neuroscience, 14:406, 2020a. ISSN 1662-453X. doi: 10.3389/fnins.2020.00406. URL https://www.frontiersin.org/article/10.3389/fnins.2020.00406.
SR Nandakumar, Irem Boybat, Jin-Ping Han, Stefano Ambrogio, Praneet Adusumilli, Robert L Bruce, Matthew BrightSky, Malte Rasch, Manuel Le Gallo, and Abu Sebastian. Precision of synaptic weights programmed in phase-change memory devices for deep learning inference. In 2020 IEEE International Electron Devices Meeting (IEDM), pp. 29–4. IEEE, 2020b.

Alexander Neckar, Sam Fok, Ben Benjamin, Terrence Stewart, Aaron Voelker, Chris Eliasmith, Rajit Manohar, and Kwabena Boahen. Braindrop: A mixed-signal neuromorphic architecture with a dynamical systems-based programming model. Proceedings of the IEEE, 107:144–164, 01 2019. doi: 10.1109/JPROC.2018.2881432.

Yurii Nesterov. A method for solving the convex programming problem with convergence rate $O(1/k^2)$. Proceedings of the USSR Academy of Sciences, 269:543–547, 1983.

J. Schemmel, D. Brüderle, A. Grübl, M. Hock, K. Meier, and S. Millner. A wafer-scale neuromorphic hardware system for large-scale neural modeling. In 2010 IEEE International Symposium on Circuits and Systems (ISCAS), pp. 1947–1950, 2010. doi: 10.1109/ISCAS.2010.5536970.

Abu Sebastian, Manuel Le Gallo, Riduan Khaddam-Aljameh, and Evangelos Eleftheriou. Memory devices and applications for in-memory computing. Nature Nanotechnology, 15(7): 529–544, 2020. doi: 10.1038/s41565-020-0655-z. URL https://doi.org/10.1038/s41565-020-0655-z.

Nitish Srivastava, Geoffrey Hinton, Alex Krizhevsky, Ilya Sutskever, and Ruslan Salakhutdinov. Dropout: A simple way to prevent neural networks from overfitting. Journal of Machine Learning Research, 15(56):1929–1958, 2014. URL http://jmlr.org/papers/v15/srivastava14a.html.

David Stutz, Nandhini Chandramoorthy, Matthias Hein, and Bernt Schiele. Random and adversarial bit error robustness: Energy-efficient and secure dnn accelerators. CoRR, abs/2104.08323, 2021.

C. S. Thakur, R. Wang, T. J. Hamilton, R. Etienne-Cummings, J. Tapson, and A. van Schaik. An analogue neuromorphic co-processor that utilizes device mismatch for learning applications. IEEE Transactions on Circuits and Systems I: Regular Papers, 65(4):1174–1184, 2018.

Naveen Verma, Hongyang Jia, Hossein Valavi, Yinqi Tang, Murat Ozatay, Lung-Yen Chen, Bonan Zhang, and Peter Deaville. In-memory computing: Advances and prospects. IEEE Solid-State Circuits Magazine, 11(3):43–55, 2019. doi: 10.1109/MSSC.2019.2922889.

Yisen Wang, Difan Zou, Jinfeng Yi, James Bailey, Xingjun Ma, and Quanquan Gu. Improving adversarial robustness requires revisiting misclassified examples. In International Conference on Learning Representations, 2020. URL https://openreview.net/forum?id=rk1Og6EFwS.

P. Warden. Speech Commands: A Dataset for Limited-Vocabulary Speech Recognition. ArXiv e-prints, April 2018. URL https://arxiv.org/abs/1804.03209.

Dongxian Wu, Yisen Wang, and Shutao Xia. Revisiting loss landscape for adversarial robustness. CoRR, abs/2004.05884, 2020. URL https://arxiv.org/abs/2004.05884.

Lei Wu, Hongxia Liu, Jiabin Li, Shulong Wang, and Xing Wang. A Multi-level Memristor Based on Al-Doped HfO2 Thin Film. Nanoscale Research Letters, 14(1):177, May 2019. ISSN 1556-276X. doi: 10.1186/s11671-019-3015-x. URL https://doi.org/10.1186/s11671-019-3015-x.

Han Xiao, Kashif Rasul, and Roland Vollgraf. Fashion-mnist: a novel image dataset for benchmarking machine learning algorithms, 2017.

Shimeng Yu and Pai-Yu Chen. Emerging memory technologies: Recent trends and prospects. IEEE Solid-State Circuits Magazine, 8(2):43–56, Spring 2016. ISSN 1943-0590. doi: 10.1109/MSSC.2016.2546199.

Hongyang Zhang, Yaodong Yu, Jiantao Jiao, Eric P. Xing, Laurent El Ghaoui, and Michael I. Jordan. Theoretically principled trade-off between robustness and accuracy. CoRR, abs/1901.08573, 2019. URL http://arxiv.org/abs/1901.08573.
Yaowei Zheng, Richong Zhang, and Yongyi Mao. Regularizing neural networks via adversarial model perturbation. *CoRR*, abs/2010.04925, 2020. URL https://arxiv.org/abs/2010.04925.
SUPPLEMENTARY MATERIAL

SPIKING RNN ARCHITECTURE

The dynamics of the spiking model (Bellec et al., 2018) can be summarised by a set of differential equations (Eq. 3).

\[
\begin{align*}
B_{t+1} &= b_0^t + \beta b_t^t \\
o_t^t &= 1(V_t^t > B_t^t) \text{ unless refractory } t_{\text{refr}} \\
B_{t+1} &= \rho b_t^t + (1 - \rho) \frac{o_t^t}{dt} \\
I_{\text{Reset}} &= \frac{o_t^t}{dt} B_t^t dt \\
V_{t+1} &= \rho V_t^t + (1 - \rho_V)(I_{\text{in}} W_{\text{in}} + \frac{o_t^t}{dt} W_{\text{rec}}) - I_{\text{Reset}}
\end{align*}
\]  

(3)

where \( \rho^t = e^{-dt/\tau_{\text{ada}}} \) and \( \rho_V = e^{-dt/\tau} \). The variables \( B \) describe the spiking thresholds with spike-frequency adaptation. The vector \( o_t^t \) denotes the population spike train at time \( t \). The membrane potentials \( V \) have a time constant \( \tau \) and the adaptive threshold time constant is denoted by \( \tau_{\text{ada}} \). The speech signals and ECG traces fed into the network are represented as currents \( I_{\text{in}} \). Since the derivative of the spiking function with respect to its input is mostly 0 we use a surrogate gradient that is explicitly defined as

\[
\frac{\partial E}{\partial V_t^t} = \frac{\partial E}{\partial z_t^t} \frac{\partial z_t^t}{\partial V_t^t} = \frac{\partial E}{\partial z_t^t} d \cdot \max(1 - |\frac{V_t^t - B_t^t}{B_t^t}|, 0)
\]  

(4)

where \( E \) is the error and \( d \) is the dampening factor. To get the final prediction of the network, we average the population spike trains along the time-axis \( z_{\text{avg}} \) and compute

\[
l = \text{softmax}(z_{\text{avg}} W_{\text{out}} + b_{\text{out}}) \\
\hat{y} = \arg\max_i l_i
\]

CNN ARCHITECTURE

Our architecture comprises two convolutional blocks (\( 2 \times [4 \times 4, 64 \text{ channels}, \text{MaxPool}, \text{ReLU}] \)), followed by three dense layers (\( N = 1600, 256, 64, \text{ReLU} \)) and a softmax layer. All weights and kernels are initialized using the Glorot normal initialisation (Glorot & Bengio, 2010). Using this architecture, we achieved a test accuracy of \( \sim 93\% \). Attacked parameters for this network included all the kernel weights, as well as all the dense layer parameters.
DERIVATION OF JACOBIAN

Under the assumption that $\alpha = \frac{\text{attack}|\Theta|}{N_{\text{steps}}}$ and $p = \infty$, we can rewrite the inner loop of Algorithm 1 to

\begin{verbatim}
begin
    $\Theta^* \leftarrow \Theta + |\Theta|\epsilon \odot R ; R \sim \mathcal{N}(0, 1)$
    for $t = 1$ to $N_{\text{steps}}$ do
        $\Theta^*_t \leftarrow \Theta^*_{t-1} + \alpha \cdot \text{sign} \left( \nabla_{\Theta^*_t} \mathcal{L}_{\text{rob}}(\Theta, \Theta^*_t, X) \right)$
    end
end
\end{verbatim}

By rewriting $\Theta^*$ in the form of $\Theta^* = \Theta + \Delta \Theta$ we get

$$
\Theta^* = \Theta + |\Theta|\epsilon \odot R + \alpha \odot \sum_{t=1}^{N_{\text{steps}}} \text{sign} \left( \nabla_{\Theta^*_t} \mathcal{L}_{\text{rob}}(f(\Theta, X), f(\Theta^*_t, X)) \right)
$$

From this the Jacobian can be easily calculated. Plugging in the definition for $\alpha$ we get

$$
J_{\Theta^*}(\Theta) = I + \text{diag} \left[ \frac{\text{sign}(\Theta) \odot (\text{attack} + \epsilon \cdot R_1)}{N_{\text{steps}}} \odot \sum_{t=1}^{N_{\text{steps}}} \text{sign} \left( \nabla_{\Theta^*_t} \mathcal{L}_{\text{rob}}(f(\Theta, X), f(\Theta^*_t, X)) \right) \right]
$$
MISMATCH MODEL

To model the parameter noise introduced by component mismatch we used a Gaussian distribution where the mean is the nominal noise-free weight value and the standard deviation depends linearly on the weight value. This model realistically captures the behaviour of parameter mismatch on a mixed-signal neuromorphic SNN inference processor (Moradi et al., 2018). Fig. S1 shows the quantified parameter mismatch recorded directly from neuromorphic HW, over a range of nominal parameter values and for several neuronal and synaptic parameters. The measured mismatch parameter variation follows an approximately Gaussian distribution where the standard deviation depends linearly on the mean.

Figure S1: Quantification of mismatch on analog neuromorphic hardware. Parameter values for several weight parameters and membrane time constants were measured for a range of nominal parameter values, using an oscilloscope directly connected to a mixed-signal neuromorphic SNN processor. (a-c) Various parameters of the chip follow a Gaussian distribution with increasing width. (d) The mismatch standard deviation depends linearly on the nominal value of each parameter.
WEIGHT LOSS-LANDSCAPE VISUALIZATION

We characterized the shape of the weight loss-landscape by plotting the categorical cross entropy loss for varying levels of noise added to the weights of the trained network. In each trial, we picked a random vector \( v \sim \mathcal{N}(0, \zeta|\Theta|) \) and evaluated the categorical cross entropy of the whole test set given the weights \( \Theta + \alpha \cdot v \), where \( \alpha \in [-2, 2] \) and \( \zeta = 0.2 \). As we show in Figure S2, the variance of the individual 1D weight loss-landscapes is small.

Table S1 quantifies the flatness of the illustrated weight loss-landscapes. As can be seen, our method combined with adding noise during the forward pass yields the flattest landscapes.

Table S1: **Average slope of estimated 1D weight loss-landscapes.** Slopes were calculated as the mean absolute differences between sample points divided by the sampling distance.

|                | F-MNIST CNN | ECG LSNN | Speech LSNN |
|----------------|-------------|----------|-------------|
| Standard       | 0.3375      | 0.1605   | 0.2702      |
| Beta           | 0.0480      | 0.0879   | 0.0633      |
| Forward Noise  | 0.0332      | **0.0180** | 0.1009      |
| Forward Noise + Beta | **0.0190** | 0.0187 | **0.0540** |
| Dropout        | 0.3022      | 0.0952   | 0.0707      |
| AWP            | 0.0841      | 0.1013   | 0.1389      |

Figure S2: **Illustration of the test weight loss-landscape highlighting individual trials to measure the loss landscapes.** See the results text for more details.
ATTACKING KL-DIVERGENCE LOSS DURING INFERENCE

While the adversary in AWP attacks the task loss directly, i.e. $\max L_{\text{ce}}(f(\Theta^*, X), y)$, the adversary in our training algorithm attacks the KL divergence $\max KL(f(\Theta, X), f(\Theta^*, X))$. This implies that our parameter attack seeks simply to change the response of the network in any way, and is agnostic to the task itself. In the main results we attack the cross-entropy task loss during inference, in order to not give an undue advantage to our training approach. Here we show that our training approach also provides robustness against parameter attacks on the KL-divergence loss during inference.

Fig. S3 shows the adversarial robustness of the methods for an adversary that attacks the KL-divergence rather than the cross-entropy loss. When the network is attacked by maximizing the KL-divergence between the normal and attacked network, our adversarially-trained networks are more robust than AWP, SGD and forward-noise. Because the parameter attack used here during inference as well as in the inner optimization loop of the training procedure is the same, this result is expected, and serves as a sanity check that our networks indeed learn to defend against the attack they were trained against.

Figure S3: **Robustness to weight attack targeting KL divergence during inference.** When the network is attacked by maximizing the KL-divergence between the normal and attacked network, our adversarially trained networks are more robust than standard SGD or AWP.
EFFECT OF CONSTANT VERSUS RELATIVE PARAMETER NOISE

As described in the main text, parameter noise that has constant magnitude (for example, Gaussian noise with fixed standard deviation) is trivial to protect against by increasing weight magnitudes. We examined this effect by training MLPs with an adversary that employs Gaussian noise with fixed std. dev. $\epsilon = 0.2$. Figure S4 (right) illustrates the test accuracy of two MLPs trained on F-MNIST over the course of training. Using an inverse weight decay term, the weight-magnitude of one network is forced to increase to 2.0 over the course of training (black crosses, $\Theta^*$). The weight magnitude of the other network (red crosses) is limited during training to 0.2 (red crosses; $\Theta$). One can observe that as the weight magnitude of the increasing magnitude network $\Theta^*$ increases, also the robustness to Gaussian noise ($\epsilon = 0.2$) increases (blue), while the performance of the small magnitude network $\Theta$ remains poor (cyan).

Figure S4: **Constant magnitude versus magnitude-relative parameter noise.** (left, middle) When parameter noise of constant magnitude (blue) is introduced by the adversarial attack during training, the networks learn to increase the magnitude of the weights to trivially improve robustness to the constant-magnitude attack. When the parameter attack is relative to each parameter magnitude, as in the main text (red), the weight magnitudes do not increase. These networks were trained for a range of $\beta$rob, i.e. varying emphasis on robustness. (right) One can also trivially increase the robustness to fixed-magnitude noise by introducing an inverse weight decay term that causes the weights to increase in magnitude (black, cross marker) while retaining performance. This causes the MLP trained on MNIST to become increasingly robust to fixed-magnitude parameter noise (blue dots; $\epsilon$-test acc., $\Theta^*$). The network where the weight magnitude was not increased over time (red, cross marker) did not improve in terms of robustness (cyan; $\epsilon$-test acc., $\Theta$), although both models perform similarly when there is no parameter noise applied (red and black dots).
Effect of Adversarial Regularization During Training

Figure S5: Our parameter attack is effective at decreasing the performance of a network during inference, and using our attack during training protects a network from later disruption. (a) A network trained using standard SGD (i.e. $\beta_{\text{rob}} = 0.0$) on the F-MNIST task is disrupted badly by the parameter noise adversary during inference ($\zeta = 0.1$; black curve). (b) When the same network is trained with parameter attacks during training ($\beta_{\text{rob}} = 0.1$), the network is protected from parameter attacks during inference (high accuracy of attacked network; black curve). (c) When trained with standard SGD ($\beta_{\text{rob}} = 0.0$), both random noise (red) and parameter attacks (black) disrupt network performance for increasing attack size $\zeta$. (d) Under our training approach ($\beta_{\text{rob}} = 0.1$), networks are significantly protected against random and adversarial weight perturbations during inference.
EFFECT OF VARYING $\beta_{\text{rob}}$

We additionally quantify the trade-off between test loss and robustness of our algorithm by repeating the experiment in Figure 1 at different values of $\beta_{\text{rob}}$. As shown in Figure S6, increasing $\beta_{\text{rob}}$ flattens the weight loss-landscape, effectively increasing the robustness of the model. To further substantiate this claim, we repeated the experiment of Figure S5 with the same values of $\beta_{\text{rob}}$. As Figure S8 shows, increasing $\beta_{\text{rob}}$ and therefore increasing the flatness of the landscape yields increased robustness to random, as well as, adversarial perturbations.

We note that, relative to the baseline, the test loss does not consistently increase with increasing values of $\beta_{\text{rob}}$. We hypothesize that this is due to the increased generalization capability of our networks. To check whether this is indeed the case, we repeated the experiment from Fig. S6 using the loss computed on the training set. As Fig. S7 shows, increasing values of $\beta_{\text{rob}}$ lead to flatter minima and higher loss on the training set, which is the exact trade-off to be expected from the formulation of our loss function. However, the increased generalization capability that follows from a flatter loss-landscape seems to disrupt this trade-off. As a result, when choosing $\beta_{\text{rob}}$ one should aim at choosing the highest value that still yields good performance on the validation set.

Figure S6: **Effect of $\beta_{\text{rob}}$ on the test loss landscape.** Increasing $\beta_{\text{rob}}$ promotes robustness by flattening the test loss landscape. However, increasing $\beta_{\text{rob}}$ does not lead to a systematic rise in loss on the test set.

Figure S7: **Effect of $\beta_{\text{rob}}$ on the training loss landscape.** Weight loss landscape computed on the training set (c.f. Fig. S6). Increasing $\beta_{\text{rob}}$ leads to flatter loss-landscapes (increased robustness) as before, but also a systematic increase in training loss (higher values for cross-entropy loss; note ordering of curves).
We additionally trained the CNN on the F-MNIST data for a range of $\beta_{\text{rob}}$, and measured the network robustness to attacks of varying magnitude $\zeta$ (Fig. S8). We compared the effectiveness of random attack versus adversarial attack during inference, evaluated on the test set. We found that increasing $\beta_{\text{rob}}$ improved the robustness of the network to both random and adversarial attack during inference.

Figure S8: Increasing $\beta_{\text{rob}}$ during training improves robustness during inference. The CNN was trained using different values of $\beta_{\text{rob}}$ and evaluated on the test set after the weights were perturbed either randomly (red) or adversarially (black).
Verifiable robustness for LSNNs

Computations through the network are performed on intervals rather than on discrete values. By propagating the intervals through a network over a test set, we obtain output logits that are also expressed as intervals and can therefore determine whether a sample will always be classified correctly.

The final classification of the network is made using an $\arg \max$ operator. For provability of network performance, we consider that a test sample $x$ is correctly classified when the lower bound of the logit interval for the correct class is the maximum lower bound across all logit intervals, and when the logit interval for the correct class is disjoint from the other logit intervals. When the logit interval for the correct class overlaps with another logit interval, that test sample is not considered to be provably correctly classified. Note that interval domain analysis provides a relatively loose bound (Gehr et al., 2018), with the implication that the results here probably underestimate the true performance of our method.

Figure S9: Networks trained with our method are provably more robust than those trained with standard gradient descent or forward noise alone. We used interval bound propagation to determine the proportion of test samples that are verifiably correctly classified, under increasing weight perturbations $\zeta$. For both the Speech and ECG tasks, computed on the trained LSNNs, our method was provably more robust for $\zeta < 5 \times 10^{-4}$. 
WIDE-MARGIN NETWORK ACTIVATIONS

Murray et al. show that adding random forward noise to the weights of a network during the forward pass implicitly adds a regularizer of the form $\Theta_{ij} \left( \frac{\partial o_{k,l}}{\partial \Theta_{ij}} \right)^2$ to the network weights (Murray & Edwards, 1994). When sigmoid activation functions are used, this regularizer favors high or low activations. When implementing interval bound propagation for LSNNs, intervals over spiking activity must be computed by passing intervals through the spiking threshold function. As a result, intervals for spiking activity become either $[0, 0]$ for neurons that never emit a spike regardless of weight attack; $[1, 1]$ for neurons that always emit a spike; and $[0, 1]$ for neurons for which activity becomes uncertain in the presence of weight attack. By definition, a robust network should promote bounds $[0, 0]$ and $[1, 1]$, where the activity of the network is unchanged by weight attack. Robust network configurations should therefore avoid states where the membrane potentials of neurons are close to the firing threshold.

To see whether this was also the case for our LSNNs, we investigated the distribution of the membrane potentials on a batch of test examples for a network that was trained with- and without noise during the forward pass. We found that robust networks exhibited a broader distribution of membrane potentials, with comparatively less distribution mass close to the firing threshold (Fig. S10). This indicates that neurons in the robust network spend more time in a “safe” regime where a small change in the weights cannot trigger an unwanted spike, or remove a desired spike.

Figure S10: Membrane potentials are distributed away from the firing threshold for networks trained with forward noise. We measured the distribution of membrane potentials in LSNNs trained using standard gradient descent (“Standard”), and in the presence of weight noise injected in the forward pass (“Forward Noise + Beta”). As predicted, networks trained with forward noise have membrane potentials distributed away from the firing threshold. This implies that weight perturbations are less likely to inject or delete a spike erroneously, improving the robustness of the network.
EFFECT OF VARYING $\epsilon_{PGA}$ IN AWP

In addition to attacking the network parameters, AWP (Wu et al., 2020) also attacks the input using PGA. Since the relation between robustness to weight- and input-space perturbations is still unclear, we performed additional sweeps over the attack size in the input space. Figure S11 demonstrates that attacking the input during training generally does not improve the robustness, with the exception for the network trained on the speech dataset. We also note that attacking the inputs improved robustness for larger mismatch values, but generally degraded performance for the small values.

Figure S11: Attacking the input using conventional PGA generally has a limited effect on robustness to weight-space perturbations. We swept various values of $\epsilon_{pga}$, the parameter that determines the maximum perturbation in $l_\infty$ for the AWP algorithm.
TRAINING OF CNN USED FOR PCM-BASED CI/M SIMULATION

The CNN that was used for this series of experiments is Resnet32 (He et al., 2015) trained on the Cifar10 (Krizhevsky, 2009) dataset. The CNN was generally trained for 300 epochs using a batch size of 256. We used SGD with an initial learning rate of 0.001 that was decreased by a multiplicative factor of 0.2 after epochs 60, 120 and 160. Additionally, Nesterov momentum (Nesterov, 1983) was used with a value of 0.9 and weight decay with a value of 5e-4.

Figure S12: The left panel illustrates the validation accuracy over the course of finetuning a pretrained model for additional 240 epochs. One should note that convergence is usually achieved much quicker (roughly after 150 additional epochs). The right panel illustrates the performance on the test set of each model trained with noise injection of magnitude $\eta_{\text{train}}$ (x-axis). Each row depicts a different noise model. It should be noted that the weights in each filter were clipped to two standard deviations during training to avoid outliers causing excessive amounts of noise following the model that relies on the maximum weight value.

VARYING THE NUMBER OF ATTACK STEPS FOR THE PCM-BASED CI/M SIMULATION

$\zeta_{\text{attack}} = 0.10$

Figure S13: One can greatly reduce the number of attack steps used during training. Our method still produces strong results for very few number of attack steps (blue) when compared to the baseline model (red, trained with Gaussian noise ($\eta_{\text{train}}$)).
Performance of varying hyperparameters for the PCM-based C1M simulation

In this experiment we show that the choice of hyperparameters is generally not very important to outperform the baseline that was trained with noise injection. However, in order to surpass the FP baseline, i.e. the model that was trained without noise injection and evaluated on a standard PC, one has to tune the hyperparameters in order to obtain a combination that yields the highest performance. Figure S14 illustrates this sweep. Each row represents a different value of $\eta_{\text{train}}$ that was used for training the baseline model (red). Each column represents a different attack size and the different hues of blue correspond to varying values of $\beta_{\text{rob}}$.

Figure S14: The choice of hyperparameters is not critical in order to beat the baseline model. Our method is resilient to variations in hyperparameters. However, to obtain configurations where even the FP baseline is surpassed, one has to fine-tune the method.
PCM noise model

Analog CiM comes in various flavors, depending on the memory technology used. In this paper, we assume the use of PCM devices, which have been heavily studied in the context of analog CiM accelerators (Joshi et al., 2020; Nandakumar et al., 2019; Boybat et al., 2018). PCM-based, or, more generally, Non-Volatile Memory (NVM)-based architectures, essentially perform Matrix-Vector-Multiplications (MVMs) using Kirchhoff’s current law. The weights of the matrix are organized as differential pairs in order to account for positive and negative weights. When storing a neural network, each weight matrix is programmed into the NVM devices by applying short electrical pulses (Nandakumar et al., 2020b). Because of various noise sources, this process is often imprecise and exhibits noise on the weights, termed “programming noise”. Additionally, PCM devices suffer from $1/f$ and telegraph noise, adding even more noise during inference (“read noise”). At last, PCM devices also drift due to the underlying physical properties (Le Gallo et al., 2018a). Although the effect of drift can mostly be alleviated by scaling the output of the MVM (a method called Global Drift Compensation (GDC)), the non-uniform drift of the devices still leads to performance degradation over time. In the simulator that we used, we model these three main sources of noise, analog-to-digital and digital-to-analog converters, GDC and splitting of the MVM to account for smaller tile sizes (typically each crossbar is $256 \times 256$).

Initially, the clipped weights are mapped to target conductances in a differential manner, i.e. the weight matrix is split into two conductance matrices that both represent the positive and negative weights as conductances. The target conductances typically range from zero to $G_{\text{max}}$, where $G_{\text{max}}$ is assumed to be $25 \mu S$. After mapping the weights to the target conductances $G_T$, the programming noise is simulated (these statistical models assume the conductances to be normalized): $G_P = G_T + \mathcal{N}(0, \sigma_P)$ where $\sigma_P = \max(-1.1731G_T^2 + 1.9650G_T + 0.2635, 0)$.

After the conductances have been programmed they drift over time, with the conductance of a device typically following $G_D = G_P(t/t_{\text{c}})^{-\nu}$, where $\nu$ is the drift coefficient, $t$ is the time at inference, and $t_c$ is the time the conductances were programmed. Additionally, the drift coefficient is modelled to follow a Gaussian distribution. This makes it typically hard to correct for drift and it is the main reason why drift is a problem in PCM based CiM devices.

Finally, the read noise is modelled using a Gaussian: $G_R \sim \mathcal{N}(G_D, \sigma_{nG(t)})$, where $\sigma_{nG(t)} = G_D(t)Q\sqrt{\log((t + t_r)/t_r)}$ with $Q = \min(0.0088/G_T^{0.65}, 0.2)$ and $t_r = 250\text{ns}$ and $t$ is the time at inference. For the experiments in this paper, we simulated the performance of the networks deployed on CiM hardware for up to one year.