Storage Efficiency Optimization of a Supercapacitor Charged by a Photovoltaic Cell using Genetic Algorithm

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Abstract: Super capacitors are very power dense devices in which charge density depends on the value of the capacitance and voltage across it. Generally, high capacitance of the Supercapacitor would support higher energy storage density, voltage remaining fixed. The load driven also does not utilize properly the entire stored charge from the Super capacitor due to inconsistency in electrical characteristics between the load and source. Storage efficiency of Super capacitor has state of charge dependencies as it is variable over the duration of charge/discharge cycles. It depends directly on the capacitance and indirectly on the ESR value. The proposed optimization technique can significantly find the maximum value of storage efficiency at a near about minimum value of ESR and maximum value of capacitance. The simulation model and results show the advantage of the said technique.

Keywords: Supercapacitor, Photovoltaic panel, RC branch model, Energy storage system, Solar photovoltaic equation, Genetic algorithm, Storage efficiency, Perturb & Observe algorithm, Buck Converter.

I. INTRODUCTION

Solar energy is one of the most widely available sources of renewable energy. It’s abundance in availability and consistent supply from our sun makes it one of the most promising source of energy to drive our future energy needs. The most common devices used to convert this energy are photovoltaic (PV) cells \cite{1}. Alternatives exist but photovoltaic being produced in mass scale reduces the cost of manufacturing and also has very low maintenance costs. They generate no pollution and there is almost zero Greenhouse gas emissions. The efficiency of photovoltaic cells is low (10-25\%) which occasionally can reach 35\% and for this reason tracking of the incident sunlight is necessary in order to extract maximum power from the PV panels \cite{2}. Many methods are present and one such efficient method is the Maximum Point tracking Algorithm (MPPT). As the intensity of solar radiation varies, the load characteristics that provide the best power transfer efficiency change and as a result maximum power extraction is affected. The MPPT keeps track on the changes in load characteristics to obtain the maximum power point. Maximum power point trackers may employ various algorithms such as Incremental conductance, Perturb & Observe (P&O), Constant Voltage, Current sweep etc. Among the mentioned algorithms the P&O algorithm shows the best performance \cite{3} \cite{6}. MPPT algorithms change the impedance of the circuit by employing DC-DC converters controlled by the MPPT controller so as to maximize the power extraction from the PV panels to the load \cite{4}. Load in most of the cases is usually a battery as they are the most widely used storage devices and are great in storing electrical energy. However they take large time to store electrical energy in the form of chemical energy and also they lack the ability to transfer power quickly to the load. They cannot handle bursts of power in applications such as torque generation or regenerative braking in motors of electric vehicles. To overcome such problems Supercapacitors are used which are basically hybrid of capacitors and batteries. Unlike batteries they can charge or discharge energy at a very fast rate similar to a normal capacitor but their energy storage capacity is much larger compared to a normal capacitor similar to a battery.

They have large charging and discharging cycles compared to rechargeable batteries. They generally use electrostatic double layer capacitance and electrochemical pseudo capacitance with variable contribution of each \cite{5}. Supercapacitor have large storage efficiencies. These are one of the most efficient storage devices. Storage efficiency of Supercapacitors is generally a function of the internal resistance also called as equivalent series resistance (ESR) and the capacitance. The storage efficiency is directly proportional to the capacitance value and inversely proportional to the internal resistance ESR.
Our main area of interest in this paper is to find optimum values of ESR and Capacitance which would maximize the storage efficiency of the Supercapacitor. In this work we have used Genetic algorithm which is an AI based searching technique to optimize the values of ESR and Capacitance so as to get the maximum possible efficiency.

The organization of this paper is as follows. In the next part (part II) a detailed description of the mathematical and electrical models used in the simulation is discussed and also the various algorithms used in these work. Part III verifies the facts and part IV provides the results obtained from our simulation and observation. Conclusion is given in Part V.

II. MATHEMATICAL MODELS AND ALGORITHMS

A. Model of the Photo-voltaic panel

Photovoltaic or solar cells are devices which are usually in the form of thin films or wafers. They are semiconductor devices with large p-n junctions usually made of Si, that convert from 3-30% of incident solar energy to DC electricity, with efficiencies depending on illumination-spectrum intensity, solar-cell design and materials and temperature. It generally behaves like a low-voltage battery whose charge is continuously replenished at a rate proportional to incident illumination. Connection of such cells into series-parallel configurations allows the design of solar panels with high current and voltages.

From the equivalent circuit it is evident that the current produced by the solar cell is [11][12]:

\[ I = I_L - I_D - I_{SH} \]

where, \( I = \) generated solar cell current, \( I_L = \) photo generated current, \( I_D = \) diode current and \( I_{SH} = \) shunt current.

The current through these elements is governed by the voltage across them:

\[ V_j = V + IR_S \ldots (1) \]

where, \( V_j = \) voltage across both diode and resistor \( R_{SH} \), \( V = \) voltage across the output terminals, \( I = \) output current and \( R_S = \) series resistance.

According to Shockley diode equation [9], the current through the diode is

\[ I_D = I_0 e^{\frac{V_j}{nV_T} - 1} \ldots (2) \]

where, \( I_0 = \) reverse saturation current, \( n = \) diode ideality factor. \( V_T = \) the thermal voltage and \( V_T \approx 0.0259 \) volt at 25°C. By Ohm’s law, the current through the shunt resistor is:

\[ I_{SH} = \frac{V_j}{R_{SH}} \ldots (3) \]

where, \( R_{SH} = \) shunt resistance. Substituting equation number 1 & 3 into the equation number 2 produces the characteristic equation of a solar cell

\[ I = I_L - I_0 \left( e^{\frac{V + IR_S}{nV_T} - 1} \right) - V + IR_{SH} \ldots (4) \]

When \( R_{SH} \) is not zero, the above equation does not give the current \( I \) directly, but it can then be solved using the Lambert W function:

\[ I = \frac{(I_L + I_0) - V}{1 + \frac{R}{R_{SH}}} - \eta V_T \left( \frac{I_0R_S}{\eta V_T \left( 1 + \frac{R}{R_{SH}} \right)} \right) e^{\frac{V + IR_{SH}}{\eta V_T \left( 1 + \frac{R}{R_{SH}} \right)}} \]

When \( R_{SH} \) is infinite there is a solution for \( V \) for any \( I \) less than \( I_L + I_0 \):

\[ V = \eta V_T \ln \left( \frac{I_L - 1}{I_0} + 1 \right) - IR_S \]
B. Output Characteristics of the Photo-voltaic Panel

When the cell is operated at open circuit \((I = 0)\) then the voltage across the output terminals is defined as the open-circuit voltage. Assuming the shunt resistance is high enough to neglect the final term of the characteristic equation, the open-circuit voltage \(V_{OC}\) is:

\[
V_{OC} \approx \frac{n k T}{ q } \ln \left( \frac{I_L}{I_0} + 1 \right).
\]

Similarly, when the cell is operated at short circuit, the current \(I\) through the terminals is defined as the short-circuit current. The short-circuit current \(I_{SC}\) is:

\[
I_{SC} = I_L
\]

It is not possible to extract any power from the device when operating at either open circuit or short circuit conditions. The output characteristics of the photovoltaic is shown in fig 2.

![Fig 2](image)

C. Simulation model of the Supercapacitor

Supercapacitor are usually double layered capacitor or DLC’s and typically have very high capacitance value allowing them to store large quantities of electrical energy but the voltage remains low (maximum 3V) [8]. So, it helps to drive motor, pump and other electro-mechanical devices which requires impulsive power. The energy stored in a Supercapacitor is the product of half of the capacitance and the square of voltage. Thus the high capacitance value is the distinguishing factor as the voltage which is fed to the system is almost constant for efficient charging and Supercapacitors can charge/ discharge rapidly. Its small leakage current helps in long period of energy storage and the efficiency could exceed 95%.

In this paper we have used two resistance model of the Supercapacitor for software simulation as shown in fig 3.

![Fig 3](image)

Here \(C_0\) is a constant capacitor, \(R_1\) is the equivalent series resistance and \(K_V\) is a voltage dependent capacitor and this first branch models the voltage dependencies of the Supercapacitor. The second branch consisting of \(R_2\) and \(C_2\) are responsible for taking into account the charge redistribution taking place within the Supercapacitor. The resistance \(R_3\) is called the equivalent parallel resistor and is used to model the leakage behaviour of capacitor. The Supercapacitor has an inherent tendency of reduction in storage efficiency with increase in the value of ESR and the slope of the curve changes with higher values of ESR. The storage efficiency is also a function of capacitance and with increasing value of capacitance the storage efficiency of the Supercapacitor increases. The slope of the curve keeps on decreasing as the capacitance reaches higher and higher values.
D. Simulation model of the buck Converter

The buck converter (shown in fig 4) or step down converter is basically a switched mode DC-DC power converter which can transfer packets of energy from its input to output depending on the frequency of the switching signal applied to the switch S, which in most cases is usually a power transistor such as MOSFET whose gate is used to control the switching action. Buck converters in most cases could be very efficient and can provide efficiencies more than 90%. Simulink model of the buck converter used in our simulation is shown in fig 5.

E. Application of Maximum Power Point Tracking for efficient power delivery to load

In our simulation to maximise the power delivery from PV panel to the Supercapacitor we have used the MPPT. Maximum power point tracking algorithm is a tracking algorithm usually employed to maximize the output power to loads from photovoltaic panels. Different MPPT algorithms such as Perturb and Observe, Incremental Conductance etc. try to find out the point from the P-V characteristics of the solar panel where the power delivered by the panel is maximum. Out of many available MPPT algorithms, Perturb and Observe algorithm appears to be most promising with better tracking efficiency. The P&O algorithm is shown in the flowchart below.

Simulink model of the Perturb and Observe algorithm used is as shown in fig 6.
G. Genetic Algorithm

Genetic algorithm (GA) is an artificial search algorithm based on principles of natural selection and natural genetics. It is a programming technique that imitates biological evolution as a problem-solving strategy [7] [10]. GA represents a popular approach to optimization because it selects the best solutions among multiple local minima. The GA assigns the present population as parents and uses them to produce the children for the future generation. As generation increases the population evolves toward an optimal solution.

Sometimes the optimal solution is achieved by employing a certain 'fitness function' which takes a candidate solution to the problem as input and produce output as how fit the solution is with respect to that problem. A fitness function should be fast to compute and it should measure quantitatively how fit the given solution is to the given problem.
III. VERIFICATION OF THE VARIATION IN STORAGE EFFICIENCY WITH ESR AND CAPACITANCE

We have simulated the model and verified the storage efficiency variations with variation in ESR and capacitance. The storage efficiency vs. ESR response obtained from the model is shown in fig 8.

![Graph showing storage efficiency vs. ESR](image)

The storage efficiency vs. Capacitance response obtained from the model is shown in fig 9.

![Graph showing storage efficiency vs. Capacitance](image)
The 3D plot of efficiency vs. ESR vs. Capacitance obtained after extrapolation of the sample data obtained from the model is shown in fig 10.

IV. EXPERIMENTAL RESULTS

| ESR(ohm) | Capacitance (Farad) | Storage efficiency (%) |
|----------|---------------------|------------------------|
| 0.05     | 100                 | 58.63                  |
|          | 200                 | 69.26                  |
|          | 300                 | 76.83                  |
|          | 400                 | 81.37                  |
|          | 500                 | 85.06                  |
| 0.1      | 100                 | 57.45                  |
|          | 200                 | 67.88                  |
|          | 300                 | 74.69                  |
|          | 400                 | 79.45                  |
|          | 500                 | 82.96                  |
| 0.15     | 100                 | 56.52                  |
|          | 200                 | 66.58                  |
|          | 300                 | 73.11                  |
|          | 400                 | 77.66                  |
|          | 500                 | 81                     |
| 0.2      | 100                 | 55.64                  |
|          | 200                 | 65.37                  |
|          | 300                 | 71.64                  |
|          | 400                 | 75.99                  |
|          | 500                 | 79.18                  |
| 0.25     | 100                 | 54.82                  |
|          | 200                 | 64.22                  |
|          | 300                 | 70.26                  |
|          | 400                 | 74.43                  |
|          | 500                 | 77.48                  |

The simulation of the model was done to get enough sample points for further processing by the Genetic algorithm. At first the model was used to determine the variation in efficiency with variation in ESR and Capacitance individually and independently and the data points were plotted as shown in the graphs in Part III. These sample data points were then used to find the polynomial function which will eventually be modified and used as a fitness function in Genetic Algorithm. The data points observed from the model are given in the table 1. Further this data have been used to get the polynomial function using the Curve fitting toolbox of MATLAB. The obtained polynomial is of order 2 and is given as:

\[ z(x, y) = p_{00} + p_{10}x + p_{01}y + p_{20}x^2 + p_{11}xy + p_{02}y^2 \]

where, x is the equivalent series resistance (ESR) and y is the capacitance of the Supercapacitor,

- \( p_{00} = 47.79 \)
- \( p_{10} = -20.41 \)
- \( p_{01} = 0.1339 \)
- \( p_{20} = 19.6 \)
- \( p_{11} = -0.04914 \)
- \( p_{02} = -0.0001104 \)
We have used this polynomial in genetic algorithm for optimization of the efficiency since the genetic algorithm tool in MATLAB is configured for data minimization we have used fitness function f(x,y), so that minimization of f(x,y) will lead us to maximization of z(x,y) which is essentially the overall efficiency.

\[ f(x,y) = \frac{1}{z(x,y)} \]

Final data obtained after optimization by the Genetic algorithm at different stall generations is as follows:

| STALL GENERATION | ESR (ohm) | CAPACITANCE (Farad) | EFFICIENCY (%) |
|------------------|-----------|---------------------|----------------|
| 20               | 1.00E-003 | 498                 | 86.86          |
| 21               | 3.00E-003 | 473                 | 86.3           |
| 22               | 0         | 458.15              | 85.95          |
| 23               | 2.30E-002 | 432.1               | 84.09          |
| 24               | 1.20E-002 | 414.35              | 83.82          |
| 25               | 1.60E-002 | 479.79              | 85.9           |

V. CONCLUSION

In conclusion, it should be clearly emphasised that a lower value of ESR and high Capacitance is desired for better storage efficiency. It can be observed from the shown graphs that storage efficiency increases with decrease in ESR and increase in capacitance of Supercapacitor.

The initial results shows that typical storage of efficiencies greater than 85% can be obtained at the above obtained values of ESR and capacitance, which clearly is an optimized one. This method explores the area of implementation of AI computing techniques in optimization of Supercapacitor performance. This method can be used to determine optimum values of ESR and capacitance for practical applications.

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