Origin of colossal dielectric permittivity of rutile $\text{Ti}_{0.9}\text{In}_{0.05}\text{Nb}_{0.05}\text{O}_2$: single crystal and polycrystalline

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In this paper, we investigated the dielectric properties of (In + Nb) co-doped rutile TiO$_2$ single crystal and polycrystalline ceramics. Both of them showed colossal, up to $10^4$, dielectric permittivity at room temperature. The single crystal sample showed one dielectric relaxation process with a large dielectric loss. The voltage-dependence of dielectric permittivity and the impedance spectrum suggest that the high dielectric permittivity of single crystal originated from the surface barrier layer capacitor (SBLC). The impedance spectroscopy at different temperature confirmed that the (In + Nb) co-doped rutile TiO$_2$ polycrystalline ceramic had semiconductor grains and insulating grain boundaries, and that the activation energies were calculated to be 0.052 eV and 0.35 eV for grain and grain boundary, respectively. The dielectric behavior and impedance spectrum of the polycrystalline ceramic sample indicated that the internal barrier layer capacitor (IBLC) mode made a major contribution to the high ceramic dielectric permittivity, instead of the electron-pinned defect-dipoles.

In recent years, the search for materials with colossal permittivity continues to attract considerable interest motivated by academic research and potential applications for smaller and faster electronics as well as high-energy-density storage$^1$. There are many excellent colossal dielectric permittivity materials, such as doped BaTiO$_3$$^2$, CaCu$_3$Ti$_4$O$_{12}$ (CCTO)$^{3,4}$, doped NiO$^5$, Bi$_{0.5}$Na$_{0.5}$TiO$_3$ (BNT)$^6$, Ni$_{0.5}$Zn$_{0.5}$Fe$_2$O$_4$ (NZF)$^7$, ZnO$^8$ etc., but the high permittivity of the ferroelectric materials, such as BaTiO$_3$ and (K, Na)NbO$_3$ can only be achieved over a narrow temperature range close to the ferroelectric phase transition$^{9,10}$. The stronger temperature dependence limits their applications. The dielectric loss of other non-ferroelectric materials with colossal values of $\varepsilon$ is too high to apply. To make matters worse, it is very hard to achieve a balance between high $\varepsilon$ and affordable low dielectric loss$^{11}$. Recently, significant dielectric behavior was reported in (In + Nb) co-doped TiO$_2$ polycrystalline ceramic$^{12}$. The coexistence of colossal dielectric constant (CP) and low dielectric loss makes it attract considerable attention. Similar dielectric behavior was found in niobium and some other trivalent cations co-doped TiO$_2$ polycrystalline ceramics$^{13,14}$. The excellent dielectric properties in (In + Nb) co-doped TiO$_2$ amorphous thin films make wide application possible$^{15}$.

A new mechanism named electron-pinned defect-dipoles was proposed by Hu $et$ $al.$ in order to explain the excellent dielectric properties$^{12}$. They suggested that the local 2In$^{3+} + V_{\text{O}}^+$ + 2Ti$^{4+} + 2Nb^{5+} + \text{Ti}^{4+}$ defect clusters were responsible for the colossal permittivity and low dielectric loss$^{12}$. The colossal dielectric permittivity of amorphous film$^{15}$ and nano-crystalline$^{16}$ of (In + Nb) co-doped TiO$_2$ samples support this theory, which is considered to be an essential property independent of microstructure. Liu $et$ $al.$ considered that electron-pinned defect-dipoles, interfacial polarization and polaron hopping polarization contribute to the colossal permittivity together$^{17}$. Li $et$ $al.$ suggested that the semiconducting grains and insulating grain boundaries contribute to the colossal permittivity behavior of (In + Nb) co-doped rutile TiO$_2$ ceramics samples, which is named the internal barrier layer capacitor (IBLC) effect. But they didn't eliminate the role of electron-pinned defect-dipoles$^{18-20}$. The origin of novel dielectric behavior for co-doped TiO$_2$ ceramics is still controversial because the two proposed

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mechanisms result in very similar dielectric behaviors in polycrystalline ceramic. Therefore, the dielectric behaviors of (In + Nb) co-doped TiO₂ single crystal are very important data to clarify the issue.

In this paper, we firstly observed the colossal dielectric permittivity up to 10⁴ at room temperature of (In + Nb) co-doped rutile TiO₂ single crystal and polycrystalline ceramic. The voltage-dependence of the dielectric permittivity and the impedance spectrum suggest that the high dielectric permittivity of single crystal originated from the surface barrier layer capacitor (SBLC). The temperature dependence of dielectric behavior and impedance spectrum of the polycrystalline ceramic sample indicate that the internal barrier layer capacitor (IBLC) mode made a major contribution to the high dielectric permittivity instead of the electron-pinned defect-dipoles.

Results and discussions

Fig. 1(a) shows the XRD patterns and SEM images of In₀.₀₅Nb₀.₀₅Ti₀.₉O₂ single crystal (black line) and polycrystalline ceramics (red line); (b) Valence states of the elements Ti in the TiO₂ single crystal co-doped with 10% (In + Nb), the insert shows the XPS data of O1s.

Figure 1. (a) XRD patterns and SEM images of In₀.₀₅Nb₀.₀₅Ti₀.₉O₂ single crystal (black line) and polycrystalline ceramics (red line); (b) Valence states of the elements Ti in the TiO₂ single crystal co-doped with 10% (In + Nb), the insert shows the XPS data of O1s.
The In/Nb co-doping induces oxygen vacancies and Ti$^{3+}$. Additionally, the oxygen vacancy caused from oxygen loss in the single crystal during growth can produce additional Ti$^{3+}$, as given in equation (3). The necessary conditions to form defect clusters is fully equipped in the co-doped rutile TiO$_2$ single crystal, so the electron-pinned defect-dipoles theory may be demonstrated the dielectric behavior in the single crystal sample$^{12,15}$. Fig. 2(a) shows the frequency dependence of the dielectric constant and dielectric loss of 10% (In + Nb) co-doped TiO$_2$ single crystal and polycrystalline ceramics at room temperature. Fig. 2(b) shows the temperature dependent dielectric constant and dielectric loss. Interestingly, a colossal dielectric permittivity up to 10$^4$ over the low frequency range was achieved for the (In + Nb) co-doped TiO$_2$ single crystal, and dropped dramatically to $\varepsilon \sim 85$ that was very close to the value of pure rutile TiO$_2$ single crystal. Additionally, a dissipation peak appears between 10$^4$ ~ 10$^5$ Hz, which indicates a dielectric relaxation process. The dielectric permittivity quickly falls two orders magnitude from 10$^4$ to 10$^2$ with temperature decreased, and the dissipation peak of dielectric relaxation is observed at a temperature of 290 K. This result further confirms that one dielectric relaxation process in the (In + Nb) co-doped TiO$_2$ single crystal.

For the ceramics sample, as shown in Fig. 2(a), a high dielectric permittivity, up to 45000, is observed in the low frequency range, and dropped to a lower dielectric permittivity, 30000, as the frequency increased up to 10$^6$ Hz. A dissipation peak toward dielectric relaxation appears between 10$^2$ - 10$^3$ Hz. Consequently, there should be another dielectric relaxation process at a higher frequency range (>10$^6$ Hz) at room temperature because the high dielectric permittivity, 30000, is much larger than that in pure rutile TiO$_2$. The temperature dependent dielectric loss tests were carried out to confirm this hypothesis, as shown in Fig. 2(b). The dielectric constant changes smoothly with decreasing of temperature and suddenly falls two orders of magnitude at 50 K. Additionally, two dissipation peaks are observed at 50 K and 320 K. Therefore, two dielectric relaxations exist in the polycrystalline ceramic while only one dielectric relaxation process exists in the single crystal.

Due to the absence of grain boundaries in single crystal, the high dielectric permittivity of (In + Nb) co-doped TiO$_2$ single crystal in the low frequency range cannot be attributed to an internal barrier layer capacitor (IBLC).

$$\text{In}_2\text{O}_3 \xrightarrow{\text{TiO}_2} 2\text{In}^{3+} + V_0 + 3\text{O}_2$$  \hspace{1cm} (1)

$$\text{Nb}_2\text{O}_5 + 2\text{TiO}_2 \xrightarrow{\text{TiO}_2} 2\text{Nb}^{5+} + 2\text{Ti}^{4+} + 8\text{O}_2 + 1/2\text{O}_2$$  \hspace{1cm} (2)

$$2\text{TiO}_2 \xrightarrow{\text{TiO}_2} 2\text{Ti}^{4+} + V_0 + 3\text{O}_2 + 1/2\text{O}_2$$  \hspace{1cm} (3)
Therefore, the electron-pinned defect-dipoles theory proposed by Hu et al.\textsuperscript{12} and surface barrier layer capacitor (SBLC) mode that is confirmed to exist in a CaCu\textsubscript{3}TiO\textsubscript{4} (CCTO) single crystal\textsuperscript{25–27} are the alternative approach to get the high dielectric permittivity in (In + Nb) co-doped TiO\textsubscript{2} single crystal. According to Hu’s theory, electrons hop in defect clusters and give high electronic conductivity, which can only be driven by thermal activation\textsuperscript{12,14}. Consequently, the dielectric behavior should change little under different testing voltage. However, the dielectric permittivity decreased with the increasing testing voltage at low frequency range, which suggested that the CP of (In + Nb) co-doped TiO\textsubscript{2} single crystal is caused by the SBLC because of the decreased of R\textsubscript{b} (the resistance of electrode contact impedance between single crystal and electrode) under high voltage\textsuperscript{25}. The relationship of dielectric permittivity and R\textsubscript{b} can be described by equation (4–7)\textsuperscript{28}.

\[ \varepsilon' (\omega) = \frac{\tau_e + \tau_b - \tau + \omega^2 \tau_e \tau_b}{C_0 (R_e + R_b) (1 + \omega^2 \tau^2)} \]  

(4)

\[ \tau_e = C_e R_e \]  

(5)

\[ \tau_b = C_b R_b \]  

(6)

\[ \tau = \frac{\tau_e R_b + \tau_b R_e}{R_b + R_e} \]  

(7)

Here, C\textsubscript{0} is a constant determined by the single crystal area and thickness, R\textsubscript{e} is the bulk resistance. C\textsubscript{e} and C\textsubscript{b} are capacitance due to SBLC and bulk, where C\textsubscript{e} \gg C\textsubscript{b}\textsuperscript{25}. Considering this factor, equation (4) can be developed to equation (8).

\[ \varepsilon' (\omega) = \frac{C_e / C_0}{1 + \omega^2 C_0 (1 + \frac{R_e}{R_b}) (1 + \frac{R_e}{R_b})^2 + \omega^2 C_e^2 R_b^2} \]  

(8)

According to above equation, \( \varepsilon' (\omega) \) reduces with increased R\textsubscript{b}/R\textsubscript{e}, because \( \omega^2 C_0 (C_e \approx 10^{-12} \text{ F}) \) and \( \omega^2 C_e^2 R_b^2 \) are much less than 1 over the frequency range where the high dielectric permittivity appears.

Fig. 3(a) shows that the dielectric permittivity of single crystal sample in low frequency range declined and the relaxation time \( \tau \) reduced continuously with the increased testing voltage. Here the equivalent circuit can be expressed in the inset of Fig. 3(a), in which, C\textsubscript{e} \gg C\textsubscript{b}\textsuperscript{25}. This point is supported by the impedance spectrum data shown in Fig. 3(b). For example, when the testing voltage is 1 V, C\textsubscript{e} and C\textsubscript{b} are about \( \sim 10^{-8} \text{ F} \) and \( \sim 10^{-12} \text{ F} \), while R\textsubscript{e} and R\textsubscript{b} are 5.5 kΩ and 20.9 kΩ, respectively. As mentioned above, R\textsubscript{b} reduced rapidly with the increased of testing voltage. Although R\textsubscript{b} also decreased at the same time, R\textsubscript{e}/R\textsubscript{b} increased continuously in this process, as shown in the inset of Fig. 3(b). This is consistent with the SBLC model. In addition, the reduced of the bulk resistance may be ascribed to the injected electrons, which are trapped by oxygen vacancies\textsuperscript{29}. This phenomenon again proves the existence of oxygen vacancies in single crystal sample. Therefore, the high dielectric permittivity of (In + Nb) co-doped TiO\textsubscript{2} single crystal is attributed to the SBLC, and has no relationship with electron-pinned defect-dipoles.

The data of 10% (In + Nb) co-doped TiO\textsubscript{2} polycrystalline ceramic can be modeled on an equivalent circuit consisting of three parallel RC elements in series, as shown in Fig. 4(a). The first plateau of the dielectric permittivity is attributed to boundary (IBLC), owing to the appearance of the microstructure consisting of grain boundaries and grains compared to single crystal. Referring to the single crystal research results, the second dielectric permittivity plateau in the polycrystalline ceramic at low frequency can be attributed to the SBLC model. The dielectric permittivity should drop to the level of \( 10^4 \) if the frequency was high enough (>10\textsuperscript{8}Hz), and then only the dielectric behavior of bulk contributes to the permittivity\textsuperscript{25}. This phenomenon can't be observed in the range of 10\textsuperscript{9} to 10\textsuperscript{12}Hz at room temperature but at lower temperature, as shown in Fig. 4(b).

Fig. 4(b) and (c) show the frequency dependence of dielectric permittivity and the impedance spectroscopy at different temperature, respectively. According to this model, there should be three arcs, of which the position in the frequency spectrum depends on their relaxation times \( \tau \), where \( \tau = RC \), the relationship \( 2\pi f \tau = 1 \) works at the arc maxima, where f is the applied frequency. Generally speaking, \( \tau_e > \tau_b > \tau_e > \tau_b > \tau_e > \tau_b \), where \( \tau_e \), \( \tau_b \), and \( \tau_b \) are relaxation time of electrode contact impedance, grain boundary impedance and bulk impedance. The dielectric relaxation time becomes smaller with the temperature increasing due to its' decreased resistance. It often occurs that the three dielectric permittivity steps can't be simultaneously achieved at a fixed temperature because the relaxation time is too large or too small compared to the applied frequency range. Meanwhile the impedance arcs show a similar behavior.

The dielectric permittivity in the frequency range from 10\textsuperscript{3} to 10\textsuperscript{7}Hz is about 130 at 40 K. Therefore, the grain boundary response does not occur because \( \tau_b \) is much larger than \( 1/2\pi f (1.6*10^{-7} \sim 1.6*10^{-8}) \). Correspondingly, only one arc is considered to be the bulk response, appears in impedance spectroscopy at 40 K during 10\textsuperscript{3} to 10\textsuperscript{7}Hz (as shown in Fig. 4(c)). The dielectric permittivity starts to ascend when the applied frequency is below 10\textsuperscript{5}Hz, and another arc thought to be the grain boundary response appears in the impedance spectrum. The dielectric permittivity reduced sharply from 10\textsuperscript{4} to 10\textsuperscript{2} magnitude in the frequency range from 20 to 10\textsuperscript{8}Hz with increasing frequency when the temperature was at 80 K; while the grain boundary response and grain response
Figure 3. Impedance analysis and dielectric behavior of 10% (In + Nb) co-doped rutile TiO₂ single crystal.
(a) Dielectric constant as a function of frequency at different testing voltages, the equivalent electric circuit is shown in the insert. (b) Impedance spectroscopy at a different testing voltage, the $R_b$, $R_e$ and $R_b/R_e$ under different testing voltages are shown in the insert.

Figure 4. Impedance analysis and dielectric behavior of 10% (In + Nb) co-doped rutile TiO₂ polycrystalline ceramics. (a) The equivalent electric circuit. (b) Dielectric constant as a function of frequency. (c,d) are impedance spectroscopy at 40 K and 80 K respectively. The solid symbols are the experimental results. The solid lines are the best fitting results.
arcs appeared successively (shown in Fig. 4(d)) due to the reduced of $\tau_{gb}$ and $\tau_b$. $\tau_b$ is so low that the bulk response does not work in the frequency range from 20 to $10^6$ Hz when the temperature rose to 300 K. The result is that the dielectric constant is maintained at a high level as large as 10$^4$ and only the grain boundary response arc appeared in impedance spectroscopy in the applied frequency range (shown in Fig. 5(a)). In addition, $\tau_e$ is small enough at room temperature, so the second plateau of dielectric permittivity appears at lower frequency. However, electrode arc can't be separated from grain boundary arc in impedance spectroscopy because $R_{gb}$ is much greater than $R_e$.

Fig. 5(a) and Fig. 5(c) show the impedance spectroscopy above 300 K and below 90 K, respectively. The bulk ($R_b$) and grain boundary ($R_{gb}$) resistances are thermally activated according to the equation:

$$\sigma = \sigma_0 \exp\left(-\frac{E_a}{k_B T}\right)$$

where $\sigma_0$ is a constant, $E_a$ is the activation energy, $k_B$ is the Boltzmann constant, and $T$ is the absolute temperature. The activation energies were calculated to be 0.052 eV and 0.35 eV for grain and grain boundary, as shown in Fig. 5(b) and (d). The grain activation energy could be attributed to the donor impurity energy level induced by In and Nb. The Nb doping into TiO$_2$ will induce a shallow donor impurity energy level at 0.02-0.03 eV, accompanying the reduction of Ti$^{4+}$. However, because of the lack of oxygen vacancy at grain boundary, the electron emitted by doped-Nb ion will be captured by doped-In ion instead of Ti ion, leading to a sharp increase of resistance at grain boundary. Thus, the (In + Nb) co-doped TiO$_2$ ceramic consists of semiconductor grains and insulating grain boundaries, and the colossal dielectric permittivity can be attributed to the microstructure. In other words, the IBLC mode works well in (In + Nb) co-doped TiO$_2$ polycrystalline ceramic, instead of the electron-pinned defect-dipoles mode.

Conclusions
In summary, 10% mol (In + Nb) co-doped TiO$_2$ single crystal and polycrystalline ceramics were fabricated by the optical floating zone method and solid phase reaction method, respectively. The microstructure, valence state, dielectric response, and impedance spectroscopy of the samples were systematically investigated. Two dielectric relaxation processes were observed in the polycrystalline ceramics sample, while only one appeared in the single crystal sample. The high dielectric permittivity that appears at low frequency originated from the SBLC, because the dielectric behavior and impedance spectroscopy of single crystal sample depended on the testing voltage. The microstructure composed of semiconductor grains and insulating grain boundaries in the polycrystalline
ceramics. The grain and grain boundary activation energies in the ceramic samples were calculated to be 0.052 eV and 0.35 eV from the impedance spectroscopy at different temperatures. Additionally, the relationship between the dielectric behavior and the electric response for the grain and grain boundary impedance spectroscopy indicates that the colossal dielectric permittivity in (In + Nb) co-doped TiO₂ polycrystalline ceramics should be benefited from the IBLC instead of the electron-pinned defect-dipoles.

Methods

The 10 mol % (In + Nb) co-doped TiO₂ samples was synthesized using the standard conventional solid-state reaction (SCSS) method, where the rutile TiO₂ (99.99%) method, and In₂O₃, and Nb₂O₅ were mixed into a test tube, typically 6 mm in diameter and 100 mm long, and heated at 1500°C for 20 hours after being hydraulic pressed under an isostatic pressure of 70 MPa. The single crystal was grown in the air by the rate of 10 mm/h.

The phase of samples was characterized by X-ray diffraction (XRD, XPERT PRO MPD, Holland). X-ray photoelectron spectroscopy (XPS, ESCALAB 250Xi, USA) was used to analyze the valence state of different elements. The microstructure was investigated by scanning electron microscopy (SEM, FEI PHENOM G1). The dielectric behavior and the electric response for the grain and grain boundary impedance spectroscopy indicated the relationship between the dielectric constant and the electric response for the grain and grain boundary.

The reference list includes a variety of materials and related research, ranging from dielectric behavior and the electric response for the grain and grain boundary to the dielectric enhancement and Maxwell-Wagner effects in polycrystalline ferroelectric multilayered ceramics. The grain and grain boundary activation energies in the ceramic samples were calculated to be 0.052 eV and 0.35 eV from the impedance spectroscopy at different temperatures.
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Author Contributions
X.W. conceived and designed the experiments. Y.S. and H.Z. prepared the polycrystalline sample. Z.L. and Y.S. prepared the single crystal sample. Y.S., B.S., L.T. and Z.L. performed the measurements. Y.S., X.W., Y.S., Y.Z., Z.L. and J.T. analysed the data. Y.S., X.W. and Y.S. wrote the paper. All the authors discussed the results and commented on the manuscript.

Additional Information
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