Dislocation-free Ge Nano-crystals via Pattern Independent Selective Ge Heteroepitaxy on Si Nano-Tip Wafers

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The integration of dislocation-free Ge nano-islands was realized via selective molecular beam epitaxy on Si nano-tip patterned substrates. The Si-tip wafers feature a rectangular array of nanometer sized Si tips with (001) facet exposed among a SiO2 matrix. These wafers were fabricated by complementary metal-oxide-semiconductor (CMOS) compatible nanotechnology. Calculations based on nucleation theory predict that the selective growth occurs close to thermodynamic equilibrium, where condensation of Ge adatoms on SiO2 is disfavored due to the extremely short re-evaporation time and diffusion length. The growth selectivity is ensured by the desorption-limited growth regime leading to the observed pattern independence, i.e. the absence of loading effect commonly encountered in chemical vapor deposition. The growth condition of high temperature and low deposition rate is responsible for the observed high crystalline quality of the Ge islands which is also associated with negligible Si-Ge intermixing owing to geometric hindrance by the Si nano-tip approach. Single island as well as area-averaged characterization methods demonstrate that Ge islands are dislocation-free and heteroepitaxial strain is fully relaxed. Such well-ordered high quality Ge islands present a step towards the achievement of materials suitable for optical applications.

The integration of high quality crystalline Ge on Si substrates has recently attracted intensive interests for its promising applications in diverse fields. In the continuous scaling of the complementary-metal-oxide-semiconductor (CMOS)-based digital devices, known as “More Moore (MM)” approach, Ge on Si is used as the channel material in p-MOS field-effect transistors (p-MOSFETs) due to its high hole mobility1. In novel low-power devices like tunneling FET (TFET), Ge can enable steep swing operation2. On the other hand, high quality Ge is also of paramount interest for the integration of various optoelectronic devices on Si (light emitters3–5, photo-detectors6 etc.), following the More than Moore (MtM) approach.

The major hurdles towards high crystal quality heterostructure growth are represented by the lattice mismatch (4.2%) and the coefficient of thermal expansion (CTE) mismatch (130%) existing between the Ge heteroepilayer and the Si substrate7. The resulting strain accumulation may be plastically relaxed through the formation of detrimental defects, such as misfit dislocations and cracks, thus hindering the realization of high quality heteroepitaxial crystals8. Furthermore, the thermodynamic-equilibrium epitaxy of Ge on Si (001) follows the Stranski-Krastanov (SK) growth mode, resulting in the nucleation of randomly distributed, size inhomogeneous9, and highly Si-Ge intermixed10 self-assembled Ge islands on top of a ~0.5 nm-thick Ge wetting layer.

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In view of their possible use in Ge-based devices, numerous solutions have been proposed to achieve spatial and shape control of these self-assembled Ge islands, such as pit-patterning\textsuperscript{11–13}, SiGe buffer layers\textsuperscript{14–17}, epitaxial oxide buffer layers\textsuperscript{18,19}, and the use of surfactants\textsuperscript{20,21}. Nevertheless, these methods did not efficiently prevent the Si-Ge intermixing. For example, the relatively high temperature of ~700 °C required for selectivity in the “pit-patterned” selective growth results in alloyed SiGe islands with high Si content (>50%)\textsuperscript{11,12,22}.

On the contrary, selective epitaxy by using SiO\textsubscript{2}-covered, patterned Si wafers allows one to obtain Ge nano-islands that are i) highly ordered in predefined seed areas, ii) size homogenous, and iii) suffer limited SiGe intermixing due to the small amount of Si at the surface. In prior studies, we have shown the successful selective chemical vapor deposition (CVD) growth of plastically relaxed Ge nanoheteroepitaxial islands on Si nano-pillar\textsuperscript{23–25} or nano-mesa\textsuperscript{26,27}. Fully-coherent Ge islands have also been demonstrated on Si/SiO\textsubscript{2} Si nano-mesas, upon exploiting a few-nm thick SiGe buffer layer\textsuperscript{28}. However, all these CVD-based approaches resulted in strongly pattern-dependent growth dynamics, inherently linked to the loading effect mechanisms of atomic species diffusing on the silica growth-mask.

Here, we report on molecular beam epitaxy (MBE) selective growth of dislocation-free Ge nano-islands on Si nano-tip wafers, fabricated by using a CMOS compatible technology. By means of transmission electron microscopy (TEM) and synchrotron radiation grazing-incidence X-ray diffraction (SR-GIXRD), we experimentally demonstrate the achievement of fully coherent Ge islands, featuring very low Si interdiffusion, very high selectivity in predefined nano-seed areas, narrow shape and size distribution. Supported by theoretical modeling, we could also define the process conditions leading to a pattern-independent selective growth.

**Results and Discussion**

**Si-tip wafers characterization.** Figure 1(a) illustrates the structure of Si-tip patterned wafers (more fabrication details can be found in Supplementary Information) while Fig. 1(b) shows a plane-view scanning electron microscopy (SEM) image obtained from a Si-tip substrate after chemical preparation, which reveals a perfectly periodic quadratic array of Si crystalline seeds (white dots) embedded in the SiO\textsubscript{2} matrix (darker region) with a tip-tip distance of ~1.4 μm and the edge of the tip-square aligned along the Si [100] direction. In Fig. 1(c) we show the relative cross-sectional TEM image along the Si [110] direction. It displays that the diameters of the base and the opening of Si tips are ~300 nm and ~40 nm, respectively. The height of the Si tips is 800 nm. The SiO\textsubscript{2} surface is atomically flat considering that the root mean square (RMS) roughness of the same wafer (after immersing in 0.5 wt.% HF for 10s) extracted from a 5×5 μm\textsuperscript{2} atomic force microscopy (AFM) image is only ~3 Å (see Figure S3 in Supplementary Information).

**Ge on Si-tip substrates: nucleation theory.** In order to realize selective epitaxy, one must identify a “process window” in which the nucleation occurs only on the patterned Si nano-tip surface of the wafer. For instance, in the case of Ge selective CVD growth on pit-patterned Si substrates\textsuperscript{29}, the selective growth is carried out at relatively high temperatures (>600 °C) and low deposition rates (0.1 ML/s or lower) in order to allow enough time for the Ge adatoms to diffuse to pit positions and nucleate there. In the Ge selective growth on Si-tip substrates, one has to find appropriate parameters to deposit Ge only on exposed Si crystalline seeds while avoiding random nucleation on the SiO\textsubscript{2} surface. The desorption energy of Ge on SiO\textsubscript{2} surface is ~0.44 eV\textsuperscript{30} while the sublimation energy of Ge from Si (001) has a much higher value of 4.25 eV\textsuperscript{31}. Therefore, there exists a sizeable energy window allowing for the selective nucleation of Ge on Si over that on SiO\textsubscript{2}. To better investigate this point, in the following we discuss the selective nucleation phenomenon relying on an atomistic modeling of the process.

Let’s first discuss the nucleation of Ge on SiO\textsubscript{2}. According to the atomistic model of nucleation for heterogeneous growth, Ge atoms impinging on a SiO\textsubscript{2} surface at a rate E either “stick” and diffuse on the surface or are re-desorbed. The diffusion of individual adatoms proceeds until they i) bind to other atoms to form clusters,
which are stable when the number of adatoms in the cluster reaches a critical value; i) 2) are absorbed by existing stable clusters having a density \( n_i \); 3) diffuse into the substrate; or 4) are captured by defect sites of the substrate surface. Since the SiO\(_2\) surface layer of our samples can be considered as an "inert" and ideal substrate (see Supplementary Information), the processes 3) and 4) are not taken into account in the following. The key to the surface dynamics, i.e. the main features impacting the selectivity, is thus the value of \( n_i \), which is dependent on the rate \( R \), the growth temperature \( T \), the adatom desorption energy \( E_{\text{des}} \), the diffusion energy \( E_{\text{diff}} \) and the binding energy \( E_i \). For systems with different growth modes and different condensation regimes, \( n_i \) can be expressed in different forms. The growth mode of the Ge/SiO\(_2\) system is expected to generate three-dimensional growth of Ge on Si-tip substrates is observed.

\[ \gamma_{\text{Ge}} + \gamma_{\text{Ge/SiO}_2} > \gamma_{\text{SiO}_2} \]  

(1)

where \( \gamma_{\text{Ge}} \) (5 eV/nm\(^2\)) and \( \gamma_{\text{SiO}_2} \) (4 eV/nm\(^2\)) are the Ge and SiO\(_2\) surface energy while \( \gamma_{\text{Ge/SiO}_2} \) (10 eV/nm\(^2\)) is the Ge/SiO\(_2\) interface energy, respectively\(^{32}\). The conditions required to achieve the adatom supersaturation needed for a stable growth, strongly depend on the property of the interface.

Three scenarios are usually observed. In the extreme incomplete regime (low \( E_{\text{des}} \) and low \( E_{\text{diff}} \)), the adatom diffusion length\(^{33,34}\)

\[ L = \left[ \frac{1}{2 \sqrt{N_0}} \exp \left( \frac{E_{\text{des}} - E_{\text{diff}}}{2kT} \right) \right] \]  

(2)

where \( N_0 \) is the number of possible adsorption sites on the substrate and \( k \) is Boltzmann’s constant (\( 1.3806 \times 10^{-23} \) J/K), is much shorter than the distance between stable clusters. As a consequence, the majority of adatoms desorb from the substrate surface. In the initial incomplete regime (high \( E_{\text{des}} \) and low \( E_{\text{diff}} \)), we have \( L < D \) but the desorption time

\[ \tau_n = \nu^{-1} \exp \left( \frac{E_{\text{des}}}{kT} \right) \]  

(4)

is relatively long (\( \nu \) is the characteristic surface vibration frequency in the range\(^{35} \) of \( \sim 10^{11} \) to \( 10^{13} \) s\(^{-1}\)). Therefore, diffusing adsorbates, although partly staying on the surface, do not reach stable clusters either. On the contrary, in the complete condensation regime (extremely high \( E_{\text{des}} \) and/or extremely low \( E_{\text{diff}} \)), the adsorbates can diffuse until they are captured by islands. Leonhardt et al.\(^{35}\) reported that Ge/SiO\(_2\) falls in the "extremely incomplete" regime even at deposition temperatures as low as 400 °C.

In this limit, the concentration of stable clusters \( n_x \) reads\(^{33,34}\):

\[ n_x \sim \eta(Z) \left( \frac{R}{N_0 \nu^p} \right)^p \exp \left( \frac{E_n}{kT} \right) \]  

(5)

where \( \eta(Z) \) is coverage-dependent parameter varying from \( \sim 0.1 \) to 0.8 quasi-linearly when the island areal coverage \( Z \) increases from \( 10^{-4} \) to 0.4 (for extreme incomplete condensation and 3D island growth\(^{33}\)). Here \( \eta(Z) = 0.1 \) was used but we note that with higher coverages, \( \eta(Z) \) and thus \( n_x \) is slightly greater); \( N_0 \) is the number of possible adsorption sites on the substrate (\( \sim 10^{15} \)). The activation energy \( E_n \) (for the decay of nucleus) is related to \( E_{\text{des}}, E_{\text{diff}} \) and \( E_i \). In case of the extreme incomplete condensation regime we have\(^{33}\)

\[ p = 2i/3 \]  

(6)

and

\[ E_n = \frac{2}{3} \left[ E_i + (i + 1)E_{\text{des}} - E_{\text{diff}} \right] \]  

(7)

Based on a spectroscopy study\(^{30}\), Li et al. found for the Ge/SiO\(_2\) system at \( T > 500 \) °C, \( i = 3, E_{i=3} = 3.7 \) eV, \( E_{\text{des}} = 0.44 \) eV, and \( E_{\text{diff}} = 0.24 \) eV (thus resulting in \( E_n = 3.48 \) eV). Using these values, we have plotted in Fig. 2(a) the concentration of stable Ge clusters on SiO\(_2\) surfaces, \( n_x(R, T) \), as a function of the deposition rate \( R \) and temperature \( T \). We observe that, the stable cluster density on SiO\(_2\) decreases for increasing \( T \). On the contrary, as we pointed out above, the desorption energy \( E_{\text{des}} \) of Ge from Si is extremely high\(^{33}\) (about 10 times higher than \( E_{\text{des}} \) of Ge from SiO\(_2\) surface) and previous studies reported that high quality heteroepitaxial grown Ge could be realized on Si even at \( \sim 800 \) °C\(^{36,30}\). That is to say, Ge growth on Si falls in the complete condensation regime in the considered temperature range\(^{33}\). As a consequence, at sufficiently high temperatures where the nucleation of Ge on SiO\(_2\) surface can be almost completely avoided, Ge adatoms will only be adsorbed on top of the Si surface. In other words, at high enough temperatures, we have "super-saturated" conditions for the growth of Ge over Si whereas "under-saturated" conditions for that of Ge over SiO\(_2\). Thus the growth commences only on Si and the selective growth of Ge on Si-tip substrates is observed.
Figure 2(b) shows the dependence of the diffusion length and re-evaporation time of Ge single adatoms over the SiO₂ surface as a function of substrate temperature. Since the Ge/SiO₂ system has low $E_{\text{des}}$ and $E_{\text{diff}}$ (extreme incomplete regime), it features extremely short diffusion length and re-evaporation time. For example, at $T = 750 \, ^\circ\text{C}$, the Ge adatom diffusion length is $\sim 0.5$ nm only, while the corresponding re-evaporation time is $\sim 1 \times 10^{-9}$ s.

We have thus shown that the high temperature and low deposition rate are predicted to favor the selective Ge growth on Si nano-tips. In this section, we will discuss the experimental growth results and compare them with the theoretical prediction.

Selective epitaxy of Ge on Si-tip wafers. We firstly discuss the morphology of a set of samples grown at different temperatures using $R = 21$ ML/min for the same time duration of 20 min. As displayed in Fig. 3(a), at $T = 500 \, ^\circ\text{C}$ the growth results in a complete 2D Ge layer covering the entire substrate, confirmed also by in-situ X-ray photoemission spectroscopy (XPS) measurements (see Figure S4 in Supplementary Information). Upon increasing the process temperature, we observe the formation of Ge islands, at first randomly distributed on SiO₂ ($T = 650 \, ^\circ\text{C}$, Fig. 3(b)) and then clustering around and on Si tip positions ($T = 750 \, ^\circ\text{C}$, Fig. 3(c)). At $T = 850 \, ^\circ\text{C}$ (Fig. 3(d)), selective growth is achieved with almost no observed Ge island on the detected SiO₂ surface area.

The samples grown at a lower deposition rate of 8 ML/min (Fig. 3(e,f)) show a similar temperature dependence as samples grown with 21 ML/min. In Fig. 3(f) for the sample grown at $T = 850 \, ^\circ\text{C}$ with 8 ML/min, one finds no Ge island on the examined SiO₂ surface area. If we now fix the growth temperature, say at $T = 750 \, ^\circ\text{C}$, and vary
the deposition rate, it can be observed in Fig. 3(g,c,e) that lower deposition rate leads to lower Ge island density on the SiO₂ surface. These results are qualitatively in good agreement with the theoretical prediction.

In order to quantitatively compare the experimental results with the nucleation theory presented in the Section 3.2, we report in Table 1 the calculated nₓ and experimental results of Ge island density. It can be seen that the experimental values agree very well with the corresponding theoretical predictions. The slight discrepancy can be attributed to three main factors: 1) the coalescence of clusters. nₓ in fact gives the maximum of the Ge island density on SiO₂ under a certain growth condition and as a function of growth time, the Ge island density first reaches nx, then decreases due to island calescence; (2) the variation of the coverage (Z)-dependent factor η(Z): η(Z) = 0.1 was used for all calculations which is in fact more precise for lower coverages (e.g. Z ~ 10⁻⁴ for 850 °C samples), while for higher coverages (e.g. at T ≤ 750°C) it should have a larger value (but less than one order of magnitude) and thus the corresponding nx should be slightly higher; and (3) particularly for 850 °C samples where very few Ge islands on the SiO₂ surface can be detected, there is a relative large error bar of the experimentally measured Ge island density. As an example, for the sample grown at 850 °C and 8 ML/min shown in Fig. 3(f), while no Ge island is found on the examined SiO₂ area, a single Ge island in the same area size would correspond to a density of ~7 × 10⁵ cm⁻².

The selectivity of Ge islands grown on Si-tip wafers with different patterns was also examined to further elucidate the growth mechanism. Figure 4(a–c) shows samples grown under the same conditions as the sample shown in Fig. 2(e), i.e. at 750 °C with 8 ML/min. The edges of the squares with tips at the corners are oriented along Si [110] and the tip-tip distances are (a) 0.5 μm, (b) 0.8 μm and (c) 2.0 μm, respectively. The scale bar is 2 μm for all images. The arrows mark a very few Ge islands observed on the SiO₂ surface and the densities are almost the same for all patterns.

Table 1. Theoretical Ge stable cluster density nₓ (Th.) and experimental Ge island density (Exp.) on SiO₂ grown under different growth conditions (unit: cm⁻²). Considering the variation of η(Z) and the possible coalescence of the islands during growth, the experimental values are in good agreement with the calculated nₓ values.

Table 2. Ge islands density on SiO₂ of Si-tip substrate with different tip square edge orientations and tip-tip distances. Growth conditions are identical with 8 ML/min at ~750 °C.
Defects in heteroepitaxial Ge islands on Si mainly include interface defects, i.e. misfit dislocations (MD) and bulk defects, including point defects, threading dislocations (TD), stacking faults (SFs) and micro-twins (μ-twins). It is known that selective epitaxy on patterned wafers is an effective approach to reduce dislocations in lattice mismatch system. Furthermore, because the lateral size of the substrate can be shrunk to nanometer size thanks to modern advanced lithography, the elastic relaxation of the growing material is significantly enhanced and strain partitioning between the epitaxial structures and the substrate could occur (so-called compliance). Therefore, the epitaxial structures can possibly undergo complete elastic relaxation with an infinite critical thickness for plastic relaxation. According to a theoretical study of the Ge/Si system, Ge islands can be fully coherent on Si (001) when the lateral size of the Si seeds is below 50 nm, a limit greater than the tip opening diameter presented in this study (~40 nm). In Fig. 5 we show TEM measurements performed on an individual typical island belonging to the sample grown at $T_0 = 750^\circ C$ using $R_0 = 8$ ML/min (see Fig. 4(a)). In the cross-sectional high resolution TEM (HRTEM) image of Fig. 5(a) we can observe the high crystalline quality of the Ge lattice showing no extended defects such as TDs or SFs/μ-twins. The nano island features nearly a hemispheric shape with tiny multi-facets. An energy-dispersive X-ray spectroscopy (EDX) line profile analysis from top to bottom along the orange solid line was performed in order to examine the island composition (Fig. 5(b)). Surprisingly, the atomic concentration of Si in the core of the Ge island ($s = [0−57]$ nm, in which “s” represents the position of the line profile) is ~6% only. Thus, the island consists of almost pure Ge even when grown at 750°C, with limited intermixing occurring in the pedestal region only. This demonstrates an important advantage of Si-tip wafer approaches compared to other substrates, i.e. geometric intermixing hindrance. As a matter of fact, the pathway of Si inter-diffusion from the substrate is limited by the Si tips with nanometer size on surface: Ge islands on planar or pit-patterned Si substrates become Si rich (with Si concentration >50%) when the temperature is above 650°C. We notice that an oxygen-related signal (~6%) was also detected in the Ge island bulk region, which is due to the surface oxidation of Ge in the TEM lamella, leading to the experimentally measured Ge concentration (~88%). Because the oxygen species only locate on the Ge island surface, the real Ge concentration in the island bulk is as high as 93%. Blue dashed rectangles mark the interface region and its detail is shown in (c) by an enlarged HRTEM image and (d) a two dimensional (2D) EDX element map with Si (red), O (green) and Ge (blue) which shows the deformed top of the Si tip with an arc shape marked by the white dashed line. (e) 2D strain out-of-plane (left) and in-plane (right) maps of the Ge/Si tip measured with respect to the Ge lattice by TEM-NBD analysis. The relaxed Ge has $\varepsilon = 0%$ and Si has $\varepsilon = -4.0%$ (marked by black dashed lines); (f) strain line profile from top to bottom of the regions marked by rectangles in (e); the solid and empty circles correspond to out-of-plane and in-plane strain, respectively. The blue dotted rectangle marks again the similar interface region as in (a). Insets show relaxed Ge (left) and 1P tensely strained Si (right).

Dislocation-free Ge crystal: individual and global structures. Defects in heteroepitaxial Ge islands on Si mainly include interface defects, i.e. misfit dislocations (MD) and bulk defects, including point defects, threading dislocations (TD), stacking faults (SFs) and micro-twins (μ-twins). It is known that selective epitaxy on patterned wafers is an effective approach to reduce dislocations in lattice mismatch system. Furthermore, because the lateral size of the substrate can be shrunk to nanometer size thanks to modern advanced lithography, the elastic relaxation of the growing material is significantly enhanced and strain partitioning between the epitaxial structures and the substrate could occur (so-called compliance). Therefore, the epitaxial structures can possibly undergo complete elastic relaxation with an infinite critical thickness for plastic relaxation. According to a theoretical study of the Ge/Si system, Ge islands can be fully coherent on Si (001) when the lateral size of the Si seeds is below 50 nm, a limit greater than the tip opening diameter presented in this study (~40 nm). In Fig. 5 we show TEM measurements performed on an individual typical island belonging to the sample grown at $T_0 = 750^\circ C$ using $R_0 = 8$ ML/min (see Fig. 4(a)). In the cross-sectional high resolution TEM (HRTEM) image of Fig. 5(a) we can observe the high crystalline quality of the Ge lattice showing no extended defects such as TDs or SFs/μ-twins. The nano island features nearly a hemispheric shape with tiny multi-facets. An energy-dispersive X-ray spectroscopy (EDX) line profile analysis from top to bottom along the orange solid line was performed in order to examine the island composition (Fig. 5(b)). Surprisingly, the atomic concentration of Si in the core of the Ge island ($s = [0−57]$ nm, in which “s” represents the position of the line profile) is ~6% only. Thus, the island consists of almost pure Ge even when grown at 750°C, with limited intermixing occurring in the pedestal region only. This demonstrates an important advantage of Si-tip wafer approaches compared to other substrates, i.e. geometric intermixing hindrance. As a matter of fact, the pathway of Si inter-diffusion from the substrate is limited by the Si tips with nanometer size on surface: Ge islands on planar or pit-patterned Si substrates become Si rich (with Si concentration >50%) when the temperature is above 650°C. We notice that an oxygen-related signal (~6%) was also detected in the Ge island bulk region, which is due to the surface oxidation of Ge in the TEM lamella, leading to the experimentally measured Ge concentration (~88%). Because the oxygen species are only located on the Ge island surface, the real Ge concentration in the island bulk is as high as 93%. At the interface region ($s = [57−72]$ nm, blue dotted rectangle), the Ge concentration decreases rapidly and reaches zero beyond ~80 nm.
Particular attention has been paid to the lattice quality in the interface region (marked by the blue dotted rectangle) in order to further examine the possible presence of MDs at the interface due to plastic relaxation of the nanometric islands. As we can see in the detailed HRTEM image shown in Fig. 5(c), no MD was observed, indicating that the strain induced by Ge-Si mismatch is entirely released through elastic relaxation, with the slightly intermixed, thin interface region acting as a graded buffer layer hindering plastic relaxation. Moreover, according to a statistical analysis carried out on more than 50 islands, the majority shows also no other defects, e.g. SFs or μ-twins. Only in a few islands, some SFs propagating from the shoulder of the Si tip were observed, which were probably formed owing to defects on the surrounding SiO2 “wall” surface (Supplementary Information, Figure S7). A color contrast can be noticed at the center of the Ge/Si interface (Fig. 5(c)), that can be attributed to a slight lattice distortion in this region. A detailed EDX element map was therefore performed and is shown in Fig. 5(d), which shows more interface details and helps to clarify the source of this lattice distortion (Si, O and Ge are represented by red, green and blue, respectively). We can observe that the top of the Si tip has a dome-like shape, (see white dashed line). This bowed geometry is probably due to the reaction of the Si tip atoms with the surrounding SiO2 at high temperatures. The thermally activated Si + SiO2 → 2SiO (g) reaction promotes the consumption of the tip apex through the formation of volatile SiO4. This phenomenon happens during the in-situ high temperature (>750°C) pre-baking required to remove the native oxide prior to the Ge deposition (Supplementary Information).

During the epitaxial growth, Ge covers the Si-tip dome thus leading to the lattice distortion observed in Fig. 5(c). It is noted that a similar effect was observed for fully coherent Ge/SiGe/Si nanostructures on SOI wafers. The dome shape of the Si tip covered by the Ge island, results in the projected atomic concentration at the interface (detected in Fig. 5(b)). In other words, the Si-Ge intermixing at the interface should be even weaker or the intermixing thickness should be even thinner. This highlights once more the geometric intermixing hindrance effect of the Si nano-tip wafer approach.

These results indicate that fully coherent, dislocation-free Ge crystals can be directly grown on Si thanks to three important mechanisms: 1) largely reduced Si substrate area with nanometer size (~40 nm in this case) yielding entirely elastic relaxation by 3D relaxation; 2) self-formed Si-Ge intermixing layer at the pedestal region of Ge islands and 3) possible compliance effect, i.e. strain partitioning between the Ge islands and the Si tip substrate.

In order to shed more light on the relevance of these three mechanisms we performed a detailed strain analysis of the island. In Fig. 5(e) we display the strain maps for both out-of-plane (OP, left panel) and in-plane (IP, right panel) strain. The OP and IP lattice parameters maps were extracted from nano-electron beam diffractions (NBD) (~5 nm diameter) taking into account (002) and (220) lattice points, respectively. Then strain maps were realized using a bulk Ge lattice constant of aGe = 5.658 Å as the reference. The relaxed Si0.07Ge0.93 lattice (corresponding to the island stoichiometry as detected by EDX) and Si have strain values of ε = −0.2% (orange) and ε = −4.0% (blue), respectively. It can be seen that in the core of the Ge island, both OP and IP maps show a similar value of strain, evidencing a completely relaxed cubic lattice. Some inhomogeneity in the strain maps of the Ge island, particularly in the IP map can be observed which is related to the technical difficulty in realizing perfect strain maps by using the TEM-NBD method. TEM samples usually are not ideal with exactly one orientation and the nanostructures are usually bent after the preparation of TEM samples, which lead to slight deviations of the diffraction spot positions thus the inhomogeneity in the strain maps. The green regions (with ε < −3%) observed at the interface in both OP and IP maps are transition regions. However, compared to the OP map, the green region in IP map is thicker and extends more to Si tip part. This can be better appreciated in Fig. 5(f), where we show a line profile of the region marked by rectangles in Fig. 5(e). Solid circles and empty circles represent OP and IP strain, respectively. The ε values for pure Ge and Si bulk are 0% and −4.0% (dashed lines in Fig. 5(f)), respectively. On the Ge surface and in the core of the Ge island, both OP and IP strains are very close to the expected value for entirely relaxed material (as shown in the inset at left part of Fig. 5(f)). They both decrease sharply at the interface (marked by the blue rectangle, similar region as in Fig. 5(a)) and reach their minima beyond the position of s ~ 70 nm. This behavior is strongly correlated to the similar rapid decrease of the Ge concentration in the same region. However, it can be observed that the OP strain decreases at a slightly slower rate than the OP counterpart, IP being larger than OP in the Si tip region, where the lattice is thus in-plane tensile strained. On one hand, this can be attributed to the presence of an intrinsic strain in the Si tips induced by the residual stress in tetraethylorthosilicate (TEOS)-CVD SiO2 surrounding the tip. On the other hand, it possibly indicates a compliant strain partitioning in the vicinal region of the Ge/Si interface between the Ge nanocrystal and the Si nano-tip.

In order to better understand the strain relaxation of the Ge/Si-tip system, with particular focus on the possible compliance effect, SR-GIXRD in-plane measurements were performed in the vicinity of Ge and Si (220) Bragg peaks, providing information on the collective behavior of all Ge islands on Si tips in a spatially-averaged way. Figure 6(a) shows the symmetric H = K measurements (derived from the in-plane 2θ–ω scan) obtained at different grazing incident angles (αi). For decreasing αi, the X-ray detection depth decreases and the technique probes sample regions closer to the surface, as shown in the inset illustration. When αi = 0.20° (black curve), the X-ray penetrates deep into the Si-tip wafer; therefore a strong and sharp Si reflection from the Si substrate can be observed at H = K = 2.000 (black dashed line). Another strong but less intense peak appears at H = K = 1.994 (black triangle, fitted by a black peak), which can be attributed to the intrinsic IP tensile strain in the Si tips, as already observed in Fig. 5(f). The corresponding strain value is ε = +0.2%, which agrees very well with the intrinsic strain value measured in the Si-pillar patterned wafers which were fabricated similarly by TEOS-CVD SiO2. The peak at H = K = 1.928 is related to Ge islands and perfectly matches the H = K value for a fully relaxed Si0.07Ge0.93 alloy. This indicates that no residual strain remains in the Ge island cores, in agreement with the strain analysis by TEM-NBD method (Fig. 5(e,f)). The continuous background existing between the Ge islands and Si-tip features the graded SiGe layer at the heterointerface.
Slightly decreasing the penetration of the X-ray beam ($\alpha_i = 0.15^\circ$) (red curve) leads to a significant decrease of the Si (220) peak intensity as well as that of the $H = K = 1.994$ Si peak, while the Ge reflection intensity increases. Another slight reduction (0.05°) of $\alpha_i$ to 0.10° induces an even more dramatic decrease (more than two orders of magnitudes) of Si (220) peak intensity compared to $\alpha_i = 0.15^\circ$ case while the Ge (220) intensity again increases. More importantly, the Si (220) intensity is smaller than that of Ge (220), indicating that the $\alpha_i = 0.10^\circ$ curve (blue) reveals mainly the information from the Ge/Si tip interface and Ge islands. It can be clearly seen in the $\alpha_i = 0.10^\circ$ curve, that the main Si (220) peak slightly shifts to a lower H-K value ($H = K = 1.999$) and, in addition to the intrinsic strained Si tip peak (black arrow), a second broad Si shoulder appears at $H = K = 1.987$ (marked by a blue star and fitted by a blue peak), corresponding to a tensile strain of $\varepsilon = +0.6\%$). For the Ge (220) peak, no shift is observed. When $\alpha_i = 0.05^\circ$, a similar behavior can be observed with increasing Ge signal but weaker Si related peaks.

Let us now consider the possible mechanism leading to the detected XRD curves. In selective epitaxy, besides 3D relaxation, the accumulated strain during the growth of Ge on Si (4.2% lattice mismatch) is released by either SiGe intermixing layers reported in literature exhibit a smooth, continuous grading increase from Si to Ge islands, as shown by the inset scheme. Dashed lines at $H = K = 2.000$ (black) and $H = K = 1.919$ (blue) represent bulk Si (220) and Ge (220) peak positions, respectively. Black triangles mark the peak at $H = K = 1.994$ (fitted by a black peak) related to the intrinsic strain in Si tips induced by TEOS-CVD SiO$_2$ and blue star (**) marks an additional peak at $H = K = 1.987$ (fitted by a blue peak) arising only for surface sensitive curves revealing probably a compliance effect (+0.6% IP tensile strain in pure Si). (b) $\omega$ scan of Ge (220) peak at $H = K = 1.928$ of $\alpha_i = 0.10^\circ$ curve (blue) (FWHM = 0.236°).

**Conclusion**

In conclusion, dislocation-free Ge crystalline nano-islands have been obtained via selective MBE on Si nano-tip wafers. The nucleation behavior of Ge adatoms on the SiO$_2$ surface was discussed in detail based on an atomistic model of nucleation theory. Theoretical calculations predict selective growth of Ge on Si nano-tip wafers occurs when the growth conditions are close to thermodynamic equilibrium, i.e. at high temperatures and low deposition rates, where arriving Ge adatoms only nucleate on the Si crystalline seed surface and all Ge adatoms on SiO$_2$ re-evaporate. The theoretical prediction was subsequently well confirmed by experimental results and such a mechanism leads to pattern-independent selective growth without the well-known “loading effect” for CVD growth. Thanks to geometric intermixing hindrance effects of Si nano-tip wafers, high growth temperatures
(>750°C) induce very weak Si inter-diffusion in Ge islands (93% Ge). Compared to Ge islands on planar Si (001) substrates, a self-formed ultra-thin SiGe intermixing layer at the pedestal region of Ge islands and probable strain partitioning in the top of the Si tips were observed. Both of them facilitate the achievement of fully coherent Ge islands on Si nano-tip wafers. The high temperatures needed to achieve the selective growth are also beneficial to the crystal quality of Ge islands. These results provide more observations and deeper understanding of emerging phenomena in the important area of nano-scale heteroepitaxy<sup>49–51</sup>. Our method for dislocation-free Ge islands on Si wafers opens a novel pathway to realize Ge on Si suitable for optical applications. Furthermore, such an approach is highly relevant to Si-based applications based on the heterogeneous integration of other alternative compounds on Si (e.g. III–V, II–VI).

**Methods**

**Si-tip wafers fabrication.** Nano-tip patterned Si (001) wafers with a wafer size of 8 inches were fabricated using 0.25 μm complementary metal-oxide-semiconductor (CMOS) technology. A CVD SiO<sub>2</sub> layer using tetraethylorthosilicate (TEOS) gas source was deposited to cover the ultra-sharp Si tips. The opening area of crystal-line Si seeds can be precisely controlled by a chemical mechanical-polishing (CMP) process and wafers with Si tips of opening of ~40 nm in diameter were employed. More fabrication details of Si-tip wafers can be found in Supplementary Information (Figure S1) and ref. 52.

**Selective epitaxy of Ge islands.** Ge growth was carried out by using molecular beam epitaxy (MBE) in a DCA chamber. Prior to the Ge growth, Si-tip wafers were chemically prepared by HF dipping and pre-baked to obtain a clean Si seeds surface without native SiO<sub>2</sub> (see details in Supplementary Information). It is noted here that the preparation method was carefully optimized to “open” the crystalline Si seeds and to avoid over-etching of SiO<sub>2</sub> or severe deformation of Si tips (see Supplementary Information). The Ge growth was performed at temperatures ranging from 500 °C to 850 °C and deposition rates from 8 monolayer per minute (ML/min) to 52 ML/min. The Ge deposition rate was calibrated by direct growth of Ge layers on Si planar substrates.

**X-ray photoemission spectroscopy.** After Ge islands growth, the physical-chemical properties of the samples were in-situ examined without breaking the vacuum using a SPECS X-ray photoemission spectroscopy (XPS) system with Al K-α radiation at 1487 eV. The Fermi level was calibrated with respect to a gold sample.

**Scanning electron microscopy.** The samples were ex-situ characterized by scanning electron microscopy (SEM) with a Zeiss Nvision equipment, in which the electron beam energy was selected as 1.5 keV in order to avoid the charge effect stemming from insulating SiO<sub>2</sub> material.

**Transmission electron microscopy.** The crystallinity, interface, strain status, and the Si-Ge intermixing of individual Ge island were studied by a FEI Osiris (operated at 200 kV) transmission electron microscopy (TEM) system equipped with components for scanning TEM (STEM) and energy-dispersive X-ray spectroscopy (EDX) measurements. High resolution TEM (HRTEM) measurements were also performed using the FEI TITAN 80–300 Berlin Holography specially operated at 300 kV.

**X-ray diffraction.** SR-GIXRD measurements were performed at BM32 beamline of the European Synchrotron Radiation Facility (ESRF) with an X-ray wavelength of 1.127 Å.

**Theoretical calculation of nucleation.** The condensation behavior of Ge adatoms on the SiO<sub>2</sub> surface was theoretically evaluated by using an atomistic model of the nucleation phenomenon. The stable nuclei density on SiO<sub>2</sub> was calculated as a function of growth temperature and deposition rate (i.e. impingement rate of arriving adatoms) while the diffusion length and re-evaporation time of Ge adatoms on SiO<sub>2</sub> at different temperatures were predicted.

**References**

1. Goley, P. & Hudait, M. Germanium Based Field-Effect Transistors: Challenges and Opportunities. *Materials* 7, 2301 (2014).
2. Ionescu, A. M. & Riel, H. Tunnel Field-Effect Transistors as Energy-Efficient Electronic Switches. *Nature* 479, 329–337 (2011).
3. Yong, Z. et al. Enhanced 1524-nm Emission From Ge Quantum Dots in a Modified Photonic Crystal L3 cavity. *IEEE Photon. J.* 5, 4500607 (2013).
4. Zeng, C. et al. Single Germanium Quantum Dot Embedded in Photonic Crystal Nanocavity for Light Emitter on Silicon Chip. *Opt. Express* 23, 22250–22261 (2015).
5. Cheng, S.-L. et al. Room Temperature 1.6μm Electroluminescence From Ge Light Emitting Diode on Si Substrate. *Opt. Express* 17, 10019–10024 (2009).
6. Wang, J. & Lee, S. Ge Photodetectors for Si-Based Optoelectronic Integration. *Sensors* 11, 696–718 (2011).
7. Capellini, G., De Seta, M., Zaimis, P., Kozlowski, G. & Schroeder, T. High Temperature X-Ray Diffraction Measurements on Ge/Si(001) Heterostructures: A Study on the Residual Tensile Strain. *J. Appl. Phys.* 111, 073518 (2012).
8. Ayers, J. E. Heteroepitaxy of Semiconductors: Theory, Growth and Characterization (Taylor & Francis Group, 2007).
9. Zeng, C., et al. Site-Controlled and Size-Homogeneous Ge Islands on Prepatterned Si(001). *Phys. Rev. Lett.* 64, 1943–1946 (1990).
10. Capellini, G., De Seta, M. & Evangelisti, F. SiGe Intermixing in Ge/Si(100) Islands. *Appl. Phys. Lett.* 78, 303–305 (2001).
11. Schull, T. U. et al. Enhanced Relaxation and Intermixing in Ge Islands Grown on Pt-Patterned Si(001) Substrates. *Phys. Rev. Lett.* 102, 025502 (2009).
12. Zhang, J. et al. Strain Engineering in Si via Closely Stacked, Site-Controlled SiGe Islands. *Appl. Phys. Lett.* 96, 193101 (2010).
13. Zeng, C., & Bauer, G. Site-Controlled and Size-Homogeneous Ge Islands on Prepatterned Si (001) Substrates. *Appl. Phys. Lett.* 84, 1922–1924 (2004).
14. Wu, H.-C. et al. One-Step Ge/Si Epitaxial Growth. *ACS Appl. Mater. Interfaces* 3, 2398–2401 (2011).
15. Currie, M. T., Samavedam, S. B., Langlo, T. A., Leitz, C. W. & Fitzgerald, E. A. Controlling Threading Dislocation Densities in Ge on Si Using Graded SiGe Layers and Chemical-Mechanical Polishing. *Appl. Phys. Lett.* 72, 1718–1720 (1998).
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Author Contributions
G.N., G.C., Y.-H.X. and T.S. planned this work. G.N. prepared the manuscript. G.N., G.C. and T.S. performed the theoretical calculation. G.N., T.N., M.A.S, P.Z., J.K., H.-M.K., O.S., M.L., Y.-H.X. and H.K. carried out the experiments. All authors contributed in results analysis and discussion of the manuscript during the preparation. All authors have given approval to the final version of the manuscript.

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