Heat Dissipation Measurement of Electronic Component on PCB by Means of External Heater and Heat Flux Sensor

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Abstract
A new heat dissipation measurement method based on an external heater and a heat flux sensor was proposed. The information of boundary conditions and thermal properties of materials, such as the thermophysical properties of printed circuit board (PCB), in the heat transfer path is not necessary for heat dissipation measurement in the proposed method. Numerical and experimental study was carried out on testing board with single component to estimate the feasibility of this method, in addition, the effects of the thermal resistance between heater and die/chip on the relative error of the proposed measurement method are investigated. This method is proved to be able to predict the heat dissipation from a single electronic component accurately, and has high applicable for the single BGA component.

Keywords: Heat Dissipation, External Heater, Heat Flux Sensor, Relative Error, Thermal Resistance

1. Introduction
As power electronics system is developing towards integration, higher density and higher operation frequencies, the operation temperature of components will be higher under the same cooling condition. In the product development phase, it is of great importance to get accurate information about heat dissipation from the electronic components in order to ensure proper thermal management. Data sheet provided by manufactures may have a large relative error compared with the real heat dissipations of components.[1] When accurate heat information is not available, using the provided data sheet may lead to over-engineering or unknown risks in thermal design.[2]

The most straightforward heat dissipation measurement method is to measure the voltage drop across a component and the current flowing through the component by electric instruments.[3] Although the heat dissipation of a component can be easily calculated by voltage times current, there is often large error in the electrical method due to the probes delays, phase shifts between sampling channels, sampling errors, nonlinearities of converter, and digital instruments sensitive to noise.[3, 4]

Calorimetric measurement method [5–7] is considered to be an efficient method to measure heat dissipation from a standalone electric device such as magnetic component, capacitor, power converter and transformer, etc. The dissipated power is measured as heat by heat flux sensors when the test chamber reaches thermal equilibrium.[8] According to literature,[9] the operation power of the tested device ranges from less than 1 W to thousands of watts while most of the researched devices have high heat dissipation. For the devices with operation power larger than 100 W, the measurement error is less than 2%, while the measurement error will be larger when the power of device is less than 1 W.

For some components with Temperature Sensitive Parameter in the package, junction temperature measurement method is an alternative method.[10] \( T_J \) is measurable from the forward voltage [11] and used as an input parameter in the thermal model of the component. The accuracy of this method is affected by the component thermal model environmental dependency, material property variation among different components, and measurement error of \( T_J \) as the measurement is conducted under different environments by different users.[2] Since temperature distribution is induced by heat dissipation under a certain...
boundary condition, heat dissipation can be inversely calculated from the measured temperature distribution. Razzaq et al. [12] applied inverse thermal modelling approach to compute the iron losses in an induction motor. Shoubin et al. [13] conducted the inverse heat conduction problem in the two-dimensional unsteady heat conduction system. Shuangfeng et al. [14] developed a steady-state inverse heat conduction model and predicted the heat dissipation of a GaN chip mounted on a PCB. The accuracy of the inverse thermal calculation method depended on the number of temperature measurement points and measurement noise.[14] With many heat sources, and poorly defined material properties and cooling conditions, it is very hard to get good heat dissipation estimations with the inverse thermal calculation method.

In this research, an innovative heat dissipation measurement method of single component on PCB (printed circuit board) is proposed by utilizing an external heater and a heat flux sensor, and calculating the heat dissipation from a thermal standpoint. Thermal resistance between heater and heat source in the tested component is considered to be main reason that leads to the relative error of the proposed measurement method. This method can predict the heat dissipation of a MOSFET within 16% error and BGA within 0.1% error, respectively.

2. Methodology of the Testing Method

In the proposed measurement method, heater is equipped on the top surface of the component and a heat flux sensor is put under the bottom of the PCB. Figure 1 shows the schematic diagram of a testing apparatus to implement this method. A sensor is equipped under PCB to record the heat flow, while the block is served as a heat dissipation enhancer to transfer heat to the experimental base. Heater and component work independently, the heat dissipation of heater can be adjusted until the measured heat flow reaches the same value as the case when component works. Heat dissipation of heater which is easy to be measured, is then considered to be the same as the heat dissipation of component.

Thermal resistance network is exploited as an electrical analogy with conduction heat transfer problem of electronic component.[2, 15, 16] For a single component mounted on PCB, the thermal resistance network is shown in Fig. 2. Heater and die generate heat independently. If $Q_0$ is the total heat dissipation from heat sources (heater or heat sources in MOSFET/BGA), the heat flow measured by heat flux sensors are $Q_{heater}$ and $Q_{component}$, respectively, which can be written as:

$$Q_{component} = Q_0 \times \frac{R_{th1}R_{th2}}{R_{th3}R_{th4} + (R_{th1} + R_{th2})(R_{th3} + R_{th4})}$$  \hspace{1cm} (1)

$$Q_{heater} = Q_0 \times \frac{(R_{th1} - \Delta R_{th})[R_{th2} + \Delta R_{th}]}{R_{th3}R_{th4} + (R_{th1} + R_{th2})(R_{th3} + R_{th4})}$$  \hspace{1cm} (2)

Where $R_1$ to $R_4$ in Eq. (1) and Eq. (2) are the sums of thermal resistances of the corresponding parts in the thermal path, and $\Delta R$ represents the thermal resistance from heater and die/chip in the component. The thermal resistances used in Eq. (1) and Eq. (2) are defined in equations as follows:

$$R_{th1} = R_{th\_insulation}$$  \hspace{1cm} (3)

$$R_{th2} = R_{th\_mold\_down} + R_{th\_solder}$$  \hspace{1cm} (4)

$$R_{th3} = R_{th\_PCB1} + R_{th\_grease\_down} + R_{th\_sensor} + R_{th\_block}$$  \hspace{1cm} (5)

$$R_{th4} = R_{th\_PCB}$$  \hspace{1cm} (6)

$$\Delta R_{th} = R_{th\_mold\_up} + R_{th\_grease\_up}$$  \hspace{1cm} (7)

where the $R_{th}$ represents thermal resistance of the corre-
responding part in the heat transfer path. $R_{th, PCB}$ represents thermal resistance of PCB in the vertical direction, while $R_{th, PCB}$ describes the thermal resistance along PCB. As a result, $Q_{heater}$ and $Q_{component}$ can be expressed as,

$$Q_{component} = f(R_{th1}, R_{th2}, R_{th3}, R_{th4}, \Delta R_{th}) \times Q_0$$

(8)

$$Q_{heater} = g(R_{th1}, R_{th2}, R_{th3}, R_{th4}, \Delta R_{th}) \times Q_0$$

(9)

where $f(R)$ and $g(R)$ are only functions of thermal resistances of materials. Relative error is defined as the error between $Q_{heater}$ and $Q_{component}$, written as

$$\text{Relative error} = \frac{Q_{component} - Q_{heater}}{Q_{component}} = \frac{f(R_{th}) - g(R_{th})}{f(R_{th})} = 1 - \frac{1}{\eta}$$

(10)

If the thermal resistances of materials are independent to temperature, the relative error will be a constant value. The value $\eta$ in Eq. (10) of the relative error denotes the ratio of $Q_{component}$ and $Q_{heater}$, and the Taylor series of which can be written as

$$\eta = \frac{R_{th1}R_{th2} + R_{th1}\Delta R_{th}}{R_{th1}R_{th2} + R_{th2}\Delta R_{th}} = \left(1 + \frac{\Delta R_{th}}{R_{th2}}\right) \sum_{n=0}^{\infty} (-1)^n \frac{\Delta R_{th}^n}{R_{th2}^n}$$

(11)

It should be noted that as $R_1$ is the thermal resistance of insulation material, $R_1$ is much larger than $\Delta R$, $\lim_{R_{th}} (\Delta R / R_1)$ would be close to zero. Further, from Eq. (8) and Eq. (9), a certain percent of heat will flow through the sensor if the heat source is working at a certain power. It means that the percent of heat flowing through sensor can be measured by changing the power of heat sources, and relative error can be then calculated.

3. Experimental and Numerical Study on the Components Under Test

Experimental setup and components used in the experiment are shown in Fig. 3, a heater is put on the top surface of component, and a heat flux sensor is equipped on the bottom surface of PCB at the position of component. The testing board is mounted on a base by an aluminum block. The heater used in this research is a ceramic square heater with a size of 5 mm×5 mm and a thickness of 2 mm. A type of BGA and MOSFET which are widely used on the ECU are considered as the testing components, the sectional views of MOSFET and BGA are shown in Fig. 4 with detailed information of the component geometrics shown in Table 1. By changing the heat dissipation of the component and heater, respectively, the corresponding heat flows transferred through heat flux sensors of the two cases are recorded. Power supplies are used to drive the heater/components, and the power dissipation of heater/components can be calculated from voltage and current. Voltage at the wire connection is used to calculate the power dissipation to avoid the voltage loss in the wire. Considering the heat dissipation range of the components under operation, the set powers of components will not exceed the normal operating powers. Each experiment is repeated for 3 times and average values are taken to reduce the experimental error.

Numerical calculations are carried out by using a com-
4. Experimental and Numerical Results

The heat flow transferred through sensor is analyzed by changing the heat dissipation of each heat source experimentally and numerically. The relationship between heat flow through the sensor and the heat dissipation of the corresponding heat sources are shown in Fig. 7. It can be seen that the heat flows through sensors are proportional to the heat dissipations of the corresponding heat sources, which is consistent with the trend obtained from the Eq. (8) and Eq. (9) by thermal resistance network. Further, the relative errors calculated by Eq. (13) are 16.34% and 0.10% for heat dissipation measurement of MOSFET and BGA, respectively. The smaller thickness of mold between heater and chip in the BGA leads to a smaller thermal resistance ($\Delta R_{th}$) between heater and die/chip, which finally results in a smaller relative error in the case of BGA testing board.

Figure 8 shows the temperature distribution of the MOSFET model when heater and die works at 1 W, respectively. The difference in the geometric sizes of die and heater leads to a different temperature field in the two cases. It can be seen that when heater works, the temperature above the heater is relatively high compared with the temperature when MOSFET works. That also implies that more heat is dissipated to ambient from component top surface and less heat is dissipated through the sensor under PCB when heater works.
5. Error Analysis

In the proposed measurement method to measure the heat dissipation from components mounted on PCB, heat flow is obtained from heat flux sensor. In reality, every measurement has error due to interaction between sensor and system, instrument error, and operation error.[17] Error analysis identifies the uncertainties in measurements and further assesses their effects on the accuracy of final results.

The heat flux sensor used in this experiment has a sensitivity of 0.0121 mV/(W/m²) according to the calibration report from manufacturer. Data acquisition system has a ±50 × 10⁻⁶ V in accuracy. When assuming a confidence level of 95%, the overall variation is ±4.13W/m² at odds of 20/1, and sigma is 2.06 W/m². The film of heat flux sensor has a small thickness of 0.2 mm, so the same amount of heat entering sensor from the PCB side will pass through the sensor from the other side. Heat flux is assumed to across the area of heat flux sensor uniformly with a nominal area of 8 mm × 9 mm.

Besides the error from sensor and instrumentation discussed above, the relative error which is defined by Eq. (10) and Eq. (11) should be also affected by the thermal resistance $R_{th}$ between heater and die in the component. The thermal resistance $R_{th}$ would be affected by heater size and heater positions equipped on the component because of a change in the heat transfer distance or the equivalent cross-sectional area. The schematic diagram of $R_{th}$ between heater and die is shown in Fig. 9.

A larger heater (10 mm × 10 mm) and a smaller heater (5 mm × 5 mm) are used in the experiment to analyze the effects of heater size on the results of relative error of

![Fig. 7](image1)
![Fig. 8](image2)
![Fig. 9](image3)
BGA. Further, by using thermal analysis software FloTHERM, the relative error is calculated by changing heater size from 2 mm × 2 mm to 15 mm × 15 mm. The boundary conditions in the simulation model are set to the same as the experiment, and grid is adjusted to improve the simulation accuracy. From experimental results shown in Fig. 10, less amount of heat is transferred through the sensor when using the larger heater. As a result, the relative error in the case of larger heater is 4.46%, which is larger than relative error of the case with smaller heater. The calculated results show the same trend with the experimental result as shown in Fig. 11, but relative error is much larger when heater size is 2 mm × 2 mm, which is inconsistent with the trend. With a smaller heater size, the temperature of heater will be higher when heater is working at a same power, more heat will be dissipated to the ambient by radiation and convection, resulting in the larger relative error when a much smaller heater is used.

As heater positions will affect the thermal resistance between heater and die/chip, it is necessary to clarify the position of the heater to reduce the relative error of the proposed measurement method. In Fig. 12, different heater positions on the components are considered. The heaters are colored white while the components are black.

Meanwhile, locations of heat sources in components are described in Fig. 12 as die in package of MOSFET and chip in package of BGA, respectively. As the symmetry of MOSFET and BGA package, the cases in Fig. 12 (a) and Fig. 12 (b) have included all the possible heater positions on the MOSFET and BGA. Both experimental and simulation research are carried out under the cases of heater positions shown in Fig. 12. Results of the relative error of this measurement method are described in Fig. 13 when heater is placed at each position. From the results shown in Fig. 13, relative error is smallest at position 4 on MOSFET and position 1 on BGA, respectively. With a larger overlap area between heater and die/chip, the effective thermal resistance is smaller, which finally results in a smaller relative error.

As thermal resistance $R_{th}$ between heater and die/chip should be the main factor that affects the relative error. The effects of $R_{th}$ on the relative error of this method is examined numerically and experimentally. In the part of numerical simulation, thermal resistance $R_{th}$ between heater and die/chip which is defined by Eq. (14), is simulated by the model from Fig. 14. The simulation model follows the experimental setup used for $R_{th}$ measurement in regulation JESD.[20] In the simulation model, the top surface of MOSFET is mounted on a cooling plate of a constant temperature of 25°C. An insulator is put on the bottom surface of PCB.

$$R_{th} = \frac{T_{die} - T_{cp}}{Q}$$  \hspace{1cm} (12)

Thermal resistance $R_{th}$ can be measured experimentally.
by using structure function method. The heating temperature is switched off after the device reached thermal equilibrium. Cooling curve of the junction temperature $T_j$ of Device Under Test (DUT) can be calculated from the voltage change measured by temperature sensitive parameter (TSP) during the cooling process. The structure function can be then calculated from cooling curve through convolution operation. The structure function represents the distribution of thermal capacitance along the heat flow path, where the position on this path is expressed by the cumulative thermal resistance $\Sigma R$ starting from the junction. The calculation process is well described in literature. A transient thermal resistance analyzer (T3Ster) that integrates these algorithms is used in thermal resistance measurement. Experimental setup is shown in Fig. 15. The $R_{th}$ measurement requires two structure function measurements of the same semiconductor device in contact with a temperature-controlled heat-sink. The first measurement is performed without any thermal interface material (TIM) between DUT and heat sink, while a thin layer of grease is applied at the interface in the second measurement. Therefore, the structure functions of the two measurements will separate at the point where the heat flow path changes, which is the case surface of the DUT. The separate point of the structure function indicates the value of $R_{th}$.

To change the thermal resistance $R_{th}$ from die to heater, different thicknesses of TIMs are covered on the components as Fig. 16. The thickness of TIM is 2.0 mm and 0.1 mm respectively with a thermal conductivity of 2.0 W/(m·K).

Structure functions of the testing MOSFETs with different thickness of TIMs are shown in Fig. 17, the thermal resistance from heater to die $R_{th}$ is measured to be 37.8 K/W and increases to 43.6 K/W when the 2 mm TIM is used.

The above two testing MOSFETs covered with different thickness TIMs are then used to analyze the effects on the relative error of the proposed method experimentally. The experimental and calculated results of the relationship between $R_{th}$ and relative error are shown in Fig. 18. It can be seen that when using the thicker TIM, the experimental and calculated relative error will be increased due to a larger thermal resistance between die and heater. However, there is still a difference in the results between
experimental and calculated results in addition to the inherent difference between simulation model and real experimental system. For example, the contact thermal resistance is ignored in the simulation. The value of $R_{th}$ at the splitting point of structure function curves is not equal to the steady-state junction to case thermal resistance defined by Eq. (14). The reason is that the steady state heat flow distribution inside the MOSFET differs from the transient heat flow distribution. Furthermore, heat is assumed to transfer through a one-dimensional heat flow path in the MOSFET in the structure function generation process.

To study the general trend of relative error with the change of $\lambda$ between heater and die from 0.1 W/(m·K) to 1.0 W/(m·K).

Fig. 16 Testing components with different thickness of TIM.

Fig. 17 Structure function of tested MOSFETs. (a) MOSFET with 0.1mm TIM; (b) MOSFET with 2.0 mm TIM.

Fig. 18 Calculated and experimental $R_{th}$ and relative error with thickness of 0.1 mm and 2 mm TIM.

Fig. 19 Simulation model to obtain relative error.

Fig. 20 Relationship between $R_{th}$ and relative error with the change of $\lambda$ between heater and die from 0.1 W/(m·K) to 1.0 W/(m·K).
die. Any methods that could reduce the thermal resistance, such as using better TIM and decreasing the thickness of mold, are conducive to reduce the relative error of the proposed measurement method.

6. Conclusion

We proposed a new heat dissipation measurement method of electronic components by using an external heater and a heat flux sensor. Boundary conditions and thermal properties of materials along the heat transfer path, such as the thermophysical properties of PCB, are not necessary for the method. This measurement method is carried out on a MOSFET and a BGA testing board, the respective relative errors of the testing boards are obtained. In error analysis, factors that affect the relative error is examined experimentally and numerically. Thermal resistance between heater and die/chip is considered to be the main reason that leads to the relative error. Furthermore, heater size and heater positions are clarified by measuring the relative error. The proposed method is proved to be able to predict the heat dissipation from a single electronic component accurately, and has high applicable for the single BGA component. In the future, further research would be carried out about the proposed method to measure heat dissipations from multiple electronic components on ECU.

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