DECORE: Deep Compression with Reinforcement Learning

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Abstract

Deep learning has become an increasingly popular and powerful methodology for modern pattern recognition systems. However, many deep neural networks have millions or billions of parameters, making them untenable for real-world applications due to constraints on memory size or latency requirements. As a result, efficient network compression techniques are often required for the widespread adoption of deep learning methods. We present DECORE, a reinforcement learning-based approach to automate the network compression process. DECORE assigns an agent to each channel in the network along with a light policy gradient method to learn which neurons or channels to be kept or removed. Each agent in the network has just one parameter (keep or drop) to learn, which leads to a much faster training process compared to existing approaches. DECORE provides state-of-the-art compression results on various network architectures and various datasets. For example, on the ResNet-110 architecture, it achieves a 64.8% compression and 61.8% FLOPs reduction as compared to the baseline model without any accuracy loss on the CIFAR-10 dataset. It can reduce the size of regular architectures like the VGG network by up to 99% with just a small accuracy drop of 2.28%. For a larger dataset like ImageNet with just 30 epochs of training, it can compress the ResNet-50 architecture by 44.7% and reduce FLOPs by 42.3%, with just a 0.69% drop on Top-5 accuracy of the uncompressed model. We also demonstrate that DECORE can be used to search for compressed network architectures based on various constraints, such as memory and FLOPs.

1. Introduction

Deep neural networks (DNNs) have led to significant advances in image recognition tasks, most notably benchmarked by the ImageNet challenge [44]. Recent trends suggest that deeper architectures with more parameters and more complex computational blocks lead to better results [14, 50]. These increases in model size, however, incur a higher inference cost, making them difficult to be deployed in embedded systems or mobile devices. Additionally, real-world applications have memory, latency, or throughput constraints which make it difficult to use large convolutional neural networks (CNNs) as it is. Therefore, lowering the inference time and memory consumption of large models would benefit the wider adoption of deep learning models for these real-world applications.

There has been a significant amount of work on reducing the inference cost via a more efficient network architecture [5, 7, 28, 45]. Although much progress has been made, the process of finding efficient architectures often requires a lot of experience and manual design. The automation of this process is an active research area of great value, as newer, more powerful DNN architectures are rapidly being discovered. Meanwhile, a large body of work has also been done in direction of network compression, such as low-rank decomposition [5, 30, 31, 62], parameter quantization [23, 43, 64], and pruning [19, 28, 29, 32, 33, 59].

These compression techniques can be broadly divided into two categories: non-structured and structured pruning. Non-structured pruning methods [10–13] directly prune weights of CNN to get sparse weight matrices. Along with compression, these approaches can provide faster network inference using specialized software [9] or hardware [41]. However, they often cause irregular memory access that adversely affects the network inference on general-purpose hardware or BLAS libraries. Structured pruning overcomes these limitations by removing structured weights like 2D kernels and feature maps (channels) or layers, which focus on network architecture changes and can be supported by off-the-shelf deep learning libraries like Pytorch [42], and TensorFlow [1] etc. Pruning structured weights is a complex problem as we have to find out which layers or channels are least important in the network and removing them will not affect the accuracy significantly. For example, in VGG16 [50] the first hidden layer has 64 channels, an exhaustive search for which channels to remove without affecting accuracy involves $2^{64}$ combinations. For the whole network with around 5,000 channels in total, it is computationally infeasible to go through all combinations. To overcome this problem many techniques have been proposed in
the following categories.

**Pruning using trained network statistics:** These approaches mostly consist of two steps, first pruning the weights using network statistics and then fine-tuning the compressed network to recover from accuracy loss. Among these techniques, magnitude-based pruning has been proposed to find filters [28] or channels (feature maps) [19] with the lowest $l_1$-norm as they are least informative and can be removed. Yang et al. [16] calculates the geometric median of each layer and prunes the filters close to it. Greedy pruning [17, 37] is also a common strategy that prunes filters by considering statistics computed from the next layer in a greedy layer-wise manner. More recently, Lin et al. [29] proposed to find the channel’s importance by calculating the rank of channels. All these methods calculate channel or filter importance at one or neighboring layers without considering all other layers in a deep neural network. Yu et al. [59] found that channels that were assumed less important and pruned in early layers could have a significant impact on the pruning of later layers. To address this issue, they calculate the importance score of the final layer and propagate it to each filter in the network to get its importance score.

Finding the least important channels using trained network statistics is fairly straightforward, but finding how many channels or filters to drop from each layer (or the network) to achieve a certain compression rate requires manual effort. For example, in [29, 59] a fixed compression rate is used before fine-tuning which limits exploration for different compression rates, as each combination requires fine-tuning to recover from accuracy loss which could be time-consuming.

**Pruning by learning channel importance:** Another group of techniques is learning which channels are important by applying compression constraints while retraining the network, such as sparse scaling parameters in batch normalization layers [36, 55, 63]. Extension to these approaches has also taken into account of resource constraints such as latency and computation [6, 7, 40]. Huang et al. [21] and Lin et al. [33] introduced a new binary parameter to each channel (mask) to find out if the channel is important or not, but learning this binary mask involves an NP-hard optimization. This joint optimization for both compression and accuracy leads to higher accuracy performance but since the (combined) loss function is different than the original network, it requires specialized optimizers [21, 33] and needs iterative fine-tuning which increases time for both model training and manual adjustments.

**Pruning by architecture search:** Given that designing deep network architectures requires significant human effort, there have been some explorations on automatically learning network architectures. These approaches build networks from the ground up using a set of custom building blocks, relying on variations of a trial and error search to find promising architectures [24, 35, 54, 55, 65], or compressed ones. In [3, 15], Reinforcement Learning (RL) is used to find compressed architectures but these techniques often take a long time to train. For example, in [3] a knowledge distillation [4, 18] approach is used to find a compressed student network from a teacher network using RL. The method utilizes the architecture search to reduce the depth of a network and size of layers which can take up to 2,500 epochs. This search cost is however prohibitively expensive for regular deep learning practitioners and further exacerbated by the growth in the dataset and network complexities. To reduce the architecture search time, generative adversarial networks have been used in [33] to find compressed structures. It shortens the search time significantly but still needs many iterations to train both generative and adversarial networks. Although these methods have led to a high compression rate, they often require a large amount of GPU time to obtain high-performing architectures.

Although the approaches mentioned above provide state-of-the-art results, they are computationally expensive and time-consuming as they require multiple iterations of pruning and fine-tuning, and often need a lot of manual effort. In this paper, we present DECORE, a multi-agent reinforcement learning (RL) framework for network compression which overcomes these limitations. In RL, an agent learns to perform a sequence of actions in an environment that maximizes some notion of a cumulative reward [52]. In our approach, we assign dedicated agents to all channels in the network which take actions to drop or keep a channel in the network. The agent gets a positive reward when it drops the channel for higher compression, but it gets a negative reward (penalty) if the accuracy is decreased due to pruning. Using the REINFORCE algorithm [57], we optimize agents’ policies to find out which channels to drop at each layer without affecting accuracy significantly and in turn maximizing the reward. Training of agents using the REINFORCE algorithm is independent of the network training loss which helps speed up the compression process.

Our main contributions are summarized as follows:

- We propose DECORE, a flexible and powerful approach to automating structure search and model compression. DECORE learns which channels are important by jointly training the network to provide high compression and FLOPs reduction rates.

- DECORE assigns an agent to each channel in the network (multi-agent learning) while each agent learns only one parameter, as opposed to other RL-based methods where the policy involves training another neural network. Learning a single parameter for each agent makes training much faster and more efficient.

- We demonstrate that our approach is able to find the
most important channels in the network which can be used to search for compressed network architectures.

- Results on both CIFAR-10 and ImageNet datasets are reported with various networks to demonstrate that DECORE is able to achieve better accuracy, as well as higher compression and acceleration (FLOPs reduction) rates, compared to other existing methods.

2. Related Work

2.1. Network Pruning

Pruning weights of DNNs has been an active research topic in deep architecture design and compression. As discussed in section 1, weight pruning is effective at removing individual weights but does not necessarily reduce the computation time or memory usage. Sparse DNN layers can only help compress and accelerate the network with custom BLAS libraries or specialized hardware [9,22], which often have limited usage and an additional overhead [8,34]. A few approaches have also been proposed to leverage sparsity in networks by removing entire channels [28,46,56].

There are other approaches to pruning channels by analyzing trained network statistics [49,60], or learning the importance of each channel or filter in the network [7,21,29,36]. Our approach is similar to these methods in terms of operating on entire channels, but using RL to identify important channels instead of relying on network statistics. Moreover, our approach learns channel importance while training the network without changing the loss function like in [21,33], which speeds up the overall training process.

2.2. Architecture Search

The design space of CNN architectures is extremely large and hence infeasible for an exhaustive manual search. Research on automating neural network design goes back to the 1980s when genetic algorithms were proposed to find both architectures and weights [47]. Modern architecture search approaches have led to state-of-the-art results for many tasks, but often require a significant amount of GPU time to obtain a high-performing architecture [3,15,54,55,65]. Our use case is different, as we take already compressed architectures from trained networks [3,15,54,55,65]. This drastically reduces the search space and thus makes our approach more efficient.

Reinforcement learning has also been used to find compressed architectures from trained networks [3, 15, 61]. These approaches focus on finding a fixed set of custom rules for architecture design and then fine-tune the best architecture found. Although the result is a compressed network, these approaches still take a large amount of GPU hours to train. However, our proposed approach does not use any custom rules to look for compressed architectures.

Figure 1. DECORE Policies (a) The policy is inserted before each convolution layer of VGG. (b) For ResNet blocks, we insert the policy sequentially (agent i) and also in the parallel path (agent i+1). We keep the same policy (agent i+1) for the last layer of parallel paths so that it doesn’t affect future operations. (c) Our agent samples actions \( a_i \) and multiplies them with \( l_i \) to drop channels and compute the reward \( R_i \).

Like some of the regularization approaches, we simply learn which channels are more or less important for prediction. We obtain a compressed network by removing the channels that have little effect on accuracy.

3. Approach

3.1. DNN compression in RL framework

A Markov Decision Process (MDP) provides a mathematical framework to model decision making in stochastic environments. At each time step, \( t \), the agent is at state \( s_t \), where it can choose any available action \( a_t \) and move to the next state \( s_{t+1} \), while accruing reward \( R(s_t, a_t) \). The probability of moving to a new state \( s_{t+1} \), given \( s_t \) and \( a_t \), is determined by the state transition probability \( P(s_{t+1}|s_t, a_t) \). The objective of RL is to find a policy \( \pi(a_t|s_t) \), which specifies the action an agent will take at \( s_t \) to maximize some notion of cumulative reward [52].

We can therefore frame the network compression process as an RL problem. At each layer \( l_i \) with \( C_i \) channels, we assign dedicated agents to all channels in that layer. Each agent decides to drop or keep its channel and subsequently receives reward \( R_{i,j} \) (channel \( j \) at layer \( i \)). The neural network does inference with channels which are not dropped and reaches the next state/layers \( l_{i+1} \). Dropped channels can then be pruned from the network to create a compressed network. We train agents with a reward that incentivizes dropping channels at each layer \( l_i \) without affecting network performance significantly. This creates a multi-agent RL framework. As compared to traditional MDP, the number of time steps \( (t) \) for each agent in this framework is just 1 as at each forward pass they take a decision to keep or
drop channel only once and receive a reward based on that decision.

3.2. State Representation

We model each layer of a neural network with a channel vector \( s_i \in \mathbb{R}^{C_i} \), where \( C_i \) is the number of channels in layer \( i \). Each element \( w_{ij} \) of \( s_i \) represents the weight (importance) of channel \( j \) in layer \( i \). A high value of \( w_{ij} \) means that channel \( j \) is very important and dropping it would affect the network accuracy significantly. We initialize all channel vectors \( s_i \) with the same value and our algorithm learns to increase the weight values of channels that lead to high network accuracy and reduce the weight of those that do not. After training, low weight channels are removed from the network, resulting in a compressed model.

Our state representation does not take into account any other layer dependent variables, such as hidden values, height, width, number of input/output channels, kernel size, etc. [3,15], which makes this approach very general and applicable to a large or small number of channels or layers if needed. For example, this approach could be used to find important input channels for network training [58] or removing channels only from early layers which have a high memory consumption and DRAM access time [2].

3.3. Policy Representation

The agent makes the decision at each layer \( i \) to maximize the reward, based on a simple policy:

\[
p_j = \frac{1}{1 + e^{-w_{ij}}}
\]

\[
\pi_j = \begin{cases} 
1 & \text{with } p_j \\
0 & \text{with } 1 - p_j
\end{cases}
\]

\[
a_i = \pi_i = \{\pi_0, \pi_1, \ldots, \pi_{C_i}\}
\]

We convert the weights into probabilities via a sigmoid function and then choose which channels to keep at layer \( i \) by taking independent Bernoulli samples. Sample values are either 0 or 1, where 0 means dropping the channel and 1 keeping it. During training, we multiply each layer’s activation \( l_i \) with \( a_i \) to drop or keep channels. This is similar to dropout [51] but here the probability of dropping the channel depends on the agent weight. There is a single parameter (weight) for each agent. This is in contrast to policies parameterized by neural networks in deep RL [38,39], which makes our approach simple and fast as we don’t need to train dedicated neural networks to find good policies.

During training, we initialize the state vectors with a value of 6.9, which corresponds to a value of 0.99 after applying the sigmoid function. We assume initially that all channels in a pre-trained network are important and thus the initial probability of keeping a channel should be high. Each channel has a 99% chance of being selected during sampling and 1% chance not, which encourages little exploration and assists policy optimization. Figure 1 shows our policy in action for a layer \( l_i \).

We insert policies only before convolution or linear layers of the network. We avoid inserting policies in batch normalization or depth wise convolution layers, as they operate on individual channels and the decision to drop channels could be affected by the next layer’s policy. Our policy insertion also depends on the network architecture. For example, ResNet or MobileNet architectures consist of residual blocks where computation happens in parallel paths and they are merged with a channel-wise addition. For these blocks, we keep the same policy at the end of parallel paths so that the same channels are dropped before merging. Figure 1 shows the policy insertion for VGG [50] and ResNet [14] architectures. We use a similar approach to insert policies in computation layers of diverse architectures like GoogleLeNet [53] and DenseNet [20].

3.4. Reward

The objective of our modeling is to compress the network while maintaining predictive accuracy. We combine two different rewards to balance these considerations.

At each layer \( i \), dropped channels represents the compression achieved at that layer. We compute compression reward \( R_{i,C} \) at layer \( i \) by summing all the dropped channels in that layer. We compute the number of dropped channels using the layer’s action vector \( a_i \):

\[
R_{i,C} = \sum_{j=1}^{C_i} 1 - a_{i,j}
\]

After taking a series of actions at each layer, the network makes a prediction, \( \hat{y} \), using only information from preserved channels. Each correct prediction suggests that the agent has taken the right actions and thus should be rewarded. An incorrect prediction incurs a penalty, \( \lambda \), which can be tuned to adjust the preference for compression or accuracy:

\[
R_{acc} = \begin{cases} 
1 & \text{if } \hat{y} = y \\
-\lambda & \text{else}
\end{cases}
\]

The penalty is a high negative value which forces the agent not to drop the channels if predictions are wrong. Our final reward at each layer combines these rewards via multiplication. The value is high only when both compression and accuracy rewards are high,

\[
R_i = R_{i,C} \times R_{acc}
\]

3.5. Optimization with Reinforcement Learning

The agent must learn to take decisions to maximize the expected sum of rewards across each layer. We can estimate the expected reward using trajectories \( \tau \) sampled from the
policy. In our framework, the agent takes the decision only once so the trajectory has only one time step. For example, the trajectory for layer $i$ is $\tau_i = (s_i, a_i)$. The optimization objective in this case is:

$$w^* = \arg \max_w J(w) = \sum_{i=1}^L \arg \max_w E_{\tau_i \sim \pi_w(\tau_i)}[R_i]$$

We use the REINFORCE policy gradient algorithm [38, 57] to learn policy parameters $w$,

$$\nabla_w J(w) = \sum_{i=1}^L \nabla_w \log \pi_w(a_i | s_i) R_i$$

$$\nabla_w J(w) = \frac{1}{L} \sum_{i=1}^N \sum_{b=1}^L \nabla \log \pi_w(a_i, s_i) R_{b,i}$$

where $N$ is the batch size used to estimate the gradient and $L$ is a depth of the network. The gradients are used to update the policy or weights at each layer. We train policies and neural networks jointly using their own loss functions.

In traditional RL approaches the state representation is stochastic and the agent takes an action by looking at each state to learn policies. This is contrary to our approach as we try to learn state representation without requiring expensive policy optimization methods like Actor-Critic [26] and PPO [48]. Given that each agent has only one parameter, the policy gradient approach learns fast and efficiently.

4. Experiments

4.1. Datasets and Model Architectures

We evaluate our approach by measuring the accuracy, compression, and acceleration (FLOPs reduction) rates of standard deep networks on common image classification benchmark datasets, such as CIFAR10 [27] and ImageNet [44]. To demonstrate effectiveness of our approach we experimented with multiple widely-used model architectures, including VGG [50] with sequential layers, ResNet [14] with residual blocks, GoogleLeNet [53] with inception module, and DenseNet [20] with dense block.

4.2. Training Details

We used PyTorch [42] to implement the proposed method but our approach is very flexible and can be integrated with any deep learning library like TensorFlow [1]. To train policies, we used the ADAM optimizer [25] with a batch size of 256 and a learning rate of 0.01. Training begins with fixed policy vectors of an initial value 6.9 (Section 3.3), yet different subnetworks are generated in each forward pass due to Bernoulli sampling. For each correct prediction, the agent gets a positive reward, and for each wrong prediction, it is penalized. We found the values of the penalty hyperparameter $\lambda$ through a grid search, running each experiment for 10 epochs. The high penalty leads to better accuracy as the agent is more penalized for every wrong prediction. As we lower the penalty it tries to compress the network more without largely affecting accuracy.

Additionally, we also fine-tune the network at each optimization step so that it may recover from drops in performance after pruning. Joint training of policies and the network is more computationally efficient and sample efficient than approaches that separate learning from search [28, 29, 55]. On the CIFAR dataset, all models are trained for 300 epochs. We stopped policy training after 260 epochs and dropped all the channels with a probability of less than 50%. We continued fine-tuning the remaining subnetwork in the next 40 epochs to improve accuracy. Finally, we removed the dropped channels from the network to get the compressed model. On the ImageNet dataset, models were trained for just 30 epochs. We stopped policy training after 20 epochs and fine-tune in the remaining epochs.

4.3. Results and Analysis

4.3.1 Results on CIFAR-10

We evaluated the performance of the proposed approach on CIFAR-10 with four diverse networks, VGGNet-16/19, DenseNet-40, GoogleLeNet, and ResNet-56/110. We used VGGNet with the variation of the original VGG-16 for CIFAR-10 similar to [28, 29, 33]. For GoogLeNet, the final output layer was changed to have 10 classes. DenseNet-40 has 40 layers with a growth rate of 12. For a fair comparison, we used the same trained networks used by [29, 33]. For all our experiments, we tried to adjust the penalty parameter ($\lambda$) to achieve similar accuracy as reported state-of-the-art results and compared the parameters/FLOPs reduction rates.

**VGGNet:** Table 1 shows that with a higher penalty ($\lambda = 500$), our approach **DECORE-500** was able to achieve 0.15% higher accuracy than the baseline model with a 63% parameter and 35% FLOPs reduction rate, which shows that this network was over parameterized for this problem and DECORE can be used as a regularizer.

As compared to approaches that prune networks using network statistics [28, 29], our approach provides higher compression and acceleration (FLOPs reduction) rates at similar or higher accuracy. As compared to L1 pruning [28], our approach **DECORE-200** was able to get a 25% higher compression rate (89% vs 64%) and a 30% higher FLOPs reduction rate (34.3% vs 64.8%) at 0.16% higher accuracy (93.56% vs 93.40%). Similarly, as compared to HRANK [29], our approach provides better results at various compression rates. For example, with a high penalty, DECORE-200 was able to achieve 6.1% higher compression and 11.3% higher FLOPs reduction rates at 0.13% higher accuracy (93.56% vs 93.43) compared to HRANK-1. Similarly, with a low penalty, DECORE-50 was able to achieve 6.3% higher compression and 11.8% higher FLOPs reduction rates at 0.45% higher accuracy.
(91.68% vs 91.23%) as compared to HRANK-3. Moreover, HRANK [29] manually sets different compression rates at each layer for higher compression or accuracy, and our method automatically (just by changing penalty $\lambda$) finds an optimal setting and generates better results overall. For the training process, HRANK trains each layer for 30 epochs (480 epochs for 16 layers), and the training time increases as the number of layers increases, while our approach only needs to train for 300 epochs for all settings.

Our approach also finds better results than existing methods which learn channel importance [21, 63]. For instance, as compared to SSS [21], DECORE-200 achieved 15.2% higher compression and 23.2% higher FLOPs reduction rates at 0.54% higher accuracy (93.56% vs 93.02%). As compared to Zhao et al. [63], DECORE-200 achieved 15.7% higher compression and 25.7% higher FLOPs reduction rates at 0.38% higher accuracy (93.56% vs 93.18%).

Compared to architecture search approaches such as [33], DECORE-100 provided 19% higher compression and 41.9% FLOPs reduction rates at 0.41% higher accuracy (GAL-0.05). For high compression (low penalty), DECORE-50 was able to achieve 16.1% high compression and 43.1% FLOPs reduction rates at 0.95% higher accuracy as compared to GAL-0.1. As compared to RL based architecture search method N2N [3], DECORE achieves 3.5% higher compression on the VGG-19 network at similar accuracy. N2N [3] first searches for compressed network architecture then fine-tunes the winner architecture to gain higher accuracy. N2N [3] training can take more than 2500 epochs to get the best architecture. DECORE achieves similar accuracy and higher compression with just 300 epochs (<9%).

**GoogleLeNet:** Table 2 shows at higher penalty DECORE-500 was able to find 0.15% higher accuracy than the baseline model with 23% compression and 19.8% Flops reduction rates. DECORE-200 was able to find higher compression (38% vs L1, 25.5% vs HRANK-1) and lower FLOPs (45.6% vs L1, 23.6% vs HRANK-1) at similar accuracy, compared to approaches using network statistics, such as L1 [28] and HRANK [29]. At a low penalty setting, DECORE-175 provided 16.3% higher compression and 14.3% FLOPs reduction as compared to HRANK-2.

Compared to architecture search methods DECORE-175 provided high compression (36.8% vs GAL-0.05 and 32.4% vs GAL-ApoZ), lower FLOPs (46.5% vs GAL-0.05 and 34.7% vs GAL-ApoZ), and higher accuracy(0.4% vs GAL-0.05 and 2.22% vs GAL-ApoZ). It shows that DECORE can be applied to inception modules to achieve high performance as compared to state-of-the-art approaches.

**DenseNet-40:** DenseNet is a sophisticated architecture with channel concat (a growth rate of 12) at each layer, and channels at one layer are used in the following layers. Dropping even a single channel in this architecture means dropping this channel from all the following layers. Table 3 shows DECORE results for the DenseNet architecture. With a higher penalty, DECORE-175 was able to achieve 0.15% higher accuracy than the baseline model with 23% compression and 19.8% Flops reduction rates. DECORE-200 was able to find higher compression (38% vs L1, 25.5% vs HRANK-1) and lower FLOPs (45.6% vs L1, 23.6% vs HRANK-1) at similar accuracy, compared to approaches using network statistics, such as L1 [28] and HRANK [29]. At a low penalty setting, DECORE-175 provided 16.3% higher compression and 14.3% FLOPs reduction as compared to HRANK-2.

Compared to architecture search methods DECORE-175 provided high compression (36.8% vs GAL-0.05 and 32.4% vs GAL-ApoZ), lower FLOPs (46.5% vs GAL-0.05 and 34.7% vs GAL-ApoZ), and higher accuracy(0.4% vs GAL-0.05 and 2.22% vs GAL-ApoZ). It shows that DECORE can be applied to inception modules to achieve high performance as compared to state-of-the-art approaches.
approach can be applied to complex architectures too.

We found for a small dataset like CIFAR10, pre-trained models can often have very high accuracy and it becomes more challenging to prune these models without accuracy degradation. To meet this end the \( \lambda \) parameter is set high so that we can achieve compression without any noticeable accuracy drop. On the other side, for more complex datasets like ImageNet, \( \lambda \) has a lower value.

### 4.3.2 Results on ImageNet

Table 5 shows our performance with different penalty values on the challenging ImageNet dataset. DECORE-8 shows at higher penalty (\( \lambda = 8 \)), our model was able to achieve 11.0% compression (25.50M to 22.69M) and 13.5% (4.09B to 3.54B) FLOPs reduction at a 0.16%/0.18% Top-1/Top-5 accuracy improvement.

With a slightly lower penalty (\( \lambda = 6 \)), DECORE-6 was able to achieve 74.56%/92.18% (1.57%/0.68% drop as compared to the baseline model) Top-1/Top-5 accuracy with the number of parameters reduced to 14.10M (44.74% of baseline) and FLOPs reduced to 2.36B (42.30% of baseline). Compared to approaches relying on network statistics such as ThiNet-70 [37], DECORE-6 achieved 2.54% higher Top-1 accuracy and a 16.77% higher compression rate with similar FLOPs. As compared to approaches that learn channel importance, such as SSS-32 [21] and He et al. [17], DECORE-6 provided higher accuracy, higher compression, and lower FLOPs. Furthermore, compared to architecture search methods such as GAL-0.5 [33], DECORE-6 also achieved 33.49% higher compression and 13.5% (4.09B to 3.54B) FLOPs reduction at a 0.16%/0.18% Top-1/Top-5 accuracy improvement.

For ResNet-110, DECORE-300 provided 64.8% compression and 61.8% FLOPs reduction without accuracy loss. For ResNet-50, DECORE-6 provided higher accuracy, higher compression, and lower FLOPs than all other approaches while achieving higher accuracy. DECORE-175 compressed the network even further to 79.63% with 76.9% FLOPs reduction and just a 0.79% drop in accuracy.

### Table 4. Pruning results of ResNet-56/110 on CIFAR10.

| Model      | Top-1(%) | Top-5(%) | Params(PR) | FLOPs(PR) |
|------------|----------|----------|------------|-----------|
| ResNet-56  | 93.34    | 92.86    | 125.49M(0.0%) | 0.77% |
| DECORE-450 | 93.34    | 92.86    | 125.49M(0.0%) | 0.77% |
| L1 [28]    | 93.06    | 92.86    | 125.49M(0.0%) | 0.77% |
| NISP [59]  | 93.01    | 92.86    | 125.49M(0.0%) | 0.77% |
| GAL-0.6 [33] | 92.98   | 92.86    | 125.49M(0.0%) | 0.77% |
| HRank-1 [29] | 93.17   | 92.86    | 125.49M(0.0%) | 0.77% |
| AMC [15]   | 91.90    | 92.86    | 125.49M(0.0%) | 0.77% |
| **DECORE-200** | 93.26  | 92.86    | 125.49M(0.0%) | 0.77% |
| He et al. [17] | 93.26  | 92.86    | 125.49M(0.0%) | 0.77% |
| GAL-0.8 [33] | 93.26   | 92.86    | 125.49M(0.0%) | 0.77% |
| HRank-2 [29] | 93.26   | 92.86    | 125.49M(0.0%) | 0.77% |
| **DECORE-55** | 93.26  | 92.86    | 125.49M(0.0%) | 0.77% |

### Table 5. Pruning results of ResNet-50 on ImageNet. X-joint means jointly pruning channels and blocks.

| Model      | Top-1(%) | Top-5(%) | Params | FLOPs |
|------------|----------|----------|--------|-------|
| ResNet-50  | 76.15    | 92.87    | 25.50M | 4.09B |
| DECORE-8   | 76.31    | 92.87    | 22.69M | 3.54B |
| SSS-32 [21] | 74.18    | 91.91    | 18.60M | 2.82B |
| He et al. [17] | 72.30    | 90.80    | -      | 2.73B |
| ThiNet-70 [37] | 72.04    | 90.67    | 16.94M | 2.44B |
| GAL-0.5 [33] | 71.95    | 90.94    | 21.20M | 2.33B |
| **DECORE-6** | 74.58    | 92.18    | 14.10M | 2.36B |
| GDP-0.6 [32] | 71.19    | 90.71    | -      | 1.88B |
| GDP-0.5 [32] | 69.58    | 90.14    | -      | 1.57B |
| SSS-26 [21] | 71.82    | 90.79    | 15.60M | 2.33B |
| GAL-1 [33] | 69.88    | 89.75    | 14.67M | 1.58B |
| GAL-0.5-joint [33] | 71.80 | 90.82 | 19.31M | 1.58B |
| HRank-1 [29] | 71.98    | 91.01    | 13.77M | 1.55B |
| ThiNet-70 [37] | 70.01    | 90.02    | 12.38M | 1.71B |
| **DECORE-5** | 72.06    | 90.82    | 12.38M | 1.71B |
| ThiNet-30 [37] | 68.42    | 88.30    | 8.86M  | 1.10B |
| GAL-1-joint [33] | 69.31    | 89.12    | 10.21M | 1.11B |
| HRank-2 [29] | 69.10    | 89.58    | 8.27M  | 0.99B |
| **DECORE-4** | 69.71    | 89.37    | 6.12M  | 1.19B |

![](image1.png)

Table 4. Pruning results of ResNet-56/110 on CIFAR10.
5. Analysis: Does DECORE find important channels?

To investigate the effect of learned policies on network compression, we reviewed policies for the VGG16 network trained on CIFAR10 for 200 epochs with 4,736 agents (just convolution and linear layers). We didn’t train the network jointly for this experiment so that the analysis of learned policies is not mixed with network update.

After training the agents, we remove channels with high weight values and observe how much accuracy drops. If removing high weight channels impacts network accuracy significantly, it suggests that the RL algorithm has correctly identified the most important channels in the network. Figure 2 shows the effect on network accuracy after dropping low and high weight channels at each layer. The pretrained network (without any channel drop) has an accuracy of 93.96%. If we just remove the highest weight channel (1 channel out of 4736), there is a large accuracy drop of 1.42% (92.54% vs 93.96%). This decrease is significant, given that dropping this channel only compresses the network by 0.004%. It suggests that DECORE is able to learn the most important channels, as dropping even one of them will impact accuracy significantly. In contrast, if we drop the lowest weight channel, the accuracy drop is negligible. If we drop 20 highest weight channels the network losses half of its accuracy (47.63%), and after dropping top 50 channels the accuracy remains only 13% which is almost the same as a random network. Dropping 50 of the lowest weight channels, however, does not impact accuracy much. This provides evidence that DECORE is able to identify channels with the least and most importance.

6. DECORE for compressed Network Architecture Search

In real-world scenarios, there are often stringent limits on resources available. Although we can not directly optimize the network with hard constraints, we can tune our penalty reward to ensure trained models fall within those ranges. In section 5, we found DECORE learns weights for each channel in the network and the weight value shows the importance of that channel in the network. We also noticed that dropping low weight channels doesn’t really impact the accuracy much. We can utilize this to sort all agents weight values and drop the lowest weight channels till the constraint is met. This approach can be used to meet different types of requirements, eliminating need to train networks with hard constraints. In this experiment, we explore the effect of hard constraints on performance. We also provide our findings on the MobileNetV2 [45] architecture which is hand designed for real world applications. It has 2.28M parameters which take 8.7MB of on-device memory and do 86.72M FLOPs during the inference on the CIFAR-10 dataset. Our baseline MobileNetV2 model has 92.58% Top-1 accuracy for CIFAR-10.

In all experiments, we learned the policies for 200 epochs and then fine-tuned the compressed network for 60 epochs. Similar to Section 5 we didn’t train the network jointly while learning policies.

Table 6 shows the performance drop of different models on the CIFAR10 dataset with different size constraints (in terms of storage memory and FLOPs). As explained above, we dropped the low weight channels till the constraint is satisfied. With a storage constraint of just 1MB, there is a maximum of 1% performance drop across all models. When we decrease the size limit to 0.5MB, there is a negligible performance difference from the 1MB results. This illustrates that our technique is robust to varying memory constraints.

In addition to size constraints, we explore constraints on the number of FLOPs in a compressed network. FLOPs are highly correlated with latency on CPU-only devices and thus the results in Table 6 reflect performance with potential latency constraints. With a very low 50M FLOP limit, we observed a maximum performance drop of 2.47% across all models. These results suggest that our approach can flexibly handle various hard constraints on computation, and by proxy latency, without significant drops in performance. More stringent constraints (size < 0.01MB or FLOPs < 10M) lead to degenerate architectures and joint
training (Section 4.2) is recommended for these scenarios. DECORE identifies the importance of each channel in the network once and then remove low priority channels to obtain a target architecture based on constraints. We don’t need to retrain the network again when the target criteria is changed, which is contrary to other methods [3, 15].

7. Conclusion

We present DECORE as an efficient reinforcement learning (RL) approach to network compression and compressed architecture search. Through our experiments on various datasets and architectures, we found that the size of compressed DECORE models is typically 3x smaller than those of similar approaches at the same level of accuracy, while the policy gradient algorithm selects the most important channels for prediction with a significant impact on model accuracy. Moreover, DECORE can find a trade-off between accuracy and compression by tuning the penalty for misclassifications, allowing for more control over the compressed model structure. Our method is simple yet very effective, and thus shows great promise as a tool for network compression. Although we demonstrate results with one type of reward, exploring other reward functions that target different resource constraints is an interesting direction to explore. We also leave the evaluation of our approach on architectures for other visual reasoning tasks (e.g., object detection, semantic segmentation, etc.) for future work. One benefit of DECORE is that it finds compressed networks without doing architecture search, which could potentially save a lot of energy by reducing GPU hours during training. The compressed neural networks have a lower computation cost and are more energy efficient, which could be suitable for online inference on edge devices. We have not noticed any obvious negative consequences of our work. However, future extensions may have to consider their impact carefully.

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