Predictive Voltage Control Operating at Fixed Switching Frequency of a Neutral-Point Clamped Converter

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Abstract—Uninterruptible power supply units are system formed by power electronics converters to supply sinusoidal voltages to feed critical loads. In this paper, a fixed switching frequency model predictive control strategy is presented for the control of the output voltage in a three-level NPC converter. The control objectives of the system are the tracking of the voltage reference and balance of the voltages of the dc-link capacitors. The mathematical model of the converter and the LC filter is developed and the control strategy is explained. Simulation results obtained in the Matlab/Simulink environment are presented to validate the control strategy.

Index Terms—Predictive control, DC-AC power converters, voltage control.

I. INTRODUCTION

When regulated sinusoidal voltages are required by a load, the solution is to use inverters with an LC output filter to generate ac output voltages with very low harmonic content [1]. One application of this is as the main inverter of an uninterruptible power supply (UPS) system. UPS units are composed of power electronics converters designed to feed critical linear and nonlinear loads such as medical and industrial equipment [2]. For the operation of the inverter the most common approach is to use linear control because of its well-known design and simple implementation. These linear control strategies are PI-based linear cascaded control loops with coordinate transformation (such as the Clarke transform and the Park transform) and modulation techniques such as carrier-based PWM and space vector modulation [3]. The modulation technique is used to linearize the converter and generate the commutation signals based on a time-average principle. Advantages of linear control strategies are the fixed switching frequency and easiness to extend the method to different converter topologies by changing the modulation technique used, etc. but it has some disadvantages such as the difficulty to include contraints and nonlinearities and the necessity of a modulator resulting in slower dynamics. The development of semiconductor technology has increased the processing capability of digital microprocessors and reduced their price allowing the exploration and implementation of new and more complex control schemes. These techniques such as fuzzy control, robust control, sliding mode control and model predictive control (MPC) are more advanced than standard PID control and thus are denominated as advanced control [4]. Predictive control refers to a wide class of controllers such as deadbeat control, hysteresis-based, trajectory-based and MPC [5]. These control techniques shares the same common characteristic which is the use of the mathematical model of the system to predict the future behaviour of the controlled variables over a prediction horizon $N$ to select the appropriate control action based on an optimization criterion [6]. Advantages of predictive control are: applicability to a variety of systems, nonlinearities can be included in the model avoiding the need of linearizing for a given operating point, constraints can be included in the optimization criterion, the multivariable case can be easily included and the possibility to avoid the cascaded structure of linear control schemes resulting in fast transient response. The disadvantages of predictive control strategies are the high computational burden and the need of very good mathematical models of the system under control [4].

FCS-MPC is a predictive control strategy which use the discrete nature of the converter. In every sampling instant the controlled variables are measured and fed to the discrete-time model of the system to predict their future behavior for all possible switching states of the converter and compute a cost function. The cost function depends on the control objectives and can be the absolute error, quadratic error or time-average error between the reference and measured variables [7]. The switching state who minimizes the cost function is stored and
applied in the next sampling instant. The strategy does not need a modulator resulting in a variable switching frequency. A FCS-MPC algorithm with constant switching frequency is preferred because it allows easy filter design in applications such as grid-connected converters and inverters with output LC filter [4, 8]. A solution to the switching frequency problem is a FCS-MPC algorithm called Modulated Model Predictive Control (M²PC). In M²PC, a modulation scheme is included in the cost function minimization by selecting and applying, in every sampling instant, two or more switching states with their corresponding application times [9]. This approach has been applied to many power converter topologies including the NPC converter [8-14].

In this paper, a M²PC strategy is proposed for the output voltage control and dc-link capacitor voltage balance of a NPC converter connected to a LC filter feeding a linear resistive load. In Section II the mathematical model of the converter and load is developed, in section III the M²PC strategy is explained and in section IV the control strategy is validated with a Matlab/Simulink simulation.

II. TOPOLOGY AND MATHEMATICAL MODEL OF THE CONVERTER

In Fig. 1 a three-level neutral-point clamped (NPC) converter connected to a resistive load through a LC filter is shown.

The NPC converter is a multilevel inverter which means that the converter transforms a fixed dc voltage to a ac voltage with variable magnitude and frequency. The dc-link is formed by two cascaded dc capacitors which provide a floating neutral point (O). The converter topology consist of three phases (or legs) connected in parallel with four high-power switching devices per phase. The high-power switching devices can be either IGBT or GCT [15]. Two series connected diodes (denominated as clamping diodes) are connected to the node between the upper switching devices (with switching signals \(S_{1x}\) and \(S_{2x}\), with \(x \in \{a, b, c\}\)) and the node between the lower switching devices (with switching signals \(S_{1x}\) and \(S_{2x}\)). Since the converter operation don’t allow all the switching devices to be in the ON state at the same time, the lower switching devices work in a complementary manner with the upper switching devices. The node between the clamping diodes is connected to the floating neutral point of the dc-link.

The switching state, \(S_x\), summarise the switching devices state (ON or OFF) of the four switches in one of the three phases. Table I shows the relationship between the switching state, the switching signals of the switching devices in one phase and the inverter terminal voltage, \(v_{xN}(t)\). The voltage \(v_{xN}(t)\) can be expressed as a function of the switching signals of the upper switching devices and the voltages of the dc-link capacitors as follows:

\[
v_{xN}(t) = S_{1x}v_{dc1}(t) + S_{2x}v_{dc2}(t)
\]

The voltages of the dc-link capacitors can be expressed with the differential equations in (2), where \(i_{dc1}(t)\) and \(i_{dc2}(t)\) are the current through the upper capacitor and the current through the lower capacitor, respectively.

\[
\frac{d}{dt}v_{dc1}(t) = \frac{1}{C_1}i_{dc1}(t)
\]

\[
\frac{d}{dt}v_{dc2}(t) = \frac{1}{C_2}i_{dc2}(t)
\]

The currents through the dc-link capacitors, \(i_{dc1}(t)\) and \(i_{dc2}(t)\), are a function of the output currents \(i_x(t)\) and the switching states of the converter and can be determined by the equation presented in [16] as follows:

\[
i_{dc1}(k) = i_{dc}(k) - H_{1a}i_a(k) - H_{1b}i_b(k) - H_{1c}i_c(k)
i_{dc2}(k) = i_{dc}(k) + H_{2a}i_a(k) + H_{2b}i_b(k) + H_{2c}i_c(k)
\]

\(H_{1x}\) and \(H_{2x}\) are piecewise-defined functions whose values depends on the switching states of the converter. The function \(H_{1x} = 1\) if and only if \(S_x = 1\); \(H_{1x} = 0\) in any other case.

The function \(H_{2x} = 1\) if and only if \(S_x = -1\); \(H_{2x} = 0\) in any other case.

This converter has 3 switching states per phase, therefore there are \(3^3 = 27\) possible combinations of the switching states. Considering a three-phase balanced load the ac side analysis can be simplified using the Clarke transform. Applying the Clarke transform to the three-phase inverter terminal voltages, 19 different voltage space vectors are generated from the 27 combinations of the switching states. Those combinations who generate the same voltage vector are called redundant. The vectors are classified based on their length in zero vector (\(v_0\)), small vectors (\(v_1-v_6\)), medium vectors (\(v_7-v_{12}\)) and large vectors (\(v_{13}-v_{18}\)). The projection of the vectors in the \(a -\beta\) plane is shown in Fig. 2. The plane is divided in six triangular sectors and each sector is divided in four regions. The following current and voltage space vectors are defined for the LC filter and load variables:

Table I

| \(S_x\) | \(S_{1x}\) | \(S_{2x}\) | \(S_{1x}\) | \(S_{2x}\) | \(v_{xN}\) |
|-------|-------|-------|-------|-------|-------|
| 1(P)  | 1     | 1     | 0     | 0     | \(v_{dc1} + v_{dc2}\) |
| 0(O)  | 0     | 1     | 0     | 1     | \(v_{dc2}\) |
| -1(N) | 0     | 0     | 1     | 1     | 0     |
\[ v(t) = (2/3) \left( v_{in}(t) + a v_{in}(t) + a^2 v_{in}(t) \right) \]
\[ i(t) = (2/3) \left( i_{in}(t) + a i_{in}(t) + a^2 i_{in}(t) \right) \]
\[ v_{c}(t) = (2/3) \left( v_{cc}(t) + a v_{cc}(t) + a^2 v_{cc}(t) \right) \]
\[ i_L(t) = (2/3) \left( i_{Lh}(t) + a i_{Lh}(t) + a^2 i_{Lh}(t) \right) \]

where \( a = e^{j(2\pi/3)} \), \( v(t) \) is the space vector of the inverter terminal voltages, \( i(t) \) is the space vector of the inductors current in the LC filter, \( v_{c}(t) \) is the space vector of the capacitors voltages in the LC and \( i_L(t) \) is the space vector of the load currents. The differential equations which describe the dynamics of the ac side are:

\[ \frac{d v_{c}}{dt} = \frac{1}{C_f} i - \frac{1}{C_f} i_L \]
\[ \frac{d i}{dt} = \frac{1}{L_f} v - \frac{1}{L_f} v_c - \frac{R_f}{L_f} i \]

where \( C_f, L_f \) and \( R_f \) are the filter capacitance, filter inductance and filter resistance, respectively. To implement the control algorithm in a digital platform the continuous-time equations need to be discretized. The discrete-time equations are obtained applying the Euler forward method:

\[ \frac{dx(t)}{dt} \approx \frac{x(k+1) - x(k)}{T_s} \]

Replacing (7) in (2), (5) and (6):

\[ v_{dcz} = v_{dcz}(k) + \frac{T_s}{C_z} i_{dcz}(k), \quad z \in \{1,2\} \]

\[ i(k+1) = \left[ 1 - \frac{R_f T_s}{L_f} \right] i(k) + \frac{T_s}{L_f} \left( v(k) - v_c(k) \right) \]

\[ v_c(k+1) = v_c(k) + \frac{T_s}{C_f} \left[ i(k+1) - i_L(k) \right] \]

III. FIXED SWITCHING FREQUENCY PREDICTIVE VOLTAGE CONTROL

\( \text{M}^2 \text{PC} \) is a combination of the operation principles of SVM and FCS-MPC. In every switching instant \( T_s \) a switching sequence is applied to the converter, the switching sequence consist in the controlled application of the switching states of the voltage vectors that form the region where \( v_{ref} \) is located. The transition between one switching state and the next should follow a criterion. The criteria are: (a) at every transition only one leg of the converter can change its switching state and (b) in one sampling interval only two switches per leg can switch states, one for turn-ON and then for turn-OFF. Following these criteria the NPC space vector diagram is analyzed finding 36 possible switching sequences. Consider region 1 in sector I which is formed by the vectors \( v_0, v_1 \) and \( v_2 \). These are redundant vectors enabling the possibility to form two switching sequences with them: \( v_0(000)-v_{1p}(000)-v_{2p}(PPO)-v_0(PPP) \) and \( v_0(000)-v_{2N}(OON)-v_{1N}(ONN)-v_0(NNN) \). The difference between both switching sequences is the use of the \( dc \)-link. The switching sequence of region 1 in sector I is shown in Fig. 3 and the switching sequence of the optimal vectors is shown in Fig. 4, the vector \( v_0 \) is the vector with most redundant states in the sequence. The control objectives are: (a) track of the reference voltage and (b) voltage balance in the \( dc \)-link capacitors. The following cost function is defined:

\[ g_j = (v_{ref} - v_{ref})^2 + (v_{ref} - v_{ref})^2 + \lambda_{dc} (v_{dc1} - v_{dc2})^2 \]

where \( \lambda_{dc} \) is a weighting factor and \( j \in \{0,1,2\} \).

IV. SIMULATION RESULTS

In this section the simulation results of the proposed control strategy are presented. In Table II the simulation parameters are shown. The results are presented for steady state and transient state with a step change of the reference voltage.
Fig. 3. Switching sequence formed by the vectors $v_0$(OOO) - $v_1$(POO) - $v_2$(PPO) - $v_0$(PPP).

Fig. 4. Switching sequence for the optimal vectors.

Fig. 5. Block diagram of M$^2$PC applied to a NPC converter connected to a LC filter.

A. Results in steady state

In Fig. 6 the reference voltage and the voltage in the capacitors of the LC filter is shown in the upper graph and the current through the LC filter inductors is shown in the lower graph. In steady state, the system is capable of following the reference voltage fulfilling the first control objective.

In Fig. 7 the voltage in the capacitors of the dc-link is shown in the upper graph and the current to the load are shown in the lower graph. The voltages of the dc-link capacitors are kept at half the voltage applied to the dc-link each and the voltage error between $v_{dc1}$ and $v_{dc2}$ oscillates between $\pm 0.1$ [V]. The M$^2$PC is capable of balance the dc-link capacitors in steady state.

In Fig. 8 the harmonic spectrum of the voltage and current
in the capacitors and inductors of the \( LC \) filter is shown. The harmonic distortion of the current and the voltages is very low with a THD of 0.42% for the voltage of the \( LC \) filter capacitors and a THD of 1.47% for the current through the inductors of the filter. These values are obtained when the system is working with a device switching frequency of 10 [kHz]. The device switching frequency is half the sampling frequency of 20 [kHz].

B. Results in transient state

In Fig. 9 the reference voltages and voltages in the capacitors of the \( LC \) filter are shown in the upper graph for a step change in the reference from 100 [V] to 150 [V] and the currents in the \( LC \) filter inductors is shown in the lower graph. For a step load in the voltage reference, the system is capable of tracking the reference with a fast transient response fulfilling the tracking objective.

In Fig. 10 the voltage in the capacitors of the \( dc \)-link is shown in the upper graph and the currents to the load are shown in the lower graph for a step change in the voltage reference of 100 [V] to 150 [V]. The voltage error between the capacitors increase from \( \pm 0.1 \) [V] to \( \pm 0.4 \) [V] at the step time. This is explained because the weighing factor of the voltage balance objective in the cost function is not adjusted to the new conditions of the system. Since the difference is small, the control strategy is capable of fulfill the voltage balance in the \( dc \)-link capacitors objective.

V. CONCLUSIONS

In this paper, a fixed switching frequency model predictive control strategy was developed for the voltage control of an \( LC \) filter connected to a three-level NPC converter. The mathematical model of the NPC converter and the \( LC \) filter is developed and used for the prediction of the states variables and the M\(^2\)PC control strategy is explained. Simulation results of the control strategy were presented for steady state and transient state with a step change of the reference voltage from 100 [V] to 150 [V]. The simulations results shows that the control strategy is capable of tracking the reference voltage and maintain balanced the voltage in the capacitors of the \( dc \)-link under both simulation conditions presented. The control strategy is based on the SVM operation principle and is restricted by the same operational limits of the modulation such as the maximum modulation index of \( \left( \sqrt{3}/3 \right) v_{dc} \).

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