Linear Parameter-Varying Control of A Power-Synchronized Grid-Following PLL-Less Inverter

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Abstract—This paper proposes a Linear Parameter-Varying loop-shaping controller for a power-synchronized Phase-locked Loop (PLL)-less grid-following inverter (GFLI). This control strategy regulates the inverter’s output active and reactive powers at the terminal instead of the point of connection and does not require a PLL for extracting the voltage phase angle. Hence, the prevalent stability issues exhibited when GFLIs are connected to weak grids are not present, and the proposed GFLI control strategy can work under both very weak and strong grid conditions without being prone to instability. In this approach, the controller parameters are functions of the operating point and changed during the real-time operation such that the closed-loop performance is preserved in all operating points. Furthermore, since the grid impedance is a factor in the design process, a robustness analysis against grid impedance estimation error is conducted, and it is shown that discrepancies in estimated real grid impedances are unlikely to make the system unstable. The performance of the proposed control design is validated in Matlab/PLECS and experiment for both strong and weak grids.

Index Terms—Linear Parameter-Varying, Loop-Shaping, Phase-Locked Loop, Power Control, Power Synchronized.

I. INTRODUCTION

IN recent years, inverter-based resources (IBRs) have gained increasing importance in power systems as a result of their decreasing cost and increasing concerns regarding CO₂ emission. Hence, their penetration in power grids is rapidly increasing, and conventional synchronous generators are being replaced by them. Despite their advantages, IBRs may face stability and grid synchronization issues due to the shortcomings of the conventional control structures used for this purpose [1].

According to their synchronization strategy, IBRs can be classified in two main groups: 1) grid-following inverters (GFLIs) and 2) grid-forming inverters (GFMIs). GFLIs employ Phase-Locked Loops (PLLS) for estimating the grid voltage angle, by which they can get synchronized to the grid [2]–[4]. This type of IBRs uses the point of connection (PoC) voltage to extract the grid voltage angle that is used in a vector current controller to create the gating signals required for the inverter [5]–[7]. GFMIs, on the other hand, adjust the phase angle of the PoC voltage by controlling the inverter’s output active power [8]. In this type of inverters, active power-frequency droop control is used to synchronize the inverter with the grid [3], [9]–[11]. Also, the PoC voltage’s magnitude is regulated by the inverter based on the output reactive powers, in contrast to GFLIs. In light of different control strategies that are used in these two types of inverters, the stability issues that they may have are different.

As the most prevalent IBR control strategy, GFLIs encounter stability issues while integrated into weak grids [12]. Different studies show the main instability motivation is particularly due to the PLL’s issues in weak grids, which might not have an equilibrium point or have an unstable one [13]–[19]. On the other hand, GFMIs face issues when connected to strong grids as it is challenging to regulate the PoC voltage in such grids [20], [21]. Since GFMIs can be modeled as a first-order system, their transient stability improves compared with GFLIs [22]. Despite that, the droop control loop used for reactive power control can negatively affect the transient stability [23]. In addition, GFMIs can induce oscillations while connected to strong or series compensated grids [21], [24], [25]. Moreover, output current limiting during a fault occurrence is challenging for this type of IBRs [26].

To overcome the stability issues corresponding to the GFLIs and GFMIs, some studies proposed direct power control (DPC) for the IBRs that control the IBRs output active and reactive power directly without having an inner control loop [27], [28]. These methods use a variable switching frequency that causes a wide range of harmonics, and hence, it is not easy to design a filter for them. To conquer this, other DPC methods are proposed that have a constant switching frequency [29]–[33]. Nonetheless, these methods could not eliminate power oscillations either, entirely.

A PLL-less voltage-modulated direct power control (VMDPC) for IBRs is introduced in [34]–[36]. Given the fact that the PLL is eliminated from this control structure, the VMDPC does not have the PLL-related issues that conventional GFLIs suffer from. Nonetheless, this method faces problems while integrated into weak grids due to the necessity of PoC voltage sensing [36]. Also, [37] shows that this approach does not possess any merit compared with conventional PLL-based GFLIs provided the PLL is tuned properly. To address the shortcomings of the previous methods for controlling grid-following IBRs, [38] proposes a power-synchronized control strategy. This method, similar to the VMDPC, does not use a PLL for the grid synchronization. However, unlike the VMDPC, since this method does not require PoC voltage measurement, it is not affected in weak-grid conditions, and thus, can perform in both stiff and weak grids. This method uses the inverter’s terminal voltage instead of the PoC voltage to control the inverter’s terminal output power. This control structure is composed of an outer loop that is responsible for controlling the IBR’s active and reactive powers, and generates the grid frequency and inner loop current references. The current controller employed in this control structure is the conventional vector current controller. Although this method is effective, and the IBR can work under stiff and weak grid conditions, the active and reactive power control design method proposed in [38] is not straightforward since it uses optimization techniques for control design. Also, the performance depends on the operating point of the system, and the IBR cannot work as a rectifier since the gain margin of the system becomes negative while working as a rectifier unless the controller is modified.

This paper aims to rectify the shortcomings that [38] has in terms of control design. To do so, a Linear Parameter-Varying (LPV) loop-shaping controller is designed for the proposed IBR control structure. Linear Parameter-Varying systems are a class of nonlinear systems that have the structure of a linear system whose parameters are a function of some scheduling variables that can be measured in real-time operation [39]. A nonlinear system which is linearized around some operating points can be represented as an LPV system in which the scheduling variables are the operating point parameters. The LPV systems are linear time-invariant for fixed scheduling variables. An LPV controller is designed such that
its parameters are adapted automatically in real-time according to the variation of the operating points. The main benefits of the proposed control design introduced in this paper are

- constant rise time for different operating points,
- straightforward control design based on the IBR operating point, and
- inversion and rectification capabilities.

Also, this study provides a robustness analysis of the proposed controller against the grid impedance estimation error. It is shown that while the order of the system increases in case the grid impedance is not accurately estimated, the system instability is unlikely.

The rest of this paper is organized as follows. Section II discusses the VMDPC and power-synchronized PLL-less grid-following inverters. Section III describes the proposed LPV loop-shaping control design and analyzes the robustness of the system with respect to discrepancies between the grid’s estimated and real impedances. The performance of the proposed method is evaluated in Section IV. Finally, the conclusions are presented in Section V.

II. GRID INTEGRATION OF INVERTERS

To have a system that can work in weak and strong grids, a novel power-synchronized control strategy for GFLIs is proposed in [38]. The block diagram of this control strategy is shown in Fig. 1. The advantage of this control strategy compared with conventional GFLIs is in this method, the PLL is not used, and hence, the related stability issues in weak grids is not exhibited. Additionally, this control strategy does not relay on sensing/controlling PoC voltage. Thus, the stability deterioration related to the VMDPC reported in [36], which is due to the affected PoC voltage, is not present in this method. Ref. [38] shows while this method can work in very weak grids, the VMDPC fails; unless the time constant of the system is decreased significantly, which makes the system much slower.

Ref. [38] assumes that the rotating reference frame is aligned with the inverter’s output current, which makes \( i_d = 0 \). Thus, the inverter’s output current and voltage can be written as

\[
i_d = I_d + j0 \quad \text{and} \quad v_{L,d} = V_I \cos \delta + j V_I \sin \delta.
\]

In (1), \( \delta \) is the angle between the inverter output voltage and current, \( I_d \) is the d-component of the inverter output current, and \( V_I \) is the amplitude of the inverter output phase-to-neutral voltage. Also, the inverter’s complex output power is

\[
P + jQ = \frac{3}{2} (V_I I_d \cos \delta + j V_I I_d \sin \delta).
\]

Supposing that there are small perturbations in \( \delta \) and \( I_d \), i.e., \( \delta = \delta_0 + \Delta \delta \) and \( I_d = I_{d,0} + \Delta I_d \), the small-signal model of the system can be derived as [38]

\[
\begin{bmatrix}
\Delta P \\
\Delta Q
\end{bmatrix} =
G
\begin{bmatrix}
\frac{3V_I I_{d,0} \sin \delta_0}{2s} H \\
\frac{3V_I I_{d,0} \cos \delta_0}{2s} H \\
\frac{3V_I \cos \delta_0}{2(\tau + s + 1)} H \\
\frac{3V_I \sin \delta_0}{2(\tau + s + 1)} H
\end{bmatrix}
\begin{bmatrix}
\Delta \omega \\
\Delta I_{d,ref}
\end{bmatrix},
\]

in which \( G \) is the system’s transfer function matrix, \( H \) is the input low-pass filter and will be neglected from now on, \( \tau = \frac{1}{\omega_s} \) represents the current controller assuming that the grid impedance is known, \( P \) and \( Q \) are inverter output active and reactive power, \( \delta_0 \) is the angle between inverter voltage and current at the operating point, \( I_{d,0} \) is the inverter output current’s d-component, \( \omega \) is the angular frequency of the grid estimated by the IBR, and finally, \( I_{d,ref} \) is the inverter output current’s d-component reference.

![Fig. 1. Power-synchronized GFLI and its control structure.](image)

![Fig. 2. The block diagram of the proposed power controller.](image)

![TABLE I](image)

| Quantity | Value         | Comment                  |
|----------|---------------|--------------------------|
| \( \tau_s \) | 95 \( \mu \)H | Inverter Filter Inductance |
| \( R_s \) | 0.01 \( \Omega \) | Series Resistance of \( \tau_s \) |
| \( S_{base} \) | 5 MVA | Inverter Rated Power |
| \( \nu_{dc} \) | 690 V | Grid Line-to-Neutral Voltage (rms) |
| \( \nu_{dc} \) | 3000 V | DC Bus Voltage |
| \( f_{sw} \) | 5 kHz | PWM Carrier Frequency |
| \( f_c \) | 50 Hz | System Nominal Frequency |
| \( f_{lim} \) | 200 Hz | Power Measurement LPF Bandwidth |

An outer-loop control schematic for regulating the output \( P \) and \( Q \) is proposed in [38], which is shown in Fig. 2. In this figure, \( K_{11}(s) \) through \( K_{22}(s) \) are Proportional-Integral (PI) compensators. Although the control strategy proposed in [38] is capable of working in very weak and very strong systems, its control design method is complex. First, it uses optimization techniques to design the active and reactive power PI controllers. Additionally, using the PI controllers proposed in [38], the system cannot work as a rectifier since the gain margin of the system becomes negative, making the system unstable. Finally, using the control design method in [38], the bandwidth of the system depends on the operating point of the system, i.e., unless the system operating point is about the operating point the controller is designed for, the system becomes slower or faster than the desired.

Hence, in the following, an alternative controller that addresses the aforementioned shortcomings of the proposed controller in [38] is devised, which is easy to design, has a fixed time constant in different operating points, and can work as an inverter and rectifier.

III. LPV LOOP-SHAPING CONTROL DESIGN

As it is seen from (3), the system under study is a MIMO system; hence, a \( 2 \times 2 \) controller must be designed for the system to guarantee stability. To do so, in this study, an LPV loop-shaping controller is designed. This controller is designed such that it addresses the drawbacks of the controller designed in [38].

To design the LPV controller, a vector of scheduling parameters should be chosen, which is selected as \( \mu = [I_{d,0}, \delta_0] \). Hence, the designed controller and the system both are functions of the vector \( \mu \).

Supposing that the desired open-loop transfer function matrix of the system, \( L_D \), is chosen as

\[
L_D = \begin{bmatrix}
kp/s & 0 \\
0 & kp/s
\end{bmatrix},
\]
and assuming that $K(\mu)$ is the designed $2 \times 2$ controller, the open-loop transfer function can be written as

$$L(\mu) = G(\mu) \times K(\mu).$$

In case the open-loop transfer function is equal to the desired transfer function introduced in (4),

$$K(\mu) = G^{-1}(\mu) \times L_D.$$ 

Note that the chosen $L_D$ guarantees stability since the closed-loop transfer functions on the diagonal entries become $K_p$, which is a stable transfer function. Additionally, $L_D$ provides a decoupled system as its off-diagonal elements are zero. To find the controller matrix, $K(\mu)$, the inverse of $G(\mu)$ can be derived as

$$G^{-1}(\mu) = \left( \begin{array}{cc} \frac{2s \sin \delta_0}{3V_s T_{L,0}} & -\frac{2s \cos \delta_0}{2(\tau s + 1) \sin \delta_0} \\ \frac{2s \cos \delta_0}{3V_s T_{L,0}} & \frac{2s \sin \delta_0}{2(\tau s + 1) \cos \delta_0} \end{array} \right).$$

By replacing (7) in (6), the controller matrix becomes

$$K(\mu) = \left( \begin{array}{cc} \frac{k_p 2 \sin \delta_0}{3V_s T_{L,0}} & \frac{k_p 2 \cos \delta_0}{3V_s T_{L,0}} \\ \frac{k_p - 2(\tau s + 1) \cos \delta_0}{2V_s T_{L,0}} & \frac{k_p - 2(\tau s + 1) \sin \delta_0}{3V_s} \end{array} \right).$$

Keeping in mind that $i_q = 0$ A, the apparent power can be written as

$$S = \frac{3}{2} V_s I_d.$$ 

By replacing (9) in (8), the designed controller can be reformulated as

$$K(\mu) = \left( \begin{array}{cc} \frac{k_p \sin \delta_0}{s \times S_0} & -\frac{k_p \cos \delta_0}{s \times S_0} \\ \frac{k_p}{s \times S_0} & \frac{k_p}{s \times S_0} \end{array} \right),$$

in which $S_0$ is the inverter apparent power at the operating point and can be written as $S_0 = \sqrt{P_{\text{ref}}^2 + Q_{\text{ref}}^2}$. Additionally, $\delta_0 = \tan^{-1} \frac{Q_{\text{ref}}}{P_{\text{ref}}}$, and $I_{d,0}$ is found via load-flow equations at steady state. Using these values, the gains of the controller shown in (10) can be adapted to the new operating point of the system upon each change in the set-points. However, the scheduling parameters should match the real values in the circuit during the transients as well. Since the system’s operating point changes with a rise time adjusted by $k_p$, the scheduling parameters can be estimated by applying a filter with a similar time constant, i.e., $\frac{1}{\tau s + 1}$, to $P_{\text{ref}}$ and $Q_{\text{ref}}$ used for adjusting the coefficients in (10) during the transients.

The main advantage of the controller proposed in (10) compared to the control design method in [38] is that (10) is easy to design and does not require system identification and/or solving an optimization-based loop-shaping problem since it provides a parametric loop-shaping controller. Also, unlike the method proposed in [38], (10) schedules its gains based on the operating point of the system. Hence, in contrast to [38], the bandwidth of the system is not changed by changing the IBR’s operating point. Finally, this controller allows the system to perform bidirectionally, i.e., in inverter and rectifier modes. However, the controller designed in [38] does not work in rectifier mode.

As mentioned above, the transfer function in (3) assumes that the grid impedance is known and the current controller can be simplified to a first-order system with a time constant of $\tau$. This transfer function is used to design the controller proposed in (8). Although there are many approaches for impedance estimation in the literature [40], [41], errors in the grid impedance estimation can occur in the system; thus, a robustness analysis of the system against impedance estimation errors is needed.

### A. Robustness Analysis Against Grid Impedance Estimation Error

Supposing that the grid inductance and resistance are known, the PI controller employed in the current controller can be devised such that it has a zero at $Ls + R$, where $L$ is the grid inductance, and $R$ is the grid resistance; hence, the open-loop transfer function of the current controller can be written as $\frac{K_p}{s + \frac{R}{L}}$ or alternatively, $\frac{1}{s + \frac{R}{L} + \frac{1}{\tau s + 1}}$, where $\frac{1}{\tau s + 1} = \tau_1$. If the exact values of the grid impedance and resistance are not known, the open-loop transfer function corresponding to the current controller becomes

$$G_{o,cc}(s) = K_{p,cc} \times \frac{Ls + \hat{R}}{s} \times \frac{1}{Ls + \hat{R}},$$

in which $\hat{L}$ and $\hat{R}$ are the estimated grid inductance and resistance, respectively. In this study, it is supposed that the resistance and inductance estimation error percentages are the same, i.e., $L = a \times L$ and $\hat{R} = a \times R$, in which $a$ denotes the estimation error. With this assumption, the open-loop zero and pole in (11) still cancel each other; however, the estimation error is reflected in the open-loop gain and makes it $a \times K_{p,cc}$. Supposing $\frac{1}{\tau s + 1} = \tau_2$, the current controller closed-loop transfer function can be written as

$$G_{cc}(s) = \frac{1}{\tau_2 s + 1}.$$ 

Replacing (12) in (3) results in

$$G(\mu) = \left( \begin{array}{cc} \frac{3V_s L_{L,0} \sin \delta_0}{2s} & \frac{3V_s \cos \delta_0}{2(\tau s + 1) + 1} \\ \frac{3V_s \cos \delta_0}{2s} & \frac{3V_s \sin \delta_0}{2(\tau s + 1) + 1} \end{array} \right).$$

By replacing (13) in (5), the open-loop transfer function of the system with the estimation error can be written as

$$L(\mu) = G(\mu) \times K(\mu) = \left( \begin{array}{cc} \frac{k_1}{s} + \frac{k_2}{s} \times \frac{\tau_1}{\tau_2 s + 1} & \frac{k_3}{s} \times \frac{\tau_1}{\tau_2 s + 1} \\ \frac{k_2}{s} \times \frac{\tau_1}{\tau_2 s + 1} & \frac{k_3}{s} \times \frac{\tau_1}{\tau_2 s + 1} \end{array} \right),$$

in which

$$k_1 = k_p \sin^2 \delta_0 ; \quad k_2 = k_p \cos^2 \delta_0 ; \quad k_3 = k_p \sin 2\delta_0.$$ 

In order to evaluate the stability of the system, the poles of the system’s closed-loop transfer function must be found. To do so, the closed-loop transfer function can be written as

$$G_{cl}(\mu) = L(\mu) \times (L(\mu) + I)^{-1}.$$

To find the closed-loop system’s poles, the characteristic equation of each entry in (16) should be found. Supposing that the system is minimal (no pole-zero cancellation occurs in the system), it can be shown that each entry’s characteristic equation, $\Delta(s)$, is the numerator of $\det(L(\mu) + I)$. Hence, to have a stable system, the roots of $\Delta(s)$ should be negative. The characteristic equation can be found as

$$\Delta(s) = \left( k_1 k_2 + k_3^2 \right) (\tau_2 s + 1)^2 + \frac{A}{k_1 k_2 + k_3^2} (\tau_1 s + 1)^2 + \frac{B}{k_1 k_2 + k_3^2} (\tau_2 s + 1) + \frac{C}{k_1 k_2 + k_3^2} s \tau_2 s + 1 + \frac{D}{k_1 k_2 + k_3^2} s \tau_2 s + 1.$$

Finally, the transfer function of the system can be written as

$$\Delta(s) = \left( (k_1 k_2 + k_3^2) \tau_2 s + 1 \right)^2 + \left( k_1 k_2 + k_3^2 \right) \tau_1 s + 1)^2 + \left( k_1 k_2 + k_3^2 \right) \tau_2 s + 1 + \left( k_1 k_2 + k_3^2 \right) s \tau_2 s + 1 + s^2 \tau_2 s + 1.$$

(17)
Thus, $\Delta(s)$ can be re-written as
\[
\Delta(s) = \tau_2^2 s^4 + (2 \tau_2 + C \tau_1 \tau_2) s^3 + \{A(\tau_2^2 + \tau_1^2) + (B + C)(\tau_1 + \tau_2) + C \tau_2 + 1\} s^2 + \{2A(\tau_1 + \tau_2) + B(\tau_1 + \tau_2) + 2C\} s + 2A + B. \tag{18}
\]

To have a stable system, the roots of (18) must be negative. To find the stability condition, the Routh-Hurwitz criterion is employed. Based on this criterion, to have a stable system,

1) $2A + B > 0$,
2) $2\tau_2 + C \tau_1 \tau_2 > 0$, and
3) $(2\tau_2 + C \tau_1 \tau_2)[A(\tau_2^2 + \tau_1^2) + (B + C)(\tau_1 + \tau_2) + C \tau_2 + 1] - \tau_2^2[2A(\tau_1 + \tau_2) + B(\tau_1 + \tau_2) + 2C]$
\[
\times \{2A(\tau_1 + \tau_2) + B(\tau_1 + \tau_2) + 2C\} - (2\tau_2 + C \tau_1 \tau_2)^2(2A + B) > 0. \tag{19}
\]

The first condition in (19) is always valid since $2A + B = (k_1 + k_2)^2$. Also, since $C = k_1 + k_2 > 0$, and considering that $\tau_1$ and $\tau_2$ are positive values, the second condition in (19) is always valid. To evaluate the last condition, it should be noted that the current controller’s time constant ($\tau$) is about a couple of ms, hence, the terms containing higher order of $\tau$, i.e., their multiplications or higher powers, can be neglected. By doing so, the third condition can be simplified to $4C \tau_2$, which is always positive.

Therefore, it can be concluded that if the inductance and resistance estimation error percentages are the same, the system will remain stable, while some dynamics are added to the system.

In a general case, when the inductance and resistance estimation errors are random and not necessarily equal, the poles and zero in (11) do not cancel each other, and hence, neglecting the coupling between current’s d and q-components, the current controller closed-loop transfer function becomes
\[
G_{cc_{eual}}(s) = K_p \frac{\hat{L}_s + \hat{R}}{L_s^2 + (R + K_p \tau \hat{L})s + K_p \tau \hat{R}}, \tag{20}
\]
and the system’s transfer function becomes
\[
G(\mu) = \left[\begin{array}{c}
3V_d \cos \delta_1 \sin \delta_2 \\
-3V_d^2 \sin \delta_1 \cos \delta_2 \\
3V_q \sin \delta_1 \sin \delta_2 \\
-3V_q^2 \sin \delta_1 \cos \delta_2 \end{array}\right] G_{cc_{eual}}(s). \tag{21}
\]

In this study, a numerical robustness analysis of the system introduced in Table I with the transfer function shown in (21) and the controller shown in (10) is done. In this analysis, it is assumed that the resistance estimation error is 10%, and the inductance estimation error varies from 5% to 95% with 5% steps. As discussed, the closed-loop system’s poles are the characteristic equation’s roots, $\Delta(s)$. Therefore, to assess the stability, the $\Delta(s)$’s roots are shown in Fig. 3. The arrows show the estimation error increase. It is seen that the overall closed-loop system remains stable despite different estimation errors resistance and inductance have.

It should be noted that the presence of error in the inductance and resistance estimations adds more poles and zeros to the closed-loop transfer function, and hence, some new dynamics will be added to the system; nonetheless, it is not likely that these estimation errors threat the system’s stability.

IV. Performance Evaluation

To assess the performance of the proposed controller, the system shown in Fig. 1 is simulated in Matlab/PLECS. Also, the performance of the designed controller is validated using an experimental setup based on Imperex B-Box Controller and Regatron AC Power Supply. In the simulation tests, the ability of the controller for coping with active and reactive power set-point changes, bi-directional performance, phase jump, and fault occurrence is validated under weak and strong grid conditions. In addition, the experimental setup is employed to validate the ability of the controller to cope with active and reactive power set-point changes.

A. Simulation Results

To evaluate the performance of the proposed controller, different tests are conducted in Matlab/PLECS when the system shown in Fig. 1 and with the parameters in Table I is subjected to faults, grid phase jumps, and set-point changes both for strong and weak grids.

1) Strong grid ($SCR=17$): In this part, the grid inductance and resistance are chosen as $L_g=51 \mu H$ and $R_g=5.4 mO$, respectively, making $SCR=17$. Also, the PI controller employed in the current controller is $2500 \times 146.0 \times 10^{-3} + 0.015s$, making the current controller a first-order system with $\tau=0.0004 s$. Besides, the $k_p$ is chosen equal to 100, causing a 10 ms overall time constant. Initially, the converter injects 1 MW and 1 MVAR of active and reactive power, respectively. At $t=0.05 s$, the active power set-point is changed to 2 MW. At $t=0.15 s$, the reactive power set-point steps up to 4 MVAR, while the active power remains unchanged. At $t=0.25 s$, the power factor is changed from 0.44 to 0.9, changing the active and reactive power set-points to 4 MW and 2 MVAR, respectively. Finally, at $t=0.35 s$, the converter’s active power set-point is changed to $-2 MW$, making the converter to work as a rectifier. Fig. 4 depicts the simulation results of this system. Fig. 4 (a) shows the three-phase output current, Fig. 4 (b) is the converter’s filter’s output line-to-neutral three-phase voltage, Fig. 4 (c) is d and q-components of the converter’s output current, Fig. 4 (d) is d and q-components of the converter’s terminal voltage, Fig. 4 (e) depicts the output active and reactive power, and finally, Fig. 4 (f) shows the angular frequency generated by the controller. It is seen that the controller tracks the reference active and reactive power with zero steady-state error, and the rise time of the system is about 10 ms, as expected.

Also, Fig. 5 shows the simulation results of the system upon a fault occurrence and phase jump. Initially, the converter injects $P=4 MW$ and $Q=2 MVAR$ into the grid. At $t=0.05 s$, a 100 ms three-phase fault that makes the grid voltage 0.2 pu occurs in the system. It is seen that the injected power is recovered since based on the physical attributes of the system, the reference active power does not exceed the line power transmission limit. At $t=0.15 s$, the fault is cleared, and the grid voltage is recovered. It is seen that the controller copes with the fault clearance. Additionally, at $t=0.25 s$, the
a 20° phase jump occurs at the grid side voltage. It is seen that the system can maintain stability and injects the reference active and reactive powers into the grid after the phase jump occurrence.

2) Weak grid (SCR=1.2): In this part, the grid inductance and resistance are chosen as $L_g=680 \ \mu H$ and $R_g=72 \ \text{m} \Omega$, respectively, making SCR=1.2. Also, the PI controller employed in the current controller is $2500 \times \frac{770 \times 10^{-6} s + 0.082}{s}$, making the current controller a first-order system with $\tau=0.0004 \text{s}$. Similar to the previous test, the $k_p$ is chosen equal to 100, causing a 10 ms overall time constant. Initially, the converter injects 1 MW and 1 MVAR of active and reactive power, respectively. At $t=0.05 \text{s}$, the active power set-point is changed to 2 MW. At $t=0.15 \text{s}$, the reactive power set-point steps up to 4 MVAR, while the active power remains unchanged. At $t=0.25 \text{s}$, the power factor is changed from 0.44 to 0.9, changing the active and reactive power set-points to 4 MW and 2 MVAR, respectively. Finally, at $t=0.35 \text{s}$, the converter’s active power set-point is changed to $-2 \text{ MW}$, making the converter to work as a rectifier. Fig. 6 depicts the simulation results of this system. Fig. 6 (a) shows the three-phase output current, Fig. 6 (b) depicts the converter’s filter’s output three-phase line-to-neutral voltage, Fig. 6 (c), is d- and q-components of the converter’s output current, Fig. 6 (d), is d- and q-components of the converter’s terminal voltage, Fig. 6 (e) depicts the output active and reactive power, and finally, Fig. 6 (f) shows the angular frequency generated by the controller. It is seen that the controller tracks the reference active and reactive power with zero steady-state error, and the rise time of the system is about 10 ms, as expected.

Also, Fig. 7 shows the simulation results of the system upon a fault occurrence and phase jump. Initially, the converter injects $P=4 \text{ MW}$ and $Q=2 \text{ MVAR}$ into the grid. At $t=0.05 \text{s}$, a 100 ms three-phase fault that makes the grid voltage 0.2 pu occurs in the system. However, it is seen that the system output power and generated angular frequency have a constant steady-state error during the fault. The reason is in (8), the controllers corresponding to $\Delta \omega$ arejust proportional controllers. This prevents creating accumulative error in the generated frequency, and hence, the output active power. After the fault clearance ($t=0.15 \text{s}$), the line’s power transferred limit is increased again, and the output active power is recovered. In addition, at $t=0.25 \text{s}$, a 20° phase jump occurs at the grid side voltage. It is seen that the system can maintain stability and inject the reference active and reactive powers into the grid after the phase jump occurrence.

3) Comparative Analysis: To compare the performance of the proposed control design with the VMDPC while connected to a weak grid, similar active and reactive reference changes are applied to a system that has the VMDPC control structure. Using the VMDPC, the system is unable to work in a weak grid unless the gain of the system, and hence its speed, is decreased drastically. Fig. 8 shows the simulation results of the VMDPC. Similar to the tests applied to the proposed controller in this study, initially, the IBR injects 1 MW and 1 MVAR. At $t=1 \text{s}$, the active power set-point is changed to 2 MW. At $t=3.5 \text{s}$, the reactive power set-point steps up to 4 MVAR, while the active power remains unchanged. At $t=6 \text{s}$, the power factor is changed from 0.44 to 0.9, changing the active and reactive power set-points to 4 MW and 2 MVAR, respectively. Finally, at $t=8.5 \text{s}$, the converter’s active power set-point is changed to $-2 \text{ MW}$, making the converter a rectifier. It is seen that the rise time of the system is about 1 s, which is 100 times slower than the controller proposed in this study. It should be
Fig. 6. The simulation results of the system with SCR=1.2 upon various active and reactive power references change: a) three-phase grid currents ($i_{abc}$), b) the grid three-phase line-to-neutral voltage, c) the dq-components of the grid current ($i_{dq}$), d) the dq-components of the terminal voltage ($v_{t,dq}$), e) the injected active and reactive power into the grid, and f) the estimated angular frequency generated by the controller.

Fig. 7. The simulation results of the system with SCR=1.2 upon a three-phase fault and grid voltage phase jump occurrence: a) three-phase grid currents ($i_{abc}$), b) the grid three-phase line-to-neutral voltage, c) the dq-components of the grid current ($i_{dq}$), d) the dq-components of the terminal voltage ($v_{t,dq}$), e) the injected active and reactive power into the grid, and f) the estimated angular frequency generated by the controller.

noted that, when connected to a weak grid, the VMDPC becomes unstable if the overall gain of the system increases (to make the system faster).

Additionally, a 100 ms three-phase grid-side fault that causes the grid voltage to drop to 0.2 pu followed by a 20° grid voltage phase jump is applied to the same system using the VMDPC connected to the same weak grid. The simulation result is presented in Fig. 9. At $t=1$, the three-phase fault takes place and gets clear after 100 ms. It is seen that although the controller can stabilize the system, the settling time is about 1.5 s, which is much slower than the proposed controller’s performance. Additionally, at $t=5$ s, a 20° phase jump occurs at the grid voltage. It is seen that, contrary to the introduced control method in this study, the VMDPC fails to stabilize the system upon the phase jump.

As a conclusion, the LPV controller proposed in this paper has a major advantage compared with the VMDPC while the system is connected to a weak grid. First, the proposed controller is quite faster compared with the VMDPC. Besides, it is seen that in case of sever disturbance occurrence, such as a phase jump, the VMDPC cannot stabilize the system while the proposed LPV controller used for the control structure in [38] rejects the disturbance properly.

**TABLE II**

| Quantity | Value     | Comment               |
|----------|-----------|-----------------------|
| $L_f$    | 14 mH     | Inverter Filter Inductance |
| $R_f$    | 0.5 Ω     | Series Resistance of $L_f$ |
| $S_{base}$ | 1 KVA     | Inverter Rated Power   |
| $v_g$    | 100 V     | Grid Line-to-Line Voltage (rms) |
| $v_{dc}$ | 300 V     | DC Bus Voltage         |
| $f_{sw}$ | 20 kHz    | PWM Carrier Frequency  |
| $f_{50}$ | 50 Hz     | System Nominal Frequency |
| $f_{100}$| 200 Hz    | Power Measurement LPF Bandwidth |
the inverter injects no active or reactive powers into the grid. At
mH and 1
power factor changes is evaluated. These tests, the performance of the controller for active, reactive structures are implemented in Matlab/Simulink, and the control interface is done via Imperix ACG BB Control software. Table II shows the system’s parameters. Also, the inner current controller’s gain is set to 2500. Additionally, the parameter \(k_p\) is set to 25 in all tests, making the rise time of the system equal to 40 ms. In these tests, the performance of the controller for active, reactive and power factor changes is evaluated.

In the first test, the grid inductance and resistance are set to 1 mH and 0.03 \(\Omega\), respectively, making the SCR=31. Initially, the inverter injects no active or reactive power into the grid. At \(t=0.1\) s, the reactive power reference is changed to 800 VAR. At \(t=0.35\) s, the active power set-point is changed to 600 W. Finally, at \(t=0.6\) s, the power factor is changed from 0.6 to 0.8, while the apparent power is kept at 1 pu. Fig. 11 shows the experimental results of this test. Fig. 11(a) is the three-phase injected currents, Fig. 11(b) is the three-phase grid line-to-neutral voltages, Fig. 11(c) shows the active and reactive power, Fig. 11(d) depicts the d and q-components of the injected current, as well as the generated \(i_d\) reference value by the controller, Fig. 11(e) depicts the d and q-components of the terminal voltage, and finally, Fig. 11(f) shows the angular frequency estimated by the controller. It is seen that the system’s rise time is about 40 ms, and the controller regulates the \(P\) and \(Q\) upon changes in their references.

In the second test, the grid inductance and resistance are set to 16 mH and 0.53 \(\Omega\), respectively, making the SCR=1.9. Initially, the inverter injects no active or reactive powers into the grid. At \(t=0.1\) s, the reactive power reference is changed to 800 VAR. At \(t=0.35\) s, the active power set-point is changed to 600 W. Finally, at \(t=0.6\) s, the power factor is changed from 0.6 to 0.8, while the apparent power is kept at 1 pu. Fig. 12 shows the experimental results of this test. Fig. 12(a) is the three-phase injected currents, Fig. 12(b) is the three-phase grid line-to-neutral voltages, Fig. 12(c) shows the active and reactive power, Fig. 12(d) depicts the d and q-components of the injected current, as well as the generated \(i_d\) reference value by the controller, Fig. 11(e) depicts the d and q-components of the terminal voltage, and finally, Fig. 12(f) shows the angular frequency estimated by the controller. It is seen that the system’s rise time is about 40 ms, and the controller regulates the \(P\) and \(Q\) upon changes in their references.

Based on the results shown in the simulation and experimental tests, it is seen that the proposed controller rectifies the issues present in [38] control design method, i.e., has a constant time constant for different operating points and can perform as a rectifier with no need to modify the controller. Besides, contrary to the proposed method in [38], the proposed controller has a parametric, easy-to-design structure.

It is worth mentioning that various experimental tests to verify the ability of the system in coping with a 20° phase jump and grid fault are done, which could not be put in the manuscript due to the length issue and could be provided in the second review.

V. CONCLUSIONS

This paper proposes an LPV loop-shaping controller for power-synchronized PLL-less GFLIs. IBRs enjoying this structure do not have any stability issues while connected to very strong or very weak grids; also, it is shown that using this controller, the performance of the system does not rely on the operating point, i.e., it varies its gains such that it maintains a constant rise time while working in different operating points. Additionally, the designed controller allows the system to work as a bi-directional converter. Besides, the proposed controller shapes the system’s
open-loop transfer function such that IBR’s output active and reactive power are decoupled, and the rise time of the system can easily be tuned by changing the system’s open-loop gain. Moreover, it is mathematically shown that the system is robust despite discrepancies that might occur in grid inductance and resistance estimation. Finally, the performance of the proposed control structure is validated via simulation and in experiment.

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