Limitations on Transversal Gates for Hypergraph Product Codes

Simon Burton and Dan Browne

Abstract—In a fault tolerant quantum computer, quantum codes are expected to serve the conflicting purposes of protecting quantum information while also allowing that information to be manipulated by fault-tolerant gates. We introduce a new technique for placing limitations on such gates, and apply this technique to a class of quantum codes known as hypergraph product codes contained within the vertical sector. These codes are constructed from input which is a pair of classical linear codes, and generalize the Kitaev surface code which is the hypergraph product of classical repetition codes. We provide a necessary condition on these input codes, under which the resulting hypergraph product code has transversal gates limited to the Clifford group. We conjecture that this condition is satisfied by all \([n, k, d]\) Gallagher codes with \(d \geq 3\) and \(k \leq n/2\). This work is a generalization of an argument due to Bravyi and König, and we also conjecture this is a refinement of the recent notion of disjointness due to Jochym-O'Connor et. al.

I. INTRODUCTION

S INCE the dawn of time humankind has striven to calculate and compute. More recently we strive to build a quantum computer. Like any computer, this has two ingredients: a reliable storage of information, and the ability to manipulate this information. (Input/output comes later.) These two ingredients are at odds with each other: one tries to prohibit any change, and the other is all about change. We seek a quantum code that can do both.

Because of this antagonistic relationship between state preservation and state manipulation, knowledge of one side often implies something about the other side. Indeed, the quantum code literature has many results that explore this boundary, both “no-go” and “go” results. The first go result was the realization that CSS codes allow for transversal CNOT gates [1], [2], [3]. One of the first no-go results was by Eastin and Knill [4] which found that for stabilizer codes the transversal gateset is finite and therefore not universal. A common proof technique has emerged, for both go and no-go results. This technique relates a condition on the intersection of generic logical operators to the existence of transversal gates. Finding codes where the logical operators have large overlap with each other leads to transversal gates, for example the triorthogonal codes lead to transversal non-Clifford gates [5], [6], [7] as well as the color codes [8]. Conversely, showing that generic logical operators have small overlap with each other restricts the available transversal gates [9], [10]. These no-go results have also been weakened to apply to locality preserving logical gates, both in the stabilizer code setting [11], [12] and for topological quantum field theories [13].

The Kitaev code, which has the geometry of a torus, and its flat cousin, called the surface code, are the leading contenders for quantum codes [14]. Industry has this goal within sight. To reach universality with these codes we can do so via braiding [15], lattice surgery [16], [17], [18], or twists [19]. Other approaches use a 3D architecture [20], possibly with time [21], [22] as one of the dimensions. The problem with all these topological codes is that they have a vanishing rate: as we build larger codes to decrease the error rate, the number of logical qubits per physical qubit tends to zero.

Looking over the implementation horizon we find codes with vastly superior performance: the family of hypergraph product codes [23]. These still have a bounded number of interactions between physical qubits; with the additional difficulty that these are non-local interactions. The above story performed with the surface code has a parallel story for hypergraph product codes, which are a direct generalization of the surface code. Good decoders exist [24], and we can braid punctures to perform Clifford gates [25]. More generally, the homological product codes [26] can be made universal with code switching techniques [27].

In this work we show that a family of hypergraph product codes are unable to achieve non-Clifford gates transversally. This is a generalization of an argument of Bravyi and König [9] which applies to surface/toric codes. Note, we are specifically not referring to the main theorem of Bravyi and König [9], which applies to topological stabilizer codes of arbitrary dimension. Instead, we focus on a motivational heuristic from this paper, which we call the Bravyi-König argument. This relies on computing the intersections of two sets of generic logical operators, and showing that this intersection is correctable. See Fig. 1. For the surface code, or toric code, this intersection has size \(O(1)\) and so cannot support any logical operator as the weight of these scales with the lattice size.

For hypergraph product codes, the situation is more delicate. In this case the intersection of generic logical operators has support on an extensive set of qubits, and so we cannot rely on constant distance bounds. This intersection region will have size \(O(k^2)\), where \(k\) is the dimension of the underlying classical codespace. At first glance this region would seem unlikely to be correctable, as \(k\) is comparable to the distance
of the quantum hypergraph product code. However, we show that under some mild assumptions on the underlying classical codes (we call this assumption robustness) this region is indeed correctable and so the Bravyi-König argument can be extended to these codes. This is the main result of the paper, Theorem 15 below.

The Bravyi and König argument has also been generalized to arbitrary stabilizer codes by the notion of disjointness due to Jochym-O’Connor et. al. [10]. This however is still only sensitive to the distance of the code. Our techniques can therefore lead the way to a refinement of these disjointness bounds.

In summary, these hypergraph product codes may be excellent for protecting quantum information, but there are restrictions on how this quantum information can then be manipulated. This work is the first result demonstrating such restrictions.

II. QUANTUM STABILIZER CODES

Fix a number of qubits $n$. The Pauli group $P_n$ is generated by $n$-fold tensor products of the Pauli matrices

$$I_2 = \begin{pmatrix} 1 & 0 \\ 0 & 1 \end{pmatrix}, \quad X = \begin{pmatrix} 0 & 1 \\ 1 & 0 \end{pmatrix},$$

$$Z = \begin{pmatrix} 1 & 0 \\ 0 & -1 \end{pmatrix}, \quad Y = \begin{pmatrix} 0 & -i \\ i & 0 \end{pmatrix},$$

and phases $\{\pm 1, \pm i\}$. Writing $V = \mathbb{C}^2$ for the single qubit Hilbert space, the group $P_n$ acts tautologically on the $n$-qubit Hilbert space $V^\otimes n$ of dimension $2^n$. Any subgroup $S \leq P_n$ will fix a subspace of $V^\otimes n$ pointwise:

$$\text{Fix}_V(S) := \{v \in V^\otimes n \mid gv = v, \quad \forall g \in S\},$$

and any subspace $W \leq V^\otimes n$ will be stabilized by a subgroup of $P_n$.

$$\text{Stab}_{P_n}(W) := \{g \in P_n \mid gw = w, \quad \forall w \in W\}.$$  

An abelian subgroup $S \leq P_n$ that does not contain $-I_2$ has particularly nice structure. We call any such subgroup $S$ a stabilizer group. Together with the tautological action this is known as a (quantum) stabilizer code. The subspace $\text{Fix}_V(S)$ is the protected codespace.

Operators $g \in P_n$ built as tensor product of $I_2$ and $X$ are called $X$-type operators and denoted $P_{X, n}$. Similarly, tensor products of $I_2$ and $Z$ are called $Z$-type operators and denoted $P_{Z, n}$. If $g \in P_n$ commutes with every $h \in S$, then $G$ will preserve the codespace, without needing to fix it pointwise. Up to stabilizers, these are the logical operators of the code which form a group isomorphic to $P_k$, for some $k$, and so there are $k$ logical encoded qubits [28].

When $S$ is generated by $X$-type or $Z$-type operators (inclusively) we call this a CSS stabilizer code [1], [29]. We state a simple lemma describing the logical operators of these codes. Enumerating the $n$ qubits as $\{1, \ldots, n\}$, we say that an operator $g \in P_n$ has support on a subset $\gamma \subseteq \{1, \ldots, n\}$ when the tensor factors of $g$ are equal to $I_2$ at indexes not in $\gamma$.

Lemma 1. In a CSS stabilizer code, if a set of qubits $\gamma$ supports a non-trivial logical operator, then $\gamma$ supports either a non-trivial $X$-type logical operator, or a non-trivial $Z$-type logical operator (or both).

Proof: An operator $g \in P_n$ commutes with every element of $S$ exactly when it commutes with every generator of $S$. Therefore, given a logical operator that is built from both $X$ and $Z$ Pauli operators, replacing $X$ by $I$ will also give an operator that commutes with the stabilizers, and similarly replacing $Z$ by $I$ will give an operator that commutes with the stabilizers. One or both of these must then be a logical operator.

We recall the following definition and Lemma about the Clifford hierarchy [30], which is found in reference [11] Appendix A. The $n$-qubit Clifford hierarchy is a sequence $\{C_{0, n}, C_{1, n}, \ldots\}$ of sets of unitary operators, $C_l \subseteq U(V^\otimes n)$. These are defined inductively as $C_0 := P_n$, and

$$C_{l+1} := \{u \in U(V^\otimes n) \mid upu^{-1} \in C_l, \quad \forall p \in P_n\}$$

for $l \geq 1$. We also define $C_{0, n}$ to be the phase group $C_0 = \{\pm I_2^\otimes n, \pm i I_2^\otimes n\}$. The sets $C_0, C_1$ and $C_2$ are closed under inverse and multiplication and so these are groups. Specifically, $C_2$ is called the $n$-qubit Clifford group. For $l > 2$, $C_0$ is no longer a group [5].

Lemma 2. A unitary $u \in U(V^\otimes n)$ is in the $n$-qubit Clifford group exactly when

$$[upu^{-1}, q] = upu^{-1} q upu^{-1} u^{-1} q^{-1} \in C_0$$

for all $p, q \in P_n$.

Proof: The set of all unitaries $u \in U(V^\otimes n)$ such that $upu^{-1}p^{-1} = \pm I$ for all $p \in P_n$, equals the Pauli group. Therefore the result follows from the definition of $C_2$.

A transversal gate for a stabilizer code $S \leq P_n$ is an operator of the form $u = u_1 \otimes u_2 \otimes \cdots \otimes u_n$, with each $u_i$ an arbitrary unitary on $V$, such that $u$ commutes with the projector onto the codespace. Just like the logical operators of the code, these transversal gates also preserve the codespace, without needing to fix it pointwise. The set of all such transversal gates is a subgroup of $U(V^\otimes n)$.

For any operator $u \in U(V^\otimes n)$ the support of $u$ is the set of qubits, or indices, on which $u$ acts non-trivially. Given a stabilizer code $S$, a subset of the qubits is called correctable when the only logical operators supported on the subset are in $C_0$. Any logical operator in $C_{l, n}$ will be called trivial logical operator.

The central idea behind the proof of Theorem 15 is summarized in the following result.

Theorem 3. (Bravyi-König argument [9]) Let $S$ be a stabilizer code such that the intersection of the support of some representatives for any two logical operators is a correctable set of qubits. Then the action of any transversal gate for $S$ on the $k$ logical qubits is an element of the Clifford group $C_2^k$. 

Proof: This is a consequence of the previous lemma. Let $u \in U(V^\otimes n)$ be a transversal gate for $S$. This operator restricts to a unitary on the logical qubits $\tilde{u} \in U(V^\otimes k)$. Similarly for
logical operators $p, q \in \mathcal{P}_n$ and restriction $\hat{p}, \hat{q} \in \mathcal{P}_k$. Then $[upu^{-1}, q] \in U(V)^{\otimes n}$ is non-trivial only on the intersection of the support of $p$ and $q$. Because this region is correctable, we have $[upu^{-1}, q] \in C^0_n$, which implies $[\tilde{u} \tilde{p} \tilde{u}^{-1}, \tilde{q}] \in C^0_k$. 

Note that in the application of this theorem below we choose representatives for the logical operators when computing these intersections. This means, for example, that a logical operator may have trivial intersection with itself, by using two different representatives.

The groups $\mathcal{P}^X_n$ and $\mathcal{P}^Z_n$ are abelian, and moreover, these groups are isomorphic to the additive group notation. Products of operators are computed as sums of $\mathcal{P}_2$ vectors. Commutators of $X$-type and $Z$-type operators are calculated via the evident $\mathbb{F}_2$ inner product. For $u \in \mathbb{F}_2^n$ an $X$-type operator, and $v \in \mathbb{F}_2^n$ a $Z$-type operator, this is just $u^T v \in \mathbb{F}_2$.

III. LINEAR CODES

We work using linear vector spaces over the field with two elements $\mathbb{F}_2$. Such vector spaces are constructed as $\mathbb{F}_2^n$ for some number $n$. In order to simplify notation, we denote such vector spaces of dimension $n$ using boldface: $\mathbf{n} \coloneqq \mathbb{F}_2^n$. The idea is to confuse the distinction between the vector spaces and their dimensions. This is because the one determines the other, and calculations done with dimensions often have formally identical calculations done with vector spaces. Dimensions of direct sums add, dimensions of tensor products multiply, etc.

In this paper we make use of an arrow notation for linear maps. In terms of matrices, a linear map $\mathbf{n} \xrightarrow{f} \mathbf{m}$ has $n$ columns and $m$ rows. This is a kind of “dimensional analysis” that keeps track of what we are allowed to multiply $f$ with. Any arrow terminating on $\mathbf{n}$ can multiply on the right of $f$, and any arrow starting on $\mathbf{m}$ can multiply on the left of $f$. This has the effect, as with all dimensional analysis, of suggesting calculations to do, by combining arrows together. We use this in the proof of Lemma 7 below.

A linear map $\mathbf{n} \xrightarrow{\partial} \mathbf{m}$ has a kernel which records dependencies among the columns of $\partial$. This is a linear map $\mathbf{k} \xrightarrow{\ker(\partial)} \mathbf{n}$ such that $\partial \ker(\partial) = 0$. Furthermore, this map is characterized by a universal property: any other linear map $\mathbf{m} \xrightarrow{\bullet} \mathbf{n}$ with $\partial \bullet = 0$ factors uniquely through $\ker(\partial)$. In other words, there is a unique map $f' : \mathbf{m} \rightarrow \mathbf{k}$ such that $f = \ker(\partial) \cdot f'$.

Similarly, the cokernel records dependencies among the rows of $\partial$. This is a linear map $\mathbf{m} \xrightarrow{\ker(\partial)^\top} \mathbf{k}^\top$ such that $\ker(\partial)^\top \cdot \partial = 0$. (Here, the symbol $^\top$ in the notation $\mathbf{k}^\top$ is a formal decoration on the symbol $\mathbf{k}$.) Any other linear map $\mathbf{m} \xrightarrow{g \partial} \mathbf{n}$ with $g \partial = 0$ factors uniquely through $\ker(\partial)$. Evidently we have the relation $\ker(\partial) = \ker(\partial^\top)$.

Using both the kernel and the cokernel we can construct the following sequence of linear maps:

$0 \rightarrow \mathbf{k} \xrightarrow{\ker(\partial)} \mathbf{n} \xrightarrow{\partial} \mathbf{m} \xrightarrow{\ker(\partial)^\top} \mathbf{k}^\top \rightarrow 0.$

This sequence is exact, so we have the fundamental identity

$k - n + m - k^\top = 0.$

(1)

This is the Euler characteristic of this sequence. (See the book [31], in particular section 5.9, for a user-friendly introduction to sequences, homology and the Euler characteristic.)

The span of $\mathbf{n} \xrightarrow{\partial} \mathbf{m}$ is a linear map $\mathbf{m} \xrightarrow{\ker(\partial)^\top} \mathbf{n}$ that factors through $\partial$. This map is also characterized by a universal property: any other linear map with codomain $\mathbf{m}$ that factors through $\partial$, factors uniquely through $\ker(\partial)^\top$. The span is also called the column-span, or image. One way to construct $\ker(\partial)^\top$ is to take $m - k^\top$ linearly independent columns of the matrix for $\partial$. Dually, the cospan of $\mathbf{n} \xrightarrow{\partial} \mathbf{m}$ is a linear map $\mathbf{n} \xrightarrow{\cospan(\partial)} \mathbf{m} - \mathbf{k}$ that factors through $\partial$ (on the other side). The universal property characterizing the cospan is that any other linear map with domain $\mathbf{n}$ factoring through $\partial$ will factor uniquely through $\cospan(\partial)$. The cospan is also called the row-span. One way to construct $\cospan(\partial)$ is to take $n - k$ linearly independent rows of the matrix for $\partial$. From Eq. (1) we find that $m - k^\top = n - k$ which is the familiar fact that the column-span and row-span of a matrix have the same dimension.

A classical linear code is usually defined to be a subspace of some finite dimensional vector space $\mathbb{F}_2^n$. We take a relentlessly “active” approach to linear algebra: the maps are primary, not the spaces. For our purposes, a classical linear code is a linear map $\mathbf{n} \xrightarrow{\partial} \mathbf{m}$. The codespace is the kernel of this map. We will avoid the temptation to define the cocodespace as the cokernel. We also call $\mathbf{n} \xrightarrow{\partial} \mathbf{m}$ a parity check matrix, and $\ker(\partial)^\top$ the generator matrix for this code.

A quantum code is defined to be a pair of linear maps

$\mathbf{m}_Z \xrightarrow{H^T_{XZ}} \mathbf{n}_X \xrightarrow{H_X} \mathbf{m}_X$

such that $H_X H^T_{XZ} = 0$. From this identity it follows we can take the quotients

$L^\top_{Z} = \ker(H_X) / \ker(H^T_{XZ}), \quad L_X = coker(H^T_{Z}) / \ker(H_X)$

which we call the logical operators for the code. We will use the following block matrix notation for these quotients:

$L^\top_{Z} = \left( \begin{array}{c} \ker(H_X) \\ H^T_{XZ} \end{array} \right), \quad L_X = \left( \begin{array}{c} \ker(H^T_{Z}) \\ H_X \end{array} \right),$

where the dashed line indicates the modulo (quotient). This is read as “logical operators modulo stabilizers.” The dashed line separates the subspace of $\mathbf{n}$ spanned by logical operators from the subspace of $\mathbf{n}$ spanned by stabilizer operators.

Fig. 1: The intersection of the support of two complete sets of logical operators is correctable under the hypothesis of Theorem 15.
Example 4. The Steane code is a quantum code that uses 7 qubits, with 3 stabilizers of X-type and 3 stabilizers of Z-type:

\[
3 \xrightarrow{H_X} 7 \xrightarrow{H_Z} 3
\]

The matrices for \(H_X\) and \(H_Z\) are the same:

\[
H_X = H_Z = \begin{pmatrix}
1 & 1 & 1 & 1 & \ldots \\
1 & 1 & . & 1 & . \\
. & 1 & 1 & 1 & 1 \\
. & . & . & . & . \\
\end{pmatrix},
\]

\[
\ker(H_X) = \begin{pmatrix}
1 \\
1 \\
1 \\
. \\
. \\
\end{pmatrix}, \quad coker(H_X^\top) = \begin{pmatrix}
1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 \\
1 & 1 & 1 & . & . & . & . & . \\
1 & 1 & . & . & . & . & . & . \\
. & . & . & . & . & . & . & . \\
\end{pmatrix},
\]

and we package all this information into the block matrix notation for the logical operators:

\[
L_Z^\top = \begin{pmatrix}
1 & 1 & 1 & 1 & . \\
1 & 1 & 1 & . & . \\
1 & 1 & . & . & . \\
. & . & . & . & . \\
\end{pmatrix},
\]

\[
L_X = \begin{pmatrix}
. & . & . & . & . \\
1 & 1 & 1 & . & . \\
1 & 1 & . & . & . \\
. & . & . & . & . \\
\end{pmatrix}.
\]

Example 5. The repetition code \(4 \xrightarrow{\partial} 3\) is given by the parity check matrix

\[
\partial = \begin{pmatrix}
1 & 1 & . \\
. & 1 & 1 \\
. & . & 1 \\
\end{pmatrix}.
\]

This linear map is only 1-puncturable because it contains all the weight two vectors in its cospan. (If we added a column of zeros to this matrix then there would be certain weight two vectors missing from the cospan, and the map would become 2-puncturable.) The generator matrix for the code \(4 \xrightarrow{\partial} 3\) is given by \(G = \ker(\partial)^\top = (1111)\), and we can choose any three element subset of \([n = 4]\) as a puncture of \(G\). Moreover, we can choose any two element subset \(\gamma \subset [4]\) as a 2-puncturable, and then letting \(\delta = [4] - \gamma\), we see that \(\delta\) is also a 2-puncturable of \(G\) such that \(\gamma \cap \delta\) is empty. Therefore we have demonstrated that \(G\) is 2-bipuncturable.

More generally, given a classical code with distance \(d\), any subset of \([n]\) with size less than \(d\) serves as a puncture for the generating matrix, and so this matrix is \((d-1)\)-puncturable.

Lemma 6. Any linear map \(\partial : n \rightarrow m\) with \(\ker(\partial) : k \rightarrow n\), is \(k\)-puncturable.

Proof: We can row-reduce \(\partial\) without changing any of the punctures. By reordering the columns, the row-reduced \(\partial\) has block matrix form

\[
\partial = \begin{pmatrix}
I_{n-k} & J \\
0 & 0
\end{pmatrix}
\]

where \(J\) is an arbitrary matrix with \(k\) columns. Puncture these \(k\) columns.

IV. Punctures

We write the generic set of size \(j\) as \([j]\), which we also identify with the canonical basis of \(\mathbb{F}_2^j\). Given a subset \(\gamma \subset [j]\), we say a vector \(v \in \mathbb{F}_2^j\) has support on \(\gamma\) if the non-zero components of \(v\) form a subset of \(\gamma\), in symbols: \(\{i \mid v_i \neq 0\} \subset \gamma\).

A linear map \(G : n \rightarrow k\) is called \(e\)-puncturable, for some natural number \(e\), if there exists a subset \(\gamma \subset [n]\) of size \(e\) such that no non-zero vector in the cospan of \(G\) has support on \(\gamma\). In terms of the matrix for \(G\) this corresponds to being able to delete the columns indexed by \(\gamma\) while maintaining the rank of \(G\). We call such a set \(\gamma\) an \(e\)-puncture of \(G\), or a puncture of \(G\) when \(e\) is understood. When \(G\) is the generator matrix of a classical code we recover the notion of puncturing a classical code (\(e\) times) well known in the literature; in this case \(\gamma\) corresponds to a correctable set of erasures for the code [32]. However, we also apply this definition to arbitrary linear maps.

Going further, a linear map \(G : n \rightarrow k\) is called \(e\)-bipuncturable, for some natural number \(e\), if there exists two disjoint subsets \(\gamma \subset [n], \delta \subset [n]\), such that \(\gamma\) is an \(e\)-puncture of \(G\) and \(\delta\) is an \(e\)-puncture of \(G\) when \(e\) is understood.

V. Hypergraph Product Codes

Given classical codes \(A = \{n_a \xrightarrow{\partial_a} m_a\}\) and \(B = \{n_b \xrightarrow{\partial_b} m_b\}\) we define a quantum code \(A \otimes B\) as:

\[
n_a \otimes n_b \xrightarrow{H_{X_Z}} n_a \otimes m_b, \quad n_a \otimes m_b \xrightarrow{H_{Y_Z}} m_a \otimes m_b.
\] (2)

Using block matrix notation,

\[
H_{Z_X} = \begin{pmatrix}
I_{n_a} \otimes \partial_b \\
\partial_a \otimes I_{m_b}
\end{pmatrix}, \quad H_X = \begin{pmatrix}
\partial_a \otimes I_{m_b} & I_{m_a} \otimes \partial_b
\end{pmatrix}.
\] (3)

A quick calculation shows that \(H_X H_{Z_X} = 2 \partial_b \otimes \partial_b = 0\) thanks to \(\mathbb{F}_2\) arithmetic, and so this is indeed a quantum code. This hypergraph product code \(A \otimes B\) can also be notated as the following diagram

\[
\begin{array}{ccc}
A & \xrightarrow{\partial_a} & B \\
\otimes & & \otimes \\
\downarrow & & \downarrow \\
C & \xrightarrow{\partial_b} & D
\end{array}
\]
This article has been accepted for publication in a future issue of this journal, but has not been fully edited. Content may change prior to final publication. Citation information: DOI 10.1109/TIT.2021.3131043, IEEE Transactions on Information Theory

...operators. A similar result is found in the reference [23], because this is indeed the tensor product of \( A \otimes B \). This product is also known as the homological product [26].

The following result counts the number of independent logical operators. A similar result is found in the reference [23], and for completeness we provide another proof.

**Lemma 7.** With the above notation, the quantum code \( A \otimes B \) has \( k \) logical qubits given by \( k = k_a b^T + k_e b_b \).

**Proof:** By inspecting (3) we find linear maps:

\[
\begin{align*}
    k_a \otimes k_b & \rightarrow_{\ker(H^T_X)} n_a \otimes n_b \\
    n_a \otimes n_b & \rightarrow_{\ker(H^T_X)} m_a \otimes m_b
\end{align*}
\]

and

\[
\begin{align*}
    m_a \otimes m_b & \rightarrow_{coker(H^T_X)} k_a \oplus k_b
\end{align*}
\]

Now use this to extend the sequence (2):

\[
0 \rightarrow k_a \otimes k_b \rightarrow_{\ker(H^T_X)} n_a \otimes n_b \rightarrow_{H^T} n_a \otimes m_b \rightarrow_{H_X} m_a \otimes m_b \rightarrow_{coker(H^T_X)} k_a \oplus k_b \rightarrow 0
\]

This sequence is exact everywhere except in the middle where the homology is \( k \) (dimensional). Therefore, the Euler character is:

\[
k = k_a b_a - n_a n_b + m_a m_b - m_a b_b + k_a \oplus k_b
\]

The qubits (vectors) in \( n_a \otimes m_b \) are called **vertical** qubits, and the qubits in \( m_a \otimes n_b \) are called **horizontal** qubits. This convention is motivated by the following example.

**Example 8.** (Surface code.) Continuing to use the above notation, we take \( A = \{ n_a \rightarrow m_a \} \) to be a repetition code and \( B = \{ m_b \rightarrow n_b \} \) to be the dual (transpose) code:

\[
\begin{align*}
    \partial_a &= \left( \begin{array}{ccc}
        1 & 1 & . \\
        . & 1 & 1 \\
        . & . & 1
    \end{array} \right), \\
    \partial_b &= \left( \begin{array}{ccc}
        1 & . & . \\
        1 & 1 & . \\
        . & 1 & 1 \\
        . & . & 1
    \end{array} \right)
\end{align*}
\]

We have \( \partial_a : 4 \rightarrow 3 \) and \( \partial_b : 3 \rightarrow 4 \), and also

\[
\begin{align*}
k_a &= 1, & k_b &= 0, \\
k_a^T &= 0, & k_b^T &= 1.
\end{align*}
\]

The quantum code \( A \otimes B \) is then:

\[
4 \otimes 3 \xrightarrow{H^T_X} 3 \otimes 3 \oplus 4 \otimes 4 \xrightarrow{H_X} 3 \otimes 4.
\]

Pictorially this corresponds to \( 4 \times 3 \) faces, \( 3 \times 3 \) horizontal edges, \( 4 \times 4 \) vertical edges, and \( 3 \times 4 \) vertices:

![Pictorial representation of a quantum code](image)

The surface code is a peculiar example because we are able to combine the \( 3 \otimes 3 \) horizontal qubits and the \( 4 \otimes 4 \) vertical qubits together into this single picture. This does not work in general; in Fig. 2 below the sectors are shown separately.

The next theorem describes the structure of the logical operators as illustrated in Fig. 2. It is a concrete restatement of the formula \( k = k_a b^T_a + k_e b_b \) from the previous lemma. The standard basis for \( n_a \) we write as \( |n_a\rangle \), similarly for \( m_a, m_b \) and \( n_b \). Notation such as \( [n_a] \times [n_b] \) denotes the basis for \( n_a \otimes m_b \), which is the space for the vertical qubits.

**Theorem 9.** Let \( A \otimes B \) be a hypergraph product code as above. A complete set of \( L_Z \) operators is supported on the qubits:

\[
L_{Z\text{-vertical}} = [n_a] \times \gamma_{Zv} \text{ where } \gamma_{Zv} \text{ is any } k_a^T \text{-puncture of } \partial_b^T
\]

\[
L_{Z\text{-horizontal}} = \gamma_{Zh} \times [n_b] \text{ where } \gamma_{Zh} \text{ is any } k_e^T \text{-puncture of } \partial_a^T
\]

A complete set of \( L_X \) operators is supported on the qubits:

\[
L_{X\text{-vertical}} = \gamma_{Xv} \times [m_b] \text{ where } \gamma_{Xv} \text{ is any } k_a^T \text{-puncture of } \partial_b^T
\]

\[
L_{X\text{-horizontal}} = [m_a] \times \gamma_{Xh} \text{ where } \gamma_{Xh} \text{ is any } k_e^T \text{-puncture of } \partial_a^T
\]

Specifically, we have the following block matrix form for the logical operators:

\[
L^T = \left( \begin{array}{ccc}
    \ker(H_X)^T : H^T_X & \ker(H_X)^T : H^T_X \\
    \ker(\partial_b) \otimes \gamma^T_{Zv} & 0 \\
    \gamma_{Zh} \otimes \ker(\partial_b) & \partial_a^T \otimes I_{n_b}
\end{array} \right)
\]
where the basis for the rows is $v = n_a \otimes m_b$ and $h = m_a \otimes n_b$, and

$$L_X = \begin{pmatrix} \ker(\partial_b) \otimes \coker(H_Z^T) \\ \phantom{\ker(\partial_b) \otimes \coker(H_Z^T)} \end{pmatrix}$$

$$= \begin{pmatrix} \gamma_{xv} \otimes \ker(\partial_b) & 0 \\ 0 & \coker(\partial_a) \otimes \gamma_{xb} \\ \partial_a \otimes I_{m_b} & I_{m_a} \otimes \partial_b \end{pmatrix},$$

where the basis for the columns is $v$ and $h$. In this notation, a puncture $\gamma \subseteq [n]$ is promoted to a $|\gamma| \times n$ matrix by taking the rows of the identity matrix $I_n$ indexed by $\gamma$. The dashed line separates “generators” from “relations”, as in Section III.

Proof: We claim that the kernel of $H_X$ is spanned by the columns of a matrix as:

$$\ker(H_X) = \text{span} \left( \begin{array}{ccc} \ker(\partial_a) \otimes I_{m_b} & 0 & I_{m_a} \otimes \partial_b \\ 0 & I_{m_a} \otimes \ker(\partial_b) & \partial_a \otimes I_{n_b} \end{array} \right),$$

and the cokernel of $H_Z^T$ is spanned by the rows of a matrix as:

$$\coker(H_Z^T) = \text{cospan} \left( \begin{array}{ccc} I_{n_a} \otimes \coker(\partial_b) & 0 \\ 0 & \coker(\partial_a) \otimes I_{n_b} \\ \partial_a \otimes I_{m_b} & I_{m_a} \otimes \partial_b \end{array} \right).$$

That these matrices lie within $\ker(H_X)$ and $\coker(H_Z^T)$ respectively can be seen by composing with $H_X, H_Z^T$ respectively. We next count the independent logical operators and find $k_a k_b^T + k_a^T k_b$ many, then by Lemma 7 we will have found all of them.

Let $\gamma_{Zv}$ be any $k_b^T$-puncture of $\partial_b^T$. This puncture always exists by Lemma 6. We claim that any solution of

$$\begin{pmatrix} \ker(\partial_a) \otimes \gamma_{Zv} \\ 0 \\ I_{m_a} \otimes \partial_b \end{pmatrix} \begin{pmatrix} x \\ y \\ z \end{pmatrix} = 0$$

will have $x = 0$. To see this, we need only consider solutions of

$$(\ker(\partial_a) \otimes \gamma_{Zv})x + (I_{m_a} \otimes \partial_b)z = 0.$$

If $z = 0$ then $x = 0$ because $\ker(\partial_a)$ has linearly independent columns. If $z \neq 0$ then we have $x = 0$ because $\gamma_{Zv}$ cannot be in the column space of $\partial_b$.

Counting the columns of $\ker(\partial_a) \otimes \gamma_{Zv}$ we find $k_a k_b^T$ linearly independent $L_Z$ logical operators:

$$\begin{pmatrix} \ker(\partial_a) \otimes \gamma_{Zv}^T \\ I_{m_a} \otimes \partial_b \\ 0 \\ \partial_a \otimes I_{n_b} \end{pmatrix}.$$
This article has been accepted for publication in a future issue of this journal, but has not been fully edited. Content may change prior to final publication. Citation information: DOI 10.1109/TIT.2021.3131043, IEEE Transactions on Information Theory

weight, for example, the sum of taut operators supported on the same vertical (or horizontal) strip is another taut operator. In general there will be many more taut logical operators than are contained within the complete set of logical operators of Theorem 9.

**Example 10.** Continuing with the surface code from example 8, for the $L_Z$-vertical logical operators we compute $\ker(\partial_a) \otimes \gamma_{Zv}^\top$:

$$
\ker(\partial_a) = \begin{pmatrix}
1 \\
1 \\
1 \\
1
\end{pmatrix}
$$

and we take $\gamma_{Zv}$ to be a $1 \times 4$ matrix that is non-zero at any one coordinate, to get a 1-puncture of $\partial_v^\top$. For the $L_X$-vertical logical operators we compute $\gamma_{Xv} \otimes \ker(\partial_b)$. In this case we have $\ker(\partial_b) = (1111)$ and $\gamma_{Xv}$ is a $1 \times 4$ matrix non-zero at any one coordinate. The $L_Z$-horizontal logical operators are trivial because $k_a^\top = 0$, and the $L_X$-horizontal logical operators are trivial because $k_b = 0$. ■

**VI. MAIN RESULT**

We say that a hypergraph product code $A \otimes B$ is contained within the vertical sector when $\ker(\partial_b)$ is trivial and/or $\ker(\partial_a)$ is trivial. See Fig. 2. This happens precisely when $k_a^\top k_b = 0$. The surface code of Example 8 is one such code. The toric code is a counter-example, in this case $k_a^\top k_b = 1$. The next lemma shows how to restrict Theorem 9 to the vertical sector.

**Lemma 11.** Given a hypergraph product code $A \otimes B$ contained within the vertical sector, we have (i) a complete set of $L_Z$ operators is supported on $[m_b] \times \gamma_Z$ where $\gamma_Z$ is any $k_a^\top$-puncture of $\partial_v^\top$, and (ii) a complete set of $L_X$ operators is supported on $\gamma_X \times [n_b]$ where $\gamma_X$ is any $k_a$-puncture of $\partial_a$.

**Proof:** We use Theorem 9 and assume $k_a^\top k_b = 0$. If $k_b = 0$ then $\ker(\partial_b) = 0$ so $L_Z$-horizontal is trivial, and any $k_a$-puncture of $\partial_b$ is empty so $L_X$-horizontal is trivial. Similarly, if $k_a^\top = 0$ then $\ker(\partial_a)$ is trivial so $L_X$-horizontal is trivial, and any $k_a^\top$-puncture of $\partial_v^\top$ is empty so $L_Z$-horizontal is trivial. The remaining operators are $L_Z$-vertical and $L_X$-vertical, as required. ■

In Theorem 9 we showed how representatives of the logical operators can be chosen to be taut. The next result shows how more general logical operators can be decomposed into taut logical operators.

**Lemma 12.** Given a hypergraph product code $A \otimes B$ contained within the vertical sector, any $Z$-type (resp. $X$-type) logical operator supported on the vertical qubits is a product of disjoint taut $Z$-type (resp. $X$-type) logical operators.

**Proof:** From the form of the logical operators in the proof of Theorem 9, Eq. (4), we have the block matrix

$$
L_Z^\top = \begin{pmatrix}
\ker(\partial_a) \otimes I_{m_b} & 0 \\
0 & I_{n_a} \otimes \ker(\partial_b) \\
I_{n_a} \otimes \ker(\partial_b) & 0
\end{pmatrix}.
$$

This notation is to be read as “column vectors modulo column vectors” with the dashed line acting as the modulo. Assuming the hypothesis we have that $k_a^\top k_b = 0$ which implies, also from Theorem 9, that the second column is trivial modulo the third column. This gives

$$
L_Z^\top = \begin{pmatrix}
\ker(\partial_a) \otimes I_{m_b} & I_{n_a} \otimes \ker(\partial_b) \\
0 & \partial_a \otimes I_{n_b}
\end{pmatrix}.
$$

An arbitrary logical $Z$-type operator $l_Z^\top$ is got from the span of this matrix, which by the hypothesis must be zero on the bottom row:

$$
l_Z^\top = L_Z^\top \begin{pmatrix} x \\ y \end{pmatrix} = \begin{pmatrix} z \\ 0 \end{pmatrix},
$$

for some matrices $x, y, z$. Solving this equation in generality, we require $(\partial_a \otimes I_{m_b})y = 0$ which gives $y = \ker(\partial_a) \otimes I_{n_b}$. Expanding out, we get:

$$
z = (\ker(\partial_a) \otimes I_{m_b})x + \ker(\partial_a) \otimes \partial_b = (\ker(\partial_a) \otimes I_{m_b})x + (\ker(\partial_a) \otimes I_{m_b})(I_{n_a} \otimes \partial_b) = (\ker(\partial_a) \otimes I_{m_b})(x + I_{n_a} \otimes \partial_b).
$$

This means that $l_Z^\top$ is got from the span of

$$
\begin{pmatrix}
\ker(\partial_a) \otimes I_{m_b} \\
0
\end{pmatrix},
$$

which is precisely the form of disjoint $Z$-type operators we seek. Note that individual columns of this matrix may not be disjoint, but the sum of any non-disjoint columns will be a new taut logical operator. See Example 13 below. The expression $\ker(\partial_a) \otimes I_{m_b}$ can be read as taking logical operators from the $A$ code and “copying” them onto vertical strips of the $n_a \times m_b$ array of vertical qubits; see Fig. 2.

The same calculation transposed shows the result for $X$-type logical operators:

$$
L_X = \begin{pmatrix}
I_{n_a} \otimes \ker(\partial_b) & 0 \\
0 & \ker(\partial_a) \otimes I_{m_b} \\
\partial_a \otimes I_{m_b} & I_{n_a} \otimes \partial_b
\end{pmatrix} = \begin{pmatrix}
I_{n_a} \otimes \ker(\partial_b) & 0 \\
\partial_a \otimes I_{m_b} & I_{n_a} \otimes \partial_b
\end{pmatrix},
$$

and so on. ■

So far, we have seen that we can construct a complete set of logical operators from taut logical operators (Theorem 9 and Lemma 11) and conversely, how to write a more general logical operator as a product of disjoint taut logical operators (Lemma 12). The following example is intended to clarify the relationship between taut logical operators in general, and those that we use to form a complete set of logical operators.
We focus on the logical $\gamma$, we choose a 1-puncture matrix:

$$\partial_a = \begin{pmatrix} 1 & 1 & 1 \\ 1 & 1 & 1 \end{pmatrix}, \quad \ker(\partial_a) = \begin{pmatrix} 1 \\ 1 \end{pmatrix},$$

$$\partial_b = \begin{pmatrix} 1 \\ 1 \end{pmatrix}, \quad \coker(\partial_b) = (1, 1).$$

The $A$ code has parameters $n_a = 3, m_a = 1, k_a = 2, k_a^T = 0,$ and the $B$ code has parameters $n_b = 1, m_b = 2, k_b = 0, k_b^T = 1,$ therefore we have $A \otimes B$ contained within the vertical sector. We focus on the logical Z-type operators. From Theorem 9 we choose a 1-puncture $\gamma_{ZV} = (1, 0)$ of $\partial_a^{i^1} = (1, 1)$. Then the logical Z-type operators correspond to the columns of the matrix:

$$L_Z^T = \begin{pmatrix} \ker(\partial_a) \otimes \gamma_{ZV}^T : I_{n_a} \otimes \partial_b \\ 0 : \partial_a \otimes I_{n_b} \end{pmatrix}.$$

Ignoring the bottom row, which corresponds to the horizontal sector, we compute:

$$\ker(\partial_a) \otimes \gamma_{ZV}^T = \begin{pmatrix} 1 & 0 \\ 1 & 1 \\ 0 & 1 \end{pmatrix} \otimes \begin{pmatrix} 1 \\ 0 \end{pmatrix} = \begin{pmatrix} 1 & 0 \\ 1 & 0 \\ 0 & 1 \end{pmatrix}.$$

Each column of this matrix is a taut logical operator, and these columns are not disjoint. So it would seem that this presents a counterexample to Lemma 12, as the logical operator defined by the sum of these columns is a sum of non-disjoint taut logical operators. However, the sum is itself another taut logical operator and so this is not a counterexample to Lemma 12. Finally, we obtain another set of taut logical operators by choosing the other 1-puncture of $\partial_a^i$, which is $\gamma_{ZV} = (0, 1)$.

Two regions (subsets) $\alpha, \beta \subset [n_a] \times [m_b]$ are called horizontally separated when for any $j \in [m_b]$ we have that $[n_a] \times \{j\}$ intersects at most one of $\alpha$ or $\beta$. Similarly, $\alpha$ and $\beta$ are called vertically separated when for any $i \in [n_a]$ we have that $\{i\} \times [m_b]$ intersects at most one of $\alpha$ or $\beta$.

**Lemma 14. (Union Lemma.)** Consider a hypergraph product code $A \otimes B$ contained within the vertical sector, with two regions $\alpha, \beta \subset [n_a] \times [m_b]$ that are separated horizontally and vertically. If there exists a non-trivial logical operator supported on $\alpha \cup \beta$ then there exists a non-trivial logical operator supported on $\alpha$, or there exists a non-trivial logical operator supported on $\beta$ (inclusive or).

**Proof:** By Lemma 1 we need only consider X-type and Z-type operators. Suppose that $\alpha \cup \beta$ supports a non-trivial X-type logical operator. Then by the previous lemma, $\alpha \cup \beta$ must support a non-trivial taut X-type operator. This operator is supported on $\{i\} \times [m_b]$ for some $i \in [n_a]$, which intersects at most one of $\alpha$ or $\beta$ because these are vertically separated. Therefore we must have that at most one of $\alpha$ or $\beta$ supports this taut X-type logical operator. Using the horizontal separation of $\alpha$ and $\beta$ a similar argument applies to the Z-type logical operators.

We call a classical code $\{n, \bar{d}, m\}$ robust if the generator matrix $\text{ker}(\bar{d})^T$ and the parity check matrix $\bar{d}$ are simultaneously k-1-puncturable, where $k$ is the dimension of the codespace.

**Theorem 15.** Let $A = \{n_a, \bar{d}, m_a\}$ and $B = \{n_b, \bar{d}, m_b\}$ be classical codes such that $A \otimes B$ is contained within the vertical sector. If $A$ is robust and $B^3 = \{m_b, \bar{d}, m_b\}$ is robust, then any transversal gate for $A \otimes B$ is restricted to the Clifford group $C^2$.

**Proof:** We assume the hypothesis of the theorem. Let $(\gamma_Z, \delta_Z)$ be a simultaneous $k_1$-puncture of $\partial_a^i$ and $\ker(\partial_b)$. Let $(\gamma_X, \delta_X)$ be a simultaneous $k_2$-puncture of $\partial_a^j$ and $\ker(\partial_b)^T$. From Lemma 11 we have a complete set of $L_Z$ operators supported on $[n_a] \times \gamma_Z$ and a complete set of $L_Z$ operators supported on $[n_b] \times \delta_Z$. Similarly for $L_X$ operators supported on $\gamma_X \times [m_b]$ and $\delta_X \times [m_b]$. Combining these, we find that $\gamma := \gamma_X \times [m_b] \cup [n_a] \times \gamma_Z$ supports a complete set of logical operators, as does $\delta := \delta_X \times [m_b] \cup [n_a] \times \delta_Z$. See Fig. 1. We now claim that the intersection of these two sets $\gamma \cap \delta$ is correctable, that is, does not support any non-trivial logical operator. The result then follows from Theorem 3.

Using the disjointness of $\gamma_X$ and $\delta_X$, and the disjointness of $\gamma_Z$ and $\delta_Z$ we find

$$\gamma \cap \delta = (\gamma_X \times [m_b] \cup [n_a] \times \gamma_Z) \cap (\delta_X \times [m_b] \cup [n_a] \times \delta_Z) = (\gamma_X \times [m_b] \cap [n_a] \times \gamma_Z) \cup ([n_a] \times \gamma_Z \cap \delta_X \times [m_b])$$

and the RHS then satisfies the hypothesis of Lemma 14. Therefore we need only show the correctability of the two pieces, $\alpha := \gamma_X \times [m_b] \cap [n_a] \times \delta_Z$ and $\beta := [n_a] \times \gamma_Z \cap \delta_X \times [m_b]$.

From the hypothesis, $\delta_Z$ punctures $\text{coker}(\partial_b)$ and so $\alpha$ cannot support any taut $L_X$ logical operator, and by Lemma 12 cannot support arbitrary $L_X$ logical operator. Also, $\gamma_X$ punctures $\ker(\partial_b)^T$ and so $\alpha$ cannot support any $L_Z$ logical operator, and by Lemma 12 cannot support arbitrary $L_Z$ logical operator. By Lemma 1 we find that $\alpha$ cannot support any logical operator, and so $\alpha$ is correctable. A similar argument applies to show that $\beta$ is correctable.

By swapping the factors $A \otimes B$ to $B \otimes A$ we see that this theorem also applies with “vertical sector” replaced with “horizontal sector”.

In the next section we show that some mild conditions on a classical code will guarantee it is robust.

**VII. CHARACTERIZING ROBUSTNESS**

In this section we take $H = \bar{d}$ to be a parity check matrix that has full rank, without loss of generality. Adding linear dependant rows to $H$ does not change the punctures on $H$. We also write $G = \ker(H)^T$ for the generator matrix.
Lemma 16. (Canonical form.) Any $k$-dimensional code with length $n$ has canonical form for generator matrix $G$ and parity check matrix $H$,

$$G = (I_k \ J)$$

$$H = (J^\top \ I_m)$$

up to reordering of columns, with $m = n - k$.

Proof: Row-reduction and column reordering gives the form

$$G = (I_k \ J)$$

$$H = (K \ I_m).$$

Using $HG^\top = 0$ we get $K = J^\top$. (Lemma 16)

Writing such a code in this form, the set of indices $\{1, ..., k\}$ are called pivots and the set of indices $\{k + 1, ..., n\}$ are called copivots. We also use this terminology for matrices in arbitrary row-reduced form (not necessarily column re-ordered to make the pivots sequential).

Next, we show how to puncture the copivots.

Lemma 17. (Copivot Lemma.) With $G$ written in canonical form $G = (I_k \ J)$ any set of indices $\gamma \subset \{k + 1, ..., n\}$ punctures $G$.

Proof: For $v \in \mathbb{F}_2^n$ any non-zero vector, $vG$ has non-zero components on $\{1, ..., k\}$ and so cannot be supported on $\gamma$. (Lemma 17)

Slightly more tricky is puncturing the pivots.

Lemma 18. (Pivot Lemma.) With $G$ written in canonical form $G = (I_k \ J)$ a set of indices $\gamma \subset \{1, ..., k\}$ punctures $G$ if and only if $\gamma$ punctures $\text{coker}(J)$.

Proof: The only way $\gamma \subset \{1, ..., k\}$ can support a non-zero vector in the cospan of $G$ is for $\gamma$ to index a linear dependant set of rows of $J$. This is precisely the cokernel of $J$. (Lemma 18)

The result we are leading to characterizes robust classical codes as being equivalent to a row-reduction condition on the generator matrix. The proof is by successive applications of the Pivot Lemma and the Copivot Lemma.

Theorem 19. (Robustness Theorem.) Let $A$ be a classical code of length $n$, with generator matrix $G$. Then $A$ is robust if and only if there is some permutation of the columns of $G$ such that $G$ has a canonical form $G = (I_k \ J)$ with $J$ full rank.

Proof: Firstly, to prove the “if” part, we assume that $G = (I_k \ J)$ with $J$ full rank. We will find disjoint sets $\delta, \gamma \subset [n]$ that simultaneously puncture $G$ and $H$.

By Lemma 16, this code has parity check matrix

$$H = (J^\top \ I_m)$$

where $m = n - k$. Choose $\gamma = [k]$, so that $\gamma$ is supported on the pivots of $G$ and the copivots of $H$. Because $J$ is full rank, the cokernel of $J$ is trivial and so by the Pivot Lemma we have that $\gamma$ punctures $G$. From the Copivot Lemma we have that $\gamma$ punctures $H$.

Also by the Copivot Lemma we have any $\delta \subset \{k + 1, ..., n\}$ punctures $G$, we just need to find such a $\delta$, with $|\delta| = k$, that also punctures $H$. This $\delta$ is a subset of the pivots of $H$ so we use the Pivot Lemma to see that $\delta$ must also puncture $\text{coker}(J^\top) = \ker(J)^\top$. We show how to achieve this. The matrix $J$ is full rank so from Eq. (1) a matrix for $\ker(J^\top)$ has $m - k$ rows and $m$ columns. This $(m - k) \times m$ matrix is full rank and so has $m - k$ pivots and $k$ copivots. We therefore choose $\delta$ to be these $k$ copivots of $\ker(J^\top)$ and by the Copivot Lemma, $\delta$ will puncture $\text{coker}(J^\top)$.

Secondly, to prove the “only if” part, assume we have disjoint sets $\gamma, \delta \subset [n]$ that simultaneously puncture $G$ and $H$. The goal is to find a canonical form $G = (I_k \ J)$ with $J$ full rank. To do this, permute the columns of $G$ and $H$ so that $\gamma = \{1, ..., k\}$ and $\delta = \{k + 1, ..., 2k\}$. With this column order, row-reduce $G$ to produce the form $G = (I_k \ J)$. Using the Pivot lemma, we see that $\gamma = \{1, ..., k\}$ punctures $\ker(J)$ which implies this cokernel is trivial, and so $J$ is full-rank. (Theorem 19)

VIII. Discussion

We have shown that this ensemble of good quantum codes nevertheless fails to support transversal gates beyond the Clifford group. This no-go result rules out a wide class of codes, and so assists the search (construction) of codes that do have non-Clifford transversal gates.

The toric code, which is a hypergraph product code, does not satisfy the hypothesis of Theorem 15. This theorem applies to codes which are a generalization of the surface code. It is a natural question to ask if this result can be strengthened to cover these other hypergraph product codes. However, the argument as it stands cannot be strengthened: we have found examples where the region $\gamma \cap \delta$ (see Fig. 1) is not correctable, even though it satisfies the hypothesis of the Union Lemma. But it may be that a more refined argument does work: numerics suggest that the support of specific commutators $pqp^{-1}q^{-1}$ of logical operators $p, q$ is always correctable.

The reader may wonder exactly how “mild” is the robustness condition. We numerically searched through the Gallager code ensembles used for building hypergraph product codes in [33], [34], and did not find any non-robust instance. Specifically, we found evidence that any $[n, k, d]$ Gallager code with $d \geq 3$ and $k \leq n/2$ is robust.

Another natural question to ask is the existence of transversal gates for higher degree hypergraph (homological) product of classical codes $A \otimes B \otimes C$, etc. [35]. We expect these codes also have a theory of taut logical operators. Given the intricacy of the calculations in the present work, it seems that better techniques, and deeper insights, are needed before these results can be pushed further.

Acknowledgements

We thank Armanda Quintavalle, Joschka Roffe, Earl Campbell, Michael Vasmor and Paul Webster for useful discussions. We also thank one of the reviewers for suggesting Example 13 which helped to clarify the definition of the taut logical operators.
REFERENCES

[1] D. Gottesman, “Class of quantum error-correcting codes saturating the quantum hamming bound,” Physical Review A, vol. 54, no. 3, p. 1862, 1996.

[2] A. R. Calderbank and P. W. Shor, “Good quantum error-correcting codes exist,” Physical Review A, vol. 54, no. 2, p. 1098, 1996.

[3] A. M. Steane, “Error correcting codes in quantum theory,” Physical Review Letters, vol. 77, no. 5, p. 793, 1996.

[4] B. Eastin and E. Knill, “Restrictions on transversal encoded quantum gate sets,” Physical review letters, vol. 102, no. 11, p. 110502, 2009.

[5] B. Zeng, X. Chen, and I. L. Chuang, “Semi-Clifford operations, structure of $C_g$ hierarchy, and gate complexity for fault-tolerant quantum computation,” Physical Review A, vol. 77, no. 4, p. 042313, 2008.

[6] S. Bravyi and J. Haah, “Magic-state distillation with low overhead,” Physical Review A, vol. 86, no. 5, p. 052329, 2012.

[7] C. Vuillot and N. P. Breuckmann, “Quantum pin codes,” arXiv preprint arXiv:1906.11394, 2019.

[8] H. Bombin, “Gauge color codes: optimal transversal gates and gauge fixing in topological stabilizer codes,” New Journal of Physics, vol. 17, no. 8, p. 083002, 2015.

[9] S. Bravyi and R. König, “Classification of topologically protected gates for local stabilizer codes,” Physical review letters, vol. 110, no. 17, p. 170503, 2013.

[10] T. Jochym-O’Connor, A. Kubica, and T. J. Yoder, “Disjointness of stabilizer codes and limitations on fault-tolerant logical gates,” Physical Review X, vol. 8, no. 2, p. 021047, 2018.

[11] F. Pastawski and B. Yoshida, “Fault-tolerant logical gates in quantum error-correcting codes,” Physical Review A, vol. 91, no. 1, p. 012305, 2015.

[12] P. Webster and S. D. Bartlett, “Locality-preserving logical operators in topological stabilizer codes,” Physical Review A, vol. 97, no. 1, p. 012330, 2018.

[13] M. E. Beverland, O. Buerschaper, R. Koenig, F. Pastawski, J. Preskill, and M. Virmani, “Quantum low-density parity-check code landscape,” Physical Review Letters, vol. 123, no. 15, p. 150502, 2019.

[14] J. Roffe, D. R. White, S. Burton, and E. Campbell, “Decoding across the quantum low-density parity-check code landscape,” Physical Review A, vol. 92, no. 4, p. 043423, 2020.

[15] A. O. Quintavalle, M. Vasmer, J. Roffe, and E. T. Campbell, “Single-shot error correction of three-dimensional homological product codes,” PRX Quantum, vol. 2, no. 2, p. 020340, 2021.

[16] C. Horsman, A. G. Fowler, S. Devitt, and R. Van Meter, “Surface code quantum computing by lattice surgery,” New Journal of Physics, vol. 14, no. 12, p. 123011, 2012.

[17] A. G. Fowler, M. Mariantoni, J. M. Martinis, and A. N. Cleland, “Surface codes: Towards practical large-scale quantum computation,” Physical Review A, vol. 86, no. 3, p. 032324, 2012.

[18] H. Bombin, “2D quantum computation with 3D topological codes,” arXiv preprint arXiv:1810.09571, 2018.

[19] T. Jochym-O’Connor, “Fault-tolerant gates via homological product codes,” Quantum, vol. 3, p. 120, 2019.

[20] M. A. Nielsen and I. L. Chuang, Quantum Computation and Quantum Information. Cambridge University Press, 2010.

[21] A. R. Calderbank, E. M. Rains, P. W. Shor, and N. J. Sloane, “Quantum error correction and orthogonal geometry,” Physical Review Letters, vol. 78, no. 3, p. 405, 1997.

[22] B. J. Brown, “A fault-tolerant non-Clifford gate for the surface code in two dimensions,” Science Advances, vol. 6, no. 21, p. eaaay4929, 2020.

[23] J. Tillich and G. Zémor, “Quantum LDPC Codes With Positive Rate and Applications of Near-Term Quantum Computers,” IEEE Transactions on Information Theory, vol. 66, no. 9, pp. 5549–5566, 2020.

[24] F. J. MacWilliams and N. J. A. Sloane, The theory of error-correcting codes. Elsevier, 1977.

[25] S. Bravyi and M. B. Hastings, “Homological product codes,” in Proceedings of the forty-sixth annual ACM symposium on Theory of computing, 2014, pp. 273–282.

[26] M. E. Beverland, O. Buerschaper, R. Koenig, F. Pastawski, J. Preskill, and R. Virmani, “Quantum low-density parity-check code landscape,” Physical Review A, vol. 92, no. 4, p. 043423, 2020.

[27] J. Roffe, D. R. White, S. Burton, and E. Campbell, “Decoding across the quantum low-density parity-check code landscape,” Physical Review Letters, vol. 123, no. 15, p. 150502, 2019.

[28] A. O. Quintavalle, M. Vasmer, J. Roffe, and E. T. Campbell, “Single-shot error correction of three-dimensional homological product codes,” PRX Quantum, vol. 2, no. 2, p. 020340, 2021.

[29] D. Gottesman and I. L. Chuang, “Demonstrating the viability of universal quantum computation using teleportation and single-qubit operations,” Nature, vol. 402, no. 6760, pp. 390–393, 1999.

[30] R. W. Ghrist, Elementary applied topology. CreateSpace Seattle, 2014.