Monolithic Active Pixel Sensors on CMOS technologies

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1 Introduction

Collider detectors have taken advantage of the resolution and accuracy of silicon (Si) detectors for at least four decades [1]. Future colliders will need large areas of silicon sensors, several hundred m², for low mass trackers and sampling calorimetry [2]. Trackers will require multiple layers, large radii, and micron scale resolution. Sampling calorimeters will also have very large areas and are improved by very thin overall packages, which reduces the Molière radius.

Monolithic Active Pixel Sensors (MAPS), in which Si diodes and readout circuitry are combined in the same pixels, and can be fabricated in some of standard CMOS processes, were shown as a promising technology for high-granularity and light in material budget detectors [3, 4, 5]. MAPS represent several advantages over traditional hybrid pixel detector technologies, as they can be inexpensively built, even in large array sizes, thinned to the needs, offer individual pixel readout, reasonable radiation hardness, low-mass, while operating at high speeds with low power consumption, from single and low voltage supply. The close connection of a sensor and front-end amplifier, without the need of externally added interconnections, reduces to the smallest achievable level the input capacitance. This leads to the reduction of the noise floor and thus operation with smaller magnitude signals from thinner sensors is possible with satisfactorily high S/N.

Interconnects, routinely realized with some kind of metal spheres or pillars bump-bonding, are on one hand comparative in size with the most recently desired pixel sizes O(10 μm). On the other hand, they are responsible for the dominating contribution to the capacitance seen at the input node of an in-pixel amplifier, effectively dominating otherwise very small sensor capacitance. Reduction of this capacitance, even thanks to the low-temperature direct bonding technology was shown in comparative studies [6] to improve performance of a pixel detector significantly.

Magnitude, speed and efficiency of collecting charge carriers, liberated in interactions of radiation with the medium of a sensor can be completely controlled in hybrid pixel detectors. Whilst MAPS are dependent on the substrate used in the CMOS process in terms of how thick the active sensor volume can be and whether a sensor can be operated depleted. Retaining the advantage of using standard CMOS processes for building MAPS, the older-generation MAPS, such as those used in the Heavy Flavor Tracker (HFT) at STAR [7], were built in fabrication processes for which foundries were using low-resistivity (about 10 Ωcm), epitaxial wafers. At that time, thermal diffusion charge collection allowed magnitudes of signals on the order of 1k e⁻, shared among the neighbouring pixels and collected in times on the order of 100 ns [3]. The significant limitations of the early MAPS was use of only one type of transistor in a pixel circuitry. The radiation hardness was on the order 10¹³ of 1 MeV neutrons equivalent per cm² and 10 Mrads of the Total Ionizing Dose (TID) [9]. This parameters were suitable for the HFT at STAR, that was the first practical use of MAPS. MAPS, that were simultaneously developed using processes suitable for High Voltage applications, have not been used in any experiment yet.

Another key feature of MAPS, that are fabricated in any standard CMOS process, is the limitation of a single chip dimensions to a reticle, whose size is about 2.6 × 3.2 cm² in process nodes such as 65 nm and it is smaller in older processes, as a result of using a stepper in for photolithographic exposures. This limitation can be overcome, either fully by the so called stitching of reticles (large area MAPS has been already demonstrated for MAPS used in X-ray detection [10]), or partially by butting [11]. An example of butting is the STAR Heavy Flavor Tracker at BNL RHIC [12], where chips were butted one next to another on the staves and the readout areas of the individual MAPS units were limited to one edge only. On the other hand, a few years after the HFT at RHIC, the upgrade of the ALICE Inner Tracker System 3 (ITS3) at the LHC is going to use the stitched arrangement of the MAPS devices [13]. Both developments are targeting inner tracking or vertexing layers for Nuclear Physics (NP) experiments, where requirements on readout
speed and radiation hardness are less stringent than for high energy physics experiments.

Years passed since the initial demonstration of MAPS showed that full depletion of the Active Sensitive Volume (ASV) of the device is required for the charge to be collected by drift and not by diffusion, allowing a fast time response and radiation hardness. Additionally, both N and P types of transistors are required in a pixel area to develop efficient processing blocks. As a result of realizing thereof, new inner trackers, such as the ITS1/2 for ALICE, managed shifting to the increased resistivity of ASV. As an example of thereof, Figure 1 shows cross-sections of structures built in the TowerSemi aka Tower-Jazz 180 nm. The implantations and distributions of resulting electric fields were optimized \[14, 15\]. A reasonably thick, of increased resistivity in the TowerSemi process Si film, translates to the maximally depleted ASV. Shielding of wells, visible in Figure 1, does not hold back the pixel electronics to only one type of transistors. The key benefit of that in tracking or vertexing, is the higher spatial resolution. The TowerSemi 180 nm process has been available for the MAPS design for a while. Very recently, an access to the TowerSemi 65 nm, process opened up, allowing its exploration to address needs for future applications. The TowerSemi 65 nm process, as opposed to the older 180 nm, allows more than four-fold increase of the number of transistors per pixel. Building a new generation of MAPS, thinned and on an increased-resistivity substrate, is an opportunity that has been enabled thanks to the developments for ITS3 of the ALICE experiment \[13, 16\].

![Figure 1: Cross-sections of MAPS structures and electric filed lines in improved process.](image)

Next MAPS developments will target a diverse set of goals and applications. This paper directly addresses the issues of making large surfaces by developing the intermediate stage of tens of $m^2$, including powering and readout of large numbers of MAPS. The ongoing efforts will enable detectors with far more pixels per unit area, and thus higher resolution, with substantially lower material budget and significantly less expensive. The next generation of MAPS, will have to address improvements to speed and resolution performance, and the system approaches needed for large scale use at a reasonable cost. In this white paper we focus on the following applications:

- MAPS with characteristics suitable for trackers and electromagnetic calorimeters (ECal) at future colliders experiments. We will use the requirements of the SiD detector for ILC \[17\] as a benchmark, with an outlook to a broader range of the future applications.

- The ongoing efforts towards dedicated MAPS for the Electron-Ion Collider (EIC) at BNL, for which the EIC Silicon Consortium was already instantiated. This aims at the High-Performance Tracking (HPT) MAPS \[18\]. This effort is synergistic with the ALICE ITS3 developments as the performance requirements are similar.

- Space-born applications for MeV $\gamma$-ray experiments with MAPS based trackers (AstroPix).
2 MAPS developments for future linear $e^+e^-$ collider

2.1 Large-area MAPS

The detectors at future lepton machines must enable unprecedented precision measurements relating to the Higgs boson and the top quark, and search for new phenomena that could be missed at the LHC, such as electroweak particles expected in some beyond the standard models. These ambitious physics goals translate into very challenging demands on systematic errors in tracking, heavy-quark tagging, jet energy measurement, and beam polarization measurement for all these machines. High precision and low mass trackers and highly granular calorimeters, optimised for particle flow analysis, will be essential for the success of the physics program. The low duty cycle of collisions (as, for instance, in the linear $e^+e^-$ machines) would enable power pulsing as a desirable feature to significantly reduce the heat load, make gas cooling appropriate, and thus significantly reduce the dead material in the detectors.

The CERN WP1.2 collaboration is investigating the possibility to realize wafer-scale MAPS devices on the TowerSemi 65 nm process with the ALICE ITS3 upgrade as the main driver of this R&D effort. Within this collaboration, SLAC is investigating challenges of wafer-scale designs optimized for detectors at linear machines, focusing in particular on Si Tracker and ECal. This effort will help identify the risks that wafer-scale MAPS pose at system-level, such as yield, power distribution and fill factor, as well as evaluate essential aspects of the integration of such devices into a detector system: i.e., cooling, assembly procedures, wafer thinning and handling and power delivery.

A large part of the international research efforts on MAPS are focused on the development of detectors for circular machines, which typically require a continuous-time pixel front-end architecture [13, 16]. The timing performance of such architectures is currently not compatible with the requirements of low duty cycle lepton (linear) colliders, which operate with bunch spacing of a few ns. SLAC is developing a readout circuitry optimized for such machines, leveraging the beam time structure. In particular, two techniques will be implemented. The readout electronics will adopt a power pulsing scheme: the analog front-end circuitry will be powered off during the dead-time between different bunch trains. With low duty cycle machines like C$^3$ and ILC, this technique enables a power reduction by more than two orders of magnitude. Power pulsing techniques were previously developed and characterised with the KPiX ASIC [19]. Second, the pixel front-end circuitry will be based on a synchronous readout architecture, where the operation of the circuitry is timed with the accelerator bunch train. In this way, the noise and timing performance of the circuitry can be maximized while maintaining low-power consumption. SLAC will leverage a decade of expertise with synchronous readout architectures operating with fast integration times [20], which have been implemented in all ASICs developed for the Linear Coherent Light Source (LCLS).

By combining all these techniques, the goal of the current R&D at SLAC is to achieve the specifications described in Table 1. We have derived the initial specifications from the C$^3$ configuration [21], as it is the one that provides the most challenging technological case for timing resolution and is compatible with the current limits of MAPS technology. Moreover, a sparse readout mechanism, based on asynchronous read-out logic, will minimise the digital power as well as make the circuitry more robust to local variations of the transistor performance. A first, small-scale prototype of such device is expected in late 2022.

The development of wafer-scale MAPS will allow designers to investigate the following challenges:

- **Power pulsing:** to take full advantage of the power pulsing technique, the current drawn from the supply needs to reach the peak value in the shortest time possible, minimizing the
Parameter | Value
---|---
Min. Threshold | 140 e⁻
Spatial resolution | 7 µm
Pixel size | 25 x 100 µm²
Chip size | 10 x 10 cm²
Chip thickness | 300 µm
Timing resolution (pixel) | ~ns
Total Ionizing Dose | 100 kRads
Hit density / train | 1000 hits / cm²
Hits spatial distribution | Clusters
Power density | 20 mW / cm²

Table 1: Target specifications for 65 nm prototype.

duty cycle and thus decreasing the average power consumption. However, the instantaneous current consumption of the pixel matrix can reach several Amperes over a few microseconds.

- **Power distribution**: the distribution of the power supply over a large area is challenging because of the non-negligible voltage drop over long metal distribution lines.

- **Yield**: the probability of fabrication defects scales with the area of the device. For reticle-size MAPS, a defect in one reticle would result in a lower number of usable dies per wafer. However, a defect on a wafer-scale device is almost inevitable and could result in the loss of a full wafer. Therefore, it is essential to develop new techniques to mitigate the effects of fabrication defects, such as shorts between supply and ground lines.

- **Stitching techniques**: the design of stitching MAPS introduces additional layout design rules and methodologies, with the goal to increase the fabrication yield. This additional set of rules is not traditionally encountered by ASIC designers, therefore exposing ASIC designers to such design rules is an essential first step towards the development of wafer-scale devices.

- **Assembly and power delivery**: preliminary mechanical and assembly tests need to be carried out to evaluate techniques to deliver power to such sensor, while minimizing the dead material of the detector.

### 2.2 MAPS Performance for Tracker detectors

A MAPS based tracker for SiD would feature a sensor of size similar to that described in the ILC TDR, 10×10 cm² devices. It would be constructed by stitching 2cm x 2cm reticles. However, such a device would provide exceptional granularity of 25µm by 100µm pixels, with the alignment placing the 25µm pixel dimension in the bend direction, providing a resolution of $25\mu m/\sqrt{12} \approx 7 \mu m$ without charge sharing. The 25µm pixel size matches the KPiX-readout, silicon-strip width of the SiD TDR design which was recently assembled, tested, and shown to achieve 7µm resolution [22]. The depleted 10µm thick epi layer charge collection of the MAPS allows a minimum threshold of 1/4 MIP, ensuring high efficiency. The pixel nature provides vastly improved pattern recognition for track finding over the strip devices. For the endcaps, such a sensor would eliminate the need for two sensors in a small-angle-stereo configuration, reducing both the material budget and cost.
2.3 MAPS Performance for ECal

The finely granular, digital readout of the SiD ECAL offered by application of MAPS sensors provides the potential for significantly enhanced performance over that envisioned in the ILC TDR [2]. One advantage of this digital approach over the TDR analog approach is the reduction of the effects due to variations in energy deposition, such as Landau fluctuations. Fluctuations in the development of the shower remain as the main contribution to resolution. The fine granularity also reduces the likelihood of overlapping particles per pixel and improves the separation of nearby distinct showers, such as from high energy π⁰'s or jets, and contributes to improved particle flow pattern recognition. Quantifying the nature of these effects is being investigated with Geant4 simulations.

The pixel configuration of 2500 µm², segmented as 25 µm x 100 µm, is designed for the tracking performance derived from the precision of the 25 µm size. Excellent performance with a purely digital ECAL based on this fine granularity is expected. Previous studies [23, 24, 25] have even indicated potential energy resolution advantages for a digital ECAL solution (see Figure 2).

![Energy Resolution Graph](image)

Figure 2: The energy resolution as a function of the incident energy for single electrons for both analog and digital readout using a Geant4 simulation. The realistic digital cases includes effects of saturation and charge sharing, leading to a degradation of 35% [23].

New simulation studies, based on this fine, digital configuration, have confirmed the previous studies referred to in Figure 2 and demonstrated additional details on the performance [26]. These studies indicate the electromagnetic energy resolution based on counting clusters of hits in the MAPS sensors should provide better performance than the SiD original design based on 13 mm² analog pixels, as shown in Figure 3.

Geant4 simulations of the performance of digital MAPS applied in the electromagnetic calorimeter has been under development and study throughout 2021, and are continuing [26]. These studies are aimed at understanding the ultimate performance and limitations, and to inform the ASIC designers on the requirements for the sensor chips. The expected performance has been found to exceed the requirements and performance of the SiD TDR ECAL design. Pixel structures of 25 x
100 $\mu m^2$ achieve equivalent performance to a 50 x 50 $\mu m^2$ design. The 5T magnetic field has been found to have a minor effect and to degrade the resolution by a few per cent due to the impact on the lower energy electrons and positrons in a shower.

Two-showers separation is excellent, as shown in Figure 4 for two 10 GeV electron showers separated by one cm and Figure 5 of two 20 GeV gamma showers from a 40 GeV $\pi^0$ decay. The fine granularity of pixels provides excellent separation. The performance for two electron showers versus their separation is summarized by Figure 6. The fine granularity allows for identification of two showers down to the mm scale of separation, and the energy resolution of each of the showers does not degrade significantly for the mm scale of shower separation.

Figure 7 shows the gamma energy resolution performance for the range of measurements from the basic mip counting (dark blue, an idealized, best possible resolution) to that achieved by analyzing hits in clusters (light blue). These simulations are now mature and are well positioned to guide the design and production of the sensors. Future planned studies include the reconstruction of showers and $\pi^0$s within jets, and their impact on jet energy resolution, and the measurements of the Higgs branching ratios.

Future studies include:

- Optimal Pixel size for a MAPS sensor-based ECal;
- Comparison of analog and digital ECal performance;
- Optimization of the overall ECal design;
- Optimization of the design for manufacturability, possibly with robots.

\[\text{gamma resolution is somewhat worse than electron resolution due to the different nature of the shower development.}\]
The large volume of data provided by a MAPS based ECal reveals details of particle showers. The extraction of the most pertinent information, for example particle energy, particle type, and the separation of nearby and overlapping showers, provides an opportunity to apply Machine Learning techniques. We propose to apply such deep learning methods to particle and jet reconstruction in the SiD collider detector ECal based on MAPS technology.

3 MAPS developments for EIC

The Electron-Ion Collider (EIC) aims at resolving the 3D partonic structure of nucleons and nuclei, to address the origin of the nucleon mass and spin, as well as the properties of a dense system of gluons. The science requirements and detector concepts for the EIC are described in [28].

3.1 Performance for Tracker detectors

MAPS technology for tracking and vertexing detectors is exploited by all the emerging detector concepts at the EIC: EIC Comprehensive Chromodynamics Experiment (ECCE), A Totally Hermetic Electron Nucleus Apparatus (ATHENA) and COmpact detectoR for the EIC (CORE). No other detector technology is capable to fulfill the requirements of compactness, high segmentation and very low material budget, which is critical to achieve the required momentum resolution. The EIC vertex layers follow the ALICE ITS-3 development of large-area, wafer-scale, stitched sensors that are thinned to less than 50μm, to allow bending them around the beam pipe to achieve the required radial coverage with the expected ability of the structure to self-support, being held in place using only low-mass carbon fiber support structures [29]. On the other hand the barrel...
Figure 5: Projection in z-layer plane of the pixel clusters in two 20 GeV gamma showers emerging from a 40 GeV $\pi^0$ decay. The z direction is the 100 $\mu$m pixel direction and the layers shown are 20 thin ($0.64 X_0$) followed by 10 thick tungsten layers. Each vertical bin is 400 $\mu$m wide. The two showers are separated by less than one cm [26].

Figure 6: Efficiency for distinguishing two 10 GeV electron showers as a function of shower separation (upper curve) and the degradation of energy resolution as a function of separation due to overlap of cluster hits (lower curve) in the new SiD digital MAPS design based on a Geant4 simulation [26].

tracker layers could be realized more conservatively as staves comprising smaller, but still requiring reticle stitching sensors. Regarding the disks for the tracker, the same MAPS technology as for the vertex and tracking barrel is planned.
Figure 7: Energy resolution for gamma showers as a function of energy. The curves show (from lower up) the resolution based on a.) counting minimum ionizing particles (dark blue, mips), b.) modified cluster counting (light blue), c.) pure simple cluster counting (red), d.) active pixels (green, hits), and e.) the required performance from the ILC TDR (brown dash-dot) [26].

Based on the TowerSemi 65 nm process and with reticle stitching thinning and bending of MAPS, a goal of a pixel pitch down to 10 µm, with power dissipation below 20 mWcm⁻² to build a vertex detector, with a space point resolution of better than 5 µm and a material thickness of 0.05% X/X₀ per layer, seems to be achievable. By comparison, the current ALICE ITS (ITS2) features vertex layers with a pixel pitch of approximately 30 µm that dissipates 40 mWcm⁻² and 0.35% X/X₀ per layer.

The EIC Silicon Consortium, of which the BNL and LBL are key members, targets co-development with ALICE-ITS3 of the wafer-scale MAPS for the vertex layers, while also developing an EIC-specific, stitched but not wafer-scale version of MAPS for the barrel layers and for the disks of the tracker. The development of the sensors is going to be assisted by the development of support structures and services. The BNL group has been advancing developments of optimized readout architectures in order to read out data from highly granular pixel detectors. An example of such a development is the Event Driven with Access and Reset Decoder (EDWARD) architecture, based on the Loca_Asyncronous-Global_Synchronous topology, with the ability to respond, without built-in prioritization, to asynchronously signaled read-requests from the channels to read out data from these channels [30].

3.2 Performance for Barrel ECal

Physics goals at the EIC lead to unique requirements for the electromagnetic calorimeter design. In the barrel region of the electromagnetic calorimeter (barrel ECal), the momentum of scattered electrons will be measured with excellent precision ($\sigma_{p_T}/p_T(\%) = 0.1p_T \oplus 0.5$) with the tracker. However, the electron energy and shower profile measurements play a crucial role in the separation of electrons from background pions in Deep Inelastic Scattering (DIS) processes. The ECal must also measure the energy and coordinates of neutral particles - photons, and identify single photons.
originating from the Deeply Virtual Compton Scattering (DVCS) process and photon pairs from $\pi^0$ decays.

The barrel region covering roughly $|\eta| < 1$, requires moderate energy resolution of approximately $(10–12)%/\sqrt{E} \oplus (1–3)\%$, but with an excellent electron-pion separation resulting in pions suppression up to $10^4$ at low particle-momenta below 4 GeV/$c$; a spatial resolution sufficient to separate photons from $\pi^0 \rightarrow \gamma\gamma$ decay with momentum up to about 15 GeV/$c$; and the capability of detecting photons with energies down to 50 MeV. As mentioned in the EIC Yellow Report, the required energy resolution is sufficient for the $e/\pi$ separation for particle momenta above 4 GeV/$c$ for studies of DIS processes in $e + p$ collisions at beam energies of $18\times275$ GeV (the highest collision energy). The required electron-pion separation at low momenta ($< 4 \text{ GeV/c}$) can only be achieved with the particle identification improved by using calorimeters with a much better resolution and/or by providing a shower-profile measurement capability, or by using different detectors, such as a Cherenkov detector.

The ATHENA proto-collaboration has proposed a unique electromagnetic calorimeter that is cost effective for its excellent performance in energy and spatial reconstruction and particle identification, fulfilling the Yellow Report requirements and opening new physics opportunities [31]. This section describes the proposed hybrid design concept and simulation based performance studies of the barrel electromagnetic calorimeter comprising scintillating fibers embedded in Pb and imaging calorimeter based on MAPS sensors (AstroPix).

Figure 8: The simulated design of barrel electromagnetic calorimeter of ATHENA. The 6 layers of imaging CMOS Si sensors are interleaved with 5 Pb/SciFi layers, followed by a large chunk of Pb/SciFi [31].

**Barrel ECal Concept**

The imaging layers of the barrel electromagnetic calorimeter are made up of 6 layers of MAPS sensors interleaved with 5 Pb/SciFi layers. It is followed by a large chunk of Pb/SciFi resulting in a
total radiation thickness of about 20 $X_0$. Each layer has 12 staves with the length of 405 cm. With the innermost imaging layer at 103 cm and outermost one at 117 cm, the electromagnetic calorimeter spans the area of about 172 $m^2$ covered with CMOS sensors. The CMOS sensors are designed using a 180 nm CMOS process and developed for NASA’s AMEGO-X mission concept [32]. A low-power CMOS sensor design is the successor of ATLASPix and called AstroPix [33]. ATLASPix are High Voltage MAPS, developed for the tracking applications at the LHC applications with a triggered readout [34]. The first and second versions of the AstroPix chip are being tested (details in Sec. 3), and a third version is under review and will be submitted for fabrication soon. These sensors have demonstrated excellent energy resolution at low energies ($\sim 7\%$ at 30 keV) coupled with low power usage and stringent cooling requirements [33]. The Pb/SciFi design is based on the existing GlueX barrel calorimeter [35]. The Si imaging layers of the ECAL enhance the global reconstruction performance of ATHENA by providing precision position resolution which also allows probing the 3D shower profile. The ATHENA detector with its high granularity and the ability to tag a final state radiative photon to a very small energy provides precision measurement of DIS variables [36]. The barrel ECAL contains the endcap calorimeter in the electron-going direction covering the $\eta$ range of about ($-1.5, 1.1$).

The performance goals (based on [33]) AstroPix for pixels are listed in Table 2.

| Feature                  | Specification                |
|--------------------------|------------------------------|
| Pixel size               | $500 \mu m \times 500 \mu m$ |
| Power usage              | $< 1 \text{ mW/cm}^2$       |
| Energy resolution        | $10\% @ 60 \text{ keV}$ (based on the noise floor of 5 keV) |
| Dynamic range            | $\sim 700 \text{ keV}$     |
| Passive material         | $< 5\%$ on the active area of Si |
| Si Thickness             | $500 \mu m$                 |
| Time tag                 | $\sim 1 \mu m$             |

Table 2: Performance goals for AstroPix pixels based on [33].

The Geant4 simulation studies using the ATHENA experiment software framework shows that the extension of the dynamic range of the digitized pixel readout to a few MeV could cover over 99% of the deposited energy (Fig. 9), possibly providing better energy resolution for EIC physics. In our simulations presented in 3.2, we explore the possibility of using the AstroPix sensor off-the-shelf, with the performance parameters from Table 2. It is possible to develop a future version of the BECal pixel sensor based on the studies using the AstroPix sensor; however, as of now, we do not see a compelling need for that.

**Barrel ECAL Performance**

The imaging layers of MAPS sensors provide excellent position resolution allowing precise 3D shower imaging on top of the excellent energy resolution provided by the Pb/SciFi calorimeter. The examples of the impact of the imaging layers relative to a design based only on scintillating fibers/Pb calorimetry are:

- Significantly improved electron-pion separation with respect to $E/p$ method - impact on DIS cross section and asymmetries

- Separation of $\gamma$s from $\pi^0$ decays at high momenta up to about 40-50 GeV/c and precise position reconstruction of $\gamma$s (well below 1 mm at 5 GeV) - impact on DVCS and photon physics
Figure 9: The accumulative energy deposit over pixel energy deposit ratio for 2 GeV particles using ATHENA simulation software. Over 99% of the energy deposit is made through hits with a deposit < 3 MeV in the AstroPix pixel sensors.

- Tagging final state radiative photons from nuclear/nucleon elastic scattering at low $x$ to benchmark QED internal corrections, by precise measurement of photon coordinates and the angle between electrons and photons

- Allowing PID of low energy muons that curl inside the barrel ECal (below about 1.5 GeV with 3T magnetic field) - impact on $J/\psi$ reconstruction and Timelike Compton Scattering (TCS)

- Improving particle identification based on other detector subsystems - providing a space coordinate for DIRC reconstruction (no need for additional large-radius tracking detector)

Performance of the barrel calorimeter is simulated using the GEANT4-based ATHENA simulation framework. The simulation includes realistic implementation of the detector geometry, signal digitization and readout electronics, as well as the reconstruction of the shower clusters with the barrel ECal.

The single-particle simulation is used to measure the energy resolution of the calorimeter with $\phi \in (0, 2\pi)$ and $\eta = 0$. The shower clusters are reconstructed using imaging layers and SciFi layers and the corresponding energy resolution of photons as a function of total energy is shown in Figure 10. The energy resolution for the SciFi layers is measured to be $\sigma/E = (5.3 \pm 0.1)%/\sqrt{E} \oplus (0.73 \pm 0.05)\%$. It is expected that the SciFi layers of the calorimeter will have significantly better energy resolution than the thin imaging Si layers, and therefore they are used for the final energy determination.

The spatial resolution for the barrel ECal is obtained using single-particle simulations, where photons were generated with energy ranging from 0.5 to 20 GeV at normal incidence to the calorimeter layers. In global coordinates of ATHENA the particles were generated with $(\phi, \theta) = (0, \pi/2)$, which corresponds to $(y, z) = (0, 0)$. The spatial resolution of the detector depends on the granularity of the detector and energy resolution. As mentioned before, the SciFi layers provide better energy resolution while the imaging MAPS layers provide superabundant granularity with pixel
Figure 10: Energy resolution for the barrel ECAL as a function of generated particle energy. The photons are generated from single-particle simulations with $\phi \in (0, 2\pi)$ and $\eta = 0$. The resolution for the SciFi layers of the calorimeter is shown in left panel, and the plot on the right shows the resolution for the 6 imaging layers.

size of $500 \mu m \times 500 \mu m$. The spatial resolution influences, e.g., separation of $\gamma$s from $\pi^0$ decays, precise determination of the coordinate of impact for photons, and, especially in the case of the imaging calorimetry, the $e-\pi$ separation.

The position resolution in $(\phi, \theta)$ or $(y, z)$ can be obtained from the reconstructed position of the cluster reconstructed with a topological cell clustering algorithm from the imaging layers. However, the resolution obtained with this method is strongly dependent on the chosen clustering algorithm and its optimization. A simple algorithm utilizing the information from the first imaging layer with a registered hit can already significantly improve the position resolution of the particle impact. This algorithm takes the position of the fired pixel from the first imaging layer with the registered hit. If more than one pixel fired within this layer, the position of the pixel closest to the cluster location, derived from the topological clustering algorithm, is chosen. Figure 11 presents the difference between generated and reconstructed $\phi$ and $\theta$ using the topological clustering and $\phi$ and $\theta$ obtained from the first imaging-layer hit information. Further improvements are expected from the ongoing reconstruction and clustering-algorithm optimizations, and the use of AI algorithms capable of more efficiently incorporating single-layer information and their correlation.

The position resolution as a function of energy has been presented in Fig. 12. The resolution obtained directly from the cluster position is $(2.32 \pm 0.06) \text{ mm}/\sqrt{E} \oplus (1.4 \pm 0.02) \text{ mm}$, however, one may see that the fit which describes the spatial resolution for sampling and crystal calorimeters with large towers ($\sim$ Molière radius), doesn’t describe the presented dependence ideally. The green points show the position resolution from the improved algorithm using the first imaging layer hit information determined from FWHM. One can see that for all energies the difference between true and reconstructed position at half maximum is within one $500 \mu \text{m}$ pixel.

The important characteristic of discriminating a single photon from the photon pair generated through the high momentum $\pi^0$ decay depends on the calorimeter granularity and spatial resolution. Figure 13 shows the event display of a single $\pi^0$ at 15 GeV/$c$ decaying to $\gamma\gamma$. The left panel depicts the 3D profile of hit positions and energy deposition in the imaging layers of the ECal. Whereas, the right panel shows the projection of the cluster hits on the first layer of the imaging calorimeter in $\eta$ and $\phi$ bins. The red cross marker describes the true position of the photons.

Figure 14 shows the merging probability of two $\gamma$s into one cluster at $r = 103 \text{ cm}$. The EIC Yellow Report suggests the separation criterion of at least one cell size for the $\gamma$s detected.
Figure 11: Difference between generated and reconstructed particle $\phi$ and $\theta$ in deg using the topological clustering (blue), and obtained from the first imaging-layer hit information (red). This simulation uses 5 GeV photons generated with $(\phi, \theta) = (0, \pi/2)$ with 3T magnetic field.

Figure 12: Position resolution for the barrel ECal cluster as a function of the generated particle energy obtained from single-particle (photons) simulations of the ATHENA detector. The black dots show the resolution obtained directly from the cluster position, the green triangles show the resolution from improved algorithm using the first imaging-layer hit information determined as FWHM.

through the ECal. For imaging ECal with the very small cell size of 500 $\mu$m, the probability of merging two $\gamma$s has been estimated using a separation criterion of $6 \times$ FWHM of the shower profile at the imaging layer with the highest separation power. Fig. 14 shows that the imaging ECal allows the separation of $\gamma$s from $\pi^0$ with high momenta up to 50 GeV/$c$.

The proposed design of the barrel ECal aims to provide a precise electron and pion identification required for inclusive DIS physics. The barrel ECal, with its high pion rejection power in the low momentum region $p \leq 2$ GeV/$c$, outperforms the traditional calorimeter with rejection based on $E/p$ or one-dimensional shower profiles. The AstroPix layers provides a 3-dimensional profile of the electromagnetic or hadronic shower development. The overall performance of the barrel ECal obtained with a neural network classification is shown in Fig. 15 along with the pion rejection.
Figure 13: The event display of $\pi^0 \rightarrow \gamma\gamma$ generated at 15 GeV/c of pion momentum. The 3D position of hits from 6 imaging layers is shown in left panel (Red dashed lines show true $\gamma$ trajectory). Right panel shows the cluster hits projected on the first imaging layer (True positions of $\gamma$s shown with red marker).

Figure 14: Merging probability of the two $\gamma$s from $\pi^0$ decay in the barrel region at $r = 103$ cm. For the ATHENA barrel ECAL the separation criteria of 6×FWHM of the shower profile in the $y$-coordinate.

with $E/p$ cut for other detector technologies taken from [38]. The proposed ECAL detector with the AstroPix layers for 3D imaging and the machine learning algorithm for pattern recognition demonstrate a higher pion rejection power than a PbWO$_4$ calorimeter or other sampling calorimeters at $p \leq 2$ GeV/c, and comparable to the more expensive PbWO$_4$ at higher momentum. All measurements in the left plot of Fig. 15 are simulated or measured using a standalone calorimeter setup without any material in front of the ECAL and no magnetic field. Including the material and 3 T magnetic field in the simulation affects the electron efficiency. The electrons with momenta below about 0.7 GeV/c do not reach the barrel ECAL. For electron energies above this value, the...
rejection power remains about $10^3$, with an electron efficiency drop to about 90% (the right plot of Fig. [15]).

The inclusion of other PID detectors is expected to improve the $e$-$\pi$ separation even further. Along with the capabilities of the electron-pion separation, the proposed barrel ECAL is also capable of identifying particles with different shower profiles, like muons, with significant impact for low particle momenta (below 1.5 GeV). Fig. [16] shows the $\pi$-$\mu$ separation capabilities of the ATHENA barrel calorimeter. On the plot one can see two distinct momentum regions for muon PID. With 3 T magnetic field, at $\eta = 0$, muons below about 1.5 GeV/$c$ curl inside the ECAL, while muons above 1.5 GeV/$c$ can reach the hadronic calorimeter (barrel HCal). For high-momentum muons, the PID criterion is a MIP-like signal required both in ECAL and HCal, whereas low-energy muons are identified with a neural network classification based on information from the ScFi/Pb and imaging layers. Moreover, a separation based on an $E/p$ cut from one or more of the ScFi/Pb layers with the highest discrimination power has been studied. The results show that the imaging layers significantly reduce pion contamination at 95% muon efficiency (10 times at 0.7 GeV/$c$, and 5.5 times at 0.5 GeV/$c$).

Figure 15: Left: The pion rejection power of the ATHENA barrel calorimeter (red solid line) and other detectors [38]. All the curves, including simulations and data, are obtained for the standalone calorimeter, i.e., no other materials are placed in front of the calorimeter and no magnetic field is involved. Right: The pion rejection achieved with the current status of the neural network training for the simulated standalone calorimeter without magnetic field (green line) and with the 3 T magnetic field (red line). The blue line shows the impact of all the ATHENA material in front of the calorimeter (note that the electron efficiency drops to about 90%).

4 Space born applications: AstroPix

The All-sky Medium Energy Gamma-ray Observatory eXplorer (AMEGO-X) is a Medium Explorer concept [39]. Recent detection of gravitational wave signals and neutrinos from gamma-ray sources highlights the importance of gamma-ray observations in the multimessenger astrophysics. AMEGO-X operates both as a Compton and pair-production telescope to achieve unprecedented sensitivity between 100 keV and 1 GeV. AMEGO-X provides better understanding of multi-messenger science and time-domain gamma-ray astronomy studying e.g. high-redshift blazars, which are probable
Figure 16: The $\pi$-$\mu$ separation capabilities of the ATHENA barrel calorimeter. The left plot presents the pion contamination, the middle plot shows the muon efficiency, and the right plot shows the pion suppression factor. The results for the three different separation methods described in the text are presented.

sources of astrophysical neutrinos, and gamma-ray bursts. AMEGO-X is composed of two detector subsystems, the Gamma-Ray Detector (GRD) and the Anti-Coincidence Detector (ACD) (Figure 17). The GRD consists of a Tracker comprised of four square towers, each 40 cm wide and 60 cm tall with 40 layers of MAPS detectors, and a calorimeter, with four layers of Cesium Iodide (CsI) scintillator bars. The goal is to provide low power sensors to provide optimized geometry for the space applications like gamma-ray astronomy.

As mentioned before AstroPix is a monolithic silicon pixel detector derived from ATLASPix which was developed as the pixel tracking detectors for LHC applications with a triggered readout [34]. Several prototypes and design variants were characterised in the lab and evaluated with test beam data [40]. A latest version of ATLASPix family [41], ATLASPix3 was produced in the 180 nm CMOS process by TSI [42] and has a sensor area of $20.2 \times 21 \text{ mm}^2$ with pixel size of $150 \times 50 \mu\text{m}^2$. The ATLASPix3 is fully operational and provides a timing resolution of about 10 ns [34]. The ATLASPix3 sensor is evaluated to determine the potential of pixelated silicon in future space-based gamma-ray experiments. In the space applications it is critical to reduce noise in order to reconstruct low energy cosmic events ($\leq 100$ keV) with better energy resolution ($\leq 10\%$ at 60 keV). The ATLASPix sensor is reconfigured for slower time response and modified for thicker sensor bulk size to absorb scattered electrons. The energy resolution and detector response of ATLASPix3 is determined using radioactive photon sources. The analog output of the ATLASPix3 sensor demonstrated the single pixel energy resolution of $7.7 \pm 0.01\%$ at 5.89 keV and $3.18 \pm 0.73\%$ at 30.1 KeV. Figure 18 shows the photon energy spectra (left) and related energy resolution (right) of the ATLASPix3 measured using analog output. On the other hand, the digital energy resolution of ATLASPix requires improvement as digital output is designed for MIPs and not photons.

The AstroPix sensor is an optimized version of ATLASPix to achieve required intrinsic energy resolution of $\leq 10\%$ at 60 keV for the Compton event reconstruction in gamma-ray astrophysics. AstroPix sensors are designed with large pixel size compared to ATLASPix which results in low power consumption, as it is dominated by the amplifiers within the pixel electronics. The amplifier on each pixel is followed by low pass filter which reduces the noise. This change in circuitry reduces timing resolution but also provides required energy resolution. The current prototype of AstroPix sensors, AstroPix v2 is designed with amplifier and comparator for each pixel all within the sensitive area. AstroPix v2 has a pixel size of $250 \times 250 \mu\text{m}^2$. AstroPix v2 is comprised of an 35 by 35 pixel matrix where the chip are of $10 \times 10 \text{ mm}^2$. The analog output can be read from the first row

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Figure 17: AMEGO-X instrument design to reveal the MeV $\gamma$-ray sky with unprecedented sensitivity.

Figure 18: The calibrated photopeaks represent the six different sources (left) and the analog energy resolution of the ATLASPix3 detector using a single pixel (right)\cite{33}.

through a daisy chain. The power consumption of the AstroPix v2 is currently being tested. The design review of AstroPix v3 is in progress with pixel size of 500 $\mu$m with sensor area of $20 \times 20$ mm$^2$ and lower power dissipation. It is expected to be submitted for fabrication in April 2022.
5 Executive Summary

Next MAPS developments will target a diverse set of goals and applications and will have to address improvements to speed and resolution performance, and the system approaches needed for large scale use at a reasonable cost.

Future colliders will need large areas of silicon sensors, several hundred $m^2$, for low mass trackers and sampling calorimetry [2]. Trackers will require multiple layers, large radii, and micron scale resolution. Sampling calorimeters will also have very large areas and are improved by very thin overall packages, which reduces the Moliere radius. MAPS with characteristics suitable for trackers and electromagnetic calorimeters at future colliders experiments are being developed. There is an ongoing effort that focuses on developing readout electronics compatible with a power pulsing scheme: the analog front-end circuitry will be powered off during the dead-time between different bunch trains. With low duty cycle machines like C3 and ILC, this technique enables a power reduction by more than two orders of magnitude. Second, the pixel front-end circuitry will be based on a synchronous readout architecture, where the operation of the circuitry is timed with the accelerator bunch train. In this way, the noise and timing performance of the circuitry can be maximized while maintaining low-power consumption. The development of wafer-scale MAPS will allow designers to investigate the power pulsing, power distribution, yield, stitching techniques, assembly and power delivery.

For the vertex detector at EIC the target point resolution is better than 5 $\mu m$ with a material thickness of 0.05% X/X0 per layer. A solution based on the TowerSemi 65 nm process and with reticle stitching thinning and bending of MAPS, with a goal of a pixel pitch down to 10 $\mu m$ and power dissipation below 20 mWcm$^{-2}$ seems promising. This effort is synergistic with the ALICE ITS3 developments as the performance requirements are similar.

Space-born applications for MeV $\gamma$-ray experiments with MAPS based trackers (AstroPix). The AstroPix sensor is an optimized version of ATLASPix to achieve required intrinsic energy resolution of $\leq 10\%$ at 60 keV for the Compton event reconstruction in gamma-ray astrophysics. AstroPix sensors are designed with large pixel size compared to ATLASPix which results in low power consumption, as it is dominated by the amplifiers within the pixel electronics.

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