Numerical Analysis on Design of High Performance CSTZ Solar Cells

Cherry Tin¹,², Saw Aung Yein Oo¹, Tin Tin Hla¹

¹Department of Electronic Engineering, Mandalay Technological University Patheingyi, Mandalay Region, Republic of the Union of Myanmar
²Department of Electronic Engineering, Government Technological College (Shwe Bo), Sagaing Region, Republic of the Union of Myanmar

Email address: macherrytin.mtu@gmail.com (C. Tin)

To cite this article:
Cherry Tin, Saw Aung Yein Oo, Tin Tin Hla. Numerical Analysis on Design of High Performance CSTZ Solar Cells. Communications. Vol. 8, No. 1, 2020, pp. 17-21. doi: 10.11648/j.com.20200801.13

Received: November 11, 2019; Accepted: December 25, 2019; Published: January 7, 2020

Abstract: The surging of photovoltaics has witnessed the boost of numerous fascinating approaches to the enhancement of power conversion efficiencies (PCE) of the devices. For the search of new metal-halide CZTS solar cell materials, tolerance factors are calculated from the ionic radius of each site and are often utilized as the critical factors to expect the materials forming CZTS structure. Significant progress in photovoltaic conversion of solar energy can be achieved by new technological approaches that will improve the efficiency of solar cells and make them appropriate for mass production. The paper presents the numerical analysis on design of high performance CSTZ solar cells with the help of MATLAB programming. The performance reliance on physical properties is estimated, together with the layer thickness, carrier density, defect density and interface defect density. The best possible the layer thickness and carrier density were originated in this study. The defect density in the absorber would be controlled for reducing the recombination. The interface between the layer of absorber and the layer of buffer is essential for the performance of that solar cell. The interface defect density is embarrassed to accomplish enviable conversion efficiency. The results confirm that the experimental works could be met with the theoretical analysis in this paper.

Keywords: Numerical Analysis, High Performance, CSTZ, Solar Cells, MATLAB

1. Introduction

Solar cells convert solar energy, in the form of electromagnetic radiation, into electrical energy. A standard solar cell is shown schematically in Figure. Sunlight is incident on the surface covered by a metallic grid acting as an electrical contact [1-4]. Between the grid lines photons are absorbed in a semiconductor which is covered by an anti-reflective coating to reduce reflection. Photons with energies larger than the band gap EG of the semiconductor excite electrons from the valence band to the conduction band, resulting in free charge carriers; electrons and holes. The charge carriers are separated by either a gradient in the charge carrier density or an electric field. In Figure, the charge carriers are separated by the electric field across the p-n junction and the electrons are transported through a load in the external circuit where they do work [1-6].

There is a wide range of other semiconductor material capable of producing solar cells of acceptable efficiencies. Both solid and liquid materials are used in solar cells. Homojunction, heterojunction, metal-semiconductor, and some dye-sensitized solar cells use all-solid structures, whereas liquid-semiconductor and many dye-sensitized cells use solid – liquid structures. These materials can be inorganic or organic. The solids can be crystalline, polycrystalline, or amorphous. The liquids are usually electrolytes. The solids can be metals, semiconductors, insulators, and solid electrolytes [7-13].

Recently, the high performance solar cell fabrication is a vital role in semiconductor optoelectronic devices design. Cu₂ZnSnS₄ (CZTS) is a talented material for the low-cost thin-film solar cells owing to it’s the best direct band gap energy of 1.5eV and huge absorption coefficient of 10⁴ cm⁻¹. The peak conversion efficiency for pure Cu₂ZnSnS₄ (CZTS) solar cells has been described with 8.4% [14]. In order to get the improvement of the conversion efficiency incessantly,
complete analysis of the device operation apparatus is essential. The semiconductor Poisson equation and continuity relations of electrons and holes are realized by using iterative solution [15-19].

The rest of the paper is organized as follows. Section II mentions the model and physical parameters of CZTS solar cell structure. Section III presents the mathematical modelling of the proposed solar cell structure. Section IV highlights the analysis on device system. Section V discusses on the simulation results and discussions those results. Section VI concludes the current work done.

2. Model and Physical Parameters of CZTS Solar Cell Structure

The model structure of the CZTS solar cell appraised in this study is revealed in Figure 1. The model of back contact/CZTS layer/buffer layer/i-ZnO/ZnO:Al/front contact systems as a source for modelling CZTS solar cells. The alternative buffer layer to replace specific materials would be considered in the prospect study. The layer properties are mentioned based on CZTS layer/buffer layer/i-ZnO/ZnO:Al system. The values of layer thickness (W) for CZTS layer/buffer layer/i-ZnO/ZnO:Al are 600,100,80 and 450 nm, respectively. The values of relative permittivity (ε/ε0) are 600,100,80 and 450 nm, respectively. The values of relative permittivity (ε/ε0) are 600,100,80 and 450 nm, respectively. The values of mobility of electrons (μe) are 5, 100, 100, and 100 cm²/Vs, respectively. The values of mobility of holes (μh) are 1, 25, 25, and 25 cm²/Vs, respectively. The values of holes density (Nh) are 1×10¹⁶, 0, 0, and 0 cm⁻³, respectively. The values of electrons density (Ne) are 0, 1×10¹⁷, 1×10¹⁷ and 1×10¹⁸ cm⁻³, respectively. The values of conduction band effective density of states (Nc) are 1.45, 2.4, 3.3, and 3.3, respectively. The values of valence band effective density of states (Nv) are 1.8×10¹⁸, 1.8×10¹⁹, 1.8×10¹⁹ and 1.8×10¹⁹ cm⁻³, respectively. The values of Gaussian defect density (Nd) are 1.8×10¹⁷ (Donor), 1.8×10¹⁷ (Acceptor), 1.8×10¹⁷ (Acceptor) and 1.8×10¹⁶ (Acceptor) cm⁻³, respectively.

Figure 1. Model Structure of CZTS Solar Cell.

3. Mathematical Modelling

The hypothetical modelling is completed in the subsequent order. For short circuit current we know, 

\[ I_{\text{short circuit}} = I_0 \left( \frac{V_{\text{open circuit}}}{kT} \right)^{0.026} \]  

where \( I_{\text{short circuit}} \) is the short circuit current, \( I_0 \) is the diode saturation current, \( q \) is electron charge, \( k \) is the Boltzman constant, \( T \) is the temperature, and \( V \) is the voltage.

\[ V_{\text{open circuit}} = \frac{kT}{q} \ln \left( \frac{1}{I_{\text{short circuit}}} \right) \]  

where \( V_{\text{open circuit}} \) is the open circuit voltage and others are as settled above.

Maximum output voltage,

\[ V_m = V_{\text{open circuit}} - \frac{kT}{q} \ln \left( 1 + \frac{1}{0.026} \right) \]

Maximum output current,

\[ I_m = I_{\text{short circuit}} \left( 1 - \left( \frac{kT}{V_m} \right)^{0.026} \right) \]

Fill factor/ideality of the cell,

\[ FF = \frac{V_m}{V_{\text{open circuit}} - I_{\text{short circuit}}} \]

Considering the best case where all the photon energy are improved into short circuit current, \( I_m = I_{\text{short circuit}} \), fill factor becomes

\[ FF \approx \frac{V_m}{V_{\text{open circuit}}} \]

\[ FF \approx \frac{V_{\text{open circuit}}}{kT} \frac{kT}{q} \ln \left( \frac{1}{I_{\text{short circuit}}} \right) \]

Considering an ideal case,

\[ FF \approx \frac{V_{\text{open circuit}}}{0.096} \]

This derived experimental equation will definitely assist to find the cells optimist from the imitation open circuit voltage result.

The conversion efficiency could be obtained from:

\[ \eta = \left( \frac{V_m I_m}{P_m} \right) = \left( \frac{V_{\text{open circuit}} I_{\text{short circuit}} FF}{P_m} \right) \]

where, \( P_m \) is the solar radiation power incident on the unit area.
4. Analyzed the Device System

The study was accomplished to analyze the performance of CZTS solar cells based on the absorber thickness, carrier density, defect density and interface defect density.

4.1. Analysis on Absorber Thickness (CZTS Layer)

The absorber thickness of CZTS was most favourable to realize peak conversion efficiency. The peak conversion efficiency could be observed in the later section.

4.2. Analysis on Carrier Density in Absorber

The rising carrier density $N_A$ can lessen lifetime for electrons and consequence in diminishing carrier collection prospect and current density for short circuit. The fewer carrier collection prospects can also depreciate the quantum efficiency of extended wavelength photons. Nevertheless the diminishing carrier density $N_A$ enlarges the resistivity of the absorber and consequently condenses the involvement of holes current to current for short circuit. It is over and done with that a most favourable carrier density $N_A$ exists for the major current for short circuit. Alternatively, the increasing carrier density $N_A$ can diminish diode saturation current and augment open circuit voltage. The escalating voltage for open circuit can be described by the PN junction form:

$$V_{\text{open circuit}} = \frac{kT}{q} \ln \left( \frac{I_L}{I_0} \right)$$  \hspace{1cm} (14)

$$I_0 = qA \left( \frac{D_{pN}^2}{L_eN_A} + \frac{D_{nN}^2}{L_hN_D} \right)$$  \hspace{1cm} (15)

It pursues a most favourable substrate carrier density will survive for maximum conversion efficiency. The result of carrier density $N_A$ on the current density for short circuit, voltage for open circuit, fill factor and efficiency is evaluated.

4.3. Analysis on Defect Density in the Absorber

The SRH recombination was realized in the absorber of CZTS. The defect density based on fill factor could be evaluated with numerical analysis.

4.4. Analysis on Interface Defect Density Between the Absorber and the Buffer Layer

The interface properties between the absorber and the buffer layer are significant to realize the attractive efficiency of heterojunction solar cells. The interface defects of dislocations are affected by lattice mismatch between the two layers that appearance the interface. The performance weakening is owing to the rising interface recombination initiated by interface defects. Interface engineering is essential to be espoused to diminish interface recombination. One of the undertaking interface engineering expertises is to compliance the surface successfully prior to the deposition of the buffer layer.

5. Simulation Results and Discussions

The bandgap analysis for semiconductor solar cells based on silicon materials has been described. The material properties for solar cell have been demonstrated based on Absorption coefficient of silicon as the function of the wavelength and reflectivity. Finally, the characteristics of the silicon solar cell have been demonstrated to meet the high performance solar cell fabrication. According to the mathematical modelling of CZTS solar cell structure, there have been three analyses in this portion. Figure 2 shows the Fill Factor with respect to Open Circuit Voltage. Due to the increasing value of open circuit voltage, the fill factor would rise with respect to open circuit voltage. The fill factors were accepting to get the high performance of CZTS solar cell design. The defect density depends on the fill factor in real device fabrication. The recombination process could be affected based on the changes of fill factor in CZTS solar cell.

![Figure 2. Fill Factor with respect to Open Circuit Voltage.](image-url)
Figure 3 illustrates the Maximum Output Voltage with respect to Open Circuit Voltage. In this results, the observation of maximum output voltage were increased according to the incensement of open circuit voltages. The bound of defect density could be found in this section. Due to the changing of open circuit voltage, the maximum output voltage has to be altered because of the bound of defect density. The result says that the high performance CZTS solar cell could be formulated based on the relationship between open circuit voltage and maximum output voltage. The high performance CZTS solar cell could be formulated based on the varying of physical parameters for real world condition.

Figure 3. Maximum Output Voltage with respect to Open Circuit Voltage.

Figure 4 demonstrates the Short Circuit Current with respect to Open Circuit Voltage. The open circuit voltage of 10V with respect to short circuit current of 2.2 µA from these results could be observed.

Figure 4. Short Circuit Current with respect to Open Circuit Voltage.

Figure 5 mentions the Conversion Efficiency with respect to Short Circuit Current. According to this analysis, the conversion efficiency directly proportion to short circuit current and the maximum value is at 2.2 µA of short circuit current in CZTS solar cell.

Figure 5. Conversion Efficiency with respect to Short Circuit Current.

6. Conclusions

The interface between the layer of absorber and the layer of buffer is essential for the performance of that solar cell. The interface defect density was embarrassed to accomplish enviable conversion efficiency. The results have confirmed the experimental works could be met with the theoretical analysis in this paper. The manipulation of properties of specific material on the performance condition of CZTS solar cells was scrutinized in the study. The complete modelling of solar cell device confirmed the most favourable layer thickness and carrier density of CZTS. The defect density in the absorber and the interface between the absorber and the buffer layer should be embarrassed to diminish recombination. The bound of defect density was originated in this analysis. The device modelling of high performance CZTS solar cells will be enhanced by extremely accepting the physical conditions of perceptible experimentation in the prospect study.

Acknowledgements

The author would like to thank many colleagues from the Department of Electronic Engineering of Mandalay Technological University.

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