Scaling properties of ballistic nano-transistors

Ulrich Wulf*, Marcus Krahlisch and Hans Richter

Abstract

Recently, we have suggested a scale-invariant model for a nano-transistor. In agreement with experiments a close-to-linear threshold trace was found in the calculated $I_D - V_D$-traces separating the regimes of classically allowed transport and tunneling transport. In this conference contribution, the relevant physical quantities in our model and its range of applicability are discussed in more detail. Extending the temperature range of our studies it is shown that a close-to-linear threshold trace results at room temperatures as well. In qualitative agreement with the experiments the $I_D - V_G$-traces for small drain voltages show thermally activated transport below the threshold gate voltage. In contrast, at large drain voltages the gate-voltage dependence is weaker. As can be expected in our relatively simple model, the theoretical drain current is larger than the experimental one by a little less than a decade.

Introduction

In the past years, channel lengths of field-effect transistors in integrated circuits were reduced to arrive at currently about 40 nm [1]. Smaller conventional transistors have been built [2-9] with gate lengths down to 10 nm and below. As well-known with decreasing channel length the desired long-channel behavior of a transistor is degraded by short-channel effects [10-12]. One major source of these short-channel effects is the multi-dimensional nature of the electro-static field which causes a reduction of the gate voltage control over the electron channel. A second source is the advent of quantum transport. The most obvious quantum short-channel effect is the formation of a source-drain tunneling regime below threshold gate voltage. Here, the $I_D - V_D$-traces show a positive bending as opposed to the negative bending resulting for classically allowed transport [13,14]. The source-drain tunneling and the classically allowed transport regime are separated by a close-to linear threshold trace (LTT). Such a behavior is found in numerous MOSFETs with channel lengths in the range of a few tens of nanometers (see, for example, [2-9]).

Starting from a three-dimensional formulation of the transport problem it is possible to construct a one-dimensional effective model [14] which allows to derive scale-invariant expressions for the drain current [15,16]. Here, the quantity $\lambda = \hbar/\sqrt{2m^*\varepsilon_F}$ arises as a natural scaling length for quantum transport where $\varepsilon_F$ is the Fermi energy in the source contact and $m^*$ is the effective mass of the charge carriers. The quantum short-channel effects were studied as a function of the dimensionless characteristic length $l = L/\lambda$ of the transistor channel, where $L$ is its physical length.

In this conference contribution, we discuss the physics of the major quantities in our scale-invariant model which are the chemical potential, the supply function, and the scale-invariant current transmission. We specify its range of applicability: generally, for a channel length up to a few tens of nanometers a LTT is definable up to room temperature. For higher temperatures, a LTT can only be found below a channel length of 10 nm. An inspection of the $I_D - V_G$-traces yields in qualitative agreement with experiments that at low drain voltages transport becomes thermally activated below the threshold gate voltage while it does not for large drain voltages. Though our model reproduces interesting qualitative features of the experiments it fails to provide a quantitative description: the theoretical values are larger than the experimental ones by a little less than a decade. Such a finding is expected for our simple model.

Theory

Tsu-Esaki formula for the drain current

In Refs. [13,14], the transport problem in a nano-FET was reduced to a one-dimensional effective problem invoking a “single-mode abrupt transition”
approximation. Here, the electrons move along the transport direction in an effective potential given

\[
V_{\text{eff}}(y) = \begin{cases} 
0 & \text{for } y \leq 0 \\
V_0 - \frac{V_D}{L} y & \text{for } 0 \leq y \leq L \\
-V_D & \text{for } y \geq L 
\end{cases}
\] (1)

(see Figure 1b). The energy zero in Equation 1 coincides with the position of the conduction band minimum in the highly n-doped source contact. As shown in [14]

\[
V_0 = E_{k-1} + E_x^0 \quad \text{with} \quad E_x^0 = \frac{\hbar^2}{2m^*} \left( \frac{\pi}{W} \right)^2 ,
\] (2)

where \(E_{k-1}\) is the bottom of the lowest two-dimensional subband resulting in the z-confinement potential of the electron channel at zero drain voltage (see Figure 4b of Ref. [13]). The parameter \(W\) is the width of the transistor. Finally, \(V_D = eU_D\) is the drain potential at drain voltage \(U_D\) which is assumed to fall off linearly.

Experimentally, one measures in a wide transistor the current density \(J\), which is the current per width of the transistor that we express as

\[
J = \frac{I}{W} = jL_0 \quad \text{with} \quad L_0 = \frac{N_{v_{\text{ch}}} I_0}{\lambda} .
\] (3)

Here \(N_{v_{\text{ch}}}\) is the number of equivalent conduction band minima (‘valleys’) in the electron channel and \(I_0 = 2eC_F/\hbar\). In Refs. [15,16] a scale-invariant expression

\[
J = (m - v_c) \int_0^\infty d\epsilon \left[ s \left( \hat{\epsilon} - \frac{m}{m - v_c} \right) - s \left( \hat{\epsilon} - \frac{m - \epsilon_0}{m - v_c} \right) \right] \tilde{T}(\hat{\epsilon})
\] (4)

was derived. Here, \(m = \mu/\epsilon_F\) is the normalized chemical potential in the source contact, \(v_D = V_D/\epsilon_F\) is the normalized drain voltage, and \(v_G = V_G/\epsilon_F\) is the normalized gate voltage. As illustrated in Figure 1(b) the gate voltage is defined as the energy difference \(\mu - V_0 = V_G\), i.e., for \(V_G > 0\) the transistor operates in the ON-state regime of classically allowed transport and for \(V_G < 0\) in the source-drain tunneling regime. The control variable \(V_G\) is used to eliminate the unknown variable \(V_0\). For the chemical potential in the source contact one finds (see next section)

\[
m(u) = \frac{u\lambda_1}{2} \left( \frac{4}{3\sqrt{\pi}} u^{-3/2} \right) ,
\] (5)

where \(u = k_B T/\epsilon_F\) is the normalized thermal energy. Equation 4 has the form of a Tsu-Esaki formula with the normalized supply function

\[
s(\hat{\alpha}) = \frac{1}{\sqrt{4\pi}} \frac{1}{\sqrt{u_F}} \left[ (v_G - m) \frac{\hat{\alpha}}{u} \right] .
\] (6)
Here, $F_{1/2}$ is the Fermi-Dirac integral $F_j(u) = \Gamma(j + 1)^{-1} \int_0^\infty dv v^{j-1} (1 + e^{-v})$ of order -1/2 and $X_\frac{1}{2}$ is the inverse function of $F_{1/2}$. The effective current transmission $\tilde{T}$ depends on $\tilde{\epsilon} = (E - E_x)/V_0$ which is the normalized energy of the electron motion in the y-z-plane while $E_x = \epsilon x^0 N_x^0, N_x = 1, 2 \ldots$ is their energy in the x-direction. In the next sections, we will discuss the occurring quantities in detail.

**Chemical potential in source- and drain-contact**

For a wide enough transistor and a sufficient junction depth $d$ (see Figure 1) the electrons in the contacts can be treated as a three-dimensional non-interacting electron gas. Furthermore, we assume that the density $N_d$ are ionized. From charge neutrality it is then obtained that the electron density $n_0$ is independent of the temperature and given by

$$N_i \sim n_0 = 2N_V \left( \frac{2\pi m_v^2 k_B T}{\hbar^2} \right)^{3/2} F_{1/2}(m). \quad (7)$$

Here $m^*$ is the effective mass and $N_V$ is the valley-degeneracy factor in the contacts, respectively. In the zero temperature limit a Sommerfeld expansion of the Fermi-Dirac integral leads to

$$n_0 = N_V \frac{8}{3\sqrt{\pi}} \left( \frac{2\pi m_v^2 e_F}{\hbar^2} \right)^{3/2}. \quad (8)$$

Equating 7 and 8 results in

$$\frac{\mu}{k_B T} = X_{1/2} \left[ \frac{4}{3\sqrt{\pi}} \left( \frac{e_F}{k_B T} \right)^{3/2} \right] \sim 1 - 1.03a^2, \quad (9)$$

which is identical with (5) and plotted in Figure 2. As well-known, with increasing temperature the chemical potential falls off because the high-energy tail of the Fermi-distribution reaches up to ever higher energies.

**Supply function**

As shown in Ref. [14] the supply function for a wide transistor can be written as

$$S(\epsilon - \mu) = \lim_{E_x \to 0} \sum_{N_x = 1}^{\infty} \left[ \frac{1}{\epsilon^2} + 1 \right]^{-1}. \quad (10)$$

This expression can be interpreted as the partition function (loosely speaking the ‘number of occupied states”) in the grand canonic ensemble of a non-interacting homogeneous three-dimensional electron gas in the subsystem of electrons with a given lateral wave vector $(k_y, k_z)$ yielding the energy $\epsilon = \hbar^2 (k_y^2 + k_z^2)/(2m^*)$ in the y-z-direction. Formally equivalent it can be interpreted as the full partition function in the grand canonic ensemble of a one-dimensional electron gas at the chemical potential $\mu - \epsilon$. Performing the limit $E_x \to 0$ the Riemann sum in the variable $N_x \sqrt{E_x}$ can be replaced by the Fermi-Dirac integral $F_{1/2}$. It results that

$$S(\epsilon - \mu) = \frac{1}{4\pi} \sqrt{\mu} F_{1/2} \left( \frac{\mu - \epsilon}{k_B T} \right), \quad (11)$$

with the normalized transistor width $w = W/\lambda$. For the scaling of the supply function in Equation 11 we define (see Ref. [14])

$$s(\tilde{\epsilon} - \tilde{\mu}) = \frac{S(\epsilon_0 - \mu_0)}{w} = \frac{1}{4\pi} \sqrt{\mu_0} \left[ \frac{\epsilon_0 - \mu_0}{w} \right], \quad (12)$$

where $\tilde{\mu} = \mu/V_0$ and we use the identity $V_0 = c_F = m - \nu_G$. For the source contact we write

$$\tilde{\mu} = \frac{m}{m - \nu_G} \quad \text{so that in (12)} \quad \tilde{\alpha} = \tilde{\epsilon} - \frac{m}{m - \nu_G}, \quad (13)$$

leading to the first factor in the square bracket of the Tsu-Esaki equation 4. In the drain contact, the chemical potential is lower by the factor $V_D$. Replacing $\mu \to \mu - V_D$ yields

$$\tilde{\mu} - V_D = \frac{m - \nu_D}{m - \nu_G} \quad \text{so that in (12)} \quad \tilde{\alpha} = \tilde{\epsilon} - \frac{m - \nu_D}{m - \nu_G} \quad (14)$$

Below we will show that for transistor operation the low temperature limit is relevant (see Figure 2). Here, one may apply in leading order $F_{1/2}(x \to \infty) \to 2x^{1/2}/\sqrt{\pi}$ (resulting from a Sommerfeld expansion) and $F_{1/2}(x \to \infty) \to \exp(x)$. Since $V_0$
> 0 the factor $v_G - m$ is negative and we obtain from (12)

$$ s(\delta) = \frac{1}{\sqrt{4\pi}} e^{-\frac{(v_G - m) \delta^2}{2}} \left[ \left( v_G - m \right) \delta \right] \sim \left\{ \begin{array}{ll} \frac{1}{\sqrt{4\pi}} e^{-\frac{(v_G - m) \delta^2}{2}} & \text{for } \delta < 0 \\ \frac{1}{\sqrt{4\pi}} e^{-\frac{(v_G - m) \delta^2}{2}} & \text{for } \delta > 0. \end{array} \right. \tag{15}$$

From Figure 3 it is seen that for $\varepsilon$ below the chemical potential the supply function is well described by the square-root dependence in the $\delta < 0$ limit. If $\varepsilon$ lies above the chemical one obtains the $\delta > 0$ limit which is a small exponential tail due to thermal activation.

**Current transmission**

The effective current transmission in Equation 16 is given by

$$ \tilde{T} = \tilde{k}_D |\tilde{s}|^2 \tilde{k}_S^{-1}. \tag{16}$$

It is calculated from the scattering solutions of the scaled one-dimensional Schrödinger equation

$$ \left[ -\frac{\hbar^2}{\beta \frac{d^2}{dy^2}} + \hat{v}(\hat{y}) - \hat{\epsilon} \right] \hat{\psi}(\hat{y}, \hat{\epsilon}) = 0, \tag{17}$$

with $\beta = 2m^*V_0 L^2/\hbar^2 = \tilde{\ell}^2(m - v_G)$, and $\hat{y} = y/L$. The scaled effective potential $\tilde{v} = \sqrt{\varepsilon} V_0 / V_0$ is given by $\tilde{v}(0 \leq \hat{y} \leq 1) = 1 - \hat{v}_D \hat{y}$, $\tilde{v}(0 \leq \hat{y} \leq 1) = 1 - \hat{v}_D \hat{y}$, and $\tilde{v}(\hat{y} \geq 1) = -\hat{v}_D$, where $\hat{v}_D = V_0 / (m - v_G)$. As usual, the scattering functions emitted from the source contact $\hat{\psi}_S$ obey the asymptotic conditions $\hat{\psi}_S(\hat{y} \leq 0, \hat{\epsilon}) = \exp(i \hat{h}_S \hat{y}) - \hat{\psi}_S(\hat{y}) \exp(-i \hat{h}_S \hat{y})$ and $\hat{\psi}_S(\hat{y} \geq 1, \hat{\epsilon}) = \hat{\psi}_S(\hat{\epsilon}; \beta, \hat{v}_D) \exp(i \hat{h}_D \hat{y}) \tag{18}$

with $\hat{k}_D = \sqrt{\beta(\hat{\epsilon} + \hat{v}_D)}$ and $\hat{k}_S = \sqrt{\beta \hat{\epsilon}}$.

As can be seen from Figure 4, around $\hat{\epsilon} = 1$ the current transmission changes from around zero to around one. For weak barriers there is a relatively large current transmission below one leading to drain leakage currents. For strong barriers this remnant transmission vanishes and we can approximate the current transmission by an ideal one.

$$ \tilde{T}_{\text{ideal}} = \Theta(\hat{\epsilon} - 1 + v_G). \tag{19}$$

To a large extent the Fowler Nordheim oscillations in the numerical transmission average out performing the integration in Equation 4.

**Parameters in experimental nano-FETs**

**Heavily doped contacts**

In the heavily doped contacts the electrons can be approximated as a three-dimensional non-interacting Fermi gas. Then from (8) the Fermi energy above the bottom of the conduction band is given by

$$ \varepsilon_F = \frac{\hbar^2}{2m^*} \left( \frac{3\pi^2 N_0}{N_V} \right)^{2/3}. \tag{20}$$

For $n^{++}$-doped Si contacts the valley-degeneracy is $N_V = 6$ and the effective mass is taken as $m^* = (m_1^* m_2^*)^{1/3} = 0.33 m_0$. Here $m_1 = 0.19 m_0$ and $m_2 = 0.98 m_0$ are the effective masses corresponding to the principle axes of the constant energy ellipsoids. In our later numerical calculations we set $\varepsilon_F = 0.35$ eV assuming a level of source-doping as high as $N_i = n_0 = 10^{21}$ cm$^{-3}$.

**Electron channel**

In the electron channel a strong lateral subband quantization exists As well-known [17] at low temperatures
only the two constant energy ellipsoids with the heavy mass \( m_2 \) perpendicular to the (100)-interface are occupied leading to a valley degeneracy of \( g_v = 2 \). The in-plane effective mass is therefore the light mass \( m^* = m_1 \) entering the relation

\[
\lambda = \frac{\hbar}{\sqrt{2m^*\varepsilon_F}} = 0.76 \text{ nm} \sim 1 \text{ nm.} \quad (21)
\]

Here \( \varepsilon_F = 0.35 \text{ eV} \) was assumed. One then has in Equation 3 \( I_0 \approx 27 \mu\text{A} \) and with \( \lambda \sim 1 \text{ nm} \) as well as \( N_{ch} v = 2 \) one obtains \( I_0 = 5.4 \times 10^5 \mu\text{A}/\mu\text{m} \).

**Results**

**Drain characteristics**

Typical drain characteristics are plotted in Figure 5 for a low temperature (\( u = 0.01 \)) and at room temperature (\( u = 0.1 \)). It is seen that for both the temperatures a LTT can be identified. We define the LTT as the \( j - v_D \) trace which can be best fitted with a linear regression \( j = \sigma^{th} v_D \) in the given interval \( 0 \leq v_D \leq 2 \). The best fit is determined by the minimum relative mean square deviation. The gate voltage associated with the LTT is denoted with \( v_{th}^G \). It turns out that at room temperature \( v_{th}^G \) lies slightly above zero and at low

![Figure 5](image-url)
temperatures slightly below (see Figure 5c). In general, the temperature dependence of the drain current is small. The most significant temperature effect is the enhancement of the resonant Fowler-Nordheim oscillations found at negative \( v_G \) at low temperatures. From Figure 5d, it can be taken that the slope of the LTT \( \sigma_{vis} \) decreases with increasing \( l \) and increasing temperature. For “hot” transistors \( (u = 0.2) \) a LTT can only be defined up to \( l \sim 10 \).

### Threshold characteristics

The threshold characteristics at room temperature are plotted in Figure 6 for a “small” drain voltage \( (v_D = 0.1) \) and a “large” drain voltage \( (v_D = 2.0) \). For the largest considered characteristic length \( l = 60 \) it is seen that below zero gate voltage the drain current is thermally activated for both considered drain voltages. A comparison with the results for \( l = 25 \) and \( l = 10 \) yields that for the small drain voltage the \( I_D - V_G \) trace is only weakly effected by the change in the barrier strength. In contrast, at the high drain voltage the drain current below \( v_G = 0 \) grows strongly with decreasing barrier strength. The drain current does not reach the thermal activation regime any more, it falls of much smoother with increasing negative \( v_G \). As can be gathered from Figure 8 this effect is seen in experiments as well. We attribute it to the weakening of the tunneling barrier with increasing \( v_D \). To confirm this point the threshold characteristics for a still weaker barrier strength \( (l = 3) \) is considered. No thermal activation is found in this case even for the small drain voltage.

### Discussion

We discuss our numerical results on the background of experimental characteristics for a 10 nm gate length transistor [4,5] reproduced in Figure 7. As demonstrated in Sect. “Parameters in experimental nano-FETs” one obtains from Equation 21 a characteristic length of \( \lambda \sim 1 \) nm under reasonable assumptions. For the experimental 10 nm gate length, we thus obtain \( l = L/\lambda = 10 \). Furthermore, Equation 20 yields the value of \( \epsilon_F = 0.35 \) eV. The conversion of the experimental drain voltage \( V \) into the theoretical parameter \( v_D \) is given by

\[
v_D = \frac{eV}{\epsilon_F} \sim \frac{V}{0.35\text{eV}}.
\]  

(22)

The maximum experimental drain voltage of 0.75 V then sets the scale for \( v_D \) ranging from zero to \( v_D = 0.75 \text{eV}/0.35 \text{eV} \sim 2 \). For the conversion experimental gate voltage \( V_G \) to the theoretical parameter \( v_G \) we make linear ansatz as

\[
v_G = \alpha V_G + \beta \quad \text{with} \quad \alpha = (1 \text{ev})^{-1} \quad \text{and} \quad \beta = V_G^{\text{th}} - \alpha V_G^{\text{th}},
\]  

(23)

where \( V_G^{\text{th}} \) is the experimental threshold gate voltage (see Figure 8a). The constant \( \beta \) is chosen so that \( V_G^{\text{th}} \) converts into \( V_G^{\text{th}} \). In our example, it is shown from Figure 8a\( V_G^{\text{th}} = 0.15 \text{ V} \) and from Figure 8b\( V_G^{\text{th}} = -0.05 \), so that \( \beta = -0.2 \text{ eV} \). To match the experimental drain characteristic to the theoretical one we first convert the highest experimental value for \( V_G \) into the corresponding theoretical one. Inserting in (23) \( V_G = 0.75 \text{ V} \) yields \( \nu_G \sim 0.5 \). Second, we adjust the experimental and the
theoretical drain current scales so that in Figure 7 the curves for the experimental current at $V_G = 0.7$ and the theoretical curve at $v_F = 0.5$ agree. It then turns out that the other corresponding experimental and theoretical traces agree as well. This agreement carries over to the range of negative gate voltages with thermally activated transport. This can be gathered from the $I_D - V_G$ traces in Figure 8. We note that the constant of proportionality in Equation 23 given by $1 \epsilon_F$ is more than $\epsilon_F$ which one would expect from the theoretical definition $v_F = V_G/\epsilon_F$. Here, we emphasize that the experimental value of $eV_G$ corresponds to the change of the potential at the transistor gate while the parameter $v_F$ describes the position of the bottom of the lowest two-dimensional subband in the electron channel. The linear ansatz in Equation 23 and especially the constant of proportionality $1 \epsilon_F$ can thus only be justified in a self-consistent calculation of the subband levels as has been provided, e.g., by Stern[18].

The experimental and the theoretical drain characteristics in Figure 7 look structurally very similar. For a quantitative comparison we recall from Sect. “Parameters in experimental nano-FETs” the value of $j_0 = 5.4 \times 10^4 \mu A/\mu m$. Then the maximum value $j = 0.15$ in Figure 7b corresponds to a theoretical current per width of $8 \times 10^3 \mu A/\mu m$. To compare with the experimental current per width we assume that in the $y$-axis labels in Figures 7a and 8a it should read $\mu A/\mu m$ instead of $A/\mu m$. The former unit is the usual one in the literature on comparable nanotransistors (see Refs. [2-9]) and with this correction the order of magnitude of the drain current per width agrees with that of the comparable transistors. It is found that the theoretical results are larger than the experimental ones by about a factor of ten. Such a failure has to be expected given the simplicity of our model. First, for an improvement it is necessary to proceed from potentials resulting in a self-consistent calculation. Second, our representation of the transistor by an effectively one-dimensional system probably underestimates the backscattering caused by the relatively abrupt transition between contacts and electron channel. Third, the drain current in a real transistor is reduced by impurity interaction, in particular, by inelastic scattering. As a final remark we note that in transistors with a gate length in the micrometer scale short-channel effects may occur which are structurally similar to the ones discussed in this article (see Sect. 8.4 of [10]). Therefore, a quantitatively more reliable quantum calculation would be desirable allowing to distinguish between the short-channel effects on micrometer scale and quantum short-channel effects.

Figure 7 Drain characteristics in experiment and theory (a) Experimental drain characteristics for a nano-transistor with $L = 10$ nm [4,5]. Our assumption for the LTTIs marked with a green dashed line leading to a threshold gate voltage of $V_{th} = 0.15V$. (b) Theoretical drain characteristics for $i = 10$ and $u = 0.1$ (see Fig. 5a) with the green dashed threshold characteristic at $V_{th} = 0.05$.

Figure 8 Threshold characteristics in experiment and theory (a) Experimental threshold characteristics for the nano-transistor in Fig. 7a. (b) Theoretical threshold characteristics for $i = 10$ and $u = 0.1$ with the blue dashed lines corresponding to thermal activation.
Summary
After a detailed discussion of the physical quantities in our scale-invariant model we show that a LTT is present not only in the low temperature limit but also at room temperatures. In qualitative agreement with the experiments the ID - VGS-traces exhibit below the threshold voltage thermally activated transport at small drain voltages. At large drain voltages the gate-voltage dependence of the traces is much weaker. It is found that the theoretical drain current is larger than the experimental one by a little less than a decade. Such a finding is expected for our simple model.

Abbreviation
LTT: linear threshold trace.

Authors’ contributions
UW worked out the theoretical model, carried out numerical calculations and drafted the manuscript. MK carried out numerical calculations and drafted the manuscript. All authors read and approved the final manuscript.

Competing interests
The authors declare that they have no competing interests.

Received: 5 November 2010 Accepted: 28 April 2011
Published: 28 April 2011

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