Research Article
All Pass Network Based MSO Using OTRA

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Received 24 August 2014; Accepted 1 December 2014

Academic Editor: Liwen Sang

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This paper presents multiphase sinusoidal oscillators (MSOs) using operational transresistance amplifier (OTRA) based all pass networks. Both even and odd phase oscillations of equal amplitudes which are equally spaced in phase can be produced using single all pass section per phase. The proposed MSOs provide voltage output and can readily be used for driving voltage input circuits without increasing component count. The effect of nonideality of OTRA on the circuit performance is also analysed. The functionality of the proposed circuit is verified through PSpice simulations.

1. Introduction

Multiphase sinusoidal oscillators (MSO) provide multiple outputs of the same frequency, equally spaced in phase, and find extensive application in the field of communications, instrumentation, and power electronics. In communications MSO circuits are used in single-sideband generators, phase modulators, and quadrature mixers [1]. Selective voltmeters and vector generator are common applications of MSOs in the field of instrumentation [2]. In power electronics three-phase MSOs are frequently utilized in PWM converters [3] and inverters [4].

A large number of MSO realizations using various analog building blocks (ABB) [2, 5–25] are available in literature. These MSOs are based on a basic design philosophy of forming closed loop using $n$ ($n \geq 3$) cascaded phase shifting networks thereby producing $n$ equally spaced phases. For phase shifting either first-order low pass networks (LPNs) [5–7, 9–21, 24, 25] or first-order all pass networks (APNs) [2, 8, 22, 23] are used. These reported structures provide either voltage [5–16] or current [2, 17–24] outputs.

The MSOs of [5–8] are realized using operational amplifiers (op-amps). However due to constant gain-bandwidth product and lower slew rate of the op-amps, their high frequency operations are limited. Additionally the active $R$ implementations of [5, 6] lack tunability as these structures make use of the op-amp parasitic capacitance. The current feedback operational amplifier (CFOA) based MSO structure [9] is capable of producing high frequencies but requires an accessible compensation terminal of a CFOA. The MSOs of [10, 11] are OTA based electronically tunable structures; however, they provide voltage output at high impedance making a buffer necessary to drive the voltage input circuits. In addition, for both the structures the output voltage is temperature sensitive too. The MSO configurations presented in [12–15] are CC based designs and provide voltage output at high impedance. Further the MSO of [14], being active $R$ structure, lacks tunability. Three topologies of OTRA based MSOs are proposed in [16] and are designed using LPNs. The structures proposed in [2, 17–25] provide current outputs which need to be converted back to voltage for circuits requiring voltage inputs, which would considerably increase the component count. A detailed comparison of these structures is given in Table 1 which suggests that OTRA based MSO is the most suitable choice for voltage output.

In this paper authors aim at presenting OTRA based MSO, designed using APNs. The proposed circuit utilizes $n$ ($n \geq 3$) APNs to produce $n$ phase oscillations of equal...
Table 1: Detailed comparison of available MSOs.

| Reference | ABB     | Phase shift network | Output | Output impedance | Tunability |
|-----------|---------|---------------------|--------|------------------|------------|
| [2]       | CCCDTA  | APN                 | Current| High             | Yes        |
| [5]       | Op-amp  | LPN                 | Voltage| Low              | No         |
| [6]       | Op-amp  | LPN                 | Voltage| Low              | No         |
| [7]       | Op-amp  | LPN                 | Voltage| Low              | Yes        |
| [8]       | Op-amp  | APN                 | Voltage| Yes              | Yes        |
| [9]       | CFOA    | LPN                 | Voltage| No               | Yes        |
| [10]      | OTA     | LPN                 | Voltage| High             | Yes        |
| [11]      | OTA     | LPN                 | Voltage| High             | Yes        |
| [12]      | CCII    | LPN                 | Voltage| High             | Yes        |
| [13]      | CCII    | LPN                 | Voltage| High             | Yes        |
| [14]      | CCII    | LPN                 | Voltage| High             | No         |
| [15]      | CCII    | LPN                 | Voltage| High             | Yes        |
| [16]      | OTRA    | LPN                 | Voltage| Low              | Yes        |
| [17]      | Current follower | LPN                 | Current| High             | Yes        |
| [18]      | CCII    | LPN                 | Current| High             | Yes        |
| [19]      | CCII    | LPN                 | Current| High             | Yes        |
| [20]      | CCII    | LPN                 | Current| High             | Yes        |
| [21]      | CDTA    | LPN                 | Current| High             | Yes        |
| [22]      | CDTA    | APN                 | Current| High             | Yes        |
| [23]      | CDTA    | APN                 | Current| High             | Yes        |
| [24]      | CC      | LPN                 | Current| High             | Yes        |
| [25]      | CFTA    | LPN                 | Current| High             | Yes        |
| Proposed work | OTRA   | APN                 | Voltage| Low              | Yes        |

amplitudes with a phase difference of \((360/n)^\circ\). The APN can produce a phase shift up to 180° as against a maximum of 90° produced by LPN. As a result the APN based scheme can be used to implement an even phase system for \(n = 4\) also whereas \(n = 6\) is the minimum for the systems designed using LPNs [8].

2. Circuit Description

In this section the generalized APN based MSO scheme [8] is described first which is followed by the design adaption using OTRA.

2.1. The APN Based MSO Design Scheme. The APN based MSO structure consists of \(n\) cascaded first-order APN blocks. The output of the \(n\)th stage is fed back to the input of the first stage thus forming a closed loop as shown in Figure 1(a). The output of \(n\)th stage is fed back directly for odd phase system whereas for even phase system it is inverted, for sustained oscillations. In case of even phased system the structure can be modified for obtaining \(n\) even phased oscillations by replacing \(n/2\) APN with \(n/2\) inverters as shown in Figure 1(b).

The transfer function \(G(s)\) of each APN block is given by

\[
G(s) = K \frac{(1 - st)}{(1 + st)}
\]

where \(K\) represents the gain and time constant \(t\) determines the corner frequency of the APN. The phase \((\phi)\) of each APN block is computed to be

\[
\phi = -2\tan^{-1}(\omega t).
\]

Thus an APN can introduce signal phase lag from 0 to 180° as frequency \(\omega\) varies from zero to infinity. From Figure 1 the open loop gain \(L(s)\) can be expressed as

\[
L(s) = K^n \left(\frac{1 - st}{1 + st}\right)^n.
\]

The Barkhausen criterion [26] for sustained oscillations at a frequency \(\omega_0\) can be expressed by

\[
|L(j\omega_0)| = K^n \left(\frac{1 - j\omega_0 t}{1 + j\omega_0 t}\right)^n = 1,
\]

\[
\angle |L(j\omega_0)| = n\phi = -2\pi.
\]

Since \((1 - j\omega_0 t)/(1 + j\omega_0 t))^n = 1, (4) results in condition of oscillation (CO) as

\[
\text{CO : } K = 1.
\]

Substituting from (2) in (5) total phase shift of the loop can be computed as

\[
n(\phi) = \left(-2\tan^{-1}(\omega t\right)) = -2\pi.
\]
Equation (7) will converge only for values of \( n \) such that \( n \geq 3 \).
This results in frequency of oscillation (FO) as
\[
\text{FO} : f_0 = \frac{1}{2\pi\tau} \tan\left(\frac{\pi}{n}\right).
\] (8)
The spacing between different phases is given by
\[
\phi = \frac{2\pi}{n}.
\] (9)
Thus the circuit gives rise to equally spaced oscillations
having a phase difference of \((360/n)\)°.

2.2. The OTRA Based MSO Implementation. The OTRA is
a high gain, current input voltage output ABB. The circuit
symbol of OTRA is shown in Figure 2 and the port
characteristics in matrix form are given by (10), where \( R_m \) is
transresistance gain of OTRA. For ideal operations the \( R_m \)
of OTRA approaches infinity and forces the input currents
to be equal. Thus OTRA must be used in a negative feedback
configuration [25, 27]:
\[
\begin{bmatrix}
V_p \\
V_n \\
V_O
\end{bmatrix} =
\begin{bmatrix}
0 & 0 & 0 \\
0 & 0 & 0 \\
R_m & -R_m & 0
\end{bmatrix}
\begin{bmatrix}
I_p \\
I_n \\
I_O
\end{bmatrix}.
\] (10)
The MSO scheme outlined in Section 2.1 can be implemented
using OTRA based first-order all pass sections. The OTRA
based APN proposed in [27] and shown in Figure 3 is used
for MSO implementation.

The transfer function of the APN can be written as
\[
G(s) = \frac{V_O(s)}{V_i(s)} = K \frac{(1-st)}{(1+st)},
\] (11)
and the phase relation is expressed as
\[
\phi = -2\tan^{-1}(\omega CR).
\] (13)
The OTRA based odd and even phased MSO structures are shown in Figures 4(a) and 4(b), respectively. The loop gain can be written as

\[ L(s) = \left( \frac{R_1}{2R} \right)^n \left( \frac{1-s/RC}{1+s/RC} \right)^n. \]  

(14)

CO and FO can be expressed as

\[ \text{CO} : R_1 = 2R, \]

\[ \text{FO} : f_0 = \frac{1}{2\pi RC} \tan \left( \frac{\pi}{n} \right). \]  

(15)

3. Nonideality Analysis

Ideally the transresistance gain \( R_m \) is assumed to approach infinity. However, practically \( R_m \) is a frequency dependent finite value. Considering a single pole model for the transresistance gain, \( R_m \) can be expressed as

\[ R_m(s) = \left( \frac{R_0}{1 + s/\omega_0} \right). \]  

(16)

For high frequency applications the transresistance gain \( R_m(s) \) reduces to

\[ R_m(s) = \left( \frac{1}{sC_p} \right) \]  

where \( C_p = \left( \frac{1}{R_0\omega_0} \right). \)  

(17)

\( C_p \) is known as parasitic capacitance of OTRA. Taking the effect of \( C_p \) into account (11) modifies to

\[ G(s) = \frac{R_1}{2R} \left( \frac{1-sCR}{1+sCR} \right) \left( 1 + sC_p/R_1 \right). \]  

(18)

Thus the \( C_p \) of OTRA results in introduction of another pole having pole frequency \( \omega_p = 1/R_1C_p \). However with the value of \( C_p \) being small (typically 5 pF) the parasitic pole frequency would be far off from the operating frequency of the APN and would not affect the MSO operation.

4. Simulation Results

The proposed circuits are simulated using PSPICE to validate the theoretical predictions. The CMOS realization of OTRA presented in [28] and reproduced in Figure 5 is used for
Simulation. The output of the MSO of Figure 4(a) for \( n = 3 \), with component values \( R = 2.5 \, \text{K}\Omega, \, R_1 = 5 \, \text{K}\Omega, \) and \( C = 0.1 \, \text{nF} \), is depicted in Figure 6. The steady state output is shown in Figure 6(a) while the frequency spectrum is depicted in Figure 6(b). The simulated frequency of oscillations is observed to be 1 MHz against the calculated value of 1.1 MHz.

Simulation results for \( n = 4 \), with component values \( R = 2.5 \, \text{K}\Omega, \, R_1 = 5 \, \text{K}\Omega, \) and \( C = 1 \, \text{nF} \), are depicted in Figure 7. The simulated frequency is found to be 61.69 KHz while the theoretical calculation yields an FO of 63.69 KHz.

It may be observed from (8) that the FO can be varied either through \( R \) or by changing \( C \). Variation of FO with respect to \( R \) while keeping \( C = 1 \, \text{nF} \) has been depicted in
Figure 8(a) whereas tuning with C is shown in Figure 8(b) with $R = 0.5 \text{ K\Omega}$. It shows that the simulated values closely follow the theoretically calculated values.

5. Conclusion

In this work OTRA based MSO circuits, designed using first-order all pass networks, are presented. The proposed structures produce "n" phase oscillations of equal amplitudes which are equally spaced in phase. These circuits provide voltage output at low impedance and thus can readily be used to drive voltage input circuits without increasing component count. The proposed circuits are very accurate in providing the desired phase shift. The workability of the circuit has been demonstrated through PSpice simulations.

Conflict of Interests

The authors declare that there is no conflict of interests regarding the publication of this paper.

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