Reconfigurable fiber-to-waveguide coupling module enabled by phase-change material incorporated switchable directional couplers

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In silicon photonics, grating-assisted fiber-to-waveguide couplers provide out-of-plane coupling to facilitate wafer-level testing; however, their limited bandwidth and efficiency restrict their use in broadband applications. Alternatively, end-fire couplers overcome these constraints but require a dicing process prior usage, which makes them unsuitable for wafer-level testing. To address this trade-off, a reconfigurable fiber-to-waveguide coupling module is proposed and designed to allow for both grating-assisted and end-fire coupling in the same photonic circuit. The proposed module deploys a switchable directional coupler incorporating a thin layer of phase-change material, whose state is initially amorphous to render the coupler activated and hence facilitate grating-assisted coupling for wafer-level testing. The state can be altered into crystalline through a low-temperature annealing process to deactivate the directional coupler, thus facilitating broadband chip-level coupling through end-fire couplers. All the components encompassing conjoined switchable directional couplers as well as the grating and end-fire couplers were individually designed through rigorous simulations. They were subsequently assembled to establish the proposed reconfigurable coupling module, which was simulated and analyzed to validate the selective coupling operation. The proposed module gives rise to a low excess loss below 1.2 dB and a high extinction ratio over 13 dB throughout the C-band, when operating either under grating-assisted or end-fire input. The proposed reconfigurable coupling module is anticipated to be a practical solution for flexibly expediting the inspection of integrated photonic circuits on a wafer scale.

Photonic integrated circuits (PICs) for quantum computing, beam steering, optical communications, and many other applications have been extensively researched and developed over the recent decade. From the perspective of practical inspection and operation of the photonic circuits, a grating-assisted coupler (GAC) and an end-fire coupler based on a spot size converter (SSC) are mainly regarded as the popularly recommended schemes for fiber-to-waveguide light coupling. The GACs are primarily geared for out-of-plane coupling, whereas the SSCs are suitable for in-plane end-fire coupling. Wafer-level testing, which is only possible with the help of the GACs, is essential to ensuring the best PIC yield during fabrication. However, for the GACs, the limited bandwidth, lower coupling efficiency, and higher sensitivity to polarization render them less desirable. On the other hand, the SSCs give rise to an outstanding coupling efficiency, broader bandwidth, and better polarization tolerance, but inevitably entail an additional process of dicing/polishing the chips; thus making them unsuitable for wafer-level testing. With the burgeoning demand on highly integrated photonic chips and the rapidly increasing size of photonic wafers, wafer-level testing has become understandably indispensable for expediting the chip development process. In this context, the choice between the GAC and SSC incites a trade-off between the fabrication quality and PIC usability. Therefore, a method that incorporates both couplers into the same circuit would be highly beneficial for achieving better fabrication quality as well as wider applicability.

Over the past few decades, several studies on end-fire and grating couplers have been conducted separately; however, to the best of our knowledge, there was no reported work discussing an integrated coupler that can cater to both in-plane coupling resorting to an end-fire coupler and out-of-plane coupling utilizing a GAC in the same
circuit. To integrate the GAC and SSC into the same circuit simultaneously, a mechanism that switches the inputs and outputs is necessary. It is unfeasible to use the commonly available photonic switches relying on thermo-optic and electro-optic effects, since their operation entails a continuous supply of power for their operation. With the advent of optical phase-change materials (PCMs), nonvolatile switching can be executed with no continuous power supply. The adoption of PCMs makes it possible to simultaneously integrate GAC and SSC into a single circuit, thereby rendering both wafer-level and chip-level coupling of light. A study reported by Y. Zhang group demonstrated a transient tap coupler capable of tapping a small portion of light from a waveguide, which could be disabled by adjusting the state of the PCM, for probable applications in wafer-level testing. However, it is limited to tapping only a small portion of light, not facilitating full-scale wafer level testing of circuits. Full-scale characterization, which can deal with an input/output power rather than a small portion thereof, is integral to taking full advantage of wafer-level testing. In this context, a reconfigurable coupling scheme that provides for both full-scale wafer-level and chip-scale testing is categorically sought for to execute speedy and flexible inspection of PICs under production.

In this paper, we propose and design a reconfigurable fiber-to-waveguide coupling (RFWC) module based on a PCM overlaid switchable directional coupler (SDC), which serves both in-plane coupling mediated by an SSC and out-of-plane coupling relying on a GAC in the same photonic circuit. A pair of PCM-incorporated SDCs is deployed to route the light from these two disparate couplers into and out of the chip. The state of the PCM can be switched between amorphous and crystalline states and vice versa, by applying heat or high-energy light pulses to the chip at a specific temperature under a specific environment. This invokes nonvolatile switching when used with directional couplers. Because the state of the PCM remains in its current state unless its state is altered by the application of heat or high-energy light pulses, the directional coupler requires no continuous heat/power supply for operation. The proposed coupling module guides the input/output through the GACs, facilitating full-scale wafer-level testing until the SDC is switched into input/output coupling via the SSCs, thereby achieving chip-level interrogation. The proposed RFWC module is categorically anticipated to usher in various types of advanced flexible coupling schemes for inspecting PICs by accommodating different types of interrogation schemes.

Proposed reconfigurable fiber-to-waveguide coupling module and its design

An illustration of the proposed RFWC module is shown in Fig. 1. It consists of a pair of PCM-overlaid switchable directional couplers (SDC1 and SDC2) connected to a pair of GACs (GAC1 and GAC2) and SSCs (SSC1 and SSC2) at both input and output facets. The two SDCs are interfaced at either end of the photonic circuit to facilitate selective switching between the GAC and SSC inputs/outputs. For the proposed RFWC module, a straight waveguide was considered as the photonic circuit under investigation, which may be replaced by other photonic circuits in future applications. Both the SDCs exploit a thin layer of PCM, whose state can be altered from amorphous to crystalline and vice versa by virtue of low-temperature annealing in the vicinity of 280 °C. The SDC is activated when the PCM is amorphous and deactivated when it is crystalline. This SDC switching permits the input and output from either the GAC or SSC to be activated and deactivated. In this work, Ge2Sb2Se4Te1 (GSST)
Design of the switchable directional coupler. Directional couplers play an integral role in the proposed RFWC module, considering they allow light from different input couplers to enter the circuit and subsequently exit through their respective output couplers. Toward that end, a three-waveguide SDC was concocted based on a silicon nitride (SiN, $n = \sim 2.0$) waveguide platform capitalizing on a silicon dioxide (SiO2, $n = 1.44$) cladding. Figure 2a shows the proposed SDC structure which comprises two identical SiN strip waveguides (WG1 and WG3) mediated by a GSST-loaded WG2 in the coupling region, forming a three-waveguide directional coupler. The waveguide WG2 has a length of $L_c$, which stands for the coupling length required to achieve complete power transfer from WG1 to WG3 through the GSST-loaded WG2 under the aGSST state. The outputs from WG1 and WG3 relate to output ports OP1 and OP2, respectively. The cross section of the directional coupler is depicted in Fig. 2b. The widths of waveguides WG1, WG2, and WG3 were $w_1$, $w_2$, and $w_3$, respectively. $w_{gsst}$ is the width of the GSST loaded waveguide WG2 in the coupling region of a length $L_c$. (a) Proposed SDC incorporating two identical waveguides WG1 and WG3, which are mediated by a GSST loaded waveguide WG2 in the coupling region of a length $L_c$. (b) Cross-sectional view of the SDC with the structural parameters indicated.

The optimal parameters of the widths and gap of the SDC were determined by meticulously analyzing the effective refractive indices of its eigenmodes. To ensure single-mode operation for a 500-nm thick SiN waveguide\(^1\), the $w_1$ and $w_3$ values were identically set at 750 nm. Figure 3a portrays the TE field distribution of the supermodes pertaining to the SDC at $\lambda = 1550$ nm for both the aGSST and cGSST states. It was observed that the aGSST-loaded SDC supported two symmetric modes and one asymmetric mode corresponding to effective refractive indices of $n_{aGSST1}$, $n_{aGSST2}$, and $n_{aGSST3}$, respectively, as shown in Fig. 3a-(i)–(iii). The cGSST-overlaid SDC could guide a symmetric and asymmetric supermode with effective indices of $n_{cGSST1}$ and $n_{cGSST2}$, respectively, as shown in Fig. 3a-(iv) and (v). The effective indices of the supermodes for different $w_2$ values are shown in Fig. 3b. The maximum power transfer from WG1 to WG3 transpires when the effective indices of the supermodes satisfy the phase-matching condition, which is given by $n_{aGSST1} + n_{aGSST2} = 2n_{aGSST3}$ and $n_{cGSST1} + n_{cGSST2} = 2n_{cGSST3}\(^2,23\)$, and satisfied when $w_2 = 497$ nm for the aGSST-loaded SDC. For the case of cGSST-loaded SDC, however, the effective indices were invariant to $w_2$, leading to no phase-matching condition. The coupling characteristics with respect to the gap width ($w_{gap}$) was then scrutinized. The coupling lengths leading to complete power transfer between WG1 and WG3 for SDCs loaded with aGSST and cGSST are denoted by $L_{aGSST}$ and $L_{cGSST}$, respectively. The coupling lengths, which are estimated from $L_{aGSST} = \pi/2(n_{aGSST1} - n_{aGSST3})$ and $L_{cGSST} = \pi/2(n_{cGSST1} - n_{cGSST3})\(^16\)$, were calculated at $\lambda = 1550$ nm; the values of $L_{aGSST}$ and the ratio of $L_{cGSST}/L_{aGSST}$ are plotted in Fig. 3c. Both $L_{aGSST}$ and $L_{cGSST}/L_{aGSST}$ values increased with the gap. A higher value of $L_{cGSST}/L_{aGSST}$ translates into a higher extinction
ratio and is achieved at a wider gap; however, a larger gap increases the L_{GSST}, implying a larger device footprint. From the perspective of the footprint and performance trade-off, a gap of 600 nm was selected for the proposed coupler. Consequently, the coupling length was determined to be L_c = L_{GSST} = 49.5 µm, leading to an optimal power transfer between WG1 and WG3 under aGSST state. The achieved coupling length is short enough from the perspective of typical photonic circuits; the ratio of L_c_{GSST}/L_{GSST} was found to be approximately 91, which is appropriate to facilitate the optical switching leading to a high extinction ratio.

Next, using the designed structural parameters, the SDC was modeled and simulated with the aid of a 3D finite-difference time-domain (FDTD) simulation tool. Figures 4a,b sketch the light propagation through the SDC when it is enabled and disabled, respectively. The SDC is enabled when it is operated under aGSST (denoted by SDC_{aGSST}). It lets incident light from the input port (IP) be cross coupled through the SDC and exit through output port OP2. Meanwhile, for the SDC operating under cGSST (denoted by SDC_{cGSST}), the input light IP undergoes no cross-coupling due to lack of phase matching, hence exiting port OP1. The performance of a directional coupler is usually characterized in terms of insertion loss (IL) and extinction ratio (ER). The IL signifies the amount of loss incurred between the input port and the targeted output port, while the ER is the ratio of the output powers transmitted through the two output ports. The IL and ER can be estimated in accordance with

\[ IL_{aGSST(cGSST)} = 10 \log_{10} \frac{T_{OP2(OP1)}}{T_{OP1(OP2)}} \]

and

\[ ER_{aGSST(cGSST)} = 10 \log_{10} \frac{T_{OP2(OP1)}}{T_{OP1(OP2)}} \], respectively. The transmission spectral response of the SDC_{aGSST} is shown in Fig. 4c. The SDC_{aGSST} was seen to attain an IL_{aGSST} of as low as 0.04 dB and an ER_{aGSST} of as high as 28.8 dB at λ = 1550 nm. For the operation under the C-band spectrum (λ = 1530 nm–1565 nm), an IL_{aGSST} of below 0.3 dB and an ER_{aGSST} of above 17 dB were obtained. Also, the transmission spectral response of the SDC_{cGSST} is shown in Fig. 4d. The IL_{cGSST} and ER_{cGSST} were 0.5 and 27 dB at λ = 1550 nm, respectively. The IL_{cGSST} and ER_{cGSST} were witnessed to stably remain below 0.6 dB and beyond 25 dB throughout the C-band, respectively. It is noteworthy that the device performance in terms of the IL and the ER are squarely susceptible to refractive index variations in GSST in action. Hence, the switching of states of GSST should be meticulously executed under specific conditions, thereby efficiently preventing undesired refractive index variations incurred by incomplete state-switching.

Design of the grating-assisted coupler and the end-fire coupler. As a proof of concept, a GAC and an SSC for end-fire coupling were attempted for the simulations of the proposed RFWC module. As shown in Fig. 5a, the GAC is comprised of a grating region and taper to convey incident light toward a 750-nm wide input waveguide. In view of the fundamental TE mode size of a standard single-mode fiber (SMF-28), the grating region was set 15-µm wide. A linear adiabatic taper was designed to be 300-µm long. The taper length may be shortened using a series of concatenated tapers. Because the grating plays a pivotal role in the coupling...
of light from the fiber to the waveguide, its parameters were fastidiously derived. A cross-sectional view of the GAC is shown in Fig. 5b. The gratings were created by inscribing an array of grooves over a distance \( L_1 \) according to a depth of \( d_g \) in a strip waveguide with a thickness of \( h_{SiN} = 500 \) nm. The fill factor of the grating is defined as the ratio of the uninscribed waveguide length \( L_2 \) to the total grating pitch \( \Lambda \). For an incident angle \( \theta \) of 13°, the grating pitch is estimated using the Bragg’s condition to be approximately 1.15 µm for the fully inscribed condition with groove depth \( d_g \) of 500 nm at \( \lambda_c = 1550 \) nm. FDTD simulations were executed to probe the dependence of the total coupling efficiency on the structural parameters. Figures 5c,d show the calculated coupling efficiencies with respect to the pitch and groove depth. The GAC is observed to achieve the maximum efficiency when \( d_g, \Lambda, \) and \( f \) are 500 nm, 1.14 µm, and 0.5, respectively. Noting the thicknesses of the upper cladding \( h_{UC} \) and bottom cladding \( h_{BC} \) affect the coupling efficiency11, simulations were carried out by varying \( h_{UC} \) and \( h_{BC} \) as shown in Fig. 5e,f, respectively. The maximum efficiency was obtained for \( h_{UC} = 3.7 \) µm and \( h_{BC} = 2.6 \) µm. The GAC was simulated to discover the throughput from the fiber to the waveguide using the specified parameters, as presented in Fig. 5g. The observed throughput was −3.2 dB at \( \lambda = 1550 \) nm and remained beyond −3.9 dB throughout the C-band.

For end-fire coupling, an SSC was suggested to deliver light from the fiber to the waveguide. The SSC incorporated a multi-stage taper which could expand the guided mode profile of the waveguide to match that of the fiber. Here, the SSC was designed in line with an ultra-high numerical aperture fiber exhibiting a tiny mode field diameter of \( \sim 4 \) µm, whose splicing loss relating to a standard single mode (SMF28) fiber was known to be as small as 0.06 dB24. As shown in Fig. 6a, the SSC is based on a 500-nm thick SiN multi-stage taper linked to a 750-nm wide output waveguide. The thicknesses of the upper and the bottom cladding were chosen to be 2.6 µm and 3.7 µm, as per the GAC design. As for the taper, the mode overlap between the fundamental TE mode of the fiber and that of the tip of the taper was estimated. The TE mode field profiles of the fiber and the 200-nm wide tip waveguide are depicted in Fig. 6b-(i) and (ii), respectively. The mode overlap integral25 was scanned in terms of the taper tip width, as plotted in Fig. 6c, signifying that a tip width of 200 nm lead to an approximate maximum overlap of 0.94. Hence, a tip width of 200 nm was selected for the SSC. Rather than exploiting a single adiabatic taper, typically measuring several hundred micrometers in length, a shortened multi-stage taper drawing upon three serially concatenated taper segments was adopted13. As delineated in Fig. 6d, the proposed taper has a total length of 65 µm. Figure 6e shows the spectral response of the SSC coupling efficiency, signaling the SSC could deliver a coupling efficiency better than −0.5 dB throughout the C-band. In order to enhance the tolerance in terms of dicing and polishing processes during practical device manufacturing, the SSC can take advantage of a short length of elongated tip waveguide, whose width is in the vicinity of 200 nm, as demonstrated in our previous work13.
Analysis of the RFWC module
When the designed basic elements were prepared, the operation of the complete RFWC module was verified through rigorous simulations. First, a conjoined switchable directional coupler (C-SDC) was devised to facilitate switching at both the input and output sides. Second, the complex amplitude reflection and transmission coefficients (S-parameters) in relation to the constituting elements, inclusive of the GAC, SSC, and C-SDCs, were extracted to develop their models, which were assembled to establish the entire RFWC module.

Design and performance analysis of the conjoined switchable directional coupler. As shown in Fig. 7a, the proposed C-SDC comprises two SDCs (SDC1 and SDC2) interfaced to either end of the photonic circuit enabling reconfigurable input/output coupling to and from the circuit. In this work, a straight waveguide acts as the photonic circuit. When the state of GSST belonging to both SDCs is aGSST, the C-SDC is enabled, thus allowing the input light from the IP2 port to couple via SDC1 into the circuit, which subsequently couples via SDC2 and exits through the OP2 port, as shown in Fig. 7b. Meanwhile, the C-SDC is disabled when both SDCs are in the cGSST state, which makes the input light from the IP1 port pass through the photonic circuit without experiencing cross coupling at either SDC1 or SDC2, and the light finally comes out of the OP1 port, as shown in Fig. 7c. Full simulations were conducted using a 3D FDTD scheme to examine the field propagation profiles and efficiencies. The field propagation profiles for the cases where the C-SDC was enabled and disabled are shown in Fig. 7d,e, respectively. The profiles corroborate the working mechanism of the C-SDC, as shown in Fig. 7b,c. The spectral response of the output power transmitted into ports OP1 and OP2 when the C-SDC is enabled is shown in Fig. 7f. The total IL of the C-SDC when passing through both SDCs is less than 0.6 dB throughout the C-band. The ER for the C-SDC-enabled case is as high as 26 dB at λ = 1550 nm and above 17 dB throughout the C-band operation. Similarly, when the C-SDS is disabled, the IL is less than 1.2 dB and the ER is above 24 dB, as shown in Fig. 7g. For further assessment of the proposed RFWC module, the performance parameters were exported in the form of S-parameters.

Performance of the RFWC module. To validate the operation of the proposed RFWC module tapping into the grating/SSC couplers on top of the C-SDC, simulations were fulfilled using the Numerical Interconnect tool, which has been widely used for full photonic circuit simulations. The S-parameter models of the constituting elements were derived and then interconnected to establish the proposed RFWC module, as delineated in Fig. 8a. Here an optical network analyzer is equipped with a light source (S1) to feed light into the circuit as...
Figure 7. (a) Schematic of the proposed C-SDC where both SDC1 and SDC2 are linked to each other via a photonic circuit. The operation of the C-SDC (b) under aGSST state where input light from IP2 couples to the photonic circuit through SDC1 and couples back to OP2 through SDC2 and (c) under cGSST state where light from IP1 undergoes no cross coupling induced by SDC1 and SDC2, eventually exiting OP1. Optical coupling characteristics under (d) aGSST and (e) cGSST states. The transmission spectra of $T_{OP1}$ and $T_{OP2}$ as observed at the OP1 and OP2 ports for the cases of C-SDC overlaid with (f) aGSST and (g) cGSST, respectively.

Figure 8. (a) Schematic layout portraying a complete RFWC module under simulation in which all functional blocks including the proposed GACs, SSCs and C-SDC are deployed. An optical network analyzer having a light source (S1) and two power monitor terminals (M1 and M2) is used to monitor the spectral transmission of the circuit. (b) Calculated IL spectra using the transmitted power recorded at the monitors M1 and M2 for both the cases of C-SDC enabled and disabled. (c) Calculated ERs of the circuit as observed at M1 and M2 of the analyzer, for the C-SDC-enabled case with the input/output through the GACs, and the case of disabled C-SDC with the input/output via the SSCs.
well as two monitors (M1 and M2) for analyzing the output from the circuit. The toggle key is a representative unit that just conveys light from S1 to the input GAC1 or SSC1 during their simulations, not belonging to the proposed RFWC module. When the C-SDC is enabled, the toggle key allows the input light to impinge upon the C-SDC via GAC1. The input light then propagates through the photonic circuit in the C-SDC and eventually exits via GAC2. Meanwhile, for the case when the C-SDC is disabled, the input light is routed via SSC1 into the C-SDC and ultimately exits through SSC2. For both the cases when the C-SDC is enabled and disabled, the output light through both SSC2 and GAC2 are recorded to estimate the ER. It is noteworthy that the S-parameter models corresponding to aGSST and cGSST were applied to the cases where the C-SDC was enabled and disabled, respectively.

The operation of the RFWC module was examined in terms of IL and ER as well. The IL, alluding to the amount of power loss incurred by the coupling module, accounts for the loss caused by both the input and output couplers (either the GACs or SSCs) along with the IL of the C-SDC. The ER is relevant to the power that escaped to the output SSC2 when the C-SDC was enabled and to GAC2 when the C-SDC was disabled. The total IL and ER of the RFWC module were calculated according to $IL_{CSDC\text{-}enabled} = 10 \log_{10} \frac{T_{GAC2}(SSC2)}{T_{SSC2}(GAC2)}$ and $ER_{CSDC\text{-}enabled} = 10 \log_{10} \frac{T_{GAC2}(SSC2)}{T_{SSC2}(GAC2)}$, respectively. Here, $T_{GAC2}$ and $T_{SSC2}$ represent the output powers transmitted to the network analyzer through GAC2 and SSC2, respectively. The IL values resulting from the analysis of the RFWC module are shown in Fig. 8b. For the circuit in which the C-SDC is disabled and the inputs and outputs are through SSCs, the total IL_{CSDC\text{-}disabled} is less than 2.1 dB throughout the C-band. Similarly, when the C-SDC is enabled with the light input/output going through the GACs, the total IL_{CSDC\text{-}enabled} is below 7.8 dB throughout the wavelength regime of concern. It is mentioned the loss can be suppressed with the introduction of an apodised or multilayered GAC. The practical implementation of the GACs based on a C-SDC might be undesirably affected by Fabry–Perot resonances between the grating region and the waveguide end in the coupling region. However, considering the proposed module can accommodate flexible schemes of GACs, it is expected that the resonance issue can be mitigated by taking advantage of focusing GACs, which are deemed to feature reduced back-reflection.

The proposed coupling module was observed to exhibit a decent level of ER for both the C-SDC-enabled and disabled cases. Throughout the C-band, the ER_{CSDC\text{-}enabled} and ER_{CSDC\text{-}disabled} were consistently above 13 and 27 dB, respectively, as plotted in Fig. 8c. Although the C-SDC is chiefly responsible for the ER, the discrepancy in the ER between the RFWC module and C-SDC alone is attributed to the disparate coupling efficiencies of the GAC and SSC for the RFWC module simulation. It should be remarked that for the ER of the C-SDC alone, there are no input and output couplers, leading to no coupling loss; hence, the ER is exclusively affected by the SDCs. However, this case does not hold true for the RFWC module that involves a GAC and SSC inducing a certain amount of coupling loss. This discrepancy of ER between the C-SDC and RFWC module may be abated by embodying GACs that could provide a coupling efficiency tantamount to that of the SSC. Nonetheless, the ER and IL of the proposed RFWC are reasonable for practical applications toward photonic integrated circuits. Based on the ILs of the input and output SSC and GACs, the excess loss pertaining to the RFWC was below 0.4 and 1.2 dB when the C-SDC was enabled and disabled, respectively. Consequently, the proposed RFWC module is highly expected to be deployed in various photonic circuits, playing a vital role in concurrently accommodating both GACs and end-fire SSCs.

Conclusion

A RFWC module that allows both the GAC and SSC to interrogate the same photonic circuit is proposed and demonstrated. The proposed module renders selective switching between the GAC and SSC input/outputs by leveraging the nonvolatile switching ability of the GSST-based directional coupler. Considering the state of GSST can be switched back and forth from amorphous to crystalline via low-temperature annealing under specific environmental conditions, the inputs and outputs can be switched back and forth from the GAC to the SSC and vice versa, not involving continuous power during the operation of the circuit. This flexibility in choice of the input and output coupler enables not only full-scale wafer-level testing via the GAC but also low-loss and broadband applications at the chip level when the chips are diced out of the wafer. In consideration of its minimal excess loss below 1.2 dB and decent ERs above 13 dB while operating under both states of GSST, it is claimed that the proposed RFWC module can be applied to practical photonic circuits without compromising the performance. Although the current demonstration is underpinned by a SiN platform in combination with a PCM of GSST, the proposed module can be readily tailored for other types of platforms and PCMs. The RFWC module is slated to serve as a springboard to resolve the existing trade-off between full-scale wafer-level testing and broadband high-efficiency coupling at the chip level, paving avenues for pursuing various hybrid photonic circuits based on PCMs.

Methods

We have used a finite-difference eigenmode (FDE) solver [Ansys Inc.] to reveal the effective refractive indices and field profiles of the directional coupler supermodes. The FDE solver calculates the mode field profiles, their effective refractive indices, group indices, and optical losses by solving the Maxwell’s equation across the meshed cross-sections of the waveguides. A three-dimensional eigenmode expansion (EME) solver from Ansys Inc. was adopted to determine the taper lengths and calculate the coupling efficiency of the end-fire coupler. The rest of the simulations were based on the three-dimensional full-vectorial FDTD solver. The S-parameters were exported for each of the elements, which were used to construct the complete module. We also resorted to a tool, Lumerical Interconnect, to conduct the full circuit simulations and assess the performance of the proposed reconfigurable coupler module. For the simulations, the refractive indices of the SiN and SiO$_2$ were assumed to be 2.0 and 1.45, respectively.
1.44 at a wavelength around 1550 nm, respectively. The complex refractive indices of both the amorphous and crystalline GSST were derived from the dispersion properties based on the experimental work by Zhang et al.21.

Data availability
The datasets generated and/or analyzed during the current study are available from the corresponding author on reasonable request.

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Author contributions
B.B. conceived the idea, performed the numerical simulations, and discussed the results. S.S.L. guided in result analysis, data interpretation and supervised the whole work. Both the authors contributed to the manuscript preparation.

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Competing interests
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Additional information
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