Quantum computing using shortcuts through higher dimensions

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Quantum computation offers the potential to solve fundamental yet otherwise intractable problems across a range of active fields of research. Recently, universal quantum-logic gate sets—the building blocks for a quantum computer—have been demonstrated in several physical architectures. A serious obstacle to a full-scale implementation is the sheer number of these gates required to implement even small quantum algorithms. Here we present and demonstrate a general technique that harnesses higher dimensions of quantum systems to significantly reduce this number, allowing the construction of key quantum circuits with existing technology. We are thereby able to present the first implementation of two key quantum circuits: the three-qubit Toffoli and the two-qubit controlled-unitary. The gates are realised in a linear optical architecture, which would otherwise be absolutely infeasible with current technology.

The realisation of a full-scale quantum computer presents one of the most challenging problems facing modern science. Even the implementation of small scale quantum algorithms requires an unprecedented level of control over multiple quantum systems and a deep understanding of quantum mechanics. Recently much progress has been made with demonstrations of universal quantum gates sets - the fundamental building blocks of a quantum computer - in a number of physical architectures including ion-traps1,2, linear optics3,4, and superconductors5. In theory these gates can now be put together to implement any quantum algorithm and build a scalable quantum computer. However, in practice there are many significant obstacles that will require much more theoretical and technological development to overcome. One of the most daunting is the sheer number of gates required to implement quantum algorithms.

Most approaches to quantum computing employ qubits—the quantum version of bits—encoded in two-level quantum systems. However, candidate systems for encoding quantum information typically have a far more complex physical structure with many readily accessible degrees of freedom, such as atoms6, ions1,2 or photons3,4. In this paper we show how harnessing these higher dimensions of quantum systems during computation can drastically reduce the number of gates required to implement key quantum circuits. This is based on a recent proposal7, which we extend and use to build two key quantum logic gates for the first time. The technique has the potential for application in a wide range of physical architectures in combination with existing technology, and offers numerous advantages inherent in a reduced circuit complexity.

One of the most important quantum logic gates that has yet to be realised is the Toffoli—a three-qubit entangling gate that flips the logical state of the ‘target’ qubit conditional on the logical state of the two ‘control’ qubits. Famously, Toffoli gates allow universal reversible classical computation. The Toffoli also plays a central role in quantum error correction8 and fault tolerance9. Furthermore, the combination of the Toffoli and the one-qubit Hadamard offers a simple universal quantum gate set10. The implementation of this gate requires a new level of coherent control over multiple quantum systems and represents a significant experimental challenge. We note that previous demonstrations in a liquid NMR architecture have since been shown to represent, at best, a classical simulation of quantum logic leading to no computational advantage11,12.

The Toffoli-sign (ts) is a three-qubit gate that applies a sign shift to one logical input state and is equivalent to a Toffoli under the action of only a few additional one-qubit gates. The simplest decomposition13 of a ts gate when restricted to qubits throughout the calculation requires five two-qubit gates. If we further restrict ourselves to controlled-Z (or CNOT) gates this number climbs to six13, Fig. 1a. A quantum logic circuit that requires only three two-qubit gates is shown in Fig. 1b7. The increased efficiency is achieved by harnessing a third level of one of the information carriers during computation—i.e. one carrier becomes a qutrit with logical states 0, 1 and 2. The action of the first Xa gate is to move information encoded in the logical 0 state of this qutrit into its third level (2), which then bypasses the subsequent two-qubit gates. The final Xa gate coherently brings this information back into the 0 state, reconstructing the log-
ical qubit. This shortcut through a higher dimension allows the two-qubit gates to operate on a subspace of the original target qubit and implementation of the TS with a reduced number of gates. Note that standard two-qubit gates are necessary, with only the additional requirement that they apply the identity to information encoded in the logical $2$ state of the qutrit. The technique can be generalized to implement higher-order $n$-control-qubit Toffoli gates ($^nT$) by harnessing a single $(n+1)$-level information carrier during computation and only $(2n−1)$ standard two-qubit gates[7] (see supplementary information).

We extend this approach to simplify the construction of another key quantum circuit: the two-qubit controlled-unitary (CU) which applies an arbitrary one-qubit gate ($u$) to a target qubit conditional on the state of a single control qubit, Fig. 1b. This circuit plays a central role in quantum computing, particularly in the phase estimation algorithm[13] which in turn finds application in quantum simulation and quantum chemistry [14]. Phase estimation also underpins Shor’s famous quantum algorithm for factoring numbers [15], the implementation of which would have significant consequence for modern cryptography. Futhermore, the set of CU’s alone is sufficient for universal quantum computing: a CU can implement a CNOT and induce any single qubit rotation at the expense of an additional ancilla qubit, for example. Our technique can be generalized to implement higher-order $n$-control-unitary ($C^nU$) gates using an $(n+1)$-level target and only $2n$ two-qubit gates (see supplementary information). Even more generally, the approach can be used to efficiently add $n$ control-qubits to an arbitrary controlled unitary that operates on $k$ qubits (see supplementary information).

The simplest known decompositions[13] of a $^nT$ and $C^nU$ into two-qubit gates requires $(12n−11)$ and $(12n−10)$ gates, respectively. In each case this is achieved by employing an additional overhead of $(n−1)$ ancilla qubits. (Note that implementations without ancillas require on the order of $n^2$ two-qubit gates[13]). By harnessing only higher dimensions of existing information carriers we achieve a significant reduction in all cases. For example, a known decomposition[13] of the $^5T$ and $C^5U$ both require 50 two-qubit gates plus 4 ancilla qubits, when restricted to qubits. Our technique requires only 9 for the former, 10 for the latter and no ancillary information carriers in either case.

With an abundance of higher dimensions available in most candidate systems for encoding quantum information, our technique has the potential for wide application.

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1 We note that an architecture-specific decomposition of the $^nT$ has been proposed for implementation in an ion trap[16]. This requires $(2n+1)$ qubit-qutrit entangling gates.
Trapped ions also offer a range of higher dimensions that can be readily exploited, including additional electronic and vibrational energy levels. Indeed, these levels are routinely used for read-out and to coherently implement individual logic gates [16, 19]. An immediate benefit of a significant reduction in the number of two-qubit gates required for quantum circuits is an equally significant speed-up in processing time. This has particular advantages for the many architectures where short coherence times are an obstacle in the path to scalability.

Here we present an implementation using photons to encode information and linear optics to construct quantum gates. Such gates are high performing, well characterised and offer fast gate speeds. Linear optics provide an excellent test-bed for studying quantum information science, and has several known paths to scalable quantum computing [20, 21]. Without the resource saving technique that we present here linear optic implementations of these gates is absolutely infeasible with current technology. We note that our resource saving scheme is fundamentally different from and potentially complementary to schemes for reducing the overhead associated with generating a universal resource [22, 23, 24]; here we are concerned with reducing the amount of that resource required to implement algorithms.

Fig. 1c shows a conceptual circuit of our implementation of the T and CU. Key steps are the expansion of the Hilbert space of the target qubit (T), effected by the first polarising beamsplitter (PBS1), and contraction back into the original space, effected by the components in the dashed box. Before PBS1 we have a two-level system in the target rail with logical states $\ket{H=0}$ and $\ket{V=1}$ (a polarisation qubit). PBS1 then moves information encoded in the logical $\ket{H}$ state into a separate spatial mode. After PBS1 we have access to a four-level system: two levels in the top rail (t) and two in the bottom rail (b), with logical basis states $\ket{H, t}, \ket{V, t}, \ket{H, b}$ and $\ket{V, b}$, respectively. While we only need to use one of the additional levels in the bottom rail, we use both in our experiment simply to balance optical path-lengths. The contraction back into the original two-level polarisation qubit is performed non-deterministically, i.e. given deterministic two-qubit gates measurement of a single photon at D1 heralds a successful run of the gate. This allows for a demonstration without the last CNOT gate of Fig. 1b, thereby making an implementation feasible with recent developments in linear optics quantum gates [25].

In the case where it is the identity, the circuit in Fig. 1c implements a T|C gate between carriers $C_2, C_1$ and $T$, applying a sign shift to the $\ket{C_2, C_1, T}=\ket{1, 0, 1}$ term. It is of no consequence which state receives the sign shift; all cases are equivalent to a Toffoli under the action of additional one-qubit gates. For polarisation encoding, such gates are trivial to implement with standard birefringent waveplates. In the case where it is $Z_\theta$ the circuit implements a controlled-$Z_\theta$ ($CZ_\theta$) between $C_1$ and $T$. $Z_\theta$ imparts an arbitrary phase between the logical states, i.e. the operation $Z_\theta \ket{0} \rightarrow \ket{0}, Z_\theta \ket{1} \rightarrow e^{i\theta} \ket{1}$. It is straightforward to show that such a gate is locally equivalent to a CU, under the action of additional one-qubit gates (see supplementary material).

Figs. 1d-e show the layout of our optical source and logic circuit in the laboratory, respectively. Our photons are generated via spontaneous parametric down conversion (SPDC), spatially filtered via single-mode optical fiber and spectrally filtered using interference filters, as detailed in the figure caption. Two-qubit linear optic gates are realised by combining one-qubit gates (waveplates) and controlled-$Z$ ($CZ=CZ_\pi$) gates based on non-classical interference at partially-polarising beamsplitters; the gates are nondeterministic and employ a measurement induced non-linearity [26, 27, 28]. Rather than chaining the gates to implement the required sequence for our implementation of the Toffoli (Fig. 1c) we employed a recently developed three-qubit quantum logic gate [27]. Ideal operation of the two-qubit gates requires non-classical interference between indistinguishable photons at the PPBS’s (Fig. 1e). Dependent photons generated from the first pass of the PDC source interfere non-classically at PPBS1. One photon then goes on to interfere with a third, independent photon from the second pass, at PPBS2. We measure relative non-classical two-photon interference visibilities between vertically po-
We observe fidelities with the ideal states, linear entropies and tangles of i) \(0.90\pm0.04, 0.21\pm0.08, 0.68\pm0.10\) and ii) \(0.75\pm0.06, 0.47\pm0.10, 0.04\pm0.06\), respectively. b) As for a) but where the roles of \(C_1\) and \(C_2\) have been swapped. We now observe i) \(0.81\pm0.02, 0.39\pm0.05, 0.53\pm0.07\) and ii) \(0.80\pm0.03, 0.40\pm0.05, 0.01\pm0.01\). The decrease in tangle in the i) cases reflects the difference between dependent and independent photon interference, as discussed in the text. c) ideal density matrices. Note in all cases only real parts are shown, imaginary parts are negligible.

The fidelity between two matrices (either two states or two processes) is \(F(\rho, \sigma)\equiv|\text{Tr}\sqrt{\sqrt{\rho}\sigma\sqrt{\rho}}|^2\). Linear entropy is \(S_L\equiv d(1-\text{Tr}[\rho^2])/(d-1)\), where \(d\) is the state dimension.

For our experimental implementation we include the additional one-qubit gates required to convert the Ts in Fig. 1c to the Toffoli. In both implementations our imperfectly manufactured beamsplitters impart systematic unitary operations on the optical modes. For simplicity we corrected for these effects numerically. Alternatively such unitaries could be corrected with standard waveplates.

For our implementation of a Toffoli we use photons from both passes of the PDC crystal and set \(r\) to the identity (Fig. 1e). A four-fold coincident measurement at detectors D1-4 signals a successful run of the gate. In linear optics implementations of two-qubit quantum gates, state dependent loss is used to rebalance amplitudes. When incorporating loss elements L1-3 the gate operates with a success probability of 1/72. Alternatively, to combat low count rates, correct balance can be achieved by removing additional loss elements and pre-biasing the input polarisation states during gate characterisation. Under these conditions we measure a four-fold coincidence rate of approximately 10 mHz when running at full pump laser power. While this is not sufficient to perform a full process tomography of the gate over a practical time period, we are able to to demonstrate all the key aspects of its behavior.

To demonstrate the classical action of our Toffoli we first reconstruct a logical truth-table (Fig. 2). In the ideal case of our implementation the target (\(T\)) undergoes a logical flip if, and only if, both control qubits are in the logical 0 state. The inquisition, \(I\), is the overlap between the ideal and measured truth tables. For our three-qubit gate we measure \(I=0.81\pm0.03\), compared to \(I=0.85\pm0.01\) achieved for optical implementations of two-qubit gates. Significant deviations from the ideal correspond to unwanted flips of the target qubit which can be understood with reference to the non-classical interferences required for correct operation in each case. In order to gauge our gates ability to apply the correct operation to a subset of logical input states we employ the flipping contrast, \(C\). (See Fig. 2). For inputs \(|C_2, C_1\rangle=|0, 0\rangle\), no non-classical interference is required for correct operation and we measure \(C=0.99\pm0.01\), averaged over both target logical input states. Inputs \(|C_2, C_1\rangle=|0, 1\rangle\) require perfect non-classical interference between dependent photons \(C_1\) and \(T\), for ideal operation. As discussed previously we achieve a near perfect interference visibility between vertical photons in this case. However, the full process suffers from the presence of higher-order PDC terms which are not observed in the visibility measurement due to higher-order interference processes. This is reflected in an average of \(C=0.95\pm0.02\). Inputs \(|C_2, C_1\rangle=|1, 0\rangle\) require perfect non-classical interference between independent photons \(C_2\) and \(T\), for ideal operation. This process is limited not only by higher-order terms but by inherent distinguishability of these photons, reflected in an average of \(C=0.80\pm0.02\). Inputs \(|C_2, C_1\rangle=|1, 1\rangle\) require perfect non-classical interference between both dependent and independent photons, and are therefore the most challenging cases. Here we observe an average of \(C=0.73\pm0.05\).

To demonstrate the quantum action of our Toffoli we test its ability to coherently control an entangling process. With an input state of \(|0, (0+1), 0\rangle/\sqrt{2}\) our Toffoli will produce the entangled state \(|0, \Psi_+\rangle\), where \(|\Psi_+\rangle\) is the maximally entangled Bell state. With an input state of \(|C_2, C_1, T\rangle=|1, (0+1), 0\rangle/\sqrt{2}\), it will produce the separable output state \(|1, (0+1), 0\rangle/\sqrt{2}\). In the former (latter) case the entangling operation between \(C_1\) and \(T\) is coherently turned on (off) by \(C_2\). We then swap the roles of the control qubits and repeat the test. These processes require coherent interaction between all three qubits. We perform over-complete full state tomography to reconstruct the density matrix of two-qubit output states, requiring 36 separate measurements.
tomography employs convex optimisation and Monte-Carlo simulation for error analysis\cite{25}. Measurements sets are taken iteratively, whereby multiple sets—each taking around 1 hr to complete—are recorded. This reduces the effect of optical source power fluctuations.

Fig. 3 shows experimentally reconstructed density matrices representing the state of a control and target qubit, at the output of our Toffoli gate. We achieve a high fidelity\cite{31} with the ideal states in all cases, as detailed in the figure caption. The entangling process required to achieve Fig. 3a)i relies on interference between dependent photons. The process required to achieve Fig. 3b)i, relies on both dependent and independent interference. This leads to the reduced fidelity and increased linear entropy\cite{31} observed in the latter case. These processes demonstrate the coherent action of our Toffoli gate.

Higher order PDC terms are caused when more than one pair of photons is created simultaneously in a single PDC pass (Fig. 1d). It is straightforward to show that the ratio of double to single photon-pair emission is proportional to the pump power. Thus reducing the power by a factor of 4 should in turn reduce these higher-order contributions from each pass by a factor of 4. Under these conditions we observe a four-fold rate at the output of the Toffoli gate of only $\sim 0.1 \text{mHz}$ and repeat measurement of the average flipping contrast for the most challenging logical inputs $|C_2, C_1\rangle=|1, 1\rangle$. We observe a clear improvement from $C=0.73\pm0.05$ to $C=0.83\pm0.04$.

For our implementation of the $cu$ circuit we inject only dependent qubits $C_1$ and $T$ into the circuit, from the forward pass through the PDC crystal (Figs 1d-e). When incorporating loss element $L_1$ (Fig. 1e) the gate operates with a success probability of $1/18$. For our demonstration this was optimised by removing $L_1$ and pre-biasing only the $C_1$ input state\cite{26, 27, 28}. A two-fold coincidence measurement at detectors D1-2 signals a successful run of the gate. When running at $1/4$ power we observe a two-fold rate at the output of our circuit of around $100 \text{Hz}$. This is more than sufficient to perform a full process tomography\cite{31} of the $cu$ on a timescale of the order of $2 \text{hrs}$, whilst retaining the advantages of a lower power.

As a demonstration, we report the implementation of four distinct $cu$ gates that apply $\theta$ rotations of $\pi/4$ (ct), $\pi/2$ (cl), $3\pi/4$ (cl) and $\pi$ (cz) to the target ($T$) conditional on the control ($C_1$), respectively. Three of these are of fundamental importance to quantum computing\cite{13}. We fully characterise these gates via quantum process tomography\cite{31}. Fig. 4 shows the experimentally reconstructed process matrices. We achieve the highest reported two-qubit logic gate fidelities, in any architecture, as detailed in the figure caption. The origins of the small deviations from ideal operation are thought to lie in residual higher order PDC emissions, imperfect mode matching and manufactured optics\cite{29, 32}. The overriding source of error in our experiments lies in our imperfect photon source. Current developments in source technology promise significant improvements in the near future. The combination of this with recently developed photon-number resolving detectors offers a path to a deterministic and scalable implementation of our gates.

We have demonstrated a new technique that harnesses the power of readily available higher dimensional quantum systems to drastically simplify the construction of key quantum circuits. As we have shown, the technique is particularly relevant to the current state of experimental quantum computing and can be integrated with ex-
isting technology to realise new quantum circuits. The demonstration of these circuits represents a crucial test of the practicality of quantum computing, raises many important questions that have yet to be solved and immediately offers valuable new tools with which to study quantum interactions.

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**Supplementary material.**

Our technique can be extended to multiple-control gates, e.g. n T and c 2 U. Figure 5 gives an implementation of n T and c 2 U. Fig. 6 shows a more general application of our technique; adding n control qubits to an arbitrary k qubit unitary.

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FIG. 5: Implementation of the next level of encoding of our technique required to realise a 3-control-qubit Toffoli Sign gate ($T^3$s) and b 2-control-qubit unitary $c^2Z_θ$. The $X_a$ gate flips information between the logical 0 and 2 state of the target. The $X_b$ gate flips information between the logical 1 and 3 state of the target. Thus we require access to a four-level information carrier in the target; two levels in the original red rail and one in each of the dashed-blue rails. The dashed box shows the 1-qubit gates required to interconvert the $T^3$s and $c^2U$ to the Toffoli and controlled-unitary equivalents, respectively. $VZ_θV^T$ is the spectral decomposition of $U$, up to a trivial global phase factor. All other gates operate as usual (on the qubit subspace: levels 0 and 1) and their operations are shown, including some used in Fig. 1 of the main text. In a the control operation occurs if $|C_3, C_2, C_1⟩=|1, 1, 0⟩$. In b the control operation occurs if $|C_2, C_1⟩=|1, 0⟩$. a) is based on Fig. 2 of Ref. 3 in the main text.

FIG. 6: Circuit for implementation of a 3-control-qubit unitary acting on $k$ qubits, $c^3(U_k)$. Given the ability to perform a single instance of a $c^3(U_k)$ additional control qubits can be added at a cost of an extra 2$n$ two-qubit gates and an additional $n$ dimensions in $C_1$. The $X_j$ perform as described in the caption of Fig. 1.