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Spatially uniform single-qubit gate operations with near-field microwaves and composite pulse compensation

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Abstract. We present a microfabricated surface-electrode ion trap with a pair of integrated waveguides that generate a standing microwave field resonant with the $^{171}$Yb$^+$ hyperfine qubit. The waveguides are engineered to position the wave antinode near the center of the trap, resulting in maximum field amplitude and uniformity along the trap axis. By calibrating the relative amplitudes and phases of the waveguide currents, we can control the polarization of the microwave field to reduce off-resonant coupling to undesired Zeeman sublevels. We demonstrate single-qubit $\pi$-rotations as fast as 1 $\mu$s with less than 6% variation in Rabi frequency over an 800 $\mu$m microwave interaction region. Fully compensating pulse sequences further improve the uniformity of $X$-gates across this interaction region.

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1. Introduction

The future development of a fault-tolerant quantum processor will require the storage and manipulation of large numbers of qubits and the ability to execute quantum gates with high fidelity [1]. Some proposed architectures use trapped atomic ions to store and transport quantum information, and recent years have witnessed significant progress in the control of these systems [2–6]. Most trapped-ion experiments utilize laser fields to control internal and motional quantum states [7]; however, the fidelity of laser-mediated gates suffers from errors induced by unavoidable spontaneous emission [8–10], pointing instabilities and variations in the laser frequency, phase and power [11]. The systems required for laser-mediated gates are also large and complex, making them difficult to scale to the large numbers of qubits required to execute algorithms or simulations of interest.

Alternatively, microwaves can be used to control the ground electronic hyperfine state of the ions, and several experiments have demonstrated high-fidelity single-qubit gates that take advantage of the long natural lifetime of hyperfine qubits and the stability of microwave sources [12–15]. The advent of microfabricated ion traps allows microwave structures to be integrated directly into the trap, thereby facilitating strong fields and consequently fast gate operations. Many quantum algorithms require the parallel application of spatially uniform single qubit gates across groups of neighboring ions. Examples of such operations include the parallel application of gates across sub-regions of a surface code architecture [16] and the initialization of qubits into a computational basis. Global single qubit operations can also be used to apply dynamical decoupling techniques across all qubits to suppress dephasing errors [17]. In addition, small integrated microwave structures can enable single ion addressing and multi-qubit gate operations by generating strong field gradients [13, 14, 18–22].

Here we present a microfabricated surface electrode ion trap with integrated microwave waveguides that can apply global single qubit operations applicable to qubit initialization and dynamical decoupling. This trap was fabricated with standard very large-scale integration techniques. The waveguides are designed to place an antinode of a 12.64 GHz standing microwave field, suitable for coupling the ground state hyperfine levels of $^{171}$Yb$^+$, at the center of the ion trap. The microwaves are polarization tunable, so that we can optimize coupling to a desired Zeeman transition while suppressing off-resonant coupling to neighboring transitions.

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Figure 1. (a) A schematic view of the $11 \times 11$ mm$^2$ silicon chip. (b) The active trapping region, showing radio-frequency (rf) and dc trapping electrodes, the shaped loading slot and the on-chip microwave waveguides. (c) A cross section showing internal layers (the vertical direction is scaled by $10 \times$ for visual clarity).

With the ion located $59 \mu$m above the trap surface, a 0.037 mT field amplitude allows execution of sub-microsecond $\pi$-rotations. The intrinsic position dependence of the standing microwave field causes the Rabi frequency to vary by less than 6% over an 800 $\mu$m linear span. We correct for this residual non-uniformity by executing gates with fully compensating broadband pulse sequences.

2. Trap design

2.1. Trapping structures

The trap conforms to a symmetric five-wire surface-electrode Paul trap geometry [23] fabricated on a $11 \times 11$ mm$^2$ silicon die (figure 1(a)) similar to the designs reported in [24–26]. Electrodess etched into three sputtered aluminum layers are separated by insulating silicon dioxide films. rf potentials applied to two parallel electrodes provide radial ion confinement (in the $x$–$y$ plane, figure 1(b)) $59 \mu$m above the Metal 2 layer (figure 1(c)). Quasi-static potentials applied to segmented dc electrodes confine and transport ions along $z$. The rf electrodes are $30 \mu$m wide along $x$ with an inner edge-to-edge separation of $92 \mu$m. The segmented dc electrodes
are 56 µm wide along z except for six 100 µm wide electrodes bordering the loading slot. Two additional dc electrodes traversing the entire length of the trapping region are used to apply uniform x–y fields and to rotate the radial principal axes. Each electrode is separated from neighboring conductors by 4 µm gaps. Each dc electrode incorporates a 60 pF plate capacitor (1 mm² area) to filter unwanted rf pickup [24]. A loading slot allows a thermal beam of neutral Yb to reach the trapping volume from an oven located below the trap.

2.2. Integrated waveguides

The trap includes a pair of conductor-backed coplanar waveguides that generate local microwave magnetic fields. Each waveguide includes a 40 µm wide electrode with 4 µm gaps to neighboring conductors, and 10 µm of SiO₂ separate the coplanar layer from the ground plane below (figure 1(b)). The waveguides support a \( \omega_{mw} = 2\pi \times 12.64 \text{ GHz} \) quasi-transverse electromagnetic (TEM) guided mode resonant with the hyperfine splitting between the \( F = 0 \) and 1 manifolds in the \( ^2S_{1/2} \) ground state of \(^{171}\text{Yb}^+\) (see figure 3(a)). In the ideal case, currents in each waveguide generate a magnetic field along the trapping axis

\[
\vec{B}_k(z, t) = (\hat{x} \beta_{x,k} + \hat{y} \beta_{y,k}) I_k(z) \cos(\omega_{mw} t + \phi_k),
\]

where \( k = 1, 2 \) is an index for the two waveguides, \( \phi_k \) is the phase of the microwave current source, \( I_k(z) \) is the current in each waveguide, and \( \beta_{x,1} = \beta_{x,2} \simeq 0.08 \text{ mT A}^{-1} \) and \( \beta_{y,1} = -\beta_{y,2} \simeq 0.17 \text{ mT A}^{-1} \) are properties of the waveguide mode. \( \beta_{y,1} = -\beta_{y,2} \) due to the symmetric placement of the waveguides around the trapping axis. The waveguides terminate in an open circuit at a position that is approximately a quarter-wavelength from the trap center, which produces a standing wave field with maximum amplitude and uniformity in the gate region. A smaller traveling wave component also exists due to on-chip attenuation that generates an amplitude difference between forward and backward propagating waves. Although a traveling wave would produce more uniform fields, a standing wave produces a larger maximum field amplitude for a given input power. A standing wave design simplifies the trap by eliminating the need for on-chip resistive termination or for coupling power off the chip.

Far from the trap center, the waveguides meander to fit a complete wavelength on the chip and then terminate on wirebond pads at the edge of the chip. Extending the waveguides to a full wavelength in this way places a current node at the wirebond pads and reduces the potential for resistive power loss in the connections. A series of 25.4 µm diameter aluminum wirebonds connect the chip waveguides to two printed circuit board (PCB) waveguides that route microwaves from the edge of the trap package (figure 2). Connections between the PCB top level ground and the on-chip metal 1 and 2 ground planes are symmetric about each microwave electrode. Quarter-wave transformers match the 50 Ω impedance of the PCB waveguide to the 27 Ω on-chip characteristic impedance. The PCB is fabricated using a 254 µm thick Rogers 4350B substrate with two 18 µm thick copper foil conductive layers and a 3–6 µm electroless nickel immersion gold (ENIG) finish. The skin depth in the PCB at \( \omega_{mw} \) is comparable to the thickness of the lossy nickel layer resulting in \( \approx 3 \text{ dB} \) of power loss between the microwave connector and wirebonds. Much of this power loss could be recovered by replacing the ENIG finish with an electroplated gold surface.
Figure 2. Components for trap packaging and microwave power delivery. (a) Schematic of a packaged trap with top ground screen removed for clarity. (b) Colorized photograph of the wirebond interface between the PCB and the trap die. (c) Photo of the installed device with coaxial microwave connections.

3. Trap performance

3.1. Microwave control of $^{171}\text{Yb}^+$ hyperfine qubits

Figure 3(a) shows the transitions in the ground state hyperfine manifold of $^{171}\text{Yb}^+$ addressed by the microwave field. A static 0.74 mT field along $y$ defines the quantization axis and lifts the degeneracy of the $F = 1$ triplet. We select the clock states $|^{2\Sigma +1/2, F = 1, m_F = 0} \rangle \equiv | \uparrow \rangle$ and $|^{2\Sigma -1/2, F = 0, m_F = 0} \rangle \equiv | \downarrow \rangle$ as the qubit states. We observe Rabi oscillations at a frequency $\Omega = 2\pi \times 0.49$ MHz (figure 3(b)) by optically pumping into $| \downarrow \rangle$, applying microwave power for a variable interval of time, and measuring the resulting population transfer into the $F = 1$ manifold through state-selective fluorescence of the 369.5 nm cycling transition (figure 3(a)) [27].

Currents in each waveguide generate an oscillating magnetic field which contains both a $\pi$-polarized component that couples to the $\Delta m_F = 0$ clock transition and also a transverse-polarized component that couples to the $\Delta m_F = \pm 1$ transitions. Figure 3(c) shows the hyperfine spectrum measured with microwave power applied to a single waveguide. From the relative Rabi frequencies of the transitions, we estimate the ratio of polarization components for each waveguide as $|\beta_{x,k}|/|\beta_{y,k}| \approx 0.46$.

The polarization of the near-field microwaves may be controlled by adjusting the relative amplitude and phase of the microwave currents in the two waveguides. In particular, the polarization may be aligned along the quantization axis, thereby maximizing the qubit transition.
Figure 3. (a) Simplified energy level structure of the $^{171}$Yb$^+$ ion (not to scale). The qubit uses the $|F = 1, m_F = 0 \rangle \equiv |\uparrow\rangle$ and $|F = 0, m_F = 0 \rangle \equiv |\downarrow\rangle$ clock states. During experiments, a 12.64 GHz microwave field produced by on-chip waveguides couples hyperfine states in the $^2S_{1/2}$ manifold. The $^2S_{1/2} \rightarrow ^2P_{1/2}$ transition at 369.5 nm is used for Doppler-cooling, qubit initialization, and qubit detection [27]. (b) Rabi oscillations between the qubit states induced by application of microwaves ($z = 300 \mu$m). (c) Resolved hyperfine transitions between $^2S_{1/2}$ sublevels.

Rabi frequency while also suppressing off-resonant $\Delta m_F = \pm 1$ transitions. The active and passive microwave components supplying energy to these electrodes are not perfectly power-balanced and phase-matched. We calibrate the microwave sources by first driving each waveguide independently to map the relationship between source power and Rabi frequency (figure 4(a)). Once the field amplitudes from the waveguides have been equalized, the relative phase between microwave currents $\phi_r$ can be varied to produce an arbitrary linear polarization in the $x$–$y$ plane. Figure 4(b) shows the resonant Rabi frequency for each of the three transitions as $\phi_r$ is varied, demonstrating the desired suppression of the $\Delta m_F = \pm 1$ transitions at $\phi_r = \pi$. The minimum measured Rabi frequencies of the $\Delta m_F = \pm 1$ transitions are less than 8% of the $\Delta m_F = 0$ frequency. We suspect that the suppression is limited by a residual $z$ component of the quantization field, and the mismatch between the $\Delta m_F = \pm 1$ curves in figure 4(b) is caused by frequency dependence in the microwave delivery system.

Polarization control is useful with any hyperfine qubit because it can suppress unwanted transitions as gate speeds are increased or Zeeman splittings are reduced. In the context of the experiments presented here, with a 10.4 MHz Zeeman splitting and $\Omega = 2\pi \times 0.5$ MHz, off-resonant coupling would limit the fidelity of a $\pi/2$ gate (square envelope) to $F < 1 - 4 \times 10^{-4}$ if only a single microwave waveguide were used.
Figure 4. (a) Plot of the qubit Rabi frequency versus microwave source attenuation, obtained with a resonant field produced by a single waveguide. The nonlinearity at high source power is caused by amplifier saturation. (b) Polarization control via microwave power balancing and phase tuning between the two waveguides. The solid curves are fits to the magnitude of a sinusoid.

Figure 5. Measured Rabi frequency of the qubit transition as a function of the axial distance from the load slot center. The solid line is a quadratic fit to the data which indicates a maximum microwave field at $z = 957 \, \mu\text{m}$.

3.2. Microwave field uniformity and compensation

The standing wave current in the waveguides produces a microwave field with non-uniform amplitude along the trap axis. Figure 5 shows the measured Rabi frequency of the qubit transition at several positions along the trap axis. We observe a maximum Rabi frequency of $2\pi \times 0.52 \, \text{MHz}$, corresponding to a field amplitude of $0.037 \, \text{mT}$, located $z_0 = 957 \, \mu\text{m}$ from the loading slot center. Finite element calculations predict an antinode location at $z = 895 \, \mu\text{m}$ in reasonable agreement with the experiment. These models indicate that the maximum field corresponds to a local current in each electrode of $I_z(z_0) \approx 0.1 \, \text{A}$. The maximum Rabi frequency
Figure 6. Population transfer into the $F = 1$ manifold after applying a logical $X$-gate produced by simple pulses (black), SK1 (red), and BB1 (blue) sequences. The pulse areas are uniformly scaled by adjusting the pulse lengths. The curves are the signals predicted by theory, adjusted to account for a known qubit-detection error. The shaded area encloses the range of systematic microwave amplitude error observed over the entire trapping region.

is presently limited by the output power of the 3 W amplifiers and losses in the microwave delivery system (see the appendix). Heating due to on-chip dissipation in the 1 $\mu$m thick aluminum electrodes would ultimately determine the maximum current density and associated Rabi frequency.

The microwave field non-uniformity appears as an effective amplitude error when implementing global single-qubit rotations on multiple ions located at different positions in the trap. To improve single-qubit gate uniformity, we implement global gates using broadband compensating pulse sequences [28, 29]. This technique replaces a simple pulse with a sequence of pulses whose phases are chosen to nearly cancel the effective microwave amplitude error. The excitation profiles of such pulse sequences enable global rotations on many qubits, although the microwave amplitude may differ significantly between distant ions.

Here we demonstrate the error-canceling properties of composite $X$-gates constructed from the first-order SK1 [30, 31] and second-order BB1 [32] sequences. The experiments prepare the qubit in $|\downarrow\rangle$, apply a logical $X$-gate, and then measure the population in the $F = 1$ manifold. To simulate the effect of systematic over/under rotations, we uniformly scale the pulse areas of every pulse in the sequence by adjusting the pulse duration. Figure 6 plots the measured excitation profiles produced by compensated gates, overlayed on the signal predicted by theory. Gates performed with the BB1 sequence are more robust to large variations in pulse area and require no additional time to execute compared to SK1, therefore BB1 is a superior choice for compensating the non-uniform field amplitude observed in figure 5. In contrast, the passband behavior of the SK1 sequence may make it a better choice for applications where localized gates are a primary objective. Assuming the field non-uniformity is the sole source of error, we calculate theoretical fidelities of $X$-gates. For the 6% amplitude deviation observed at $z = 300$ $\mu$m, a simple rotation performs a global $X$-gate with a minimum fidelity $\mathcal{F} \geq 0.995$, whereas SK1 and BB1 perform the same gate with minimum fidelities of $\mathcal{F} \geq 1 - 1.5 \times 10^{-4}$ and $1 - 2.2 \times 10^{-7}$, respectively.
As a demonstration of uniform global gates, we perform an experiment where \( n \) sequential logical \( X \)-gates are applied to a qubit initialized in \( \left| \downarrow \right\rangle \). We calibrate gate times so that an ion located at the microwave amplitude maximum (\( z_0 = 957 \mu m \)) experiences nearly perfect rotations. Qubits displaced from the field maximum rotate at lower Rabi frequencies, acquiring an under-rotation error that accumulates as \( n \) increases. We measure the \( F = 1 \) population as a function of ion axial position and number of sequential \( X \)-gates. For \( X \)-gates implemented by simple rotations we observe fringes (figure 7(a)) arising from the local qubit falling behind by an entire Rabi cycle relative to the maximal Rabi frequency. Instead, when implementing SK1 or BB1 pulses (figures 7(b) and (c)) the error accumulates so slowly that the excitation profile remains flat over the trapping region after \( n = 55 \) logical \( X \)-gates. Our ability to resolve fringe structure in these cases is currently limited by systematic state-preparation and measurement errors and by the number of simple pulse operations we can implement. In theory, neither the BB1 nor the SK1 sequence imposes a penalty on the maximum achievable fidelity when the primary sources of error can be compensated by the sequences, and we observe no reduction in \( F = 1 \) population due to the pulse sequences at the field calibration position, \( z_0 \), within our measurement error.

We analytically calculate the fidelity scaling of the sequential logical \( X \)-gates as a function of the microwave field strength. For simple rotations, the fidelity drops as \( F = \left| \cos[\epsilon(z)\pi n/2] \right| \), where \( \epsilon(z) = [\Omega(z) - \Omega(z_0)]/\Omega(z_0) \) is the fractional difference in Rabi frequencies between
the ion location \( z \) and the field maximum. For SK1 pulses the fidelity scales as \( F = |1 - \frac{15}{128} \pi^4 \epsilon(z)^4 n + O(\epsilon(z)^6)| \), and for BB1 the fidelity scales as \( F = |1 - \frac{5}{1024} \pi^6 \epsilon(z)^6 n + O(\epsilon(z)^8)| \).

4. Conclusion

We developed a microfabricated surface-electrode ion trap with integrated microwave waveguides that performs arbitrary single-qubit gates on the \(^{171}\text{Yb}^+\) hyperfine qubit. The polarization of the local microwave field can be tuned to minimize off-resonant coupling to adjacent Zeeman sub-levels. This suppression of off-resonant coupling will be most useful when Rabi frequencies are large compared to the Zeeman splittings, thus causing reduced spectral resolution of neighboring transitions. We demonstrate the use of fully compensating pulse sequences to reduce single-qubit gate variation caused by a spatially non-uniform microwave amplitude. Similar passband and narrowband pulse sequences [29] could be used to exploit microwave amplitude variations to enable single-ion addressing without requiring perfect field suppression at the location of neighboring ions.

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Appendix. Methods

A.1. Microwave delivery system

A schematic view of the microwave supply system is shown in figure A.1. An Agilent 83623B Swept-Signal Generator provides a stable microwave local oscillator (LO) signal that is approximately 300 MHz detuned from the qubit resonant frequency near 12.64 GHz. Delivery of this signal is controlled by an American Microwave SW-218 high-speed rf switch. The signal is split to supply the local oscillator ports of two frequency mixers. Two separate direct digital synthesis (DDS) boards with synchronized clocks supply the intermediate frequency (IF) signals near 300 MHz. The DDS outputs have independently controllable amplitude, phase and frequency and these signals are amplified prior to mixing with the LO signal. The mixers reject the carrier frequency and produce two rf sidebands at \( \omega_{\text{mw}} = \omega_{\text{lo}} \pm \omega_{\text{dds}} \) where one of these sidebands is tuned to the frequency of the desired hyperfine transition, while the other is far off resonance. Both signals are amplified in separate Mini-Circuits ZVE-3W-183+ amplifiers and routed to a coaxial feedthrough port on the UHV chamber. A pair of circulators protect the amplifiers from accidental high power reflections and also provide a dc ground on the microwave lines. Inside the chamber the microwaves are carried by two coaxial lines with Kapton dielectric and braided conductor, selected for UHV compatibility. The combined power loss in the feedthrough and Kapton cables is \( \approx 5.3 \) dB.
Figure A.1. Schematic view of microwave delivery system connected to the outside of the UHV chamber. The two DDS sources are mixed with a stable local oscillator and provide independent control over the amplitude, phase, and frequency ($\omega_{mw} = \omega_{lo} \pm \omega_{dds}$) of the microwave signal applied to the trap waveguides, though during typical operation $\omega_{dds1} = \omega_{dds2}$. DDS—direct digital synthesizer, LO—local oscillator, LNA—low noise amplifier, HPA—high power amplifier, MW—microwave.

A.2. Trap packaging

We mount chips to a gold electroplated stainless steel plate that also serves as a structurally rigid platform for mounting other components, including microwave connectors, PCB, grounded screen and a 100-pin ceramic pin-grid array (CPGA). Vacuum compatible Au/Sn solder bonds the microwave connectors (Corning Gilbert GPO) to the top metal layer of the PCB, and a retention block secures these connectors to the steel plate to reduce stress in the solder joints. The chip is bonded to the interface plate with electrically conductive epoxy (Epoxy Technology H21D) which accommodates the differential thermal expansion between the stainless steel mounting plate and the silicon chip during vacuum bake-out. Wire bonds at the chip perimeter establish electrical connections to the CPGA to provide both dc and rf trapping potentials, while wire bonds to the PCB supply microwave energy to the chip (figure 2(b)). To minimize scattered laser light, wire bonds are excluded from regions where cross-chip laser access is required.

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