Simulation on adjacent line mutual interference of high-speed railway track circuit

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Abstract. As the density of the high-speed railway network continues to increase, the problem of electromagnetic interference on adjacent lines has become increasingly prominent. This paper focuses on the electromagnetic interference of adjacent lines caused by rail and line in the signal transmission process of the high-speed rail track circuit. Firstly, complete the establishment of the four-terminal network model of the ZPW-2000A track circuit system and the cab signal entry current crosstalk model, calculation of interference voltage under different parallel length of signal frequency. Then the interference factors and coupling mechanism of adjacent lines are analysed to realize calculation of interference amount. Finally, according to the sensitivity index of the cab signal, the maximum parallel length of adjacent sections is given respect, and the interference protection suggestions of adjacent lines are put forward. The research work of this paper provides a theoretical basis for suppressing the interference of adjacent lines and guarantees the safe and efficient operation of high-speed trains.

Keywords: High-speed railway, ZPW-2000A track circuit, Adjacent line interference, Rail mutual inductance, Inductive coupling.

1 Introduction

The track circuit is an important device for detecting train occupancy, rail integrity, and train-ground information transmission in the railway signal system. Its reliability is directly related to the safety and efficiency of trains. In the high-speed and heavy-haul electrified railway, the track circuit is affected by the surrounding strong electromagnetic interference sources (such as the traction power supply system) [1]. Due to the increase in the density of the road network, the interference between adjacent lines in the railway site shows the characteristics of irregular interference waveforms, uncertain sources of interference, and difficulty incomplete elimination, which is easy to cause missing or upgrading cab signal code [2]. Therefore, it is significance to establish the simulation model of adjacent line interference of track circuit to realize the protection of electromagnetic interference of railway signal system to ensure the safe and efficient operation of trains.

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As a traditional and inherent problem of railway signals, adjacent line interference has always attracted the attention of domestic and foreign researchers. Lucca[3] et al. studied the influence of inductive interference caused by the electrified double track on the track circuit, and proposed an algorithm for sensitivity analysis of the electromagnetic interference level generated; E. M. Tarasov[4] et al. proposed a method to determine the parameter matrix coefficients of railway multiple lines and analyzed the adjacent line interference between asymmetrical track circuits; Manakov AD[5] et al. proposed a method to determine the interference current of the double-track circuit; Cuiqin Zhao[6] et al. established a multi-line parallel ballasted track coupling interference equivalent circuit model; Yuequan Li[7] et al. studied the coupling interference at the transverse connecting line at the entrance of electrified railway; Jialiang Liu[8] and others realized the numerical calculation of interference current in the adjacent segment. Most of the above-mentioned research is based on general-speed railways, while high-speed railways have different configurations. And in the face of more stringent safety requirements and a more complex electromagnetic environment, the mechanism of adjacent line interference is still worthy of further study.

2 Overview of adjacent track circuit interference factors

The track circuit of this line can receive not only the signal of this line but also the track circuit signal of adjacent lines. When analyzing the interference problem of adjacent lines, the track circuit of this line is called the interfered loop, and the track circuit of adjacent lines is the interfering loop.

China's high-speed railway mainly uses ZPW-2000 series track circuits, with a carrier frequency ranging from 1700 to 2600 Hz. Because the signal current flowing through the track circuit is a high carrier frequency, and the inductive characteristics of the rail, the varying signal current in the interfering loop forms an interference signal in the interfered loop through mutual inductance between rails, which is called mutual inductance coupling. The existence of capacitance between parallel lines makes the energy of other lines couple to interfered loop, which is called capacitive coupling. The current in the rail will leak to another rail through the track bed, forming many parallel leakage channels between the interfering loop and the interfered loop, so that the signal current of the interfering loop is conducted to the interfered loop via the ballast leakage resistance, which is called Ballast resistance leakage conduction. Under actual conditions on-site, due to factors such as humidity, rainfall, snowfall, etc., the resistance of the ballast is reduced, and the leakage of the ballast bed is more serious, which not only causes the red band phenomenon in this track section but also makes the adjacent line interference problem more serious. In the shunt state, the shunt current is relatively large, and the problem of adjacent line interference is particularly complicated.

To sum up, mutual inductance coupling, capacitance coupling, ballast resistance leakage conduction and other electromagnetic interference work together, resulting in adjacent line interference problem. When the interference is serious, it may cause distortion of cab signal receiving current waveform, affecting the normal operation of the track circuit.
3 Modelling and analysis of adjacent line interference in co-directional non-insulated track circuit

3.1 Modelling and verification of ZPW-2000A track circuit

The track circuit can be regarded as an electrical circuit with uniformly distributed parameters. Generally, the uniform transmission line theory is used to establish a four-port network model for analysis and calculation. This paper has completed the modelling and simulation of ZPW-2000A type non-insulated track circuit, and considers the mutual crosstalk system model of adjacent jointless track circuits in the same direction. And analyze the influence of electromagnetic interference caused by mutual inductance coupling, capacitive coupling, and track bed leakage conduction on cab signals.

According to the structural characteristics of the ZPW-2000A track circuit, a hierarchical four-port network model of component level, module level, and system level is established as shown in Figure 1. In shunt state, because the transient component of the signal attenuates quickly when the train slides on the track circuit, it is only necessary to consider the cascaded shunt resistance \( R_f \) (taking 0.15Ω) at different positions.

The equivalent model of cab signal voltage in shunting state is shown in Figure 2. \( \hat{Z}_{sr} \) is the apparent impedance of the receiving end, which can be expressed as:

\[
\hat{Z}_{sr} = \frac{N_{d11}(n-x)\hat{Z}_r + N_{d12}(n-x)}{N_{d21}(n-x)\hat{Z}_r + N_{d22}(n-x)}
\]  

The cab short circuit current \( \hat{i}_{gy}(x) \) is:

\[
\hat{i}_{gy}(x) = \frac{\hat{U}_i}{N_{s11}(x) \cdot R_f + N_{s12}(x)(R_f + \hat{Z}_{sr}(x)) / \hat{Z}_{sr}(x)}
\]

The following quantitative relationship between the cab signal receiving voltage and current is[9]:

\[
\hat{V}_{gy}(x) = \frac{200}{255} \hat{i}_{gy}(x)
\]

Without loss of generality, take the track circuit length \( L_x \) as 1200m, the compensation capacitor \( C \) as 25μF, the cable transmission length \( L_d \) as 10km. The transmission voltage is three levels (140V) and the ballast resistance \( R_g \) as 2Ω·km. Through MATLAB software simulation, the cab signal receiving voltage under different carrier frequencies is obtained, as shown in Figure 3. It can be seen that due to the influence of the compensation capacitor, the received voltage value of the cab signal fluctuates and rises from the receiving end, and a turning point appears at each compensation capacitor. Its function is to reduce line loss, increase transmission distance, and improve the reliability of information exchange between vehicles and ground. In addition, due to the difference in resistance...
value, inductance value and compensation capacitor spacing of the track circuit in different carrier frequency sections, the current received by the cab is lower in the section with higher carrier frequency.

\[
\begin{align*}
I_{a1} & = U_{a1} I_{a2} = U_{a2} \cdot I_{a} \\
I_{p1} & = U_{p1} \cdot I_{p2} = U_{p2} \cdot I_{p}
\end{align*}
\]

**Fig. 3.** Simulation results of cab signal receiving voltage.  
**Fig. 4.** the calculation model of adjacent line coupling interference.

### 3.2 Theoretical calculation of adjacent line interference modelling

Figure 4 shows the calculation model of adjacent line coupling interference. The calculation formula of mutual inductance coefficient \(M_{ap}\) of parallel running railway lines \(a\) and \(p\) is as follows:

\[
M_{ap} = (k \times 200 \times \ln \frac{D_{ap12} \times D_{ap21}}{D_{ap11} \times D_{ap22}}) \times 10^{-6}
\]

(4)

In the formula, \(k\) is the correction factor, which is related to the carrier frequency. Since the carrier frequency of the signal flowing through the rail is much less than 4 MHz, the mutual impedance between two parallel track circuits \(a\) and \(p\) can be calculated according to Carson's theory [10] as:

\[
Z_{ap} = jwM_{ap} = \pi^2 f \times 10^{-4} + j0.00289 f \ln \frac{D_e}{D_{ap}}
\]

(5)

Among them, \(f\) is the carrier frequency of the signal flowing through the rail; \(D_e = 660,000 \sigma_d / f\) is the equivalent depth of the line, \(\sigma_d\) is the earth conductivity (take the average, \(\sigma_d = 10^2 \Omega \cdot m\)). Then the mutual inductance voltage generated can be obtained as follows:

\[
\dot{U}_{p}^{(MI)} = Z_{ap} \dot{I}_{a} D_{ap}
\]

(6)

Capacitance coupling is also called electric field coupling. For the convenience of modelling, capacitance coupling and ballast resistance conduction are combined for analysis in Fig. 5. The interference voltage \(\dot{U}_{p}^{(CR)}\) generated by capacitance coupling and ballast resistance leakage conduction interference in the interfered loop is as follows:

\[
\dot{U}_{p}^{(CR)} = \dot{I}_{z1} \dot{Z}_{cr2} - \dot{I}_{z2} \dot{Z}_{cr4}
\]

(7)

Adjacent line interference will directly affect the value of cab signal entry current in the series circuit. \(\dot{U}_{a}\) is the interference voltage of the interfering loop to the interfered loop.
In order to study the influence of carrier frequency on adjacent line coupling interference voltage, the interference voltage of the interfered loop is simulated under the conditions of 1700Hz, 2000Hz, 2300Hz and 2600Hz carrier frequency respectively of the interfering loop. The simulation results are shown in Figure 6. It can be seen that when the carrier frequency is 2600Hz, the interference voltage to the interfered loop is the largest.

4 Index evaluation and safety protection of adjacent line interference

4.1 Index evaluation of adjacent line interference

The interference of adjacent lines is easy to cause the wrong action of on-board equipment, so it is necessary to evaluate the interference of adjacent lines according to the safety index of cab. When the interfered loop is in the adjustment state, the interference of adjacent lines shall not exceed the sensitivity of cab signal to prevent the "red light band" phenomenon; when the interfered loop is in shunting state, the interference from adjacent lines shall not exceed the reliable falling value of cab signal to prevent signal upgrading accident. Under the above calculation conditions, the maximum parallel length is shown in Table 1. If it exceeds the recommended value, it may cause equipment misoperation and increase the hidden danger of driving safety.

Table 1. Maximum parallel length under different carrier frequencies.

| carrier frequency (Hz) | adjustment state | shunting state |
|------------------------|------------------|---------------|
|                        | Maximum interference(mV) | Maximum parallel length(m) | Maximum interference(mV) | Maximum parallel length(m) |
| 1700                   | 92.5              | 712            | 69.4             | 446             |
| 2000                   | 92.5              | 623            | 69.4             | 383             |
| 2300                   | 92.5              | 551            | 69.4             | 363             |
| 2600                   | 92.5              | 533            | 69.4             | 314             |

4.2 Suggestions on adjacent line interference protection

The existence of adjacent line interference greatly improves the probability of locomotive signal upgrading or code dropping, and increases the hidden danger of train operation. Based on the above theoretical analysis, the following measures can be taken to protect the engineering design.

(1) Try to avoid the configuration of the same carrier frequency between adjacent lines;
(2) Appropriately increase the line spacing of lines with the same carrier frequency;  
(3) The tuning area is staggered to reduce the parallel length;  
(4) The spacing of compensating capacitors is different to reduce the area of equivalent loop.

5 Conclusion

This paper focuses on the problem of electromagnetic interference between adjacent lines in multi-line parallel section of high-speed railway. The specific conclusions are as follows:  
(1) In the context of high-speed railway, the main factors causing adjacent line interference is rail mutual inductance coupling, which can reach 100mV under the most unfavorable conditions.  
(2) From the perspective of normal operation of cab equipment, it is suggested to increase line spacing and reduce the parallel length between lines to reduce the interference of adjacent lines.

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