Design an Identification Function to Reduce the Computational Resources on the Testing Process of an Analog Electronic Circuit

Sebastian Temich, Tomasz Golonek, Damian Grzechca
Faculty of Automatic Control, Electronics and Computer Science, Institute of Electronics, Silesian University of Technology, Akademicka 16, 44-100, Gliwice, Poland
damian.grzechca@pols.pl

Abstract—In modern electronic circuits, imperfectness in the technological process can cause errors in reaching the correct values of the functional parameters. In order to solve this problem, a novel approach of analog and mixed-signal circuit testing methodology is used. The presented approach allows the testing complexity to be reduced and the testing time to be decreased. For this paper, selected signal features were designated from the transient output signal response. Using regression models with the extracted signal features, the functional parameters of a circuit were determined. An evolutionary determination of the regression models enabled the efficiency of the identification process to be maximized. The proposed methodology is presented for an exemplary CMOS Dickson charge pump circuit.

Index Terms—Dickson charge-pump; Functional test; Incipient faults; Evolutionary regression; CMOS circuit identification.

I. INTRODUCTION

The rapid developments in electronic circuit design have not been accompanied by the same degree of progress in analog and mixed-signal electronic maintenance [1]. The standard production test approach for analog and mixed-signal circuits is based on measuring the functional parameters that are given on the data sheet and comparing them to the designed specifications in order to make the correct (go/no-go) decision [2]–[6]. Some specification parameters are measured directly, while others require a dedicated infrastructure in order to be measured [2], [7], [8].

The fault diagnosis methods for analog electronic circuits (AEC) are usually classified depending on whether a simulation is carried out before or after the testing [1]. The parameter identification techniques provide the values of all of the circuit parameters within a faulty circuit.

The internal circuit variables as well as the inputs and outputs are generally continuous and are difficult to quantize. The testability of analog circuits also depends on the component parametric deviations and the influence of environmental factors (temperature, electromagnetic interference, etc.) whose impact is very important [9].

A wide range of methods for testing the functional parameters of analog circuits have been proposed in the literature. An optimization formulation approach is a commonly used tool that is based directly on the circuit structure [9]. It is intuitive and proves to be effective on many occasions. Other approaches use neural networks [4], [10]–[12], classification methods that are based on a support vector machine, etc. [13]. In addition to neural networks, other artificial intelligence algorithms are also commonly used to test the specification parameters: genetic algorithms [13]–[15], fuzzy logic systems [16], dependency models [14], etc.

In the classical approach for testing analog or mixed-signal systems to determine the selected parameters of the design specification in order to check them in the go/no-go, several dedicated devices, several different stimulus signals and several modifications of the tested system (load, no load, etc.) can be used (Fig. 1). Such an approach, even if it is fully automated, requires a great deal of time to decide about go/no-go on the circuit. In the testing methodology proposed in this paper, the classical approach is replaced by using the regression of selected characteristics of the output signal, which allows selected functional parameters to be identified. The proposed method enables only one test signal and testing circuit to be used in only one configuration (no load) (Fig. 1). In the presented approach, the evolutionary determination of the terms of the approximate polynomial enables the estimation accuracy of the functional parameters of the system to be increased.

Fig. 1. The concept of the proposed test compared to the classical approach.

Manuscript received 23 September, 2018; accepted 4 January, 2019.
This work was supported by the Ministry of Science and Higher Education for statutory activities.
Reducing the number of test signals enables the testing time of the system to be shortened, which is very important on a production line.

The presented method was implemented for a Dickson charge pump circuit constructed with CMOS technology for which production process errors were considered (incipient faults).

A. Circuit under Test

Charge pumps are power converters that adjust the power supply voltage to higher or lower constant voltages. Charge pumps transfer charge packets from the power supply to the output terminal using switches to generate the required voltage level. In micro-systems, charge pumps are usually fully built on-chip rather than off-chip. The Dickson charge pump circuit is presented in Fig. 2.

![Dickson charge pump circuit](image)

Fig. 2. Dickson charge pump circuit [18].

The functional identification of a Dickson charge pump should be focused on measuring the different parameters of the output signal for the selected stimuli [18]–[20]. In this research, the following functional parameters of the circuit being tested were analysed [18], [19], [21]:

- \( p_1 \) – gain of the circuit;
- \( p_2 \) – load-dependent losses (PLD) – measured with a 100 kΩ load;
- \( p_3 \) – power consumption.

Table I presents the nominal values of the Dickson charge pump functional parameters.

TABLE I. NOMINAL VALUES OF THE DICKSON CHARGE PUMP SPECIFICATION PARAMETERS.

| \( p_1 [/V] \) | \( p_2 [/mW] \) | \( p_3 [/mW] \) |
|---------------|---------------|---------------|
| 1.08          | 1.28          | 4.57          |

B. Technological Process Imperfectness

The circuit that was tested that is presented in this paper is based on MOS transistors (Fig. 2) for which incipient faults can occur at the production stage because of the imperfectness of the technological processes. The authors focused on the exemplary faults that are caused by the movement of the photolithographic mask, which changes the parametric values of the transistor structure – the length \( L \) and width \( W \) of a channel [13]. A change in the \( W \) and \( L \) of MOS transistors has a significant impact on the transistor specification parameters (i.e. the threshold voltage or channel length modulation parameter) [22], [23], which has affects the functional parameters of a Dickson charge pump.

In this research, the nominal values of length \( L \) and width \( W \) in MOS transistors are defined with \( L_{nom} \), \( W_{nom} \). The maximum range for the \( L \) and \( W \) parameters were set to:

\[
\begin{align*}
W & \in \langle W_{min}: W_{max} \rangle, \\
L & \in \langle L_{min}: L_{max} \rangle,
\end{align*}
\]

where \( W_{min}/W_{max} \) and \( L_{min}/L_{max} \) are calculated as:

\[
\begin{align*}
W_{min} / L_{min} & = W_{nom} / L_{nom} \times 0.95, \\
W_{max} / L_{max} & = W_{nom} / L_{nom} \times 1.05.
\end{align*}
\]

In the entire range from \( W_{min}/L_{min} \) to \( W_{max}/L_{max} \), the parameters of the length and width of the transistor channel are calculated as

\[
W_{i} / L_{i} = W_{nom} / L_{nom} \times \delta_l,
\]

where \( \delta_l \in \{0.95, 0.96, 0.97, \ldots, 1.05\} \).

The above change in the technological parameters of the transistors permits a precise determination of any changes in the selected functional parameters (performance) of a CUT as a function of incipient faults.

C. Simulation of a CUT

The Dickson charge pump was modelled using Spice software and the transistor models are represented by a BSIM numerical description [24].

The transistors from Fig. 2 are represented by the BSIM models of 50 nm technology; the \( L \) and \( W \) values are listed in Table II.

TABLE II. LENGTH AND WIDTH OF THE TRANSISTOR CHANNELS.

| \( L \) of the channel | \( W \) of the channel | \( L \) of the channel | \( W \) of the channel |
|------------------------|------------------------|------------------------|------------------------|
| M1                     | 50 n                   | 500 n                  | M11                    | 200 n                   | 200 n                   |
| M2                     | 50 n                   | 100 n                  | M12                    | 200 n                   | 200 n                   |
| M3                     | 50 n                   | 100 n                  | M13                    | 200 n                   | 200 n                   |
| M4                     | 50 n                   | 500 n                  | M14                    | 50 n                    | 250 n                   |
| M5                     | 50 n                   | 100 n                  | M15                    | 50 n                    | 500 n                   |
| M6                     | 50 n                   | 100 n                  | M16                    | 50 n                    | 250 n                   |
| M7                     | 50 n                   | 100 n                  | M17                    | 50 n                    | 500 n                   |
| M8                     | 200 n                  | 200 n                  | M18                    | 50 n                    | 500 n                   |
| M9                     | 200 n                  | 200 n                  | M19                    | 50 n                    | 500 n                   |
| M10                    | 200 n                  | 200 n                  | M20                    | 200 n                   | 200 n                   |

At the simulation stage, the CUT was stimulated using a pulse waveform that ranged from 0 V to 3.3 V with a rising time \( t_r = 10 \) ps a falling time \( t_f = t_r \) and a period time \( T_{period} = 20 \) ns, which were delivered to the ‘INPUT’. In addition, the circuit was sourced by a 3.3 VDC that was delivered to the ‘VDD’ as is illustrated in Fig. 3.

![Exemplary responses of the Dickson charge pump circuit](image)

Fig. 3. Exemplary responses of the Dickson charge pump circuit.

In accordance with the assumptions of the SBT method [14], the presented circuit, along with any subsequent...
failures was simulated once, and all of the data that was collected during the simulation process formed the basis for further analysis using the presented method.

D. Output Signal Feature Selection

The main goal of the approach that is presented in this paper is to shorten the test time while simultaneously maximizing the identification accuracy using the evolutionary determined regression model. For this purpose, the \( N = 7 \) signal features \( (x_1, ..., x_N) \) from the \( u_{out}(t) \) for the selected stimuli \( u_{in}(t) \) were designated. The designated features were [25], [26]:

1. Rising time of the \( u_{out}(t) (T_R) \) – Time that the \( u_{out}(t) \) required to rise from 10% to 90% of the steady-state response;
2. The peak value of the \( u_{out}(t) (Peak_{out}) \);
3. Settling time of the \( u_{out}(t) (S_{out}) \) – Time after which the ripples of the \( u_{out}(t) \) are less than 2%;
4. A variance of the \( u_{out}(t) (Var_{out}) \) – The time after which the ripples of the \( u_{out}(t) \) are less than 2%;
5. Settling minimum \( (S_{min}) \) value of the \( u_{out}(t) \);
6. Settling maximum \( (S_{max}) \) of the \( u_{out}(t) \);
7. Mean value of the span of the \( u_{out}(t) (M_{out}) \), calculated as

\[
M_{out} = \frac{S_{max} + S_{min}}{2}. \tag{4}
\]

The selected features of the \( u_{out}(t) \) created a feature vector \( X \) for each of the simulated cases

\[
X = [T_R, Peak_{out}, S_{out}, Var_{out}, S_{min}, S_{max}, M_{out}], \tag{5}
\]

on the basis of which a regression model to identify the selected functional parameters of the Dickson charge pump was created.

E. Time Analysis

In order to determine the most accurate mapping of the functional parameters of an AEC, the time of the test should also be as short as possible.

The overall time of a test is equal to the sum of the times of the individual stages. In the classical approach, this is the time required to analyse the selected parameters of the \( u_{out}(t) \) using external measuring devices, to modify the circuit to enable the measurement of the duty parameter that is characteristic for a given work point, etc. In the presented approach, the test time \( (T_{overall}) \) was equal to the sum of the times of all of the subsequent steps: the time required to determine all the features of the \( u_{out}(t) \) – \( T_f \) and the time required to identify selected functional parameters \( T_{p1}, T_{p2}, T_{p3} \)

\[
T_{overall} = T_f + T_{p1} + T_{p2} + T_{p3}. \tag{6}
\]

The AEC that was used to verify the correctness of the test specification methodology is presented in Fig. 4. The analog circuit response signature \( u_{out}(t) \) was acquired directly after its stimulation during the test stage using a dedicated signal \( u_{in}(t) \). Typically, a step or pulse stimulus as well as square waveform can be applied to the AEC to test the excitation. These kinds of signals can be generated precisely in a simple way and they ensure rich spectra of the frequency components that will explore the signal path of the tested circuit well.

II. ANALOG ELECTRONIC CIRCUIT SPECIFICATION TESTING

A crucial point of the proposed testing methodology is determining the most appropriate \( M \) mathematical models, i.e. the functions \( f_1(X), f_2(X), ..., f_M(X) \) that will ensure precise and quick estimations of the tested performance parameters of an AEC, i.e. at a low cost and less time-consuming laboratory measurements. In the proposed solution, these models are non-linear ones and they have the structure of the sum of \( K \)th order regression polynomials, which are explained in detail in Section II. The coefficients (terms) of these polynomials were searched before test stage in the dedicated evolutionary system that is described in Section III. For the proposed approach, the main computational effort is only required before the exact testing (i.e. only at the before test stage) during the evolutionary search for the mathematical models. However, at the specification test stage, they are known and the mathematical estimators that are found ensure a high-quality, low-cost and very quick procedure. Finally, when all of the parameters that have been estimated in this way fulfill the assumed design restrictions (specification), then a verified AEC successfully passes the test or it is determined to be faulty ([go/no go decision]). Another advantage of this technique is the full identification of the complete set of the tested parameters. This property permits the automatic classification of the AECs that are produced to the quality classes according to the performances are actually achieved.

III. STRUCTURE OF THE REGRESSION MODELS

The proposed polynomial regression models are mathematical relationships between the assumed features (collected in vector \( X \) of the registered test signature \( u_{out}(t) \) and the set \( P \) of the \( M \) performance parameters of the AEC that is to be verified

\[
P = [p_1, p_2, \ldots, p_M]. \tag{7}
\]
This kind of relationship, which is designated during the before test computations, permits an AEC tester that is oriented to the verification of the specification using the precisely estimated actual values of the performance parameters that are observed at the testing stage to be created. The main advantage of this concept is the possibility of an easy and quick specification testing of the realization of an AEC using a low-cost tester that can be implemented in a mixed-signal system in a simple way. This capability also makes it possible to reach the build in self-testing (BIST) procedure of the controlled electronic system.

The general mathematical formula of the proposed regression model that was created to estimate the m-th (m = 1, ..., M) AEC performance parameter is

\[ p_m(X) = \sum_{n=1}^{N} \sum_{k=1}^{K} a_{mnk} \times x_n^k + b_m, \]  

where \( X \) is the vector that consists of the \( N \) features that were extracted from the time test signature (defined initially to the investigated parameter \( p_m \) explanation) and \( a_{mnk} \) and \( b \) are the variable elements and constant terms of the K-th order approximation polynomial, respectively. During the AEC testing stage, the time response to the assumed testing excitation is acquired only once and its selected \( N \) features (such as the rise time, overshoot, steady-state level) create the explanatory variable vector \( X \)

\[ X = [x_1, x_2, ..., x_N]. \]  

The elements of (9) can be defined based on an engineer’s experience and knowledge about the classes of the explained performance parameters (e.g. step response rise time is correlated to the bandwidth well and the steady-state DC voltage level describes the DC gain of the tested AEC well); they can be also selected experimentally. As can easily be noticed from the expanded form of the model (10)

\[ p_m(X) = a_{m1}x_1^K + \ldots + a_{m1}x_1^1 + b_m + a_{m2}x_2^K + \ldots + a_{m2}x_2^1 + b_m + \ldots + a_{mN}x_N^K + \ldots + a_{mN}x_N^1 + b_m, \]

Each element of the explanatory variables that are collected in (9) is an argument of the dedicated individual polynomial that is a part of the complex formula that is able to model strongly non-linear relationships. This property makes it possible to create mathematical equations respective to describing tested parameter \( p_m \) in a wide range of variations. The above model (10) must be defined independently for all of the diagnosed \( M \) performance parameters, so that finally, the tester is completely oriented to the requirements of the specification validation

\[ L = M \times (N \times K + 1), \]

coefficients where \( N \) and \( K \) are the power of set (9) and the assumed order of the polynomials, respectively.

IV. EVOLUTIONARY SEARCHING FOR OPTIMAL MODELS

The evolutionary computations have been applied to many difficult tasks successfully [5, 15, 27]. Although these kinds of calculations are heuristic, they are a powerful tool for probing and searching a huge data space for the most appropriate solution of problems that are tedious for performing deterministic analysis in a reasonable time. The evolutionary system that is described in this paper applies differential evolution (DE) to the multidimensional equations (9) determination [28]. The genotype structure and evolutionary computation scheme are illustrated in Fig. 5. The optimization of the regression polynomial coefficients proceeds in a population of \( I \) individuals, which keep \((N\times K + 1)\) real numbered values. During the start (primary) generation creation, the linear section terms of (10) (i.e. \( a_{m11}, \ldots, a_{mN1} \) and \( b_m \)) of the first genotype are initialized to the values that were obtained from the standard algorithm of linear multiple regression, respectively [28]. However, the remaining terms (i.e. for the squared and higher order variables) are initialized with respective values that are dispersed randomly from those that were calculated as linear ones. This solution enables a good start point for the exploring the search space to be defined and, as a result, it improves the convergence to an optimal solution speed. After completing the primary population genes, the fitness value \( Q_m \) is calculated for each one. The coefficient of determination (squared R) is applied in the designed evolutionary system as a measure of the quality of the searched relationships (phenotype) that were determined

\[ Q_m = R_m^2 = 1 - \frac{\sum_{j=0}^{I-1} \left( p_{mj} - \tilde{p}_{mj} \right)^2}{\sum_{j=0}^{I-1} \left( p_{mj} - \bar{p}_m \right)^2}, \]

where \( m \) is the index of the tested AEC performance parameter, \( p_{mj}, \tilde{p}_{mj} \) are its accurate, estimated (obtained from the evaluated formula) values and \( \bar{p}_m \) is the average value of the parameter in the set of \( J \) training patterns.

The nominator and denominator in function (12) are the sum of squared errors and the total sum of squares that were calculated in the training set. This function is a standard measure of fitness between the analysed function and the statistical data, which reaches a unit value for the ideally defined relationship using an analytical form of the mathematical model. Next, when the primary population is ready, the DE algorithm is run for the \( T \) generations for which each offspring genotype is created in the way that is illustrated in Fig. 2. In order to obtain a new individual, the processed \( i \)-th parental genotype \( G_i \) is crossed over with \( \tilde{G}_i \) one that was obtained after the recombinaction of the differential material

\[ \tilde{G}_i = G_i + w \times (G_{hi} - G_i) + (1-w) \times (G_{r1} - G_{r2}), \]

where \( G_{hi}, G_{r1}, G_{r2} \) are the best ones that were found during all of the previous iterations and two randomly selected genotypes; however, \( w \) is the weight that balances the
recombination levels from the specific parts. During the crossover operation, the original genes can be replaced by the new ones (i.e. with a 0.5 probability), which is calculated as the arithmetic average values between the genes of \( G_i \) and \( \hat{G}_i \), respectively. This procedure allows some of capabilities of the best currently known solution as well as some of the new characteristics from the random ones to be inherited (a kind of mutation process). The last step is the selection of better evaluated genotype code for the child, i.e. preserved, the original one (without any modifications) or the one that was obtained after the crossover, which can be added to the offspring population, respectively. When the whole newly gathered generation is ready, it replaces the old one during succession. Finally, after \( T \) cycles of the evolutionary system, the best found genotype code vectors of the coefficients are ready to estimate the tested parameter with the highest score level (12).

![Diagram of the evolutionary system cycle.](image)

The training patterns that are used to determine the fitness function are the simulation results of the tested AEC, which are obtained after Monte Carlo analysis of the circuits with the assumed perturbations (e.g. with the dimensions of the structures of integrated on a silicon die elements dispersed randomly), which imitate the production or post-production defects of the circuit. This set of \( J \) before test analysis results defines the signatures of the AEC with various levels of the observed performance parameters \( p_m \) (\( m = 1, \ldots, M \)), which makes it possible to investigate the statistical relationships between them and the delegated easily measurable explanatory features of the time response signature. Besides, it should be emphasized that the all of the simulation calculations are executed during the testing formula (8) designing stage only; however, the resulting specification test requires only \( L-1 \) multiplications and \( L \) summations, respectively, i.e. the test computational cost is low and is related to (11).

V. RESEARCH RESULTS

The following section presents the results of the proposed method for identifying parameters. All of the simulations were performed on a computer with the following specifications: Intel Core i5 3.2 GHz with 8 GB RAM. For identifying all of the parameters, the 3rd order polynomial was used. According to (11): \( K = 3 \), \( N = 7 \) and \( M = 3 \), the evolutionary calculations were started for \( T = 2000 \) iterations and \( J = 200 \) individuals in the population. Searching for each mathematical formula (10) using the specialized terms to identify a specific tested parameter took about 20 minutes.

After identifying the selected functional parameters, a \( \delta \) range of the correct parameter tolerance was imposed on each of the \( p_i \). This process allowed the accuracy of classification for each of the functional parameters of the presented method to be determined. The described method was also compared to the support vector regression model (SVR) with a Gaussian kernel function [28].

A. Gain Parameter

The first functional parameter for the proposed algorithm to identify was the gain parameter. By analysing the changes in parameters \( L \) and \( W \) of the transistor, the linear dependence of the effect of any damage to the photolithographic mask in a tolerance range equal to 5 % of the value of the parameter \( p_1 \) should be observed. In connection with the above, for a value of parameter \( p_1 \) inside the tolerance range, a correlation coefficient was determined and it was equal to 98.72 %.

In the next stage, a 3rd order polynomial was created to increase the identification efficiency. For the gain parameter, the determination of (10) error was equal to \( \text{Err} = 0.08 \% \).

The time required to obtain the identity parameter was equal to \( T_{\text{id}} = 1.87 \mu\text{s} \).

In accordance with (10), 22 coefficients were determined for 3rd order polynomial. Each of their values is presented in (14):

\[
\begin{align*}
h_0 &= 410, \\
\alpha_{ij} &= \{0.02, -7394, 0.31; -0.0033, 0, -2221, 0, -2605, -3016; -0.40, 7e^{-3}, 0, 1424, 0, 2380, 4546, 0.47, 3e^{-3}; 0, -702.4, 0\}.
\end{align*}
\]

Table III presents the exemplary values of \( p_1 \) that were calculated by the proposed algorithm compared to the expected values.

Figure 6 presents the differences between the calculated and expected value of the gain functional parameter for the length deviation. As can be seen, the differences between the expected and calculated values are almost unnoticeable.
The correlation between the obtained identification and correct identification is presented in Fig. 7. The differences between the respective values were unnoticeable.

Because of the classification process, the nominal value of the $p_1$ parameter for an undamaged circuit is equal to $Gains_{nom} = 1.08 \, \text{V/V}$. By imposing a $\delta_1 = 2\%$ $p_1$ tolerance range, the error matrix and sensitivity values for the proposed method were determined. Proposed method was compared to the SVR method. The results are presented in Table IV. The maximum error (for 3rd order polynomial) of identification gain parameter inside tolerance range was equal to 0.16 \%.

**TABLE IV. CONTINGENCY TABLE WITH SENSITIVITY VALUE FOR $p_1$**

| Error matrix | Proposed method | SVR |
|--------------|-----------------|-----|
| True positive| 2449            | 2394|
| False negative| 18             | 73  |
| Sensitivity [%] | 98.73       | 97.04|

**B. Load-dependent Losses**

The second functional parameter to identify was the load-dependent losses. Similar to the $p_1$ functional parameter, for the $p_2$, changes in parameters $L$ and $W$ of the transistor have a linear dependence on the effect of damage to the photolithographic mask in a tolerance range equal to 5 \%.

The correlation coefficient that was determined for $p_2$ was equal to 97.78 \%. Next, a 3rd order polynomial was created to increase the identification efficiency. For this parameter, the determination error of relationship (10) was equal to $Err = 0.09 \%$.

The time required to obtain the $p_2$ was equal to $T_{p2} = 1.5 \, \mu s$. In order to determine parameter $p_2$ in the classical approach, it is necessary to modify the AEC by adding an appropriate load to the circuit output. This treatment extends the time of testing the $p_2$ by the time that is required for an additional stimulation $u_{stim}(t)$, and an additional extraction of the features of $u_{stim}(t)$. This time may increase up to two-fold compared to the described method.

In accordance with (9), (15) presents the polynomial coefficients:

\[
\begin{align*}
&b_0 = -0.1/2\Delta,
& a_{ij} = [-1e^{-4}, -26.95, 6e^{-5}; 1.7e^{-5}; 0.4, 4.44, \\
&0, -0.58, 32.82; -5e^{-4}, -5e^{-7}; 0; -2.16, 0, -0.15, \\
&-40.28, 2.5e^{-5}, 2.3e^{-5}; 0, 4.73, 0].
\end{align*}
\]

Figure 8 presents the differences between the calculated and expected values of the load-dependent functional parameter for the length deviation of an incipient fault. The difference between these two values was as small as the gain parameter and the deviations were also minor.

Because of the classification process, the nominal value of the $p_2$ parameter for an undamaged circuit is equal to $Gains_{nom} = 1.08 \, \text{V/V}$. By imposing a $\delta_1 = 2\%$ $p_1$ tolerance range, the error matrix and sensitivity values for the proposed method were determined. Proposed method was compared to the SVR method. The results are presented in Table IV. The maximum error (for 3rd order polynomial) of identification gain parameter inside tolerance range was equal to 0.16 \%.

**TABLE III. EXEMPLARY VALUES OF THE EXPECTED AND CALCULATED GAIN SPECIFICATION PARAMETER.**

| Exemplary Difference between the expected and calculated gain value | Expected | Calculated |
|------------------------------------------------------------------|----------|------------|
| zi                                                              | 0.725    | 0.721      |
| $b_0$                                                            | 40.28    | 40.29      |
| $a_{ij}$                                                         | -32.82   | -32.83     |
| $\delta$                                                         | 2.5      | 2.51       |
| $\Delta$                                                         | 2.3      | 2.29       |
| $\epsilon$                                                       | 0        | 0.15       |
| $\epsilon_0$                                                     | 4.73     | 4.73       |

**Fig. 6.** Difference between the calculated and the expected gain parameter for the length deviation of the photolithographic mask.

**Fig. 7.** Correlation between the correct and obtained identification of the gain parameter.

**Fig. 8.** Differences in the calculated and expected values of the load-dependent functional parameter for the length deviation of the photolithographic mask.

The correlation between the obtained identification and correct identification is presented in Fig. 9. As the figure shows, the differences between the respective values were minimal.

**Fig. 9.** Correlation between the correct and obtained identification of the $p_2$ parameter.
Because of the classification process, the nominal value of the PLD parameter for an undamaged circuit was equal:  $PLD_{\text{nom}} = 4.57 \, \text{mW}$. By imposing a $\delta_5 = 2 \%$ $p_2$ tolerance interval, a contingency table and sensitivity value for the proposed method were determined. The results with comparison to the SVR method are presented in Table V. The maximum error of $p_2$ identification inside the tolerance range was equal to 16 %.

### TABLE V. CONTINGENCY TABLE WITH THE SENSITIVITY VALUE FOR $P_2$.

| Error matrix | Proposed method | SVR |
|--------------|----------------|-----|
| True positive | 1258           | 1254 |
| False negative | 11           | 15 |
| Sensitivity [%] | 99.13         | 98.82 |

### C. Power Consumption

The third functional parameter to identify was power consumption. Similar to the previous functional parameters, the linear dependence of the effect of damage to the photolithographic mask in a tolerance range equal to 5 % for $L$ and $W$ were determined. The determined correlation coefficient of $p_3$ was equal to 88.8 %. Next, a 3rd order polynomial was created to increase the identification efficiency. For this parameter, the error in the determination was equal to $\text{Err} = 1.06 \%$.

The time required to obtain $p_3$ was equal to $T_{p3} = 1.78 \, \mu\text{s}$. In accordance with (9), (16) presents polynomial coefficients:

\[
\begin{align*}
\lambda_3 &= 661.4, \\
\alpha_3 &= \{4.195, -2642, 0.1;2.6e^{-3}, 0, 2328, \\
0, -4672, -7739;1.7e^{-3}, 0.6e^{-3}, 0; 1127, 0, -398.4, \\
-2928, -0.013, 6e^{-3}; 0, -223.1, 0\}. \\
\end{align*}
\]  

(16)

Table VI presents the exemplary values of $p_3$ that were calculated using the proposed algorithm compared to the expected values.

### TABLE VI. EXEMPLARY VALUES OF THE EXPECTED AND CALCULATED SPECIFICATION PARAMETER.

| Expected | Calculated |
|----------|------------|
| 4.61 mW  | 4.60 mW    |

Figure 10 presents the difference between the calculated and expected value of the power consumption functional parameter for the width deviation of the photolithographic mask. The difference between these two values is not as small as for the gain parameter but the deviations were also minor.

The correlation between the obtained identification and correct identification is presented in Fig. 11. As the figure shows, the differences between the respective values were noticeable and an offset error was observed for the analysed cases.

Because of the classification process, the nominal value of the $p_3$ parameter for an undamaged circuit was equal:  $P\text{Disp}_{\text{nom}} = 1.28 \, \text{mW}$. By imposing a $\delta_3 = 0.5 \%$ $p_3$ tolerance interval, a contingency table and sensitivity value for the proposed method were. The results with comparison to the SVR method are presented in Table VII. The maximum error of $p_3$ identification inside the tolerance range was equal to 0.802 %. In accordance with (12), Table VIII presents $Q_{\alpha \beta}$ value of the identification process.

### TABLE VII. CONTINGENCY TABLE WITH THE SENSITIVITY VALUE FOR $P_3$.

| Error matrix | Proposed method | SVR |
|--------------|----------------|-----|
| True positive | 6834           | 6843 |
| False negative | 253           | 231 |
| Sensitivity [%] | 96.43         | 96.73 |

### TABLE VIII. EXEMPLARY VALUES OF THE EXPECTED AND CALCULATED SPECIFICATION PARAMETER.

| Specification parameter | Coefficient of the determination value [%] |
|-------------------------|------------------------------------------|
| Gain                    | 99.92                                    |
| Load dependent losses   | 99.91                                    |
| Power consumption       | 98.94                                    |

Table IX presents the time required to complete the individual steps of the presented method.

### TABLE IX. TIME VALUES OF THE COMPONENTS OF THE PRESENTED METHOD.

| Time to determine each of the elements | Time |
|---------------------------------------|------|
| $T_{f}$                               | 9ms  |
| $T_{rd}$                              | 1.87μs |
| $T_{al}$                              | 1.5μs |
| $T_{d}$                               | 1.78μs |
| $T_{\text{total}}$                    | 9.05μs |
element and to stimulate it with $u_{in}(t)$ once again. The features of vector $X$ also have to be appointed. In this case, the time for test using the classical approach is equal to $T_{\text{overall}} = T_{\text{init}} + T_f + T_{p2}$ and is twice as long as in the proposed method.

D. Proposed Method Efficiency Testing

![Graph showing the function $p_{test}(x_1, x_2)$](image)

Fig. 7. Results of the exemplary, benchmark function optimization.

Optimization convergence of nonlinear mathematical model by means of the proposed evolutionary technique may be confirmed by a simple experiment. The exemplary test function characterized with strong nonlinear relation between explanatory and explained variables may be searched within wide range of their arguments variation. For this experiment purpose, the representative, exemplary function was delegated

$$p_{test}(x_1, x_2) = e^{x_1} + \sqrt{x_2} + \sin(2\pi x_1 x_2). \quad (17)$$

Next, the algorithm described in this paper was started with $I = 200$ individuals in population for $T = 2000$ iterations. The 200 values of arguments $x_1 = x_2 = x$ dispersed randomly with uniform probability in assumed probing range $0, 1$ were sampling tested function (17). The effects of approximation with $9^\text{th}$ ordered model (10) of the gathered points may be seen in Fig. 12, where dashed and solid lines curves are precise and estimated ones respectively. Concluding, the function determined by multiple linear regression (dashed-dotted line) has quite low determination coefficient 0.543 of the tested reference one, however the found nonlinear model is very well fitted with 0.998 value of quality (12).

VI. CONCLUSIONS

A specification-driven testing approach is proposed in this paper. Multiple regressions supported by evolutionary computations were used to identify the selected functional performance of a Dickson charge pump circuit.

The selection of the appropriate features of the signal circuit response $u_{out}(t)$ to the proposed stimuli $u_{in}(t)$ enabled the time required to test the signature analysis to be shortened: The time needed to obtain the last feature of the $u_{out}(t)$ is equal about 0.7 $\mu$s and the overall time needed to identify all selected functional parameters value is equal 9.05 ms. Proposed methodology allow to identify 3 functional parameters of the CUT (gain, load-depend losses and power consumption), with 98 % of the average sensitivity inside the tolerance range.

The proposed methodology is easy to implement on a production line. The polynomials, which were created using a regression algorithm, consist of only multiplications and additions, which enables the basic circuits of microcontrollers to be used.

REFERENCES

[1] S. D. Djordjevic, “Analog circuit diagnosis based on the nullor concept and multipoint description of the circuit”, Analog Integrated Circuits and Signal Processing, vol. 95, no. 1, pp. 141–149, 2018. DOI: 10.1007/s10470-018-1123-7.

[2] S. D. Djordjevic, “Analog circuit diagnosis based on the nullor concept and multipoint description of the circuit”, Analog Integrated Circuits and Signal Processing, vol. 95, no. 1, pp. 141–149, 2018. DOI: 10.1109/TCAD.2017.2783302.

[3] M. J. Barragan, H.-G. Stratigopoulos, S. Mir, H. Le-Gall, N. Bhargava, A. Bal, “Practical simulation flow for evaluating analog/mixed-signal test techniques”, IEEE Design & Test, vol. 33, no. 6, pp. 46–54, 2016. DOI: 10.1109/MDAT.2016.2590985.

[4] S. Temich, D. Grzechca, “Application of neural network for testing selected specification parameters of voltage-controlled oscillator”, Int. J. Electron. Telecommun., vol. 64, no. 2, pp. 203–207, 2018. DOI: 10.24425/119371.

[5] T. Golonek, J. Machniewski, “Analog circuit specification testing by means of Walsh-Hadamard transform and multiple regression supported by evolutionary computations”, Circuits Syst. Signal Process., vol. 37, no. 7, pp. 2736–2771, 2018. DOI: 10.1007/s00034-017-0708-1.

[6] L. Chruszczyk, “Automatic test bench for selected transmission parameters of power line conductors”, Int. J. Electron. Telecommun., vol. 61, no. 1, pp. 59–65, 2015. DOI: 10.1515/eletel-2015-0008.

[7] M. Tadeusiewicz, S. Halgas, “Diagnosis of soft spot short defects in analog circuits considering the thermal behaviour of the chip”, Metrol. Meas. Syst., vol. 23, no. 2, 2016. DOI: 10.1515/mams-2016-0023.

[8] S. Halgas, M. Tadeusiewicz, “Improvement of the search method for parametric fault diagnosis of analog integrated circuits”, in Proc. 23rd Int. Conf. Mes. Des. Integr. Circuits Syst. M., 2016, pp. 359–362. DOI: 10.1109/MIXDES.2016.7529765.

[9] L. Ji, X. Hu, “Analog circuit soft-fault diagnosis based on sensitivity analysis with minimum fault number rule”, Analog Integr. Circuits Signal Process., vol. 95, no. 1, pp. 163–171, 2018. DOI: 10.1007/s10470-018-1111-7.

[10] Y. Deng, Y. Shi, W. Zhang, “Diagnosis of incipient faults in nonlinear analog circuits”, Metrol. Meas. Syst., vol. 19, no. 2, pp. 203–218, 2012. DOI: 10.2478/v10178-012-0018-7.

[11] D. Grzechca, “Soft fault clustering in analog electronic circuits with the use of self organizing neural network”, Metrol. Meas. Syst., vol. 18, no. 4, pp. 555–568, 2011. DOI: 10.2478/v10178-011-0054-8.

[12] A. Vasan, B. Long, M. Pecht, “Experimental validation of LS-SVM based fault identification in analog circuits using frequency features”, Lect. Notes Mech. Eng., 2014. DOI: 10.1007/978-1-4471-4993-4_54.

[13] S. Temich, L. Chruszczyk, D. Grzechca, “Identification of the specification parameters for a voltage controlled oscillator using an artificial neural network with a genetic algorithm”, Elektrotechnika ir Elektrotechnika, vol. 24, no. 6, pp. 42–49, 2018. DOI: 10.5755/j01.ele.24.6.20945.

[14] S. P. Karthi, M. Shanithi, M. C. Bhuvaneswari, “Parametric fault diagnosis in analog circuit using genetic algorithm”, in Int. Conf. Green Computing Communication and Electrical Engineering (ICCGCEE 2014), Coimbatore, India, 2014, pp. 1–5. DOI: 10.1109/ICCGCEE.2014.6921410.

[15] T. Golonek, J. Rutkowski, “Genetic-algorithm-based method for optimal analog test points selection”, IEEE Trans. Circuits Syst. II Express Briefs, vol. 54, no. 2, pp. 117–121, 2007. DOI: 10.1109/TCSIL.2006.884112.

[16] D. E. Grzechca, “Construction of an expert system based on fuzzy logic for diagnosis of analog electronic circuits”, Int. J. Electron. Telecommun., vol. 61, no. 1, pp. 77–82, 2015. DOI: 10.1515/eletel-2015-0010.

[17] R. J. Baker, CMOS: Circuit Design, Layout, and Simulation, Hoboken, NJ, USA: John Wiley & Sons, Inc., 2010. DOI: 10.1002/9780470891179.

[18] H. Dubich, V. Maura, K. Verna, S. Jaiswal, “Design and analysis of different type of charge pump using CMOS technology”, in Int. Conf. Advances in Computing, Communications and Informatics
[19] S. R. Anumula, “Fully integrated CMOS charge pump design”, M.S. thesis, Dept. Electrical and Computer Engineering, Univ. Texas at Austin, Austin, Texas, USA, p. 63. [Online]. Available: repositories.lib.utexas.edu/handle/2152/ETD-UT-2010-08-1852

[20] T. Hillebrand, N. Hellwege, N. Heidmann, S. Paul, D. Peters-Drolshagen, “Charge-based stochastic aging analysis of CMOS circuits”, in IEEE International Integrated Reliability Workshop (IIRW 2015), South Lake Tahoe, CA, USA, 2015, pp. 126–129. DOI: 10.1109/IIRW.2015.7437084.

[21] S. Johnson, “Design for a discrete charge pump”, Texas Instruments. [Online]. Available: http://www.ti.com/lit/an/slva398a/slva398a.pdf

[22] C. L. Singh, A. J. Gogoi, C. Anandini, K. L. Baishnab, “Low-noise CMOS differential-amplifier design using automated-design methodology”, in Devices for Integrated Circuit (DevIC 2017), Kalyani, India, 2017, pp. 326–330. DOI: 10.1109/DEVIC.2017.8073962.

[23] Z. Liu, S. K. Chaganti, D. Chen, “Improving time-efficiency of fault-coverage simulation for MOS analog circuit”, IEEE Trans. Circuits and Systems I: Regular Papers, vol. 65, no. 5, pp. 1664–1674, 2018. DOI: 10.1109/TCSI.2017.2751561.

[24] T. H. Morshed et al., “BSIM4+4. 7 MOSFET model”, Dept Elect Eng Comput Sci Univ Calif, Berkeley Berkeley CA USA Tech Rep, 2011. [Online]. Available: http://cmosedu.com/cmosi/BSIM4_manual.pdf

[25] Control System Toolbox Documentation. [Online]. Available: https://www.mathworks.com/help/control

[26] J. Ardizzoni, “High-speed time-domain measurements—practical tips for improvement”, p. 6, 2007. [Online]. Available: https://www.analog.com/en/analog-dialogue/articles/high-speed-time-domain-measurements.html

[27] K. Price, R. M. Storn, J. A. Lampinen, Differential evolution: a practical approach to global optimization. Berlin Heidelberg: Springer-Verlag, 2005. DOI: 10.1007/3-540-31306-0.

[28] A. Sen, M. Srivastava, Regression Analysis. New York, NY: Springer New York, 1990. DOI: 10.1007/978-1-4612-4470-7.