Nonlinearity Analysis of Quantum Capacitance and its Effect on Nano-Graphene Field Effect Transistor characteristics

Declaration

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Nonlinearity Analysis of Quantum Capacitance and its Effect on Nano-Graphene Field Effect Transistor characteristics

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Abstract: A simple, compact, and fundamental physics-based quasi-analytic model for Single layer graphene field effect transistors (GFETs) with large area graphene is presented in which the quantum mechanical density gradient method is utilised. The basic device physics of the two-dimensional (2D) graphene channel is studied analytically. This modeling leads to the precise drain current calculation of the GFETs. The drain current calculation for GFETs starts from charge carrier concentration, its density of states and quantum capacitance (QC). QC depends on the channel voltage as a function of gate to source voltage $V_{gs}$ and drain to source voltage $V_{ds}$ primarily. The formulation of the drain current with velocity saturation has been done by the Monte Carlo simulation method. The performance of the analytical GFETs model is present the precise values of QC, its impact on drain current and transfer as well as output characteristics. The impact of QC at nanometer technology adds the nonlinearity to characteristics curves. The proposed method provides better results as compared with the previous analytical and simulated results.

Keywords – quasi-analytical model, quantum capacitance, GFETs, nonlinearity

I. INTRODUCTION

Heart of the electronic devices is always the ability of a particular material to control its electronic properties in the presence of another electric field. Since the semiconductor industry is already out of scaling limits for silicon-based devices. So, this industry needs the next excellent alternate of silicon. Graphene, after its first physically stable noble experimentation in 2004 is promising to be the next alternate of silicon. Graphene is a single atomic (carbon) thin or thick layer (~3.34 x 10^{-10} m) so it is called as 2-D material [1-5], along with high charge carrier mobility [2] of value more than 10^5 cm^2/V-sec and even thermodynamically stable. The CVD grown graphene layer and graphene growth over base other then silicon could be a possible option for high frequency and analog electronic devices and circuits [4-5]. The very first physical demonstration of field emission effect in graphene layers comes in the form of an experimental device in 2007 [6]. As the technology advancement is very fast, in few next years interesting GFETs models with the intrinsic transit frequency and the maximum frequency of GFETs operation is comparable and higher than similar size CMOS nanometer technology [7-10]. As the current saturation effect in a single layer [11] and bilayer graphene [12], with a high value of transconductance and transistor resistance directly affect the GFETs performance. Gate/channel length of graphene has an impact on the operating frequency of the proposed GFETs model, therefore, besides the high resistance and
transconductance values, graphene RF circuits find their application in defense in real life, so this is the main focus of this particular work. Other important, compacts and fundamental physics-based quasi-analytic modelling for (GFETs) with large area graphene is also given in literature [13]. Some other work like GFETs modelling based on basic physics explores drain current, capacitance, and charge parameters for graphene channel [14-17]. Explicit compact model of drain current [14] has been extended in [15] to explore AC and transient behaviour of the GFETs. Both of these physical framework is field effect model based on drift-diffusion charge carrier transport mechanism. The Ward-Dutton charge partition scheme is implemented to model the source-drain charge together with transconductance and self-capacitance. These [16-17] compact model is benchmarked with measured current-voltage characteristic with accuracy for prototype devices and predictive behaviour. Also, few comprehensive simples and compact mathematical model provides graphene field effect transistor’s physical parameters closed form formula’s like $g_m$, $r_0$, $C_{gs}$, and $C_{gd}$. Many other comprehensive, compact and analytical modeling of GFETs gate capacitance and quantum capacitance as dependent modeling parameter already done. But all of them either are limited to modelling and simulation of GFETs characteristics like some latest GFETs models [18-20], or are complex or are practically absurd [21-23] with no future application in device and circuits. Latest GFETs model is [24]. The proposed model is keep its nature lucid mathematically, present the explicit quantum capacitance, drive the drain current formulation using it and also present the nonlinearity of characteristic curves of the GFETs at nanometer technology. Since noise is always a key element to disturb the performance of the FETs and GFETs in real world electronic device and circuits, so the study of nonlinearity to the transfer as well as output characteristics is our addition to the modelling and simulation of the GFETs. Dual gate control along with the nonlinearity study has been given in transfer characteristic curves in result and discussion section. Dual gate control to large area graphene GFETs is also a new concept for the proposed GFET modeling at 300nm technology.

Figure 1 2-D Cross-sectional view of the proposed GFETs.

Figure 2 (a), (b), (c) Equivalent circuit of the proposed GFETs (all h-parameter symbols have their usual meaning).
The structure of this paper is as follows, the detailed explanation of physic-based GFET model starts from charge carrier concentration and density of states in the section II analytical model. Furthermore, section II explains quantum capacitance which leads to the drain current formulation and help to plot the graphical results for the GFET at 1000nm and 300nm channel length. Where this is noticeable that the source is grounded. Section III discussed and compare the experimental results with the proposed model results. Finally, section IV concludes this GFET model.

II. ANALYTICAL MODEL

A. Device structure of GFETs

The proposed compact model is based on well-established dual-gate MOS structure which is shown in Fig. 1 as a 2-D cross-sectional view of proposed GFETs, which consists two gates (top and back gate), a large area graphene sheet as channel placed on the thick SiO₂ back-gate oxide layer (300 nm and 1000nm), where heavily doped silicon wafer act as a back gate. This article emphasis on p-type GFETs, while a similar approach is applicable to the n-type GFETs also. The \( C_{gs} \) and \( C_{gd} \) are gate to source and gate to drain capacitance. \( R_{ds} \), \( R_{d} \) and \( R_{s} \) are the drain to source resistance, drain resistance and source resistance respectively as shown in Fig. 2. (a), all of these parameters nonlinear effect at nanometer technology is considered minimum since these can be control externally by suitable parameter modelling. \( V(x) \) is shown above in Fig. 2 (b) as channel voltage, which is variable and depends on x’s value from 0(0) to \( L(V_{ds}) \). While \( C_{t} \), \( C_{b} \) and \( C_{q} \) are top gate capacitance, back gate capacitance and, quantum capacitance respectively and \( V_{gs_t} \) and \( V_{gs_b} \) applied internal voltages. \( V_{ds} \) and \( V_{BG} \) are the external applied drain to source and back gate voltage. Small signal equivalent circuit of the proposed GFETs is also shown in Fig.2 (c) where \( R_{s} \), \( R_{d} \) and \( R_{g} \) are the source, drain and gate resistance respectively.

B. Formulation of charge carrier sheet density (\( \rho_{gr} \)) and quantum capacitance calculation (\( C_{q} \))

2-D Energy and wave vector relation for the mobile \( \pi \) electrons in graphene can be formulated [25] as \( E(k) = s \ h \ v_{f} |k| \) while \( s \) is equal to +1 in the conduction band and \( s \) equal to -1 in valance band for the first Brillouin Zone (BZ) in graphene layers. Where \( (h) \) is the reduced form of Planck’s constant and Fermi velocity \( (v_{f}) \) is of high magnitude equal to 1x10⁸ cm/s along with \( |k| \) a 2D x-y plane wave vector at the origin \( |k| = 0 \) is referred to “Dirac point.” Each Dirac point is double fold spin degeneracy \( g_s = 2 \) and contains double valley \( g_v = 2 \), thus the number of state in a graphene layer is

\[
N(E) = g_s g_v \frac{A k^2}{4 \pi (h v_f)^2}
\]  

(1)

here \( A \) is an area of the particular layer since the linear charge density of states [22] for an intrinsic graphene layer is a differential form of the number of the state of the particular layer with respect to the energy of the particular states, which can be expressed as,

\[
\rho_{gr} = \frac{1}{A} \frac{dN}{dE} = \frac{2}{\pi} \frac{|E|}{(h v_f)^2}
\]  

(2)
and so, to calculate the charge carriers (hole) concentration in 2D graphene sheet based on fundamental physics can be formulated as

\[ p = \int_{-\infty}^{E_{cv}} \rho_{gr}(E)[1 - f(E)]dE \quad (3) \]

after replacing \( f(E) \) as the Fermi-Dirac distribution function and \( \rho_{gr}(E) \) too, the no. of holes in the graphene sheet can be written as follows, where \( E_{cv} \) work as reference energy (i.e., it will be taken \( E_{cv} = 0 \))

\[ p = \int_{-E_{cv}}^{\infty} \frac{2}{\pi (\hbar v_f)^2} \left[ \frac{1}{\exp\left(\frac{E-E_p}{K_B T}\right)+1} \right] dE \quad (4) \]

similarly, no. of the electron in graphene channel with respect to field dependent charge carrier can be represented as

\[ n = \int_{0}^{\infty} \frac{2}{\pi (\hbar v_f)^2} \left[ \frac{1}{\exp\left(\frac{E-E_p}{K_B T}\right)+1} \right] dE \quad (5) \]

thus the total charge (\( Q_{net} \)) is equal to \( (p - n) \) and could be utilized to estimate total charge sheet density \( Q_{gr} \), which is equal to \( q * (p - n) \) as follows

\[ Q_{gr} = \frac{2}{\pi (\hbar v_f)^2} \int_{-E_{cv}}^{0} \left[ \frac{1}{\exp\left(\frac{E-E_p}{K_B T}\right)+1} - \frac{1}{\exp\left(\frac{E+E_p}{K_B T}\right)+1} \right] dE \quad (6) \]

the quantum capacitance of the proposed device is \( C_q \) which can be calculated [22] as the differential of the channel sheet charge density \( Q_{gr} \) with respect to \( V_{gr} \), where \( V_{gr} \) is graphene channel voltage

\[ C_q = -\frac{dQ_{gr}}{dV_{gr}} \quad (7) \]

while minus sign here reflects the impact of more and more positive voltage applied to gate directly increase the positive channel voltage i.e. more negative charge results in the graphene layer.

\[ C_q = -\frac{d}{dV_{gr}} \left\{ \frac{2}{\pi (\hbar v_f)^2} \int_{0}^{\infty} \left[ \frac{1}{\exp\left(\frac{E-E_p}{K_B T}\right)+1} - \frac{1}{\exp\left(\frac{E+E_p}{K_B T}\right)+1} \right] dE \right\} \quad (8) \]

To solve the integral term in the above equation take constants outside and substitute exponential term \( \exp \left( \frac{E\pm E_p}{K_B T} \right) \pm 1 \) by \( x \) and \( dE \) by \( \frac{K_B T}{x} dx \) we get simplified equation as
\[ C_q = - \frac{d}{dV_g} \left( \frac{2}{\pi} \frac{q}{(\hbar e_f)^2} \right) \left\{ \left[ \int_A^\infty |K_B T \ln x - E_F| \frac{1}{x} \cdot \frac{K_B T}{x} \right] dx - \left[ \int_B^\infty |K_B T \ln x + E_F| \frac{1}{x} \cdot \frac{K_B T}{x} \right] dx \right\} \]  

(9)

A simplified quantum capacitance formulated above in Eqn. (9) can be rewritten as

\[ C_q = - \frac{d}{dV_g} \left( \frac{2}{\pi} \frac{q}{(\hbar e_f)^2} \right) \{ \text{part } A' + \text{Part } B' \} \]  

(10)

by dividing integral into two parts and naming as part A' and Part B' as

\[ \int_A^\infty |K_B T \ln x - E_F| \cdot \frac{1}{x} \cdot \frac{K_B T}{x} \cdot dx \]  

(11)

\[ \int_B^\infty |K_B T \ln x + E_F| \cdot \frac{1}{x} \cdot \frac{K_B T}{x} \cdot dx \]  

(12)

where A is \( \exp(E_F/K_B T + 1) \) and B is \( \exp(-(E_F/K_B T + 1)) \), so after putting limits in exponential term and replacing \( x = 0 \) and by solving Eqn. (11) and (12) by two-part integral substitution method we get combined solution for both part A' and Part B' and further depends on positive and negative sign as given in Eqn. (13), where \( K_B T \ln x \pm E_F \) as part I and \( \frac{1}{x} \cdot \frac{K_B T}{x} \) as part II,

\[ \int_{A,B}^\infty |K_B T \ln x \pm E_F| \cdot \frac{1}{x} \cdot \frac{K_B T}{x} \cdot dx \]  

(13)

Now assuming \( E_F \) as constant since \( E_F = q. V_{ch} \) and by following standard mathematical rules to solve this integral and after applying limits we get solved part as given below

\[ \frac{(K_B T)^2}{A} \left( \ln A + 1 \right) - E_F \frac{(K_B T)}{A} \]  

(14)

similarly, after solving part II

\[ \frac{(K_B T)^2}{B} \left( \ln B + 1 \right) + E_F \frac{(K_B T)}{B} \]  

(15)

Now by sum-up part A' and Part B', one could represent a solved integral in quantum capacitance as follows

\[ C_q = - \frac{d}{dV_g} \left( \frac{2}{\pi} \frac{q}{(\hbar e_f)^2} \right) \left\{ \left[ \frac{(K_B T)^2}{A} \left( \ln A + 1 \right) - E_F \frac{(K_B T)}{A} \right] - \left[ \frac{(K_B T)^2}{B} \left( \ln B + 1 \right) + E_F \frac{(K_B T)}{B} \right] \right\} \]  

(16)

By substituting the value of A and B, and after performing some basic algebraic mathematics, the quantum capacitance can be represented in a simple formula as given below

\[ C_q = - \frac{d}{dV_g} \left( \frac{2}{\pi} \frac{q}{(\hbar e_f)^2} \right) \left\{ \left[ \frac{(K_B T)^2}{\exp(E_F/K_B T + 1)} - \frac{(K_B T)^2}{\exp(-E_F/K_B T + 1)} \right] \right\} \]  

(17)
By making further math’s effort to the above exponential terms and replacing by the hyperbolic trigonometric formulas quantum capacitance could be present as

\[
C_q = -\frac{2q}{\pi} \frac{(K_B T)^2}{(hv)^2} \frac{d}{dv_{gr}} \left[ \frac{2\sinh \frac{q v_{gr}}{K_B T}}{2 + \cosh \frac{q v_{gr}}{K_B T}} \right]
\] (18)

By using reverse engineering, we can write quantum capacitance [18] even more simplified as given in log term

\[
C_q = -\frac{2q v_{gr} K_B T}{\pi} \ln \left[ 2 \left( 1 + \cosh \frac{q v_{gr}}{K_B T} \right) \right]
\] (19)

For the \( q V_{gr} \gg K_B T \) condition, Eqn. (19) will be simplified to the following single term formula

\[
C_q = -\frac{2q^2 q |v_{gr}|}{\pi} \left( \frac{v_{gr}}{hv} \right)^2
\] (20)

As like total charge density in Eqn. (6) has been used to formulate quantum capacitance accurately, channel voltage \( V_{gr} \) and quantum capacitance \( C_q \) have useful significance to formulate \( \rho_{gr} \). So, that by using basic mathematics technique of two equations and two variable drain current can be formulated by using \( \rho_{gr} \).

\[
Q_{gr} = -\int C_q * dV_{gr} = -\frac{1}{2} C_q V_{gr}
\] (21)

\[
\rho_{gr} = \frac{Q_{gr}}{q} = \left| \frac{-\frac{1}{2} C_q v_{gr}}{q} \right|
\] (22)

while channel voltage \( V_{gr} \) directly can be calculated by using basic technique KVL and KCL form the given Fig. 2 (c) and can be expressed as

\[
V_{gr} = \frac{1}{C_t + C_b + 0.5 C_q} \left\{ [V_{gs_t} - V(x)] C_t - [V_{gs_b} - V(x)] C_b \right\}
\] (23)

The pre-factor of \( \frac{1}{2} \) with \( C_q \) is because of its dependency on \( V_{gr} \) and now by putting Eqn. (23) in Eqn. (22) we get a simple a second order algebraic Eqn.

\[
C_t^2 + (C_t + C_b) C_q - [V_{gs_t} - V(x)] C_t - [V_{gs_b} - V(x)] C_b = 0
\] (24)

Thus by rearrange the equation we get a quadratic equation of \( C_q \) in terms of \( V_{gs_t}, V_{gs_b} \) and \( V(x) \). Where \( V(x) \) is the channel voltage depend on \( x \) (0 to L) and the value of \( V(x) \) of at \( x = 0 \) is 0V, while at \( x = L \) is \( V_{ds} V \).

**C. Drain current calculation**

For drain current calculation at very first we need to focus on charge charier velocity \( v = v(x) \) mainly through the channel (\( x = 0 \) to L) from (1). A low saturation velocity characteristic is shown by Monte Carlo simulation [26] at steady state is
\[ v = \frac{\mu E}{1 + \frac{\mu E}{v_{sat}}} \]  

(25)

Where \( E \) represents the electric field, \( \mu \) is the carrier mobility and \( v_{sat} \) is saturation velocity at low field. The carrier velocity, especially \( v_{sat} \) of graphene channel at the SiO\(_2\) platform is strongly affected by rare interfacial scattering of phonons. Experimental work has been talked about the two dominated optical phonons at SiO\(_2\) and graphene interface. They have been shown a magnitude of 59meV and 155meV respectively but one can describe this phenomenon by a single phonon only with enough accuracy and with the energy of \( \hbar \Omega \). By the approach used [27] to calculate \( v_{sat} \), \( v_{sat} \) is depends on charge carrier sheet density \( (\rho_c) \) and the phonons effective energy \( \Omega \).

\[ v_{sat} = \frac{\Omega}{\sqrt{\pi \rho_c}} \]  

(26)

Charge carrier-interaction phenomenon in graphene-MOSFET channel introduces a new term as \( 0.5 + A V^2(x) \) [26] to correct the calculation of \( v_{sat} \) to one decimal point at the channel drain end. Now Eqn. (26) will become as,

\[ v_{sat} = \frac{\Omega}{(\pi \rho_c)^{0.5 + A V^2(x)}} \]  

(27)

here \( A \) is a dimensionless important factor of order \( 10^{-3} \), when \( \rho_c \) is of magnitude \( 10^{12} \) cm\(^{-2}\) and the final unit will be cm/s. Now using \( E = \left(-\frac{dv}{dx}\right) \) and Eqn. (20) drain current \( (I_d) \) becomes

\[ I_d = -q\rho_c \frac{\mu(-\frac{dv}{dx})}{1 + \frac{\mu(-\frac{dv}{dx})}{v_{sat}}} W \]  

(28)

after integrating left side 0 to \( x \) and 0 V to \( V_{ds}V \) right side, since \( V = V(x) \), is a function of the channel length and by changing the variable of separation \( I_d \) becomes

\[ I_d = q\mu W \frac{\mu \int_0^{V_{ds}} \rho_{gr} dV}{L - \mu \int_0^{V_{ds}} \frac{1}{v_{sat}} dV} \]  

(29)

\[ I_d = -\frac{\mu W}{2 L_{eff}} \left[ \frac{c_t}{C_t + C_b + 0.5 c_{eq}} \right] \left\{ \left( V_{g_{st}} - V(x)/2 \right) - \left( V_{g_{sb}} - V(x)/2 \right) \right\} \]  

(30)

\[ L_{eff} = L - \frac{\mu}{\Omega} \left\{ \left[ \log \left(\frac{\pi c_{eq}}{2 q} \right) + \log \left(\frac{(V_{g_{st}} - V_{ds})C_t + (V_{g_{sb}} - V_{ds})C_b}{C_t + C_b} \right) \right] \right\} \]  

\[ \left[ 0.5 V_{ds} + \frac{AV_{ds}^2}{3} \right] \]  

(31)

III. RESULTS AND DISCUSSION

To investigate the quantum capacitance effect on gate capacitance, a basic MOS structure without the back gate of oxide thickness (300nm) and top gate oxide of the thickness (3nm.), zero voltage as \( V_{g_{st}}, V_{g_{sb}} \) and \( V_{ds} \) is arranged for the proposed GFETs model. Quantum
capacitance of the amplitude of the 10th multiple micron is enhanced overall gate capacitance with respect to the relation $C_q = C_{ox} * C_q / (C_q + C_{ox})$.

Figure 3 Quantum capacitance effect on overall gate capacitance vs gate voltage. Line graph is simulated proposed model results, while symbolic results graph are analytical results and this is same for all presented result graphs. One of the important aspects of nanometer technology GFETs is that its quantum capacitance plays an important role in terms of device characteristics. Fig. 3 elaborate graphically how the quantum capacitance has an impact on the total gate capacitance at 300nm channel length. The proposed model provides better results analytical (with line) and the simulated (with symbol) as compared with [11] for QC as well as gate capacitance at 300nm. Table 1 explains the numerical values of all important parameters and dimensions of the GFETs model.

Table 1 Dimensions and parameters of the modeled GFETs devices

| Parameter      | GFET 1 [11] | GFET 300nm(proposed) |
|----------------|-------------|-----------------------|
| $L$ (µm)       | 1           | .3                    |
| $W$ (µm)       | 2.1         | 2.1                   |
| $t_{ox}$ (nm)  | 15          | 15                    |
| $t_{box}$ (nm) | 285         | 285                   |
| $V_{gs-top}$ (V)| 1.45        | .95                   |
| $V_{gs-back}$ (V)| 2.7         | 2.7                   |
| $p$            | 1.5*10^{12} | 3*10^{12}            |
| $\mu_n$ (cm²/V.s) | 1500       | 2500                 |
| $\mu_p$ (cm²/V.s) | 1500       | 2500                 |
| $H$ (meV)      | 56          | 99                    |
| $\lambda$ (nm)       | 10-100       | 100-300              |

Fig. 4 and Fig. 5 have been explained graphically the transfer characteristic and output characteristic of the proposed GFETs model and compare it with the experimental work [11] mentioned as GFET1 in table 1. GFET1 featuring a high dielectric 2D gate material HfO$_2$ on exfoliated graphene channel and SiO$_2$ is the back-gate dielectric. The output characteristic of the proposed model in Fig. 5, has been shown coherence with the [11], with the applied voltage at source (0V), negative drain voltage (-2 to 0V), top-gate (sweep from -0.75 to 0.75 V) as it
can easily view in a graph with the back-gate voltage of -40 V. In the same manner transfer characteristic of the GFETs both analytical and simulated is compared with [11] in Fig. 5. The nonlinearity characteristic behavior of the GFET at 300nm channel has been shown in Fig. 4, 5 and 6. Dirac-point shift has been shown in Fig. 4 after applying external gate to source voltage for single layer graphene, while for the conventional MOSFETs Dirac point has same contact point of Conduction and valance band. This Dirac-point shift in transfer characteristic and kink presented in the output characteristics is a proof of nonlinear behavior of the GFETs. This nonlinear behavior is tending to enhanced when the channel length of the GFETs is being decreased from 1000nm to 100nm and below that value as this is given in the latest accepted paper [24]. The dual gate control has been graphically present in the Fig. 6 at same time nonlinear behavior of the GFETs can be observe very easily from Fig 6. e and f. Here we concentrate on the nonlinearity of the transfer characteristics of the Proposed model. A flat line drain current has been shown in the fig. 6.e for -0.1 to 0.1V gate to source voltage and beyond this it has shown gradual increment, which is normal in electronic device characteristic curves but a flat drain current shows a nonlinearity in the transfer characteristic curve.

Figure 4 Transfer characteristic of the proposed model i.e. drain current vs top-gate voltage for the proposed GFETs model.

Figure 5.a and 5.b: Output characteristic of the proposed model i.e. drain current vs drain to source voltage for the GFETs model at 1000nm in 5.a and 300nm in 5.b.
IV. CONCLUSION

A quasi-analytic model for single layer GFETs with large area graphene is presented. Drain current formulation with velocity saturation effect and role of quantum capacitance over the total capacitance is explained with the precise output and transfer characteristics of the proposed GFET model, which facilitate the readers a simple mathematical GFET model. The effect of enhanced quantum capacitance value at nanometer technology adds nonlinearity to the transfer and output drain characteristics of the proposed GFETs model has been presented. Thus the modeling and simulation of GFETs model has present and compare the results with the well established experimental work and it will surly befits the reader to understand basic physic of the GFETs.

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