Simulation Study of the Use of AlGaN/GaN Ultra-Thin-Barrier HEMTs with Hybrid Gates for Achieving a Wide Threshold Voltage Modulation Range

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Abstract: In this work, novel hybrid gate Ultra-Thin-Barrier HEMTs (HG-UTB HEMTs) featuring a wide modulation range of threshold voltages ($V_{TH}$) are proposed. The hybrid gate structure consists of a p-GaN gate part and a MIS-gate part. Due to the depletion effect assisted by the p-GaN gate part, the $V_{TH}$ of HG-UTB HEMTs can be significantly increased. By tailoring the hole concentration of the p-GaN gate, the $V_{TH}$ can be flexibly modulated from 1.63 V to 3.84 V. Moreover, the MIS-gate part enables the effective reduction in the electric field (E-field) peak at the drain-side edge of the p-GaN gate, which reduces the potential gate degradation originating from the high E-field in the p-GaN gate. Meanwhile, the HG-UTB HEMTs exhibit a maximum drain current as high as 701 mA/mm and correspond to an on-resistance of 10.1 $\Omega$ mm and a breakdown voltage of 610 V. The proposed HG-UTB HEMTs are a potential means to achieve normally off GaN HEMTs with a promising device performance and featuring a flexible $V_{TH}$ modulation range, which is of great interest for versatile power applications.

Keywords: AlGaN/GaN HEMTs; threshold voltage modulation; ultra-thin barrier; normally off; enhancement-mode; p-GaN gate

1. Introduction

High-electron-mobility transistors (HEMTs) based on AlGaN/GaN heterostructures are promising for use in high-frequency and high-power applications due to their high breakdown voltage (BV) and polarization-induced two-dimensional electron gas (2DEG) at the interface of the AlGaN/GaN heterojunction [1]. However, the AlGaN/GaN HEMTs are inherently depletion-mode (D-mode) devices. In order to ensure power system security and simplify the gate drive circuit design, enhancement-mode (E-mode) devices with a sufficiently high $V_{TH}$ are urgently wanted [2,3]. Several device structures have been reported to be able to realize normally off behavior. However, it is still quite difficult to effectively modulate the $V_{TH}$ with a large range (e.g., >3 V) to accommodate various applications.

AlGaN/GaN metal-insulator (oxide) semiconductor (MOS/MIS) HEMTs with a positive $V_{TH}$ can be achieved by a partially or fully recessed AlGaN barrier below the gate [4–6]. The 2DEG under the gate is effectively depleted and maintains a low gate leakage by introducing the gate dielectric. However, undesirable device performance degradation may occur due to the etching damage and presence of defect-related trap states. The presence of interface states introduced by etching in III-nitride compound semiconductor is one of the main factors that causes the instability of $V_{TH}$ and decreases carrier mobility due to
the scattering effect. The electron-trapping and detrapping during the device turn on and off may induce a dynamic shift in threshold voltage [7–10]. It is challenging to obtain a uniform $V_{TH}$ in the manufacturing of MIS-HEMTs, particularly when the barrier thickness is less than several nanometers (e.g., <7 nm) [11,12]. Moreover, the presence of interface states may also lead to interface states-assisted gate leakage current.

Currently, the p-GaN gate HEMTs are commercialized to obtain E-mode operation, in which the p-GaN cap layer is inserted between the gate metal and AlGaN barrier to deplete the 2DEG beneath the gate [13]. Nevertheless, due to the low ionization rate of p-type dopants in GaN material, it is difficult to realize a high hole concentration to shift the $V_{TH}$ even higher (e.g., $>+3$ V). Moreover, the time-dependent gate leakage was found to be consistent with the percolation of defects and gate reliability deterioration (e.g., the instability of the $V_{TH}$ and even failure of the gate) normally originating from the E-field crowing effect in the p-GaN layer [14–16], which is critical in order for p-GaN gate HEMTs to deliver stable operation.

In recent years, ultra-thin barrier (UTB) HEMTs have obtained great attention. Benefiting from their unique advantage of being recess-free, the gate recess nonuniformity and the corresponding lattice damage and trap states can be effectively alleviated [17]. The as-grown thickness of the AlGaN barrier is less than 7 nm, which enables an intrinsic E-mode channel in the UTB AlGaN/GaN heterostructure. The high 2DEG density can be retained in the access region by depositing the LPCVD-SiN passivation due to the surface potential modulation effect and the presence of fixed positive charges at the SiN/AlGaN interface. The density of $3.26 \times 10^{13}$ cm$^{-2}$ for the positive fixed charges at the SiN/AlGaN interface has been confirmed [18,19]. The positive charges will enhance the 2DEG density in the access region, which can reduce the on-resistance of the device. In this manner, the intrinsic E-mode channel is reserved beneath the gate region, while the access region features a low on-resistance [20–22]. However, it has been reported that the $V_{TH}$ of fabricated UTB HEMTs is less than 0.3 V (e.g., 0.27 V) [19]. In order to avoid a false turn on in power applications due to gate noise or the overshoot of gate control signal during the high speed switching, it is necessary to further increase the $V_{TH}$. Therefore, to realize a high positive $V_{TH}$ in UTB HEMTs is still challenging and demanding.

In this work, a hybrid gate structure featuring a p-GaN gate connected with a MIS-gate was introduced in the conventional UTB HEMTs to obtain a higher $V_{TH}$. Owing to the effect of further depletion by the p-GaN cap, the range of $V_{TH}$ modulation could be sufficiently increased by tailoring hole concentration in the p-GaN layer. The impact of the hole concentration, thickness, and Al-mole fraction of the AlGaN barrier on $V_{TH}$ were investigated. Meanwhile, the MIS-gate effectively optimized the E-field distribution in the gate region. The E-field peak located at the drain-side edge of the p-GaN gate was well reduced, which was beneficial for obtaining a low gate leakage and improved the gate reliability. The proposed device structure and the concept of hybrid gate design are of great interest for fabricating the E-mode GaN power HEMTs with a flexible $V_{TH}$ modulation range to accommodate different applications.

2. Device Structure and Characteristics
2.1. Device Structure

The structure of the proposed HG-UTB HEMTs is shown in Figure 1. The hybrid gate structure consisting of a p-GaN/MIS-gate is introduced in the UTB HEMTs to break through the trade-off between $V_{TH}$ modulation and increased channel resistance. Benefitting from the hybrid gate structure, the device could realize a wide range of threshold modulation and optimized E-field distribution near the gate.

Due to the reduced polarization effect in the UTB HEMTs, the potential well at the heterointerface was raised above the Fermi level, which led to a native E-mode channel in the UTB heterojunction. The $V_{TH}$ of conventional MIS-gate UTB HEMTs is mainly dominated by the thickness and Al-mole fraction of the AlGaN barrier. Even though the intrinsic E-mode channel resulted in a low positive $V_{TH}$ (e.g., <0.3 V) [19], this was not
high enough to deliver a safe operation in power electronic applications. The depletion region distribution under the hybrid gate region is shown in Figure 2. Owing to the sharp decrease in polarization intensity in UTB HEMTs, the depletion region mildly extended to the GaN buffer to achieve charge balance. It is noteworthy, that the p-GaN layer and 2DEG at the AlGaN/GaN heterojunction could be modeled as a p-i-n diode, the additional depletion region appeared and 2DEG density was dramatically decreased. Furthermore, the intensity of depletion under the gate region could be adjusted by tailoring the hole concentration in the p-GaN gate, which would enable the flexible threshold modulation.

![Figure 1](image1.png)

**Figure 1.** Schematic cross-sectional structure of the HG-UTB HEMTs.

Moreover, the drain-side MIS-gate part acted as a junction termination, which could transfer the E-field peak from the vulnerable p-GaN gate to the robust MIS-gate and thus reduce the gate leakage; this was also beneficial for gate reliability improvement.

To further study the device physics in depth, Sentaurus-TCAD was carried out. As a mainstream device simulation tool for micro-electronic devices, Sentaurus can be used to evaluate the device characteristics, understand the operation mechanism, and optimize the device structure. The critical device parameters used in the simulation are summarized in Table 1. The hole concentration in the p-GaN layer \((N_p)\) and the thickness of the AlGaN barrier layer \((T_{AlGaN})\) are variables that can be used for studying their impact on the \(V_{TH}\) of HG-UTB HEMTs. Taking the low activation ratio of Mg dopant in GaN into account, a hole concentration of less than \(5 \times 10^{17} \text{ cm}^{-3}\) was used in simulation, which is reported as experimentally achievable in the literature [14]. In HG-UTB HEMTs, gate metal formed Schottky contact with the p-GaN gate and the corresponding barrier height was 1.65 eV, which is comparable to previous reports in the p-GaN gate HEMTs [15,16].

![Figure 2](image2.png)

**Figure 2.** Depletion distribution under the gate region of HG-UTB HEMTs.
Table 1. Key parameters of the HG-UTB HEMTs used in simulation.

| Structural Parameters | Unit | Values |
|-----------------------|------|--------|
| Source length (Ls)    | µm   | 0.5    |
| Drain length (Ld)     | µm   | 0.5    |
| Distance from source to gate (Lgs) | µm | 1.0 |
| Gate length (Lg)      | µm   | 1.0    |
| p-GaN length (Lp)     | µm   | 0.5    |
| MIS-gate length (Lm)  | µm   | 0.5    |
| Gate field plate length (Lfp) | µm | 0.0 /1.0 |
| Distance from gate to drain (Lgd) | µm | 5.0 |
| Thickness of p-GaN (Tp) | nm  | 70.0   |
| Thickness of gate dielectric (Toxide) | nm | 26.0 |
| Thickness of passivation layer (TSiN) | nm | 35.0 |
| Thickness of GaN buffer layer (Tbuffer) | nm | 2000.0 |
| Thickness of transition layer (Ttrans) | nm | 100.0 |
| Al-mole fraction in AlGaN barrier (XAl) | / | 0.18,0.20,0.23,0.25 |
| Thickness of AlGaN barrier (TAlGaN) | nm  | 3.45,6.7 |
| Hole concentration in p-GaN (Np) | cm⁻³ | 1.5,8,10⁻⁶ |

* XAl = 0.20, TAlGaN = 5 nm, Np = 1 × 10¹⁷ cm⁻²; Lfp = 0 µm are the default values.

2.2. $V_{TH}$ Modulation Effects

To reveal the in-depth mechanism of HG-UTB HEMTs and evaluate the enhancement of the characteristics, conventional MIS-gate UTB HEMTs with the same device parameters (e.g., $L_g$, $L_{gd}$, $T_{AlGaN}$, $X_{Al}$, $T_{buffer}$) were used for reference. As shown in Figure 3a, the $V_{TH}$ was enhanced from 0.64 V in the conventional MIS-gate UTB HEMTs to 2.03 V in the proposed HG-UTB HEMTs with a comparable drain current. It is worth noting from Figure 3b that the energy band diagram at the AlGaN/GaN heterointerface was raised by 0.76 eV by the hybrid gate design. Hence, compared with the conventional MIS-gate UTB HEMTs, the 2DEG density dramatically decreased from $1.63 × 10^{12}$ cm⁻² to $6.10 × 10^{-5}$ cm⁻². Consequently, the HG-UTB HEMTs required a higher gate voltage to pull down the conduction band below the Fermi level to accumulate electrons and form the conduction channel, which led to much a higher $V_{TH}$ being achieved in the HG-UTB HEMTs.

Figure 3. (a) Transfer characteristics of UTB HEMTs and HG–UTB HEMTs; (b) band energy profile at the left middle area of the gate region. The gate oxide and p–GaN for conventional MIS–gate UTB HEMTs and HG–UTB HEMTs, respectively, are shown.
3. Results and Discussion
3.1. Impact of Hole Concentration and Polarization on $V_{TH}$

In Figure 3b, it is shown that the elevation of 2DEG potential well under thermal equilibrium is proportional to the increase in $V_{TH}$. The heterojunction polarization intensity (e.g., the thickness and the Al-mole fraction of the AlGaN barrier) and doping concentration in the p-GaN gate have a significant impact on the electron distribution under the gate and $V_{TH}$. Thus, $X_{Alb}$, $T_{AlGaN}$, and $N_p$ are regarded as variables to study their impact on device characteristics (i.e., $V_{TH}$). In order to enable normally off operation, the Al-mole fraction and barrier thickness of the UTB heterojunction were varied between 0.18 and 0.25 and 3 and 7 nm, respectively.

Figure 4a shows the impact of hole concentration on the transfer characteristics when the barrier thickness and Al-mole fraction are 5 nm and 0.20, respectively. The HG-UTB HEMTs are capable of attaining $V_{TH}$ increments over 2.2 V and maintaining a considerable conduction current with $N_p$ varying from $1 \times 10^{16}$ cm$^{-3}$ to $5 \times 10^{17}$ cm$^{-3}$. Figure 4b shows the energy band diagram under the p-GaN gate with various hole concentrations. A P-GaN cap with a higher hole concentration (e.g., $5 \times 10^{17}$ cm$^{-3}$) can lift the potential well of the 2DEG channel higher (e.g., 1.20 eV). In this way, the 2DEG density beneath the p-GaN gate dramatically reduces from $1.78 \times 10^{16}$ cm$^{-2}$ to $4.10 \times 10^{15}$ cm$^{-2}$. Compared with the p-GaN gate, the electron concentration under the MIS-gate is at least two orders of magnitude higher (e.g., $1.63 \times 10^{2}$ cm$^{-2}$). This explains why the hybrid gate structure can break the limitation of the $V_{TH}$ modulation of conventional MIS-gate UTB HEMTs.

![Graph showing $V_{TH}$ modulation and energy band diagram.](image)

Figure 4. (a) $V_{TH}$ modulation of HG–UTB HEMTs by varying $N_p$; (b) energy band diagram under the gate region at AlGaN/GaN heterojunction with various $N_p$.

Figure 5a,b, respectively, show the effect of the Al-mole fraction and barrier thickness on $V_{TH}$ as a function of hole concentration in the p-GaN gate. As shown in Figure 5a, with the increase in $X_{Al}$ from 0.18 to 0.25, the polarization intensity of the heterojunction increases and the $V_{TH}$ decreases with a maximum variation range of 0.41 V. When the barrier thickness and hole concentration are 5 nm and $5 \times 10^{17}$ cm$^{-3}$, respectively, the $V_{TH}$ reduces from 3.94 V to 3.53 V. Meanwhile, when the thickness and Al-mole fraction of the AlGaN barrier is 5 nm and 0.23, the $V_{TH}$ increases from 1.48 V to 3.70 V, while the hole concentration increases from $1 \times 10^{16}$ cm$^{-3}$ to $5 \times 10^{17}$ cm$^{-3}$, respectively. Increasing the hole concentration can effectively raise up the energy band at the heterointerface and further reduce the electron concentration in the potential well, which is more effective in shifting the $V_{TH}$ rather than varying the Al-mole fraction.

Similarly, increasing the thickness of the AlGaN barrier will increase the polarization charge density and enhance the 2DEG density, which may account for the downward trend of the $V_{TH}$ shown in Figure 5b. When the Al-mole fraction and hole concentration are
and $5 \times 10^{17}$ cm$^{-3}$, respectively, the $V_{TH}$ decreases from 4.04 V to 3.40 V with $T_{AlGaN}$ being enhanced from 3 nm to 7 nm. When the Al-mole fraction and thickness of the barrier are 0.20 and 5 nm, the $V_{TH}$ increases from 1.60 V to 3.82 V with $N_p$, the increasing from $1 \times 10^{16}$ cm$^{-3}$ to $5 \times 10^{17}$ cm$^{-3}$. Compared with the decrease in the thickness of AlGaN barrier, the $V_{TH}$ shift range is expanded by 2.5 times by tailoring the hole concentration.

![Figure 5](image)

**Figure 5.** $V_{TH}$ modulation of HG–UTB HEMTs with various $N_p$ (a) and Al–mole fractions ($X_{Al}$) (b) and thicknesses ($T_{AlGaN}$).

Therefore, the introduction of the p-GaN gate in conventional MIS-gate UTB HEMTs can reconcile the contradiction between $V_{TH}$ shift and drain current reduction, and the $V_{TH}$ modulation range can be significantly enhanced by tailoring the hole concentration in the p-GaN layer.

### 3.2. E-Field Distribution in the Gate Region

From the discussion above, the p-GaN gate in the hybrid gate structure can effectively enhance the $V_{TH}$ shift range with a negligible drain current reduction. On the other hand, the MIS-gate part of the hybrid gate structure in the HG-UTB HEMTs acts as a termination of the p-GaN gate, which transfers the E-field peak from the vulnerable p-GaN edge to the MIS-gate part. In this manner, the high peak E-field is sustained by the more robust MIS-gate for the improved gate reliability of the device, since the high E-field may induce undesirable electron trapping or hole injection effects in the p-GaN gate, which may lead to $V_{TH}$ instability and even gate failure [14–16,23].

To study the performance of the E-field distribution of the hybrid gate structure, devices with a single p-GaN gate featuring identical device dimensions (e.g., $L_g$, $L_{gd}$, $T_{AlGaN}$, $X_{Al}$, $T_{buffer}$) were utilized for comparison. The E-field profile along the 2DEG channel of the conventional p-GaN gate HEMTs and HG-UTB HEMTs in forward gate bias (e.g., +7 V) are shown in Figure 6a,b, respectively. Since cylindrical junction at the gate corner was formed, the E-field peaks appear at both gate edges (i.e., 1.5 μm and 2.5 μm). It can be seen that the E-field peak located at the p-GaN edge is 0.47 MV/cm in the conventional p-GaN gate HEMTs, as depicted in Figure 6a. In contrast, as for HG-UTB HEMTs shown in Figure 6b, the E-field peak at the drain-side gate edge of the p-GaN gate part is respectably suppressed while the E-field peak is removed to the drain-side edge of the MIS-gate part. Similarly, the E-field peak at the source-side edge is also reduced. In this manner, the high E-field at the gate region in the proposed HG-UTB HEMTs is sustained by the MIS-gate part, which may feature lower gate leakage due to the presence of the gate dielectric. Accordingly, the optimized E-field distribution in the gate area enables improved the gate reliability of the HG-UTB HEMTs.
More importantly, the E-field strength was much higher in reverse blocking than that in forward conduction. Hence, the E-field distribution in reverse blocking is also investigated and compared in Figure 7 to validate the superiority of E-field distribution in the HG-UTB HEMTs by using the hybrid gate structure. The E-field profile along the channel of the conventional p-GaN gate HEMTs and the proposed HG-UTB HEMTs in off-state at 335 V, are shown in Figure 7a,b, respectively. Similarly, the E-field peak at the drain-side edge of the p-GaN gate was dramatically reduced by the hybrid gate structure from 1.32 MV/cm to 0.15 MV/cm. In Figure 7b, the high E-field of 1.67 MV/cm is sustained by the more robust MIS-gate. The MIS-gate part of the hybrid gate can promote the broadening of depletion along the drift region and thus improve the E-field crowing effect, which can reduce the possible gate leakage current and even alleviate the gate degradation that could be triggered in the Schottky type p-GaN gate. Moreover, the removal of high E-field away from the p-GaN gate, the typical electron trapping, and the hole injection observed in p-GaN gate can also be mitigated [23–26], which is beneficial for obtaining a more stable $V_{TH}$ of the device.

**Figure 6.** Distribution of the E-field beneath the gate region with gate bias of 7 V of (a) conventional p-GaN gate HEMTs and (b) HG-UTB HEMTs.

**Figure 7.** Distribution of E-field beneath the gate region in reverse blocking with a drain bias of 335 V of (a) conventional p-GaN gate HEMTs and (b) HG-UTB HEMTs.

3.3. **Forward & Reverse Characteristics**

In the UTB HEMTs, the LPCVD-SiN was used to restore the high-density 2DEG [19–22], while the fixed positive charge with a density of $3.26 \times 10^{13}$ cm$^{-2}$ at the SiN/AlGaN

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interface was proposed to be responsible for the 2DEG accumulation [19,27–30]. In this work, the fixed positive charge of $3.10 \times 10^{13} \text{ cm}^{-2}$ was used to imitate the LPCVD-SiN passivation effect in the access region to restore the 2DEG in the HG-UTB HEMTs.

Figure 8 illustrates the output characteristic of the proposed HG-UTB HEMTs. The maximum output current density up to 701 mA/mm was obtained, which yielded an on-resistance of 10.1 $\Omega \cdot \text{mm}$.

![Figure 8. Output characteristics of the HG-UTB HEMTs.](image)

Figure 9a shows the breakdown characteristics of HG-UTB HEMTs without a gate field plate and with a 1 $\mu$m gate field plate. The impact of a gate field plate on the E-field distribution along the channel at drain bias of 335 V is shown in Figure 9b. The field plate plays a role in enhancing the expansion of the depletion region in the block-state to obtain more uniform E-field distribution. The field plate reduced the E-field peak at the drain-side gate edge by 40% by introducing a new E-field peak with a comparable E-field strength at the gate field edge, which was beneficial to avoid the premature breakdown of the device. Therefore, the breakdown voltage of the device was significantly improved from 335 V to 610 V with a drift length $L_{gd}$ of 5 $\mu$m.

![Figure 9. The impact of gate field plate on reverse characteristics of the HG–UTB HEMTs’ (a) breakdown performance and (b) E–field distribution along the channel.](image)

4. Conclusions

In this work, normally off HG-UTB HEMTs featuring a p-GaN/MIS-gate hybrid gate enabling flexible modulated $V_{TH}$ are proposed. The p-GaN gate facilitates the enhancement
of the depletion of the 2DEG channel, which further raises the energy band of the ultra-thin barrier heterojunction and results in a more positive $V_{TH}$ in the proposed HG-UTB HEMTs. Moreover, the E-field crowing located at the p-GaN edge is effectively mitigated by the MIS-gate part, which is beneficial for reducing the gate leakage through the Schottky type p-GaN gate introduced by the high E-field. This work provides a simple approach to achieve normally off GaN HEMTs with promising device performance and particularly featuring flexible $V_{TH}$ modulation ranges, which is of great interest for accommodating versatile power applications.

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References

1. Chen, K.J.; Hberlen, O.; Lidow, A.; Tsai, C.L.; Ueda, T.; Uemoto, Y.; Wu, Y. GaN-on-Si power technology: Devices and applications. IEEE Trans. Electron. Devices 2017, 64, 779–795. [CrossRef]

2. Zhou, Q.; Liu, L.; Zhang, A.; Chen, B.; Jin, Y.; Shi, Y.; Wang, Z.; Chen, W.; Zhang, B. 7.6 V Threshold Voltage High-Performance Normally-Off Al2O3/GaN MOSFET Achieved by Interface Charge Engineering. IEEE Electron. Device Lett. 2015, 37, 165–168. [CrossRef]

3. Xu, Z.; Wang, J.; Cai, Y.; Liu, J. High Temperature Characteristics of GaN-Based Inverter Integrated With Enhancement-Mode (E-Mode) MOSFET and Depletion-Mode (D-Mode) HEMT. IEEE Electron. Device Lett. 2014, 35, 33–35. [CrossRef]

4. Wang, Y.; Wang, M.; Xie, B.; Wen, C.P.; Wang, J.; Hao, Y.; Wu, W.; Chen, K.J.; Shen, B. High-performance normally-off Al2O3/GaN MOSFET using a wet etching-based gate recess technique. IEEE Electron. Device Lett. 2013, 34, 1370–1372. [CrossRef]

5. Liu, S.; Yang, S.; Tang, Z.; Jiang, Q.; Liu, C.; Wang, M.; Chen, K.J. Al2O3/AlN/GaN MOS-Channel-HEMTs with an AlN Interfacial Layer. IEEE Electron. Device Lett. 2014, 35, 723–725.

6. Huang, S.; Jiang, Q.; Wei, K.; Liu, G.; Zhang, J.; Wang, X.; Zheng, Y.; Sun, B.; Zhao, C.; Liu, H.; et al. High-temperature low-damage gate recess technique and ozone-assisted ALD-grown Al2O3 gate dielectric for high-performance normally-off GaN MIS-HEMTs. Electron. Devices Meet. IEDM. Tech. Digest. Int. 2015, 2015, 17.4.1–17.4.4.

7. Yang, S.; Tang, Z.; Wong, K.-Y.; Lin, Y.-S.; Lu, Y.; Huang, S.; Chen, K.J. Mapping of interface traps in high-performance Al2O3/AlGaN/GaN MIS-hetero-structures using frequency- and temperature-dependent C-V techniques. Electron. Devices Meet. IEDM Tech. Digest. Int. 2013, 2013, 6.3.1–6.3.4.

8. Tan, S.; Egawa, T. Influence of growth conditions of oxide on electrical properties of AlGaN/GaN metal-insulator-semiconductor transistors. J. Semicond. 2019, 40, 042801. [CrossRef]

9. Zhao, J.; Xing, Y.; Fu, K.; Zhang, P.; Song, L.; Chen, F.; Yang, T.; Deng, X.; Zhang, S.; Zhang, B. Influence of channel/back-barrier thickness on the breakdown of AlGaN/GaN MIS-HEMTs. J. Semicond. 2018, 39, 094003. [CrossRef]

10. Kanamura, M.; Ohki, T.; Kikkawa, T.; Imanishi, K.; Imada, T.; Yamada, A.; Hara, N. Enhancement-mode GaN MIS-HEMTs with n-GaN/i-AlN/n-GaN triple cap layer and high-k Gate dielectrics. IEEE Electron. Device Lett. 2010, 31, 189–191. [CrossRef]

11. Saito, W.; Takada, Y.; Kuraguchi, M.; Tsuda, K.; Omura, I. Recessed-gate structure approach toward normally-OFF high-Voltage AlGaN/GaN HEMT for power electronics applications. IEEE Trans. Electron. Devices. 2006, 53, 356–362. [CrossRef]

12. Lu, B.; Sun, M.; Palacios, T. An etch-stop barrier structure for GaN high-electron-mobility transistors. IEEE Electron. Device Lett. 2013, 34, 369–371.

13. Uemoto, Y.; Hikita, M.; Ueno, H.; Matsuo, H.; Ishida, H.; Yanagihara, M.; Ueda, T.; Tanaka, T.; Ueda, D. Gate injection transistor (GIT)-a normally-off AlGaN/GaN power transistor using conductivity modulation. IEEE Trans. Electron. Devices 2007, 54, 3393–3399. [CrossRef]

14. Jiang, H.; Zhu, R.; Lyu, Q.; Lau, K.M. High-Voltage p-GaN HEMTs with OFF-State Blocking Capability after Gate Breakdown. IEEE Electron. Device Letter. 2019, 40, 530–533. [CrossRef]
15. Wu, T.L.; Marcon, D.; You, S.; Posthuma, N.; Bakeroott, B.; Stoffels, S.; van Hove, M.; Groeseneken, G.; Decoutere, S. Forward Bias Gate Breakdown Mechanism in Enhancement-Mode p-GaN Gate AlGaN/GaN High-Electron Mobility Transistors. *IEEE Electron. Device Lett.* **2015**, *36*, 1001–1003. [CrossRef]

16. Tallarico, A.N.; Stoffels, S.; Magnone, P.; Posthuma, N.; Sangiorgi, E.; Decoutere, S.; Fiegna, C. Investigation of the p-GaN Gate Breakdown in Forward-Biased GaN-Based Power HEMT. *IEEE Electron. Device Lett.* **2017**, *38*, 99–102. [CrossRef]

17. Ohmaki, Y.; Tanimoto, M.; Akamatsu, S.; Mukai, T. Enhancement-mode AlGaN/AlN/GaN high electron mobility transistor with low ON state resistance and high breakdown voltage. *Ipn. J. Appl. Phys.* **2006**, *45*, 1168–1170. [CrossRef]

18. Derluyn, J.; van Hove, M.; Visalli, D.; Lorenz, A.; Marcon, D.; Srivastava, P.; Geens, K.; Sijmus, B.; Vaene, J.; Kang, X.; et al. Low leakage high breakdown E-mode GaN DHFET on Si by selective removal of in-situ grown Si3N4. *Electron. Devices Meet. IEDM Tech. Digest.* **2009**, *2009*, 7.4.1–7.4.4.

19. Huang, S.; Liu, X.; Wang, X.; Kang, X.; Zhang, J.; Bao, Q.; Wei, K.; Zheng, Y.; Zhao, C.; Gao, H.; et al. High Uniformity Normally-OFF GaN MIS-HEMTs Fabricated on Ultra-Thin-Barrier AlGaN/GaN Heterostructure. *IEEE Electron. Device Lett.* **2016**, *37*, 1617–1620. [CrossRef]

20. Zhu, L.; Zhou, Q.; Chen, K.; Yang, X.; Zhang, B. A Novel Ultra-thin-barrier AlGaN/GaN MIS-gated Hybrid Anode Diode Featuring Improved High-temperature Reverse Blocking Characteristic. In *Proceedings of the 2020 IEEE 15th International Conference on Solid-State & Integrated Circuit Technology (ICSICT)*, Kunming, China, 3–6 November 2020.

21. Zhu, L.; Zhou, Q.; Yang, X.; Zhang, B. High-Performance Ultrathin-Barrier AlGaN/GaN Hybrid Anode Diode with AlO Gate Dielectric and In Situ SiN-Cap Passivation. *Trans. Electron. Devices* **2020**, *67*, 4136–4140. [CrossRef]

22. Zhou, Q.; Zhang, B.; Xiong, W.; Yang, X.; Chen, W. Ultrathin-Barrier-AlGaN/GaN Hybrid-Anode-Diode with Optimized Barrier Thickness for Zero-Bias Microwave Mixer. *Trans. Electron. Devices* **2020**, *67*, 1–6. [CrossRef]

23. Shi, Y.; Zhou, Q.; Cheng, Q.; Wei, P.; Zhu, L.; Wei, D.; Zhang, A.; Chen, W.; Zhang, B. Carrier Transport Mechanisms Underlying the Bidirectional Vth Shift in p-GaN Gate HEMTs Under Forward Gate Stress. *Trans. Electron. Devices* **2019**, *66*, 876–882. [CrossRef]

24. Chen, J.; Wang, C.; Jiang, J.; Hua, M. Investigation of Time-Dependent VTH Instability Under Reverse-bias Stress in Schottky Gate p-GaN HEMT. In *Proceedings of the 2020 IEEE 9th International Power Electronics and Motion Control Conference (IPEMC2020-ECCE Asia)*, Nanjing, China, 29 November–2 December 2020.

25. Murukesan, K.; Efthymiou, L.; Udrea, F. Gate stress induced threshold voltage instability and its significance for reliable threshold voltage measurement in p-GaN HEMT. In *Proceedings of the 2019 IEEE 7th Workshop on Wide Bandgap Power Devices and Applications (WiPDA)*, Raleigh, NC, USA, 29–31 October 2019.

26. Efthymiou, L.; Murukesan, K.; Longobardi, G.; Udrea, F.; Terrill, K. Understanding the threshold voltage instability during off-state stress in p-GaN HEMTs. *IEEE Electron. Device Lett.* **2019**, *40*, 1253–1256. [CrossRef]

27. Capriotti, M.; Alexewicz, A.; Fleury, C.; Gavagnin, M.; Bethge, O.; Visalli, D.; Derluyn, J.; Wanzenbock, H.D.; Bertagnolli, E.; Pogany, D.; et al. Fixed interface charges between AlGaN barrier and gate stack composed of in situ grown SiNx and Al2O3 in AlGaN/GaN high electron mobility transistors with normally off capability. *Appl. Phys. Lett.* **2014**, *104*, 113502. [CrossRef]

28. Liu, Z.; Huang, S.; Bao, Q.; Wang, X.; Wei, K.; Jiang, H.; Cui, H.; Li, J.; Zhao, C.; Liu, X.; et al. Investigation of the interface between LPCVD-SiNx gate dielectric and III-nitride for AlGaN/GaN MIS-HEMTs. *J. Vac. Sci. Technol. B.* **2016**, *34*, 41202. [CrossRef]

29. Huang, S.; Liu, X.; Wang, X.; Kang, X.; Zhang, J.; Fan, J.; Shi, J.; Wei, K.; Zheng, Y.; Gao, H.; et al. Ultrathin-Barrier AlGaN/GaN Heterostructure: A Recess-Free Technology for Manufacturing High-Performance GaN-on-Si Power Devices. *IEEE Trans. Electron. Devices* **2018**, *65*, 207–214. [CrossRef]

30. Shi, W.; Huang, S.; Wang, X.; Jiang, Q.; Yao, Y.; Bi, L.; Li, Y.; Deng, K.; Fan, J.; Yin, H.; et al. Low-thermal-budget Au-free ohmic contact to an ultrathin barrier AlGaN/GaN hetero-structure utilizing a micro-patterned ohmic recess. *J. Semicond.* **2021**, *42*, 092801. [CrossRef]