Thermally activated recovery of electrical conductivity in LaAlO$_3$/SrTiO$_3$

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Patterned structures of LaAlO$_3$/SrTiO$_3$ that exhibit a decrease in their electrical conductivity below 30 K, recover their higher conductivity upon warming in a thermally activated process. Two dominant energy barriers $E_b$ are identified: $E_{b1} = 0.224 \pm 0.003$ eV related to conductivity recovery near 70 K and $E_{b2} = 0.44 \pm 0.015$ eV related to conductivity recovery near 160 K. We discuss possible linkage to structural defects such as dislocations and twin boundaries.

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An attractive feature of the interface between the insulating oxides SrTiO$_3$ and LaAlO$_3$ (LAO/STO) [1, 2] is the ability to tune its transport properties by gate voltage [3–10]. However, it appears that there are other mechanisms that yield effectively the same effect without gating, including similar correlations between sheet resistance, carrier density and mobility. In a recent report [11], we showed that LAO/STO patterns with current path width smaller than 10 microns may exhibit below 30 K a significant decrease in their electrical conductivity, in connection with driving a sufficiently large current through the sample and/or applying an in-plane magnetic field. The initial high conductivity is recoverable upon applying a warming cycle.

Concomitantly with the field- and current-induced decrease in conductivity, the sheet carrier density ($n_s$) and mobility decrease, magnetotransport features linked to magnetism [12–14] are suppressed, and the nonuniformity of the sample increases. Namely, without applying a gate voltage, there are mechanisms that decrease conductivity. Furthermore, the mechanism also increases the nonuniformity of the conductivity, a feature that was directly observed with scanning probe microscopy [15, 16].

Here, we explore in detail the time and temperature dependence of the conductivity recovery as the sample is warmed up and show that it is well described by a thermally activated process. We extract two energy barriers: $E_{b1} = 0.224 \pm 0.003$ eV for the conductivity recovery near 70 K and $E_{b2} = 0.44 \pm 0.015$ eV for the conductivity recovery near 160 K. The conductivity exhibits a noticeable time dependence also above room temperature; however, it can not be correlated with a single thermally-activated process.

The results not only provide an explanation for a puzzling behavior reported previously [17, 18], they also provide quantitative details on what appears to be a low-temperature charge trapping mechanism that reduces the carrier density and increases nonuniformity. Thus, they provide new insights regarding two of the main issues concerning the transport properties of the LAO/STO interface: the existence and nature of localized charge carriers [19–21], and the origins of interface nonuniformity [12, 15, 16]. The identified energy scales and length scales are instrumental in identifying the trapping sites which would enable better understanding and control of the transport properties of the LAO/STO interface. Based on the relevant length scale of the phenomenon, we suggest that the trapping sites might be linked to crystal imperfections with similar length scales, such as dislocations and twin boundaries. Interestingly, a recent report shows accelerated recovery of conductivity (after its suppression with gating) in LaTiO$_3$/SrTiO$_3$ system at temperatures close to 70 and 160 K [22], raising the possibility that the relevance of the reported phenomenon exceeds the LAO/STO interface.

The samples were grown by pulsed laser deposition in an oxygen atmosphere of $7 \times 10^{-5}$ mbar on TiO$_2$ termi-
nated (001) STO surfaces at 770°C. The LAO thickness is 4 unit cells. The samples were cooled to room temperature in 400 mbars of O₂, including one hour oxidation step at 600°C. The laser fluence was about 0.8 J/cm², with repetition rate of 1 Hz. Patterning was done by photolithography as described in Ref. 23. Typical geometry of our samples is shown in Fig. 1c (inset). The current path width (d) in the patterns used for this research is 5 and 10 µm. The contact arrangement allows for simultaneous longitudinal and transverse voltage measurements.

Figure 1 shows typical protocols used to increase the sheet resistance $R_s$ at 5 K of a pattern with a current path width $d = 10$ µm. The figure shows the time dependence of $R_s$ while a current of 10 µA is driven through the pattern and a magnetic field is applied parallel to the LAO/STO interface (see Figs. 1b and 1c). Before and after the application of the high current and the high in-plane field, $R_s$ is measured with a low current (0.1 µA) and a zero magnetic field. The different curves in Fig. 1a are obtained with the same pattern in different cooling cycles. We note that the induced increase in $R_s$ may vary significantly in different cooling cycles.

Figure 2 shows the temperature dependence of $R_s$ during cooling (dashed line, blue) and warming (solid line, black) after increasing $R_s$ at 5 K (using the procedures shown in Fig. 1). Inset: Blow up of the low temperature data.

Figure 3 shows the sheet resistance ($R_s$) as a function of time after increasing $R_s$ at 5 K (see Fig. 1), as the temperature is increased in steps near 70 K (a) and 160 K (b). After each measurement, $R_s$ was recovered to its as-cooled value by warming the sample to 350 K. The lines are fits to Eq. 4.

Figures 4a and 4b demonstrate the recovery in a different way. They show the time dependence of the sheet conductivity $\sigma_s$ at different temperatures near 70 and 160 K.

Increased in steps near 70 and 160 K, respectively. For the two temperature intervals, the recovery rate increases with increasing temperature until it saturates.

Figures 4a and 4b show $R_s$ as a function of time (after a low-temperature increase in $R_s$) as the temperature is increased in steps near 70 and 160 K, respectively. For the two temperature intervals, the recovery rate increases with increasing temperature until it saturates.
The temperature dependence of $\sigma$ sheet conductivity ($\sigma_s$) was used for all measurements. Nevertheless, non-monotonic behavior of $\sigma_s$ as a function of temperature is observed in Figs. 1a and 1b, due to the different induced $R_s$ increases obtained in the different cooling cycles, as shown in Fig. 1.

Figures 5a and 5b show the change in the sheet carrier density $n_s$, extracted from Hall effect measurements, as a function of $\sigma_s$ as it recovers with time at 75 and 160.5 K, respectively. The inset of Figure 5b shows the temperature dependence of the extracted $n_s$ in the relevant temperature range during cooling the sample, when no relaxation effects exist. The Hall effect resistance was extracted by exchanging the current and voltage leads without reversing the field, as described in Ref. 12. For the Hall measurements we apply perpendicular fields up to 9 T with no observable effect on the rate of the conductivity recovery. We note that there is a linear correlation between $n_s$ and $\sigma_s$ during the conductivity recovery;

\[ dN_1 \frac{dt}{dt} = -W_{12} \cdot N_1 + W_{21} \cdot N_2 \]  
\[ dN_2 \frac{dt}{dt} = W_{12} \cdot N_1 - W_{21} \cdot N_2 \]

where $N_1$ and $N_2$ are the number of trapped and untrapped charge carriers, respectively, and $W_{12}$ ($W_{21}$) is the probability for a trapped (untrapped) charge carrier to be released (trapped).

FIG. 5: The sheet carrier density ($n_s$) as a function of the sheet conductivity ($\sigma_s$) at 75 K (a) and 160.5 K (b). Inset: The temperature dependence of $n_s$ in the relevant temperature range during cooling.

We assume that the low-temperature increase in $R_s$ is due to induced trapping of charge carriers and that the high-temperature recovery is due to their thermally-activated release. The corresponding rate equations are

\[ \frac{d\sigma}{dt} \propto (N_1^0 \cdot W_{12} - N_2^0 \cdot W_{21}) \cdot e^{-\frac{W}{k_B T}} \]  
\[ W(t) = f_0 \cdot e^{-E_b/k_B T} \]

suggesting an Arrhenius-type behavior. The lines in Figs. 6a and 6b are fits to Eq. 5.
We identify two dominant energy barriers: $E_{b_1} = 0.224 \pm 0.003 \text{ eV}$ related to conductivity recovery near 70 K and $E_{b_2} = 0.44 \pm 0.015 \text{ eV}$ related to conductivity recovery near 160 K. The value of $f_0$ is on the order of $10^{11} \text{s}^{-1}$ for the conductivity recovery near 70 K and on the order of $10^{10} \text{s}^{-1}$ for the conductivity recovery near 160 K.

The time dependence of the conductivity above room temperature is more complicated and pattern dependent. In several patterns, the conductivity increases initially; however, after some time it starts decreasing and appears to saturate. This may indicate that the time dependence of the conductivity above room temperature is affected by several processes. We note that assuming two competing relaxation processes yields

$$\sigma(t) = -A e^{-W^*t} + A' e^{-W't} + B,$$

which fits the data quite well. However, the fitting parameters are strongly pattern dependent so no clear conclusion can be obtained.

Based on our measurements, a plausible scenario is that the current- and field-induced suppression of conductivity below 30 K is due to trapping of charge carriers in sites characterized by well-defined trapping energies of $E_{b_1} = 0.224 \pm 0.003 \text{ eV}$ and $E_{b_2} = 0.44 \pm 0.015 \text{ eV}$. As trapping occurs only below 30 K, the existence of trapping sites does not affect conductivity in cooling; however, they do lead to the observed breaks in resistivity upon warming [17,18], provided low-temperature charge trapping occurred. The two trapping energies are responsible for the recovery in the vicinity of 70 and 160 K. Some conductivity recovery takes place also above room temperature; however, we could not extract specific trapping energies responsible for the recovery, probably due to the fact that more than one process takes place simultaneously.

We cannot identify based on our results what are the trapping sites. A very significant hint, however, is the characteristic length scale. As we noted [11], the low-temperature current- and field-induced conductivity suppression becomes significant in patterns with length scale on the order of microns. Furthermore, in patterns with length scale on the order of 2 microns the conductivity is occasionally suppressed by orders of magnitude. In addition, the conductivity suppression is accompanied by increased spatial variation in conductivity. Therefore, it appears that the trapping sites are related to crystal imperfections of similar length scales. Possible candidates are dislocations that were found to reduce conductivity and mobility of the LAO/STO interface with unusual temperature dependence during warming which is reminiscent to our results [18], or twin boundaries in SrTiO$_3$ which form due to structural phase transitions of the STO [24,26].

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