Analysis of base characteristics of trench gate field termination IGBT

Wang Bo1,*
1Wuhan Donghu University, CHINA

Abstract. Trench gate structure represents the latest structure of Insulated Gate Bipolar Transistor(IGBT). Because there are great differences in model analysis coordinate system and carrier transport between trench gate structure and planar gate structure, the modeling method using planar gate structure will inevitably have great deviation. Based on the characteristics of trench gate structure and model analysis coordinate system, the base region is divided into PNP and PIN by considering the two-dimensional effect of carriers. According to whether the trench of PIN part can be covered by depletion layer of PNP part, the specific base region current is analyzed. Finally, simulation and experimental verification are carried out.

1 Introduction

Insulated Gate Bipolar Transistor(IGBT) is a kind of composite device which combines the structure of field effect transistor and bipolar transistor, and absorbs their advantages at the same time. It is widely used in various medium and high power electronic devices. With the continuous improvement of the design level and manufacturing process of power semiconductor devices, the internal gate structure of IGBT has changed from the early planar gate to the later trench gate[1]. This trench gate structure absorbs the advantages of previous generations, and adopts some new structures and technologies. Its performance indexes, such as withstand voltage capability, conduction current capability, on-state voltage drop and switch damage, have been significantly improved, and it has excellent comprehensive performance[2-3].

Since the advent of IGBT in 1980s, many literatures have carried out relevant research on IGBT mathematical model, and the early theory of planar gate structure has been relatively mature. The most representative Hefner model studies the steady-state and transient mathematical models of IGBT by using the theories of bipolar carrier transport in base region, quasi-static approximation and transient carrier linear distribution, and the existing IGBT mathematical models are basically developed on the basis of this model[4-5]. However, this model only makes a one-dimensional approximation to the distribution of excess carriers in the base region, and seldom studies the influence of the change of trench gate structure on the two-dimensional distribution. Other literatures have adopted the method of two-dimensional distribution of carriers, which promotes the research of trench gate structure[6].

In this paper, based on the characteristics of trench gate structure and model analysis coordinate system, by dividing the base region into PNP and PIN and considering the two-dimensional effect of carriers in transient process, the transient specificity of the base region current is analyzed according to whether the trench of PIN can be covered by depletion layer of PNP. Finally, simulation and experimental verification are carried out.

2 Theoretical analysis

2.1 Trench gate IGBT model coordinate system

The planar schematic diagram of trench gate field termination IGBT structure is shown in Figure 1, which is usually regarded as a BJT structure driven by MOSFET. The vertical width $L_x$ of the trench reflects the depth of the trench into the base region, and the horizontal width $L_y$ of the trench reflects the proportion of the base region occupied by the trench. Among them, the P body region on the upper part of the base region is very small relative to the width of the base region, which is similar to the planar gate type. While the trench gate on the upper part of the base region goes deep into the base region, and the depth can’t be ignored compared with the width of the base region. Therefore, different from planar gate structure, trench gate structure needs to consider the influence of trench.
The mathematical model analysis coordinate system of trench gate field termination IGBT is shown in Figure 2. The horizontal axis of FS layer coordinate system is \( x^* \), the horizontal axis of base area coordinate system is \( x \), and the vertical axis of coordinate system is \( y \). The width of base region is \( W_B \), and the width of FS layer is \( W_{H} \). According to the carrier movement characteristics of \( p \) body region and trench gate in the base region, the base region is divided into PNP at the lower part of \( p \) body region and PIN at the lower part of gate. The depletion layer width of PNP part is \( W_{dep,PNP}(t) \), and the quasi-neutral base region width is \( W_L \). The electron current and hole current of PNP part and PIN part are calculated respectively, and then the electron current and hole current in the base region are summarized.

2.2 Analysis of characteristics of base region

Under IGBT switching transient, the excess carrier concentration in the base region meets the conditions of large injection and low gain, and the continuity equation under bipolar transport condition is:

\[
\frac{d^2(\delta p(x,t))}{dx^2} + \frac{1}{L_L^2} \frac{d}{dt}(\delta p(x,t)) = 0
\]  
(1)

Where: \( \delta p(x,t) \) refers to excess carrier concentration; \( L_L \) is the base carrier bipolar diffusion length; \( D_L \) is the bipolar diffusion coefficient of base carriers.

When the depletion layer in the PNP part of the base region widens to the PIN part, it bends due to the influence of the gate voltage. The width of the depletion layer formed by the collector-emitter voltage and the gate voltage in the PIN part is:

\[
W_{dep,PIN}(t) = \sqrt{\frac{2\varepsilon_{si}(V_{bc}(t)-V_{GS}(t)+V_{Td})}{qN_L}}
\]  
(2)

In which: \( \varepsilon_{si} \) is the dielectric constant of silicon; \( V_{bc}(t) \) is BJT base-emitter voltage drop; \( V_{GS}(t) \) is the gate voltage; \( V_{Td} \) is that threshold voltage of the gate-drain overlap depletion layer; \( q \) is the electronic quantity; \( N_L \) is the base doping concentration.

According to whether the depletion layer of PNP part can be widened to cover the trench gate of PIN part, that is, the relationship between the width of depletion layer of PIN part and the width of trench gate deep into the base region, two cases are analyzed.

The first case: the depletion layer of PIN part failed to widen the trench gate area covering PIN part, that is:

\[
W_{dep,PIN}(t) \leq L_x
\]  
(3)

The depletion layer of PNP part expands along the Y-axis direction and fails to surround the trench area of PIN part, as shown in Figure 2, the depletion layer expands to the 1 position. Then the PNP part and PIN part are described separately, and then the electron current flowing into the trench and the hole current flowing into the cathode are synthesized. The movement process of carriers is understood as follows: electrons are injected from the trench gate and are continuously converted into holes in the process of moving to the FS layer; When holes are injected from the anode and move to the cathode after passing through the FS layer, the hole concentration in the PNP part decreases gradually, and is considered to be approximately zero at the depletion layer. The hole concentration in the PIN part decreases gradually similar to that in the PNP part, and turns to flow into the depletion layer along the trench wall at the trench.

For PIN, the depletion layer width and quasi-neutral width are:

\[
\begin{align*}
W_{dep,PIN}(t) &= \sqrt{\frac{2\varepsilon_{si}V_{ce}(t)}{qN_L}} \\
W_L(t) &= W_B - W_{dep,PNP}(t)
\end{align*}
\]  
(3)

The two boundary conditions of excess carrier concentration are:

\[
\begin{align*}
\delta p(x = 0,t) &= P_0(t) \\
\delta p(x = W_L(t),t) &= 0
\end{align*}
\]  
(4)

The carrier concentration distribution in the transient process is approximated by a moving linear distribution, and the expression of space-time distribution of excess carrier concentration in the base region is obtained. The distribution process can be represented as Figure 3.
The distribution of excess carrier concentration in base region during switching transient process

For the PIN part, the excess carrier concentration has the same distribution as that of PNP part in most areas of X axis. When near the trench, the excess carrier bends to PNP part and is swept into the depletion layer together with the excess carrier of PN part. Therefore, the excess carrier concentration distribution of the PIN part is approximately considered to be the same as that of the PNP part at the same distance ($0 - (W_{d} - L_{s})$).

The quasi-neutral width of the PIN portion is determined by the width of the base region and trench metallurgy junction, and the excess carrier concentration distribution of the PIN portion has the same form as that of the PNP portion. The expressions of electron current and hole current in PNP part and PIN part are obtained by using bipolar transport equations of electron current and hole current respectively.

Among them, the currents of PNP part and PIN part satisfy:

$$\frac{I_{PNP}}{I_{PIN}} = \frac{y - L_{y}}{L_{y}}$$

The second case: the depletion layer of PNP part can widen the trench gate area covering PIN part, that is $W_{dep,PIN}(t) > L_{x}$.

The depletion layer formed by PNP expands in the Y-axis direction to cover the trench area of PIN. As shown in Figure 2, when the depletion layer expands to the 2 region, the expressions of carrier concentration distribution, electron current and hole current in the base region are obtained by a processing method similar to that of planar gate. The difference from planar gate is the calculation of junction capacitance of depletion layer under the gate.

The transient description of trench gate structure is similar to that of planar gate structure. The bipolar transport equation of electron current and hole current in base region is:

$$\begin{align}
I_{el}(x,t) &= \frac{b_{1}}{1 + b_{1}} I + qD_{y} \frac{d(\delta n(x,t))}{dx} \\
I_{ph}(x,t) &= \frac{1}{1 + b_{1}} I - qD_{x} \frac{d(\delta p(x,t))}{dx}
\end{align}$$

The expression of hole current is obtained, and the hole current flowing into and out of the base region is obtained. Therefore, for the above two cases, the hole current flowing into the base region and the hole current flowing out of the base region are obtained respectively.

### 2.3 Simultaneous analysis of base area and FS layer

The hole current and electron current flowing through the boundary between FS layer and base region are equal, that is, the current at the boundary is continuous, but the excess carrier concentration is discontinuous, as shown in Figure 4.

$$I_{pl}(0,t) = I_{ph}(x^*,t)$$

According to whether the depletion layer of the base region can be widened to cover the trench gate region and the relationship between the total charge, the expression of the charge of the base region from the total charge is obtained.

Then, according to the quasi-thermal balance relationship between FS layer and base region and the current at the boundary, the relationship between the hole current and the total charge of internal BJT collector and base is obtained continuously, thus eliminating the intermediate quantity of the assumed boundary concentration.

### 3 Experimental verification

A trench gate field termination IGBT module with KW60N65S model is selected, and the rated voltage and current levels are 650V and 60A respectively. The experimental test conditions are as follows: the bus voltage is set to 400V, the driving voltage is 15V when switching on, the load inductance is 500uH, and the room temperature. Double pulse test method is used to test IGBT collector-generator voltage and collector current, and the IGBT turn-off waveform is shown in Figure 5.

With the trench gate model established in this paper, the parameters required for simulation are provided by the device manufacturer, and the stray inductance of the line is set to 1uH according to the experimental test circuit. After the initial value is input, it is solved, and the turn-off transient simulation waveforms of IGBT are obtained as shown in the Figure 5.
It can be seen from Figure 5 that the IGBT model considering trench gate structure is basically consistent with the experimental waveform. The error causes are analyzed as follows:

1. The gate drive circuit, load circuit and stray inductance of the device itself are difficult to be accurate;
2. It is difficult to unify the average effect of carrier lifetime in the model with the dispersion in practical work;
3. It is difficult to give consideration to the idealization and uniqueness of structural parameters and the dispersion of device technology in model simulation;
4. Reasonable assumptions and simplification in the modeling process;
5. Errors introduced in the process of experimental testing.

4 Summary

Compared with the traditional planar gate structure, this paper divides the base region into PNP and PIN according to the characteristics of carrier movement in the trench gate structure, and then analyzes the carrier transport and current in the base region according to whether the depletion layer of PNP can cover the trench of PIN, which reflects the characteristics of trench gate structure.

Acknowledgments

This work was supported by the 2018 year Youth Foundation of Wuhan Donghu University(2018dhzk005), and the Scientific Research Project of Hubei Education Department in 2019(B2019267).

References

1. Chen Xudong, Cheng Jianbing, Teng Guobing, et al. Novel trench gate field stop IGBT with trench shorted anode[J]. Journal of Semiconductors, 2016, 37(5): 61-64.
2. Qin Mengliang, Li Zehong, Zhang Bo, et al. Trench gate IGBT structure with floating P region[J]. Journal of Semiconductors, 2010,31(2):11-13.
3. Fu Qiang, Zhang Bo, Luo Xiaorong, et al. A dual-gate and dielectric-inserted lateral trench insulated gate bipolar transistor on a silicon-on-insulator substrate[J]. Chinese Physics B, 2013,22(7):1-5.
4. Bryant A T, Liu Liqing, Santi E, et al. Modeling of IGBT resistive and inductive turn-on behavior[J]. IEEE Transactions on Industry Application, 2008,44(3):904-914.
5. Iannuzzo F, Busatto G. Physical CAD model for high voltage IGBTs based on lumped-charge approach[J]. IEEE Transactions on Power Electron, 2004,19(4):885-893.
6. Udrea F, Chan S, Thomson J. Development of the next generation of insulated gate bipolar transistors based on trench technology[C]. The 27th European Solid-State Device Research Conference,1997:504-507.