Novel Wilkinson Power Divider with an Isolation Resistor on a Defected Ground Structure with Improved Isolation

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Abstract: A modified Wilkinson Power Divider is proposed in this paper that utilizes defected ground structure (DGS) in parallel with an isolation resistor. The proposed DGS section is incorporated between the output ports, and the isolation resistor is soldered in parallel with the DGS in the ground plane, instead of on the top plane as in a conventional Wilkinson power divider, to achieve improved or preferable isolation. The proposed design is comprised of two pairs of microstrip transmission lines with equal impedances and varied electrical lengths. The parameters of the main circuit and the DGS section are acquired separately. The parameters of the proposed main circuit are derived by applying conjugate matching theory. Dumbbell-shaped DGS is introduced in the ground plane between the output ports, which acts as a parallel resonator, yielding an attenuation pole at the resonant frequency that contributes to improved isolation. By applying the previous well-known circuit theory, the lumped elements of the equivalent circuit of the DGS were achieved. The physical dimensions of the equivalent circuit for the DGS section were obtained by three-dimensional EM simulation. The measured results show improved isolation, return loss and better bandwidth as compared with other similar works. Furthermore, the proposed circuits designed at resonating frequencies of 3 and 2 GHz presented comparatively good return losses, \( S_{11} \) of about \(-25.54\) and \(-31.24\) dB, respectively, and achieved improved isolations, \( S_{32} \) between the output ports, in an order of about \(-40.83\) and \(-36.05\) dB, respectively, which is rather exceptional and desirable.

Keywords: Wilkinson power divider; isolation; defected ground structure

1. Introduction

Wilkinson Power Divider (WPD) is a vital component in communication systems and plays an important role in many significant devices, such as mixers and antenna systems [1]. Since the conventional Wilkinson power divider has a simple design method, well-matched ports and relatively good isolation between output ports, it has been widely used as an equal power divider and in several wireless systems. The conventional WPD is an equal power divider and consists of two quarter-wavelength transmission lines. Each transmission line has an impedance of \( \sqrt{2}Z_0 \), and the isolation resistor of \( 2Z_0 \) is plugged between these lines to achieve good isolation. In contrast, an unequal Wilkinson power divider is comprised of quarter-wave transmission lines having different impedances [2]. Although several reports have addressed the DGS and Wilkinson power divider, the majority of such works were related to the unequal Wilkinson power divider and hybrid couplers by exploiting the effect of change in effective inductance or capacitance of DGS in the ground of a substrate, to reduce the overall size of the circuit or suppress the harmonic effects [3–6]. In [7], etched DGS is employed in the ground plane to yield an additional effective inductance that results in an unequal impedance between the output ports. Two dumbbell-shaped DGSs are utilized to suppress the harmonics, thus resulting in
the reduction of the overall circuit size and complexity [8]. In [9], DGS is introduced in the
ground plane to reduce the size of the power divider as well as to improve the performance
of the power divider.

As the DGS can also delay the group velocity, with the help of a single cell DGS
or single dumbbell-shaped DGS, it is possible to obtain a single attenuation pole or a
frequency response characteristic of a low-pass filter [10]. In [11], DGS is proposed to
utilize as a resonator by illustrating its applications in various bandpass filter designs.
Two dumbbell-shaped DGSs are used to achieve a two-pole bandpass filter, hence proving
the equivalent potential of DGS to design dividers, couplers and filters and enhance the
performance of respective microwave components. As the dumbbell-shaped DGS can act
as a parallel resonator, it thus produces an attenuation pole at a certain frequency [12].
Therefore, this characteristic of DGS can be used to achieve preferable isolation by utilizing
it in parallel with the isolation resistor.

In contrast to conventional WPD, which comprises two $\lambda/4$ transmission lines and
an isolation resistor soldered on a top plane between these two quarter-wavelength trans-
mision lines, this paper proposes a modified Wilkinson power divider that introduces
dumbbell-shaped DGS in the ground plane and the isolation chip resistor is soldered
on the DGS to yield desired or better isolation. Each quarter-wave branch of a conven-
tional Wilkinson power divider is substituted by two sections of transmission line with
the characteristic impedance of $Z_1$ and $Z_2$ and the length of $l_1$ and $l_2$, respectively. The
characteristic impedances of both sections $Z_1$ and $Z_2$ are fixed at 60 $\Omega$. To design the
proposed DGS-powered WPD circuit, the physical dimensions and equivalent lumped
circuit parameters of the DGS section and the proposed WPD were extracted by applying
the field analysis method and circuit analysis theory. The physical dimensions of the DGS
section were obtained separately by performing EM simulation. These methods were
successfully applied to extract the required values and dimensions of the proposed equal
power divider. The dumbbell-shaped DGS pattern is realized between the output ports,
to obtain comparable or preferably better isolation as compared to conventional WPD.
Figure 1 shows the layout of the proposed WPD with its DGS section. The goal of the
proposed circuit is to achieve better return loss in order of more than $-20$ dB and improved
isolation, i.e., $S_{32} \leq -30$ dB in the operating bandwidth.

![Figure 1. Three-dimensional view of the proposed Wilkinson Power Divider (WPD) at the operating frequency of 3 GHz with the DGS section etched in the ground plane.](image-url)
2. Design Equations

2.1. Even and Odd Mode Analysis without DGS Elements

The conventional WPD is comprised of two transmission lines having an electrical length of quarter wavelength with line impedance of \( \sqrt{2} Z_0 \) and an isolation resistor having a resistance of \( 2Z_0 \) between these transmission lines. Figure 2a shows the proposed circuit, which substitutes these two lines by transmission lines with line impedances \( Z_1 \) and \( Z_2 \) and electrical lengths \( \theta_1 \) and \( \theta_2 \), respectively. The admittances of the transmission lines with the impedances \( Z_1 \) and \( Z_2 \) are \( Y_1 \) and \( Y_2 \), respectively, where

\[
Y_1 = \frac{1}{Z_1}, \quad Y_2 = \frac{1}{Z_2}, \quad \text{and} \quad Y_0 = \frac{1}{Z_0}
\]

![Diagram](image)

(a) An equal-split WPD with transmission lines.

![Diagram](image)

(b) Normalized and symmetric form of WPD.

**Figure 2.** Proposed WPD circuit with \( \lambda / 4 \) transmission lines of characteristic impedances of \( Z_1 \) and \( Z_2 \), respectively.

A lumped resistor \( 2R \) is introduced between the transmission lines to achieve maximum isolation. A schematic diagram of the proposed divider, which realizes an equal power division at an arbitrary frequency, is illustrated in Figure 2b. The reference impedance of the circuit is \( Z_0 \). The modeling starts with the even mode analysis by referring to Figure 2b, where the loading impedances \( Z_0 \) are considered purely resistive to simplify the circuit evaluation. The circuit given in Figure 2b is symmetric across the midplane, i.e., along the dotted line. Therefore, there is no current flow through the \( 2R \) resistor. Thus, the circuit in Figure 2b can be bisected with open circuits at these points to achieve the circuit shown in Figure 3a. Then, the input admittance at Port 2 is obtained as:

\[
Y_{in}^{e} = Y_1 \left( \frac{\frac{Y_0}{2} + jY_1 \tan \theta_1}{Y_1 + \frac{Y_0}{2} \tan \theta_1} \right) = Y_1 \left( \frac{\frac{Y_1 Y_0}{2} + \frac{Y_1 Y_0}{2} \tan^2 \theta_1}{Y_1^2 + \left( \frac{Y_0}{2} \tan \theta_1 \right)^2} \right)
\]

(1)
By applying conjugate matching in Figure 3a, the input admittance at Port 2 is obtained:

\[ Y_{in} = Y_0 - jY_2 \tan \theta_2 \]  

(2)

By comparing Equations (1) and (2) with respect to their real and complex parts, Equations (3) and (4) are obtained:

\[ \frac{1}{\left( \frac{1}{2} \right) \sec^2 \theta_1} = 1 \]  

(3)

\[ Y_1 \tan \theta_1 \left( Y_1^2 - \left( \frac{Y_0}{\pi} \right)^2 \right) = -Y_2 \tan \theta_2 \]  

(4)

In the case of odd-mode excitation, because of the symmetry, there is null voltage along the mid-section of the circuit in Figure 2. Considering Port 2, the resistance of the isolation resister is realized as half, i.e., \( R \), as shown in Figure 3b.

The input admittance at Port 2 can be calculated as:

\[ Y_{in} = Y_2 \left( \frac{\frac{1}{Y_2} + jY_2 \tan \theta_2}{Y_2 + \frac{1}{R} \tan \theta_2} \right) = Y_2 \left( \frac{\frac{Y_2}{\pi} + \frac{Y_2}{\pi} \tan^2 \theta_2}{Y_2 + \left( \frac{1}{R} \right)^2} \right) \]  

(5)

By applying the conjugate matching in Figure 3b, the input admittance at Port 2 is obtained:

\[ Y_{in} = Y_0 + jY_1 \cot \theta_1 \]  

(6)
By comparing Equations (5) and (6) with respect to their real and complex parts, Equations (7) and (8) are obtained:

\[
Y_2 \left( \frac{Y_2 \sec^2 \theta_2}{Y_2^2 + \left( \frac{1}{R} \tan \theta_2 \right)^2} \right) = Y_0
\]  

(7)

\[
\tan \theta_2 \left( \frac{Y_2 - \left( \frac{1}{R} \right)^2}{Y_2^2 + \left( \frac{1}{R} \tan \theta_2 \right)^2} \right) = Y_1 \cot \theta_1
\]  

(8)

By solving the Equation (3), the electrical length of the admittance line \( Y_1 \) is obtained:

\[
\tan^2 \theta_1 = T_1 = \frac{1}{1 - 2 \left( \frac{Y_0}{2Y_1} \right)^2} = \frac{1}{1 - 2\beta_1}
\]  

(9)

where

\[
\beta_1 = \left( \frac{Y_0}{2Y_1} \right)^2
\]  

(10)

By evaluating Equation (7), the electrical length \( \theta_2 \) of the transmission line (TL2) is achieved. The electrical length of the admittance line \( Y_2 \) is then:

\[
\tan^2 \theta_2 = T_2 = \frac{\frac{Y_0 R - 1}{1 - Y_0 R \left( \frac{1}{R} \right)^2}}{1 - \frac{\beta_2 R (Y_0 R - 1)}{\beta_2 R - 1}}
\]  

(11)

where

\[
\beta_2 = \left( \frac{Y_2}{Y_0} \right)^2
\]  

(12)

Furthermore, subtracting Equation (4) from Equation (8) yields:

\[
\frac{Y_2 \tan \theta_2 \left( \frac{Y_2 - \left( \frac{1}{R} \right)^2}{Y_2^2 + \left( \frac{1}{R} \tan \theta_2 \right)^2} \right)}{Y_1 \left( \frac{Y_2 - \left( \frac{1}{R} \right)^2}{Y_2^2 + \left( \frac{1}{R} \tan \theta_2 \right)^2} \right)} = Y_1 \cot \theta_1 + Y_2 \tan \theta_2
\]  

(13)

Equation (13) is rearranged as:

\[
Y_2 \tan \theta_2 \left[ \left( \frac{Y_2 - \left( \frac{1}{R} \right)^2}{Y_2^2 + \left( \frac{1}{R} \tan \theta_2 \right)^2} \right) - 1 \right] = Y_1 \cot \theta_1 \left[ 1 + \frac{\tan^2 \theta_1 \left( \frac{Y_2 - \left( \frac{1}{R} \right)^2}{Y_2^2 + \left( \frac{1}{R} \tan \theta_2 \right)^2} \right)}{Y_1^2 + \left( \frac{Y_0 \tan \theta_1}{2} \right)^2} \right]
\]

Thus, by inserting \( T_2 \) from Equation (11) into Equation (13) and rearranging it, Equation (14) is obtained:

\[
f(Y_1, Y_2, R) = Y_2 \sqrt{T_2 \left[ \left( \frac{Y_2 - \left( \frac{1}{R} \right)^2}{Y_2^2 + \left( \frac{1}{R} \tan \theta_2 \right)^2} \right) - 1 \right]} - \alpha = 0
\]  

(14)
where

$$\alpha = Y_2 \tan \theta_2 \left[ \frac{\left( Y_2^2 - \left( \frac{1}{R} \right)^2 \right)}{Y_2^2 + \left( \frac{1}{R} \tan \theta_2 \right)^2} - 1 \right]$$

(15)

As Equation (14) is a function of $Y_0$, $Y_1$, $Y_2$ and $R$, Equations (9), (11) and (14) can be plugged into a script or spreadsheet utilizing Matlab or Microsoft Excel, in order to calculate the values of $\theta_1$, $\theta_2$ and $R$.

2.2. Parameters Extraction of DGS Section

The parameters of the DGS section were extracted separately. The DGS circuit exhibits the characteristics of a parallel LC circuit. Thus, it can serve as a parallel LC resonator circuit for various purposes in a variety of components. Figure 4a,b shows the equivalent circuits of a one-pole Butterworth-type low-pass filter and its equivalent DGS circuit, respectively [13].

The reactance of the DGS unit is as follows:

$$X_{LC} = \frac{1}{\omega_0 C} \left( \frac{\omega_0}{\omega} - \frac{\omega}{\omega_0} \right)$$

(16)

where $\omega_0$ is the resonant angular frequency. The series inductance of the Butterworth low-pass filter can be acquired as follows:

$$X_L = \omega' Z_0 g_1$$

(17)

where $\omega'$ is the angular frequency of the 1st-order, one-pole Butterworth prototype low-pass filter and $g_1$ is the element value, i.e., $g_1 = 2.0$. To achieve low-pass filter response, the DGS circuit should get an equal net reactance at cutoff frequency $\omega_c$ as the prototype low-pass filter at $\omega' = 1$, i.e.,

$$X_{LC|\omega=\omega_c} = X_L|_{\omega'=1}$$

(18)

Thus, from this approximation,

$$C = \frac{\omega_c}{Z_0 g_1} \left( \frac{1}{\omega_0^2 - \omega_c^2} \right)$$

(19)

and

$$L = \frac{1}{4\pi^2 f_0^2 C}$$

(20)
Equations (19) and (20) demonstrate the lumped parameters, i.e., the capacitance and inductance of the DGS circuit. Where $\omega_c$ and $f_0$ are cutoff angular frequency and resonant frequency, respectively.

3. Proposed Theory and Design Flow

As the dumbbell-shaped DGS exhibits the properties of a parallel resonant LC circuit, the equivalent circuit of the DGS with an isolation resistor can be depicted (Figure 5). The figure shows that, after incorporating the isolation resistor, the DGS circuit has three paths, i.e., $x$, $y$ and $z$ via $R$, $L$ and $C$, respectively. Thus, the idea is that at the resonant frequency $f_0$, paths $y$ and $z$ resist the flow of current. Therefore, these paths demonstrate the characteristic of open circuit and the current will follow the alternative path i.e., path $x$, which has the resistor across it. Hence, the divider attains maximum isolation due to resistance across path $x$, as the current faces maximum resistance. Thus, in this way, by introducing DGS in the ground plane and installing the isolation resistor across it, rather than directly mounting it on a top plane of the transmission line, improved isolation can be achieved.

![Figure 5. Dumbbell-shaped DGS section between the transmission lines TL2 with its equivalent lumped elements and physical dimensions.](image)

Figure 6a,b shows step-by-step algorithms for the design of the proposed WPD and DGS section, respectively, via flow charts. Although the initial calculated values of parameters will not yield an absolute required result, they pave the way towards obtaining an approximate result. In this way, the variations in the variables of different parameters and their overall effect can be judged and approximated, which leads the way to tune the circuit to achieve the preferable outcome. The steps for the design of the circuit may seem complex and rigorous, but the design flow illustrated in Figure 6a,b can provide an easy and comprehensible way to obtain an acceptable result.
4. Design and Measurement

To demonstrate the proposed design method, two Wilkinson power dividers were designed at the resonant frequencies of 2 and 3 GHz by following the algorithm below, which is illustrated in the flowchart of Figure 6a.

1. Draw the proposed WPD circuit and label its essential parameters.
2. Apply circuit analysis and conjugate matching theory to extract the required parameters.
3. Utilize Microsoft Excel or MATLAB to compose a spreadsheet program or script, in order to calculate the unknown parameters, i.e., $\theta_1$, $\theta_2$ and isolation resistance $R$. 
4. Perform circuit simulation and three-dimensional field analysis using ANSYS Electronics Desktop.

(a) Modified WPD.

(b) DGS section.
(5) Insert the DGS section into the ground plane utilizing the physical dimensions \( a \), \( b \) and \( g \), which are acquired by following the procedure illustrated in Figure 6b, and perform EM simulation.

(6) If the achieved results are desirable, then fabricate the proposed circuit to check the validity of the circuit.

As the proposed circuit should be viable and compact for fabrication and measurement in order to achieve preferable results, a compromise was made between the availability of the substrate and the compactness of the device as well as the constraints of the fabrication facility. Hence, the impedances of both pairs of transmission lines of the divider, i.e., \( Z_1 \) and \( Z_2 \), are fixed at 60 \( \Omega \) to obtain improved results with the best substrate at hand. Thus, by following the design method given in Figure 6a, the electrical lengths of both transmission lines with impedances \( Z_1 \) and \( Z_2 \) are calculated. Other parameters are obtained by using ANSYS HFSS via circuit and EM Simulation.

The proposed circuit is simulated with different substrates (available) to examine the performances of substrates and choose the best accessible, available and reliable one which can accommodate the preferred outcome of the proposed WPD. Figure 7 shows that all substrates under test have the capacity to achieve good results with minor variations in return loss and isolation, as shown in Table 1. However, according to the data shown in Table 1, Taconic substrate is well suited for the proposed WPD as it yields better isolation, i.e., \( S_{32} = -55.84 \) dB, and return loss, i.e., \( S_{11} = -55.02 \) dB, as compared to other substrates. Furthermore, in the case of FR4 and Rogers, due to low build quality, noise creates signal integrity problems during measurement. Hence, these factors can affect the efficiency and overall performance of the WPD as compared to WPD powered with Taconic substrate.

Therefore, the proposed circuits were simulated and fabricated on a Taconic substrate with a dielectric constant of \( \varepsilon_r = 2.97 \), loss tangent of \( \tan \delta = 0.0012 \) and thickness of \( H = 0.76 \) mm. Table 2 shows the calculated optimized values and dimensions of both WPDs.

**Table 1.** Circuit simulation results of the proposed WPD designed at 3 GHz with different substrates.

| Substrate     | \( \varepsilon_r \) | \( H \) (mm) | \( S_{11} \) (dB) | \( S_{32} \) (dB) | Bandwidth (GHz) |
|---------------|----------------------|--------------|-------------------|-------------------|-----------------|
| Rogers RO3006 | 6.15                 | 1            | -43.55            | -46.36            | 0.56            |
| FR4           | 4.4                  | 1            | -38.98            | -38.21            | 0.57            |
| Taconic       | 2.97                 | 0.76         | -55.02            | -55.84            | 0.56            |

**Table 2.** Calculated design parameters using the proposed method (\( \varepsilon_r = 2.97 \), \( H = 0.76 \) mm, \( R = 36 \) \( \Omega \)).

| Parameter     | Impedance \( Z \) (\( \Omega \)) | Electrical Length \( \theta \) | Physical Width \( w \) (mm) | Physical Length \( l \) (mm) |
|---------------|----------------------------------|-------------------------------|-----------------------------|-----------------------------|
| TL1:3GHz      | \( Z_1 = 60 \)                   | \( \theta_1 = 117.88 \)       | \( w_1 = 1.368 \)           | \( l_1 = 21.41 \)           |
| TL2:3GHz      | \( Z_2 = 60 \)                   | \( \theta_2 = 27.88 \)        | \( w_2 = 1.368 \)           | \( l_2 = 5.065 \)           |
| TL1:2GHz      | \( Z_1 = 60 \)                   | \( \theta_1 = 117.88 \)       | \( w_1 = 1.368 \)           | \( l_1 = 32.16 \)           |
| TL2:2GHz      | \( Z_2 = 60 \)                   | \( \theta_2 = 27.88 \)        | \( w_2 = 1.368 \)           | \( l_2 = 7.606 \)           |
| 50 \( \Omega \) Line | \( Z_0 = 50 \)           | -                             | \( w_3 = 1.8707 \)          | \( l_3 = 13.699 \)          |
Therefore, Equations (9), (11) and (14) were plugged into Microsoft Excel or MATLAB by fixing $Y_1$ and $Y_2$, while changing the value of $R$ from 1 to $nR$, i.e., 1 to 114 $\Omega$. Hence, applying this method, the solution of Equation (14) approached zero when $R$ approached 36 $\Omega$.

The physical dimensions of the DGS cell, i.e., $a$, $b$ and $g$, were achieved by following the steps illustrated in Figure 6b:

1. Guess the dimensions of the DGS section (trial-and-error method) by taking an approximation from its calculated lumped elements i.e., capacitance and inductance given in Equations (19) and (20).
2. Select dielectric material and thickness.
3. 3D simulate the DGS section separately by taking the TL2 line and fabricate the DGS section below the TL2 transmission line.
4. Perform full-wave analysis and extract the S-parameters versus frequency, as shown in Figure 8.
5. Change the dimensions of the DGS iteratively, until the frequency response meets the required acceptable result.
6. Take the dimensions, i.e., $a$, $b$, and $g$, if the simulated circuit exhibits the required preferable S-parameters, i.e., $S_{21}$ at the attenuation pole location is less than $-20$ dB.

Therefore, to achieve the required parameters of the DGS section, i.e., $a$, $b$ and $g$, a separate EM simulation was performed on the transmission line, TL2, of length $2 \times l_2$, as shown in Figure 9, to get the approximate values of $a$, $b$, and $g$ from the preferred results, as shown in Figure 8. Figure 9 shows the photographs of the fabricated one-pole Butterworth low-pass filter designed at 3 GHz with the DGS section under the TL2 microstrip line of length $2 \times l_2$ for the approximation of the physical dimensions of the DGS section. The preferred isolation between Ports 2 and 3 is more than $20$ dB, and the proposed WPDs are designed at 3 and 2 GHz. Therefore, to achieve this target, an equivalent low-pass filter with a dumbbell-shaped DGS etched in the ground plane below the transmission line TL2 of length $2 \times l_2$ needs to be designed for each WPD. The dimensions $a$, $b$ and $g$ of the DGS are changed iteratively until the $S_{21}$ is $<-20$ dB. The preferred $S_{21}$ of both WPDs are obtained, as illustrated in Figure 8. The figure shows that the $S_{21}$ parameters of both WPDs are in the order of approximately equal to or more than $-20$ dB. Hence, these results can be taken as adequate reference points for the preferred attenuation poles and

![Figure 7. Simulated S-parameters results of the proposed circuit with different substrates.](image-url)
the cutoff frequencies. The dimensions a, b and g of the DGS section are obtained from this EM simulation.

Figure 8. Simulated S–parameters results of the DGS sections for the approximation of the physical dimensions.

(a) Top View. (b) Bottom View.

Figure 9. Photographs of the fabricated one-pole Butterworth low-pass filter designed at 3 GHz with the DGS section under the transmission line of the length $2 \times l_2$ for the approximation of the physical dimensions a, b and g of the DGS section.

To get the parameters of the DGS sections, Figure 8 clearly shows that the resonant frequency or attenuation pole location and cutoff frequency are 3 and 1.5 GHz, respectively, for WPD designed at 3 GHz and 2 and 1 GHz, respectively, for the WPD designed at 2 GHz. Thus, the approximated values of the preferred resonant frequency and cutoff frequency were plugged into Equations (14) and (15) to obtain the values of Capacitance and inductance of an equivalent circuit of the DGS for each divider. To achieve the required cutoff frequency and attenuation pole at a certain frequency, the simulation result should illustrate the characteristics of both low pass and bandstop filter responses simultaneously. To make an effective guess for the initial dimensions of the DGS section, this phenomenon is crucial, i.e., as the etched area, i.e., $a \times b$ increases, the cutoff frequency and attenuation pole location move to lower frequencies. Although the variation in the gap distance g results in no change in the cutoff frequency, as the gap distance increases, the location of the attenuation pole shifts to higher frequency [14]. Thus, to attain the preferred result, these approximations are very helpful and are handy to tune and comprehend the best
possible effect of each step. The calculated values of the equivalent lumped elements of the
equivalent DGS circuits and their respective physical dimensions are shown in Table 3 for
both WPDs.

Table 3. Lumped values and physical dimensions of DGS sections ($\varepsilon_r = 2.97, H = 0.76\, \text{mm}$).

| Operating Frequency | C (pF) | L (nH) | a (mm) | b (mm) | g (mm) |
|---------------------|-------|--------|--------|--------|--------|
| 3 GHz               | 0.267 | 10.54  | 5      | 13     | 0.2    |
| 2 GHz               | 0.3789| 16.71  | 6      | 21     | 0.2    |

To check the validity of the proposed design technique, ANSYS High Frequency
Structure Simulator (HFSS) was used. The design values and dimensions for the proposed
circuit are calculated and entered using the values in Table 2, and S-parameter simulation
was applied to the proposed circuits. The equivalent LC resonant circuit of the dumbbell-
shaped DGS section and the isolation resistor were inserted between the TL2 transmission
lines, as shown in Figure 10, to achieve an acceptable isolation.

The circuit simulation was performed by considering and realizing the equivalent lumped elements of the DGS section, as shown in Figure 10. The S-parameters results of the circuit simulations for the proposed circuit, i.e., $S_{11}$, $S_{21}$, $S_{31}$ and $S_{32}$, are illustrated as dotted lines in Figure 11a,b, which were designed at 3 and 2 GHz, respectively. It is clear from the results that the designed circuits and their parameters fulfill the requirements for the proposed Wilkinson power dividers, as they achieved good return losses $S_{11}$ and isolations $S_{32}$ of the order of $-55.84$ and $-55.02$ dB, respectively, for the WPD designed at 3 GHz and $-60.61$ and $-40.64$ dB, respectively, for the WPD designed at 2 GHz.

ANSYS HFSS Software package (ANSYS Electronics Desktop 2020 R2) was utilized to extract the final layout of the proposed WPD. Figure 11a,b shows the S-parameters results, i.e., $S_{11}$, $S_{21}$, $S_{31}$ and $S_{32}$, of an EM simulation for the respective WPDs as solid lines. The calculated values and their respective dimensions are given in Tables 2 and 3. To achieve an acceptable result, the circuits were tuned slightly. Therefore, the final layouts of the circuits were adjusted slightly, as compared to its initial dimensions. From the simulation results, the proposed circuits achieved almost equal power division, i.e., $S_{21} \approx S_{31} \approx -3\, \text{dB}$. 

Figure 10. Circuit schematic analysis of WPD designed at 3 GHz with physical dimensions and equivalent DGS circuit.
Figure 11. Simulation results of the proposed WPD circuit.

(a) Novel WPD designed at 3 GHz.

(b) Novel WPD designed at 2 GHz.
Most importantly, they show very good isolations between the output ports, i.e., about $S_{32} = -29.36$ dB and about $S_{32} = -35.42$ dB for the WPDs designed at 3 and 2 GHz, respectively. The results shown in Figure 11a,b also demonstrate good return losses of about $S_{11} = -28.18$ and $-20$ dB for the WPDs designed at 3 and 2 GHz, respectively. The comparison between the circuit and EM simulation illustrate that the isolation and the return loss are reported to be decreased slightly in case of EM simulation as compared to the results achieved in the circuit simulation. The difference of isolation and return loss are $-25.66$ and $-27.66$ dB, respectively, in the case of the WPD designed at the resonant frequency of 3 GHz and approximately $-5.22$ and $-40.61$ dB, respectively, in case of the WPD designed at the resonant frequency of 2 GHz.

The proposed Wilkinson power dividers were fabricated on a specified substrate using a special technique called photolithography. Figures 12 and 13 show the pictures of the fabricated circuits designed at the operating frequencies 3 and 2 GHz, respectively.

The simulated (EM simulation) and measured results of both circuits are illustrated in Figure 14 and compared in Table 4. In the case of the measured results, Table 4 shows that, due to attenuation and signal distortion in the microstrip line, the return loss and isolation at the resonating frequencies of 3 and 2 GHz in the fabricated WPDs are decreased to a small degree as compared with the simulation results. The return loss $S_{11}$ of the simulated circuit designed at 2 GHz is about $-20$ dB, which is less preferable. Therefore, the fabricated circuit is tuned slightly by the trial-and-error method to get the preferred measured return loss. Hence, the measured return loss of the divider at 2 GHz is better than the simulated circuit. Moreover, the attenuation pole in both WPDs is shifted slightly to higher frequencies with better dips as compared with the simulation results. Figure 15a,b shows the measured S-parameters results of the fabricated circuits designed at the operating frequencies of 3 and 2 GHz, respectively. Furthermore, conventional WPDs without DGS were also fabricated and compared with the proposed circuits, which are illustrated in Figure 15. Additionally, the measured results of the proposed WPD were compared with conventional WPDs without DGS designed at the same frequencies, i.e., 3 and 2 GHz, as shown in Table 5. Furthermore, the respective parameters, i.e., $S_{21}$, $S_{31}$, $S_{11}$ and $S_{32}$, of the proposed circuit and the conventional WPD are compared with other works in Table 5.

Both circuits designed at the resonating frequencies of 2 and 3 GHz are well matched at all ports. Reflection losses, $S_{22}$ and $S_{33}$ of both WPDs are reported be $<-20$ dB, which are rather promising results. Figure 16a,b shows the measured S-parameter results, $S_{22}$ and $S_{33}$ of the fabricated circuits.

![Photographs of the fabricated WPD circuit designed at 3 GHz.](image-url)
Figure 13. Photographs of the fabricated WPD circuit designed at 2 GHz.

It is clear from the measured results that good agreement is achieved, and the overall results match the required specifications of the proposed Wilkinson power divider. The measured results are approximately identical to the simulated results and have acceptable results. The fabricated circuits designed at resonating frequencies of 3 and 2 GHz have relatively good return losses $S_{11}$ of about $-25.54$ and $-31.24$ dB, respectively, and achieve good isolations between its output ports, in an order of about $S_{32} = -40.83$ and $-36.05$ dB within the operating bandwidth, respectively, which is rather exceptional and acceptable. Both Ports 2 and 3 of both dividers achieve approximately equally divided power, i.e., $S_{31} \approx S_{21} \approx -3$ dB. The measured results, i.e., $S_{11}, S_{21}, S_{31}$ and $S_{32}$, of both WPDs designed at operating frequencies of 2 and 3 GHz are illustrated in Table 5.

| Operating Frequency (GHz) | Return Loss $S_{11}$ (dB) | Isolation $S_{32}$ (dB) | Coupling $S_{21}$ dB | Coupling $S_{31}$ dB |
|---------------------------|---------------------------|------------------------|---------------------|---------------------|
| 3                         | Simulated $-28.18$        | $-29.36$               | $-3.12$            | $-3.17$            |
|                           | Measured $-24.01$         | $-27.65$               | $-3.23$            | $-3.24$            |
|                           | Error ($\pm$dB) $-4.17$   | $-1.71$                | $-0.11$            | $-0.07$            |
| 2                         | Simulated $-20$           | $-35.42$               | $-3.13$            | $-3.13$            |
|                           | Measured $-30.95$         | $-29.53$               | $-3.24$            | $-3.30$            |
|                           | Error ($\pm$dB) $10.95$   | $5.89$                 | $0.11$             | $-0.17$            |

The improved isolation between the output ports yields good bandwidth for the proposed circuit. Therefore, the calculated operating bandwidths, $f_H - f_L$ (where $f_H$ is the higher frequency and $f_L$ is the lower frequency of the passband) of the proposed circuit are 0.49 and 0.65 GHz of the WPDs designed at the center frequencies of 2 and 3 GHz, respectively (considering $S_{11} \leq -15$ dB). The bandwidths of the proposed work are compared with other similar works in Table 5. Table 5 shows comparatively good bandwidths as compared to other works.

The measured results of this work were compared with other similar works as well as with conventional WPDs without DGS. Furthermore, the measured s-parameters results, $S_{11}, S_{21}, S_{31}$ and $S_{32}$, of the conventional WPDs without the DGS were compared with the results of the proposed circuit and other major works. Table 5 shows desirable and improved return loss $S_{11}$ and better isolation $S_{32}$ results as compared with other major similar works as well as conventional WPDs. In some cases, the proposed work demonstrates better results than other works. Hence, the improved results of the proposed structure show considerable reliability and efficiency. Furthermore, the effective occupied areas of the fabricated WPDs were also compared with other works, as shown in Table 5, which demonstrates comparable or more desired effective areas.
Figure 14. Simulated and measured S–parameters results of the proposed WPDs.
Figure 15. Measured S-parameters results of the fabricated conventional WPDs and the proposed WPDs with DGS.
(a) Novel WPD designed at 3 GHz. (b) Novel WPD designed at 2 GHz.

**Figure 16.** Measured S-parameter results (S\(_{22}, S_{33}\)) of the proposed WPD circuits.

| Operating Frequency (GHz) | Return Loss S\(_{11}\) (dB) | Isolation S\(_{32}\) (dB) | Coupling S\(_{21}\) (dB) | Coupling S\(_{31}\) (dB) | Occupied Effective Area \(l \times w\) (mm\(^2\)) | Bandwidth \(f_h - f_l\) (GHz) |
|-------------------------|-----------------------------|-------------------------|----------------------|----------------------|---------------------------------|-------------------|
| [15] 1                   | −27.9                       | −30                     | −3.34                | −3.31                | 21 × 18                         | 0.27              |
| [16] 0.9                 | >−30                        | >−23                    | ≈−3                 | ≈−3                 | 12.9 × 13.6                     | 0.43              |
| [17] 2.43                | >−26                        | >−20                    | −                    | −                    | 55 × 31                         | 0.1               |
| [18] 0.8                 | >−20                        | >−27                    | −                    | −                    | 16 × 18                         | 0.12              |
| [19] 3.5                 | >−25                        | >−25.9                  | −3.32                | −3.37                | 54.8 × 22.36                    | 0.24              |
| [20] 0.46                | >−20                        | >−33.5                  | −3.261               | −3.242               | 40.5 × 20.9                     | 0.3               |
| [21] 2.9                 | −23                         | −31                     | −3.8                 | −3.74                | 19.37 × 19.8                    | 0.14              |
| C                       | 2                           | −24.81                  | −26.62               | −3.32                | 22.15 × 15.2                    | 1.2               |
| *                       | 2                           | −31.24                  | −36.05               | −3.24                | 23.3 × 12                       | 0.49              |
| *                       | 3                           | −25.54                  | −40.83               | −3.23                | 14.9 × 8.12                     | 0.65              |

C: Conventional WPD; *: This work.

**Table 5.** Comparison of fabricated WPDs’ results with other major similar works.

5. Conclusions

In this work, a modified Wilkinson power divider is proposed that has the isolation resistor soldered on the ground plane across an etched dumbbell-shaped DGS. The proposed circuit consists of two pairs of transmission lines having fixed impedances and certain electrical lengths. DGS is introduced between the output ports, such that a resistor can be mounted across the DGS section, in order to achieve better or acceptable isolation. The parameters of the proposed circuit are extracted by applying even and odd mode analysis and conjugate matching theory. Circuit simulation and EM simulation were performed to verify the validity of the proposed circuit and to achieve an acceptable result. This paper also presents the design methodology and the flow charts for both the Wilkinson power divider and the DGS section. The proposed method, parameters and circuit were verified by the fabrication and measurement of modified proposed WPDs circuits designed at the operating frequencies of 2 and 3 GHz. The measured data show approximately equally divided power at the output ports, and improved isolation was achieved between the output ports on both fabricated circuits. The fabricated circuits achieved good return loss characteristics as well. The measured results are acceptable and meet the criteria of the preferred results. An adequate agreement was achieved between the measured performance of the fabricated Wilkinson power divider and the EM simulation results. The measured results were compared with respective conventional WPDs as well as other similar works. The comparisons showed acceptable results with good or better S-parameter
results. Furthermore, the results show that the proposed Wilkinson power divider achieved improved isolation as compared to other works.

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