Design of Super Twisting Sliding Mode Controller for a Three-Phase Grid-connected Photovoltaic System under Normal and Abnormal Conditions

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Abstract: The novelty behind the research in this paper is to investigate the Super Twisting Sliding Mode Controller (ST-SMC) for efficiently injecting both active and reactive power under normal and abnormal operating conditions for a three-phase grid-connected photovoltaic (PV) system. The ST-SMC is aimed to inject sinusoidal current to the grid with low Total Harmonic Distortion (THD), to avoid chattering with easy real implementation, and to enhance the quality of disturbance rejection and sensitivity to parameter variation. The test under normal conditions includes initialization, steady state behavior, dynamic behavior, and interrupting the injection of acting and reactive power while the abnormal conditions consists of voltage sag, voltage swell, frequency variation, DC-link variation, and inclusion of 5th harmonics, etc. The phase lock loop used for synchronization is based on a synchronous reference frame that works well under distorted grids and nonideal. Automatic code is generated in PSIM 9.1 for hardware implementation in the DSP board TMS32F28335 from Texas Instruments while code composer studio 6.2.0 is used for debugging. The real time testing is executed using Typhoon Hardware in Loop (HIL) 402 device on the DSP board. The results authenticate the fastness, effectiveness, and robustness for both steady state and dynamic behavior under various scenarios of the designed controller.

Keywords: Super Twisting Algorithm; Sliding Mode Controller; total harmonic distortion; grid-connected photovoltaic system; Typhoon HIL

1. Introduction

Exponential decrease in fossil fuel, rapidly varying climate, and high electric power demand are currently severe problems due to the abruptly increasing population throughout the world. To fulfill the power demand, the alternative way is to adopt Renewable Energy Resources (RER) [1,2]. An abrupt growth has been seen in Solar Photovoltaic (SPV) system by the past several years; however, its importance will be further increased in the near future as its integration with the grid becomes more sophisticated [3–5]. The SPV system is considered one of the favorite RERs due to its abundance,
low installation cost, good looks, and noiselessness as there are no rotating parts. The coupling of SPV with distribution substation are in major part and is known as a grid-connected PV system [4].

Generally, a grid-connected PV system is composed of PV array which takes solar irradiance as input and generates electric power as output and another part is power electronic-based converters, which are responsible for the extraction of power from PV array and interconnection with grid [6]. A series/parallel connection of PV cells makes a PV array whose output power depends on the irradiance and temperature of the sun [7]. A specific point is calculated through the P-V curve known as the Maximum Power Point (MPP) for different conditions of temperature and solar irradiance, at which power with high efficiency is received from the PV array [8]. The MPP can be located through an algorithm known as the Maximum Power Point Tracking (MPPT), by controlling a DC-DC converter. Different types of techniques are being used to acquire MPPT such as hill-climbing, incremental conductance, perturb and observe, and some others [9–14].

In the grid-connected PV system, converters are mostly used, which are composed of power electronic components [15], and time by time strict limitations have been imposed on grid-connected PV systems [16]. Due to non-linear and intermittency response of power-electronic converters, harmonics are usually associated with it. To overcome this problem, advanced and robust control schemes can be designed [17]. Mostly, a dual cascaded control scheme is used for a grid-connected PV system. To control the current feed-in to the grid, the inner current controller remains the main focal area for the researchers as it is associated with power quality issues [16]. However, for reference current generation, the outer-loop is used to control the DC-link voltage or power. The voltage and current of the grid should be in phase and synchronized, Phase Lock Loop (PLL) is mostly used for this purpose [18–20]. The signals given by PLL are very important for the current controller, as it is used throughout the control structure and conditioning [21,22].

Hence, the design of a current control structure that is robust, accurate, dynamic, and resilient towards grid faults according to grid codes is the key problem for researchers. Previously, different types of control strategies have been introduced to track nominal current and DC voltage for the grid-connected PV system. Conventionally, the PI controller has been used for electrical variables optimization due to its simplicity, minimum iterative variables, and easy implementation [23]. Still, the PI controller results in some issues such as sensitivity and low accuracy toward variable uncertainties and sudden faults. That is why researchers are looking the solution in nonlinear controllers such as Fuzzy Logic Controller (FLC), Neural Network (NN), Sliding Mode Controller (SMC), model predictive controller, deadbeat controller, repetitive controller, hysteresis controller, adaptive controller, neuro-fuzzy controller, feedback linearization technique [24–45], etc.

In [24], a predictive fuzzy logic-based energy management system was proposed for standalone and grid-connected operation of residential microgrids. A Legendre-based neural network controller for operation in grid-connected mode to improve the power quality is proposed in [25]. In [26], an artificial neural network-based vector controller is designed for the integration of residential solar PV with a utility grid. The controller reduces the harmonics contents and improves adaptivity under uncertainty. Rapid terminal SMC based on Lyapunov is presented for a hybrid system, i.e., PV and wind connected to the grid in [27]. This improves the performance under disturbances in the converter, inverter, and hybrid system. An Space Vector Modulation (SVM) based SMC for 3L-NPC inverter and integral SMC for grid-connected PV system is presented in [28] and [29], respectively. Each controller improves the control dynamics and tracking performance of the PV system. In [30] and [31], a fuzzy-SMC based on disturbance observer and sliding-mode voltage observer are presented, respectively. The design controllers handle the chattering and uncertainties effectively. In [32–35], MPC controllers are designed for a grid-connected PV system that improves both the dynamic and steady-state performance. In [36], an adaptive backstepping controller is designed for DC-side of Z-source inverter connected to the grid that guarantees stable and robust performance under variation in temperature and solar irradiance. A fractional repetitive control and multi-rate fractional order control are discussed, described, and
designed in [37,38], respectively. The above controllers improve the control performance; however, they lack the analysis of ST-SMC under normal and abnormal conditions.

The SMC is a non-linear approach that has the remarkable features of easy tuning, robustness, accuracy, and insensitivity to uncertainties and external disturbances [46]. The main shortcomings of standard (first-order) SMC are chattering, which results in electromagnetic interference (EMI) noise and instability issues. First-order SMC with sign function is replaced by a smooth function, such as the practical saturation function [47], sigmoid function [48], or ideal saturation function [49], to tackle the chattering problem. However, they suffer from the degradation of tracking performance and robustness. To overcome this, several approaches are developed; higher order SMC is one of the alternatives to overcome chattering [50]. The information demand increases in the higher order SMC, which is the main problem in its implementation. The Super Twisting SMC (ST-SMC) is one of higher order SMC that does not require additional information [51]. The ST-SMC has become a benchmark for the second-order SMC, it has the capability of chattering reduction even in the presence of uncertainties, finite-time convergence to the sliding surface, easy real-time implementation, and system robust stabilization [52]. Moreover, it converges the system states to the sliding variable abruptly. In this paper, ST-SMC is designed for a three-phase grid-connected PV system to efficiently inject active and reactive power under various normal and abnormal perturbations.

The main contributions of the manuscript are listed below:

- Design of the ST-SMC for efficiently injecting both active and reactive power to the utility grid.
- Mathematically modeling of the three-phase grid-connected PV system in a comprehensive manner.
- Employing the SRF-PLL that works accurately under distorted and nonideal grids.
- HIL testing under normal conditions that includes initialization, steady-state behavior, dynamic behavior, and interrupting the injection of active and reactive power.
- Testing under abnormal conditions, i.e., voltage sag, voltage swell, frequency variation, DC-link variation, inclusion of 5th harmonics, etc.

The remaining of the paper is structured as follows: Section 2 provides the mathematical modeling, system design, and synchronous reference frame phase lock loop for a three-phase grid-connected PV system; SMC and the design of ST-SMC are broadly outlined in Section 3; Section 4 assesses hardware in loop results for the grid-connected PV system under normal and abnormal perturbations; and finally, the paper is concluded in Section 5.

2. Grid-connected PV System

2.1. System Modeling

The control of the direct-quadrature (dq) synchronous reference frame is decoupled. It has a negligible steady state error and fast transient response [53]. The grid-connected PV system is normally controlled in dq frame. The three-phase line voltages provide the single-phase voltages of the inverter as:

\[
\begin{bmatrix}
    v_a \\
    v_b \\
    v_c \\
\end{bmatrix} = \frac{1}{3} \begin{bmatrix}
    1 & 0 & -1 \\
    -1 & 1 & 0 \\
    0 & -1 & 1 \\
\end{bmatrix} \begin{bmatrix}
    v_{ab} \\
    v_{bc} \\
    v_{ca} \\
\end{bmatrix}
\]  

In above Equation \( v_a, v_b, v_c \) are phase voltages in natural-frame and \( v_{ab}, v_{bc}, v_{ca} \) are the line voltages. Additionally, using Clark’s transformation the three-phase voltages are converted into stationary reference-frame \( v_d \) and \( v_f \) [54]:

\[
v_{df} = \sqrt{2} \begin{bmatrix}
    1 & -\frac{1}{2} & -\frac{1}{2} \\
    0 & \frac{\sqrt{3}}{2} & -\frac{\sqrt{3}}{2} \\
\end{bmatrix} \begin{bmatrix}
    v_d \\
    v_b \\
    v_c \\
\end{bmatrix}
\]
Next, Using Park’s transformation the $v_\alpha$ and $v_\beta$ frame is transformed into synchronous or dq reference-frame:

$$
\begin{bmatrix}
  v_d \\
  v_q
\end{bmatrix} =
\begin{bmatrix}
  \cos(\theta_e) & \sin(\theta_e) \\
  -\sin(\theta_e) & \cos(\theta_e)
\end{bmatrix}
\begin{bmatrix}
  v_\alpha \\
  v_\beta
\end{bmatrix}
$$

(3)

In Equation (3), the synchronous frame axis is presented by a dq subscript. Using PLL, the synchronous frame angle $\theta_e$ is acquired [53]. Moreover, Equation (4) converts the current into a dq reference frame:

$$
\begin{bmatrix}
  i_d \\
  i_q
\end{bmatrix} = \sqrt{2}\begin{bmatrix}
  \cos(\theta_e) & \cos(\theta_e - \frac{2\pi}{3}) & \cos(\theta_e + \frac{2\pi}{3}) \\
  -\sin(\theta_e) & -\sin(\theta_e - \frac{2\pi}{3}) & -\sin(\theta_e + \frac{2\pi}{3})
\end{bmatrix}
\begin{bmatrix}
  i_a \\
  i_b \\
  i_c
\end{bmatrix}
$$

(4)

The active and reactive power of the inverter can be calculated using the dq components of currents and voltages:

$$
\begin{align}
  P &= \frac{3}{2}(v_d i_d + v_q i_q) \\
  Q &= \frac{3}{2}(v_q i_d - v_d i_q)
\end{align}
$$

(5)

Supposing that the grid voltage and current has zero phase shift, i.e., in phase, which gives $v_d = 0$. Thus, the above Equation (5) can be expressed as:

$$
\begin{align}
  P &= \frac{3}{2}(v_q i_q) \\
  Q &= \frac{3}{2}(v_q i_d)
\end{align}
$$

(6)

The real power depends on $i_q$ and reactive power depends on $i_d$, respectively.

### 2.2. System Description

The block diagram of the design system is presented in Figure 1, while Table 1 lists the nominal parameters. The proposed system has the capability to control both active and reactive power. The grid-connected mode is tested under normal and abnormal conditions. The capacity of the inverter is 15 kVA with the output voltage 120 VAC RMS and the DC-link voltage 380 V.

| Parameters                  | Values          |
|-----------------------------|-----------------|
| Capacity                    | 15 kVA          |
| Grid frequency ($f_g$)      | 60 Hz           |
| Output AC voltage ($V_{rms}$)| 120 V           |
| Sampling frequency          | 10 kHz          |
| Switching frequency ($f_{SW}$)| 10 kHz         |
| DC-link voltage ($V_{dc}$)  | 380 V           |
| Filter inductor ($L_1$)     | 2.88 mH         |
| Filter capacitor ($C$)      | 15 µF           |
| Filter inductor ($L_2$)     | 0.050 mH        |

The type III controller-based DC-DC boost converter is assumed to supply 380 V whose input voltage is 80 V [55]. The LCL filter is used to connect the inverter with the grid [56,57]. The MPPT controller for RER or high-level energy management controller is utilized to set the $P_{ref}$ and $Q_{ref}$ power references, respectively. For controller designed, Park and Clarke’s transformation based on dq theory is used to define control signals. The tuning of the d-axis well tune q-axis as the dynamics of both axes are similar [55]. The inverter is employed with Sinusoidal Pulse Width Modulation (SPWM) technique. Sliding mode Controller (SMC) based on the Super Twisting algorithm is designed to inject high quality fed in current into grid. This design will be discussed in the next section.
The readers are encouraged for further details about PLL [54,55,58–60]. The range of the PLL techniques is from a few hertz to gigahertz. There are three main PLL procedures for phase tracking, e.g., (a) zero-crossing based PLL, (b) stationary reference frame, and (c) Synchronous Reference Frame (SRF)-based PLL [54]. The SRF-PLL works accurately under distorted and nonideal grids and is applicable to both single phase and three-phase systems. The overall structure is presented in Figure 2. The voltage $V_d$ and $V_q$ are calculated from phase voltages $v_{A}$, $v_{B}$, $v_{C}$ using $a\beta$ and $dq$ transformation. The angle $\theta^*$ required in the transformation is computed by integrating the $\omega^*$. The readers are encouraged for further details about PLL [54,55,58–60].

2.3. SRF-PLL Design

Currently, Phase Lock Loop (PLL) is widely used to synchronize PV plants to the power grid. The range of the PLL techniques is from a few hertz to gigahertz. There are three main PLL procedures for phase tracking, e.g., (a) zero-crossing based PLL, (b) stationary reference frame, and (c) Synchronous Reference Frame (SRF)-based PLL [54]. The SRF-PLL works accurately under distorted and nonideal grids and is applicable to both single phase and three-phase systems. The overall structure is presented in Figure 2. The voltage $V_d$ and $V_q$ are calculated from phase voltages $v_{A}$, $v_{B}$, $v_{C}$ using $a\beta$ and $dq$ transformation. The angle $\theta^*$ required in the transformation is computed by integrating the $\omega^*$. The readers are encouraged for further details about PLL [54,55,58–60].
3. Design of Super Twisting SMC

3.1. Sliding Mode Controller

In SMC, the systems states are driven toward Sliding Surface (SS) [61,62]. The graphical interpretation of SMC is presented in Figure 3 [62]. After reaching SS the SMC keeps the states on the close neighborhood of the SS. The design of SMC consists of two parts SS and control law design.

SS: A typical form for SS is given as follows:

\[
\sigma = e^k + \sum_{i=0}^{k-1} c_i e^i
\]

(7)

where \( k = r - 1 \) with \( r \) equal to a relative degree between \( u \) and \( y \). The choice of \( c_i \) is an arbitrary positive constant. The selection of \( \sigma \) should be in such a way that \( \sigma = 0 \) gives a stable differential equation. For different values of \( k \) the SS is given as follows:

\[
\begin{align*}
  k = 1 & \quad \sigma = \dot{e} + \lambda e \\
  k = 1 & \quad \sigma = \dot{e} + 2\lambda e + \lambda^2 e
\end{align*}
\]

(8)

Control law design: The second part is the control law design that should steer the variable \( \sigma \) to zero in finite time; in other words, should steer the system trajectories onto the sliding manifold. The control law will be either first order (conventional) or higher order SMC. The discontinuous \( \text{sgn} \) function causes oscillation in various areas; using continuous functions \( \text{sat} \) and \( \text{tanh} \), some chattering is attenuated but the robustness is not maintained. An alternative solution is using higher order SMC that maintain robustness and completely solves the chattering issues. Table 2 presents the comparison of various first order SMCs based on continuous and discontinuous functions [61].
Table 2. Comparison of various first-order SMC [61].

| S.NO | Function | Equation | Constants | Nature | Chattering |
|------|----------|----------|-----------|--------|------------|
| 1    | \(\text{sgn} \) | \(u = -U \text{sgn}(\sigma)\) | \(u = \begin{cases} U & \sigma > 1 \\ -U & \sigma < 1 \end{cases}\) | Discontinuous | High |
|      |          |          | \(U\) is sufficiently large constant |        |            |
| 2    | \(\text{SAT} \) | \(u = -U \text{sat}(\sigma; \varepsilon)\) | \(\varepsilon > 0\) | Continuous | Low |
|      |          | \(= -U \frac{\sigma}{\varepsilon + \varepsilon} \) | \(\varepsilon \approx 0\) |        |            |
| 3    | \(\text{TANH} \) | \(u = -U \text{tanh}(\sigma / \varepsilon)\) | \(\varepsilon > 0\) | Continuous | Low |
|      |          |          | \(\varepsilon \approx 0\) |        |            |

3.2. Super Twisting Sliding Mode Controller Design

Without affecting the tracking performance and to avoid chattering, the Super Twisting ST-SMC is a viable alternative to the standard first-order SMC particularly intended for systems with relative degree 1. After describing a trajectory similar to one of the Twisting algorithms, this algorithm converges in finite time with a proper choice of parameters [63]. In ST-SMC the trajectories are twisting around the origin on the 2-sliding plane as presented in Figure 4a. The ST algorithm is a nonlinear version of the conventional PI controller presented in Figure 4b. The ST-SMC is highly suitable for real implementation and more immune to output measurement noise and possible errors in the estimation of \(\dot{\sigma}\) [64]. The ST-SMC control law \(u(t)\) is composed of two terms. The discontinuous time derivative defines the first term, while the second term is a continuous function of the available sliding variable, which is present during the reaching phase only. The following control law defines the control algorithm [63,65,66]:

\[
\begin{align*}
u(t) & = \dot{u}_1(t) + u_2(t) \\
\dot{u}_1(t) & = \begin{cases} -u & \text{if } |u| > 1 \\ -W \text{sign}(\sigma) & \text{if } |u| \leq 1 \end{cases} \\
u_2(t) & = \begin{cases} -\lambda |\sigma| \rho \text{sign}(\sigma) & \text{if } |\sigma| > \sigma_0 \\ -\lambda |\sigma| \rho \text{sign}(\sigma) & \text{if } |\sigma| \leq \sigma_0 \end{cases}
\end{align*}
\]

where \(U\) is the control value boundary and \(\sigma_0\) is a boundary layer around the SS \(\sigma\). The sufficient conditions for finite time convergence to the sliding mode manifold are:

\[
\left\{ \begin{array}{l} W > \frac{\Phi}{\Gamma_m} \\
\lambda^2 \geq \frac{4\Phi \Gamma_m (W + \Phi)}{\Gamma_m (W - \Phi)} \\
0 < \rho \leq 0.5\end{array} \right. \]

\(\Phi, \Gamma_m, \Gamma_M, \sigma_0\) are positive constants and \(W, \lambda, \rho\) are control gains.

When controlled systems are linearly dependent on the control, then \(\sigma_0 = \infty\) and \(u\) does not need to be bound [67]; thus, the final equation of the controller may be simplified as:

\[
\begin{align*}
u(t) & = -\lambda \sqrt{|\sigma|} \text{sign}(\sigma) + u_1 \\
\dot{u}_1(t) & = -W \text{sign}(\sigma)
\end{align*}
\]

In Equation (13), \(\lambda = \sqrt{U}\) and \(W = 1.1U\). Where \(U\) is a positive constant with sufficiently large values, its value is tuned by using the “trial and error” method until good performance in closed-loop is achieved. The various values of controller constants are: \(\varepsilon = 30,000, U = 600,000,\) and \(W = 1.1U = 600,000,\) and \(\lambda = \sqrt{U} = \sqrt{600,000}\). The control law in Equation (13), used in the controller design, is stable and its step by step analysis of stability is given in [68,69].
Leading to the results very near to practical conditions, Hardware in Loop (HIL) for the design of converters and control in power electronics has been gradually identified as an effective tool [70]. In grid-connected inverters, the practical implementation of some tests is difficult due to the fact they require alterations in the grid conditions or filter or in the inverter; this can be easily executed in HIL. In this paper, the effectiveness and the feasibility of ST-SMC are validated using a Hardware in Loop (Typhoon HIL) 402 device for real-time testing [71]. The power and control structure of Figure 1 is drawn in PSIM version 9.1. The code composer studio 6.2.0 is used for debugging the code, which is suitable for real implementation and more immune to output measurement noise and possible errors 

\[
\Phi \Gamma_{\alpha} = \Phi \Gamma_{\beta} (13) 
\]

In Equation (13), \(\Phi\) and \(\Gamma\) are positive constants and \(\Phi\) is a boundary layer around the SS.

\[
\Phi \Gamma_{\alpha} = \Phi \Gamma_{\beta} (12) 
\]

In Figure 2, the trajectories are converges in finite time with a proper choice of parameters [63]. In ST-SMC the trajectories are twisting around the origin on the 2-sliding plane as presented in Figure 4a. The ST algorithm is a viable alternative to the standard first-order SMC particularly intended for systems with relative degree 1. After describing a trajectory similar to one of the Twisting algorithms, this algorithm is stable and its step by step analysis of stability is given in [68, 69].

4. Results and Discussion

4.1. Hardware in Loop

Leading to the results very near to practical conditions, Hardware in Loop (HIL) for the design of converters and control in power electronics has been gradually identified as an effective tool [70]. In grid-connected inverters, the practical implementation of some tests is difficult due to the fact they require alterations in the grid conditions or filter or in the inverter; this can be easily executed in HIL. In this paper, the effectiveness and the feasibility of ST-SMC are validated using a Hardware in Loop (Typhoon HIL) 402 device for real-time testing [71]. The power and control structure of Figure 1 is drawn in PSIM version 9.1. The code composer studio 6.2.0 is used for debugging the code, which is suitable for real implementation and more immune to output measurement noise and possible errors.
4.1.1. Normal Conditions

The voltage setting for channel 1, channel 2, and channel 3 are set 133.33 V/1 V division, while the current division is set for channel 1, channel 2, and channel 3 as 33.33 A/1 V. The voltage and currents sensor gains are set to $k_i = 0.03$ and $k_v = 0.0075$, correspondingly. Figure 6 presents a three-phase output current during initialization from $\text{Pref} = 0$ to $\text{Pref} = 10$ kW and $\text{Qref} = 0$ kVAr. Phase A, B, C are represented by yellow, blue, and magenta colors, respectively. The current reference abruptly reaches to its reference value by injecting the required power without distortions. Figure 7 shows three-phase output current in steady-state for $\text{Pref} = 10$ kW and $\text{Qref} = 0$ kVAr. The currents are smooth and symmetrical. Figure 8 describes the voltage and current for phase A during $\text{Pref} = 0$ to $\text{Pref} = 10$ kW and $\text{Qref} = 0$. The green color shows the voltage and yellow color shows the current for phase “A.” The current switches to its required value as soon as a step-in power is introduced, similar for Phase “B” and “C.” Thus, active power is injected. The currents and voltages are in phase authenticating the efficient performance of SRF-PLL. Figure 9 depicts the voltage and current for phase B during $\text{Pref} = 0$ to $\text{Pref} = 10$ kW. Figure 10 illustrates the voltage and current for phase C during $\text{Pref} = 0$ to $\text{Pref} = 10$ kW. The starting transient is negligible validating the robust performance of ST-SMC. Figure 11 demonstrates three-phase currents during stopping from $\text{Pref} = 10$ to $\text{Pref} = 0$ kW. The inverter is turned off and stops injecting active and reactive power. The response is faster during this case. Figure 12 represents the voltage and current for phase A during $\text{Qref} = 0$ to $\text{Qref} = 10$ kVAr and $\text{Pref} = 0$. A phase shift is introduced indicating the injection of reactive power. Figure 13 signifies the steady state of Fig10. There is no oscillation and transients during steady-state operation. Figure 14 elaborates the voltage and current for phase B during $\text{Qref} = 0$ to $\text{Qref} = 10$ kVAr and $\text{Pref} = 0$. A phase shift is noticed, whenever a step in reactive power is introduced, thus injecting reactive power as well. Figure 15 discusses the voltage and current for phase A during $\text{Pref} = 10$ kW and the transition from $\text{Qref} = 0$ to $\text{Qref} = 15$ kVAr. Before the injection of reactive power, the current and voltages are in phase as soon as a step-in reactive power is produced, a phase shift in current and voltage is noted. Step A is clearly highlighted in red color. Similar is the case for phase “B” and “C.” Figure 16 introduces the voltage and current for phase B during $\text{Pref} = 10$ kW and the transition from $\text{Qref} = 0$ to $\text{Qref} = 15$ kVAr. Figure 17 expresses the voltage and current for phase C during $\text{Pref} = 10$ kW and the transition from $\text{Qref} = 0$ to $\text{Qref} = 15$ kVAr. The designed controller shows less sensitivity towards the sudden disturbance. In short, the performance of the designed controller is faster, accurate, efficient, and robust when tested under various scenarios.

![Figure 6](https://example.com/figure6.png)

*Figure 6.* Three-phase output current during initialization from $\text{Pref} = 0$ to $\text{Pref} = 10$ kW and $\text{Qref} = 0$ kVAr.
Figure 7. Three-phase output current in steady state for $P_{ref} = 10$ kW and $Q_{ref} = 0$ kVAR.

Figure 8. Voltage and current for phase A during $P_{ref} = 0$ to $P_{ref} = 10$ kW and $Q_{ref} = 0$.

Figure 9. Voltage and current for phase B during $P_{ref} = 0$ to $P_{ref} = 10$ kW.
Figure 9. Voltage and current for phase B during Pref = 0 to Pref = 10 kW.

Figure 10. Voltage and current for phase C during Pref = 0 to Pref = 10 kW.

Figure 11. Three-phase current during a stop from Pref = 10 kW to Pref = 0 kW.

Figure 12. Voltage and current for phase A during Qref = 0 to Qref = 10 kVAr and Pref = 0.
Figure 12. Voltage and current for phase A during $Q_{\text{ref}} = 0$ to $Q_{\text{ref}} = 10\, \text{kVAR}$ and $P_{\text{ref}} = 0$.

Figure 13. Steady-state of Figure 12.

Figure 14. Voltage and current for phase B during $Q_{\text{ref}} = 0$ to $Q_{\text{ref}} = 10\, \text{kVAR}$ and $P_{\text{ref}} = 0$.

Figure 15. Voltage and current for phase A during $P_{\text{ref}} = 10\, \text{kW}$ and transition from $Q_{\text{ref}} = 0$ to $Q_{\text{ref}} = 15\, \text{kVAR}$. 
When the normal value of grid voltage has restored, the effect of the disturbance is negligible indicating the fault-tolerant behavior of ST-SMC. Figure 16 introduces voltage and current for phase A during Pref = 10 kW and transition from Qref = 0 to Qref = 15 kVar.

Figure 17. Voltage and current for phase C during Pref = 10 kW and transition from Qref = 0 to Qref = 15 kVar.

4.1.2. Abnormal Conditions

The design system is subjected to various perturbations and non-ideal conditions, i.e., voltage sag, voltage swell, frequency variation, DC-link variation, inclusion of 5th harmonics during injection of active power, reactive power, both cases, etc. Figure 18 presents voltage and current for phase A during a drop in the grid voltage from 120 Vrms to 108 Vrms with Pref = 10 kW and Qref = 0 kVAr, clearly highlighted by arrows. A small transient is shown at the start, which is recovered abruptly. The response is faster and chatter-free. Figure 19 shows the voltage and current for phase A during an increase in the grid voltage from 108 Vrms to 120 Vrms with Pref = 10 kW and Qref = 0 kVAr. When the normal value of grid voltage has restored, the effect of the disturbance is negligible indicating the fault-tolerant behavior of ST-SMC. Figure 20 introduces voltage and current for phase A during f = 61 Hz, Pref = 10 kW, and Qref = 0 kVAr. The controller shows insensitivity to frequency variation. Figure 21 depicts voltage and current for phase A during the inclusion of oscillation in the DC-link voltage 38V at 120 Hz with Pref = 10 kW, and Qref = 0 kVAr. The controller shows insensitivity to frequency variation.

Figure 22 illustrates voltage and current for phase A during V_{DC} = 380 V + 38 V at 120 Hz with...
Pref = 10 kW and Qref = 10 kVar. Due to the injection of reactive power, a phase shift is noticed in the current and voltage; moreover, there is no effect on the symmetry of waveforms validating the effectiveness of the design controller. Figure 23 portrays voltage and current for phase A during a swell in the grid voltage from 120 Vrms to 132 Vrms. A small transient shows the insertion of the swell point out with double-headed arrow. The voltage and current are in the phase where the waveform is distortion free. Figure 24 describes voltage and current for phase A during inclusion of 3% of 5th harmonic in the grid voltage with 120 Vrms, Pref = 10 k, and Qref = 0. Figure 25 demonstrates voltage and current for phase A during the inclusion of 3% of 5th harmonic in the grid voltage (120 Vrms and Pref = 10 k Qref = 10 kvar). The above two Figures show the behavior of the currents and voltages during the inclusion of 5th harmonics. The injected currents have high quality under harmonic distortions, thus authenticating that the design control is reliable, efficient, and less insensitive to abnormal conditions and uncertainties.

Figure 18. Voltage and current for phase A during a drop in the grid voltage from 120 Vrms to 108 Vrms. Pref = 10 kW and Qref = 0 kVAr.

Figure 19. Voltage and current for phase A during an increase in the grid voltage from 108 Vrms to 120 Vrms. Pref = 10 kW and Qref = 0 kVAr.
Figure 20. Voltage and current for phase A during $f = 61$ Hz. $\text{Pref} = 10$ kW and $\text{Qref} = 0$ kVAR.

Figure 21. Voltage and current for phase A during $V_{dc} = 380$ V + 38 V at 120 Hz. $\text{Pref} = 10$ kW and $\text{Qref} = 0$.

Figure 22. Voltage and current for phase A during $V_{dc} = 380$ V + 38 V at 120 Hz. $\text{Pref} = 10$ kW and $\text{Qref} = 10$ kVAR.
Figure 23. Voltage and current for phase A during a swell in the grid voltage from 120 to 132 Vrms.

Figure 24. Voltage and current for phase A during inclusion of 3% of the 5th harmonic in the grid voltage (120 Vrms and Pref = 10 kW Qref = 0).

Figure 25. Voltage and current for phase A during inclusion of 3% of 5th harmonic in the grid voltage (120 Vrms and Pref = 10 kW Qref = 10 kvar).
5. Conclusions and Future Work

This paper successfully investigated the modeling and design of ST-SMC for efficiently injecting both active and reactive power under normal and abnormal operating conditions for a three-phase grid-connected PV system. The ST-SMC is analyzed and tested both under normal operation i.e., initialization, steady state behavior, dynamic behavior, and interrupting the injection of active and reactive power for normal operation as well as abnormal conditions, i.e., voltage sag, voltage swell, frequency variation, DC-link variation, inclusion of 5th harmonics, etc. The obtained finding shows that ST-SMC injects high-quality sinusoidal current with low THD, which is insensitive to a sudden disturbance, chatter-free, and fault-tolerant. The ST-SMC also ensures controllability, reliability, and safety for the interface with the utility grid. The HIL results authenticate the fastness, effectiveness, robustness, and stable operation for both steady-state and dynamic behavior of the designed controller.

In the near future, we will replace the simple inverter with a Modular Multilevel Inverter. Inserting an FRT strategy in compliance with modern grid code during symmetrical/asymmetrical faults and utilizing advance PLL is also one of direction. Moreover, a comparative assessment of advanced controller, i.e., H-infinity, Adaptive-SMC, Neuro-Fuzzy, etc., can also be performed. To further enhance the efficiency of the inverter, the silicon carbide (SiC) or gallium arsenide (GaN) devices will be used, instead of IGBT or MOSFET.

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