Noise-Aided Logic in an Electronic Analog of Synthetic Genetic Networks

Edward H. Hellen1*, Syamal K. Dana2, Jürgen Kurths3, Elizabeth Kehler1, Sudeshna Sinha4

1 Department of Physics and Astronomy, University of North Carolina Greensboro, Greensboro, North Carolina, United States of America, 2 Council of Scientific and Industrial Research-Indian Institute of Chemical Biology, Kolkata, India, 3 Potsdam Institute for Climate Impact Research, Potsdam, Germany, 4 Indian Institute of Science Education and Research Mohali, SAS Nagar, Mohali, Punjab, India

Abstract

We report the experimental verification of noise-enhanced logic behaviour in an electronic analog of a synthetic genetic network, composed of two repressors and two constitutive promoters. We observe good agreement between circuit measurements and numerical prediction, with the circuit allowing for robust logic operations in an optimal window of noise. Namely, the input-output characteristics of a logic gate is reproduced faithfully under moderate noise, which is a manifestation of the phenomenon known as Logical Stochastic Resonance. The two dynamical variables in the system yield complementary logic behaviour simultaneously. The system is easily morphed from AND/NAND to OR/NOR logic.

Introduction

Realization of logic functions in different physical systems is one of the key questions that commands widespread research interest in science and engineering. Universal general-purpose computing devices can be constructed entirely from NOR/NAND logic gates [1,2]. It is particularly interesting to investigate if systems of biological relevance can also yield logic outputs consistent with the truth tables of different logic functions (see Table 1). Biological systems are capable of stochastic resonance [3–6], a process in which a small signal is amplified due to the presence of an appropriate level of noise, leading to the possibility of a biological system performing robust noise-aided logic operations in response to weak input signals.

A new idea in this direction uses the interplay between noise and nonlinearity constructively to enhance the robustness of logic operations. Namely, in an optimal window of noise, the input-output characteristics of a logic gate is reproduced faithfully. This phenomenon is termed Logical Stochastic Resonance (LSR) [7–12]. Specifically, in LSR we consider the state of a nonlinear system when driven by input signals, consisting of two randomly streaming square waves. It was observed that the response of such a system shows a remarkable feature: in an optimal band of noise, the output of the system, determined by its state, is a logical combination of the two input signals in accordance with the truth tables of fundamental logic operations.

An important motivation for further studying LSR stems from an issue that is receiving widespread attention currently. The number of transistors in an integrated circuit has approximately doubled every year in accordance with Moore’s law. The rapid shrinking of computing platforms with smaller power supplies has brought with it problems of smaller noise margins and higher error rates. Namely, as computational devices and platforms continue to shrink in size, we encounter fundamental noise that cannot be suppressed or eliminated. Hence an understanding of the cooperative behavior between a device noise-floor and its nonlinearity plays an increasingly crucial role in paving the way for smart computing devices. In this direction, LSR indicates a way to turn potentially performance degrading noise to assist the desired operation. Further, it is of far reaching interest to obtain analogous behaviour, not merely in human engineered physical systems, but also in systems of chemical and biological relevance, in order to explore the information processing capacity of naturally occurring systems where noise is ubiquitous.

Since the idea of LSR was first introduced [7], several systems implementing and displaying LSR have been found. To begin with, the basic electronic realizations of simple bistable potentials were reported [7,8]. Subsequently, noise-aided reprogrammable

| Input Set $(I_1,I_2)$ | OR | AND | NOR | NAND |
|----------------------|----|-----|-----|------|
| (0,0)                | 0  | 0   | 1   | 0    |
| (0,1)/(1,0)         | 1  | 0   | 0   | 1    |
| (1,1)               | 1  | 1   | 0   | 0    |

Relationship between the two inputs and the output of the fundamental OR, AND, NOR and NAND logic operations. Note that the four distinct possible input sets (0,0), (0,1), (1,0) and (1,1) reduce to three conditions as (0,1) and (1,0) are symmetric. Note that any logical circuit can be constructed by combining the NOR (or the NAND) gates [1,2].

doi:10.1371/journal.pone.0076032.t001
logic gates have been implemented with noisy nanomechanical oscillators [12], chemical systems [12] and optical systems [13,14].

Most recently, in the context of biological systems, theoretical ideas have been proposed [15–19] on the implementation of LSR in a synthetic genetic network [19]. Now, in this work, we will provide experimental realizations of these ideas in an electronic analog of a noisy synthetic gene network. Specifically, then, we will investigate the possibility of obtaining reliable logic outputs, and explicitly demonstrate the pivotal role of noise in the optimization of the logic performance in this circuit. Further, we will show that the system is easily changed from AND/NAND logic to OR/NOR demonstrating potential for re-programmability [15,16]. Our results will thus provide verification and further understanding of noise aided logic in systems that are of considerable importance in biology.

Since understanding the intracellular processes in a network of interacting biomolecules is difficult, an alternative approach has been started recently [20], to design artificial genetic networks to derive desired functional behaviors. One important early design is a clock using three genes inhibiting each other in a cyclic order [21]. Taking into account the standard chemical kinetics for expression, degradation and inhibition, a dynamical system model was proposed where the repressor-protein concentrations and mRNA concentrations were expressed as dynamical variables. Another design is a synthetic genetic toggle-switch network [22] whose potential for noise-aided logic operation is investigated here.

The significance of using both numerical simulation and electronic circuits to model a potential synthetic genetic network is two-fold. Firstly, the numerical and circuit methods provide different imperfect models of a potential biological system. Agreement between these two models indicates robustness in the system and therefore greater likelihood that the same behavior could be realized in the proposed biological system. The biological system is generally much more difficult to construct, and therefore investigating proposed networks in simpler systems is prudent. Secondly, modeling with stochastic differential equations is nontrivial compared to ordinary differential equations, so that the addition of experimental measurements from a physical system such as an analog circuit provides valuable verification. Thus the circuit is an additional tool for investigating potentially interesting biological networks in the presence of noise.

Here we use two repressors and constitutive promoters as our model system for implementing logic functions. We begin with a brief description of LSR, then we describe the synthetic gene network model, and define what constitutes logic inputs and logic outputs in this system. We then go on to present the electronic analog of the system followed by a comparison of numerical simulation and experimental measurement.

Methods

We begin with a short description of the general principle of LSR. Consider a general nonlinear dynamic system, given by

\[ \frac{dx}{dt} = F(x) + I + D\eta(t) \] (1)

where \( F(x) \) is a generic nonlinear function which has or nearly has two distinct stable energy wells. \( I \) is a low amplitude input signal and \( \eta(t) \) is an additive zero-mean Gaussian noise with unit variance with \( D \) being the noise strength.

We achieve a logical input-output correspondence with such a system by encoding \( N \) inputs in \( N \) square waves. Specifically, for two logic inputs, we drive the system with a low amplitude signal \( I \), taken to be the sum of two pulse trains: \( I_1 + I_2 \), where \( I_1 \) and \( I_2 \) encode the two logic inputs. Now the logic inputs can be either 0 or 1, giving rise to 4 distinct logic input sets \((I_1, I_2): (0,0), (0,1), (1,0) \) and \((1,1)\). Since the input sets \((0,1)\) and \((1,0)\) give rise to the same \( I \), the input signal generated by adding two independent input signals is a 3-level aperiodic waveform.

The output of the system is determined by its state. For instance, for a bistable system with wells at \( x = x_1 \) and \( x_2 \), the output can be considered a logical 1 if it is in the well at \( x_1 \), and logical 0 if it is in \( x_2 \). If we consider the opposite assignment, namely logical 1 if the state is in well \( x_2 \) and logical 0 if the state is in well \( x_1 \), we obtain a complementary logic operation. Specifically we can have an output determination threshold \( x^* \), located near the barrier between the wells, and the logical outputs are then simply given by the state being greater than or less than \( x^* \). It is possible that the input \( I \) induces the appearance of the second energy well if it was not already there.
The central result of LSR is as follows: for a given set of inputs \((I_1, I_2)\), a logical output, in accordance with the truth tables of the basic logic operations, is consistently obtained only in an optimal window of noise. Namely, under very small or very large noise the system does not yield reliable logic outputs, while in a band of moderate noise it produces the desired output.

Synthetic Genetic Network Model and Logic Operation

We consider the previously used variation [15] of the genetic toggle switch model comprised of two genes inhibiting each other [22]. The concentrations of the two expressed proteins are \(x\) and \(y\), and their rates of change are:

\[
\frac{dx}{dt} = \frac{z_1}{1 + x^n} - \beta_1 x + g_1 + D\eta(t')
\]

\[
\frac{dy}{dt} = \frac{z_2}{1 + y^n} - \beta_2 y + g_2 + I_1 + I_2 + D\xi(t')
\]

where \(\beta_1, \beta_2\) are the rates of decay of each expressed protein and \(n\) is the Hill coefficient. The \(z_1, z_2\) describe the maximum expression rates in absence of inhibitor and they are used here as tunable parameters. In the original model \(g_1\) and \(g_2\) represent the basal synthesis rates of the promoters [23], however we use them as constant bias. The additive noise has strength \(D\) and \(\eta, \xi\) are chosen from unit variance zero mean Gaussian distributions. Such an additive noise source alters the background repressor production and represents the inherent stochasticity of biochemical processes such as transcription and translation, and the fluctuations in the concentration of a regulatory protein. \(I_1\) and \(I_2\) are two low amplitude inputs providing independent parallel production pathways of repressor \(y\). The \(t'\) indicates dimensionless time.

The system above may have two stable configurations in the \(xy\)-plane: one state has a high value of \(x\) \((x_u)\) and a low value of \(y\) \((y_l)\); the other state has a low value of \(x\) \((x_l)\) and a high value of \(y\) \((y_u)\). That is, the two dimensional potential underlying this system has two wells, \((x_u, y_l)\) and \((x_l, y_u)\), in the \(xy\)-plane. Varying the parameters changes the depth and position of these wells, and also determines whether there are one or two wells. For example, Fig. 1 shows that for the case \((g_i, I_i, D) = 0\) the system in Eqs. (2)–(3) is bistable and therefore has two stable wells only when \(\beta_1\) is close to 1.

Figure 3. Circuit for synthetic genetic network. Encoding inputs are 0 to 5 V pulse trains creating transistor currents of 0.1 and 0 mA, respectively, for \(R_i = 10k\Omega\). The \(x\) and \(y\) gene circuits are shown in Fig. 2. Each noise input is connected to its own noise circuit shown in Fig. 4. doi:10.1371/journal.pone.0076032.g003

Figure 4. Noise circuit and its connection to resistor of gene circuit. Source of noise is the reverse-biased base-emitter junction of the 2N3904 npn transistor on left. OPA2228 dual op-amps supplied from +/−12 V regulators. OPA2228 has gain-bandwidth product of 33 MHz. doi:10.1371/journal.pone.0076032.g004

Figure 5. Measured frequency spectrum of noise. For 2-stage noise amplifier shown in Fig. 4 with second stage gain 11 x. Horizontal scale is 500 kHz/Div, so cursor indicates 1.5 MHz as location of cut-off frequency. From FFT function on Tektronix TDS 2024B oscilloscope. doi:10.1371/journal.pone.0076032.g005
there is a stable state with low-
input {I_1} and {I_2} equal to 1_{ON} if the logic input is 1, and 1_{OFF} being 0 if the logic input is 0. So we have:

(i) If {I_1} + {I_2} = 0 corresponds to logic input set (0,0),
(ii) If {I_1} + {I_2} = 1_{ON} corresponds to logic input set (0,1)/(1,0).
(iii) If {I_1} + {I_2} = 1_{OFF} corresponds to logic input set (1,1).

Output. The outputs of the system are determined by the level of the dynamical variables {x(t)} and {y(t)}. For instance the output can be considered a logical 1 if the state is at the high level, and logical 0 if it is at the lower level. That is:

(i) If {x} < {x^*}, then Logic Output is 0.
(ii) If {x} > {x^*}, then Logic Output is 1.

Here {x^*} is the output determination threshold that lies between the two states, e.g., at the position of the barrier between the wells. The results presented here are not sensitive to the specific value of {x^*}.

Specifically, in this work, we consider the logic output to be 1 when the state is close to the upper well, and 0 when the state is close to the lower well, for both x and y variables. So when the system switches wells, the output is “flipped” or “toggled”.

The model in Eqs. (2)–(3) is based on the synthetic genetic toggle switch previously expressed in E. coli [22]. Parameter values used in [22] correspond here to {x_t} = (15.6,156), {n} = (1,2,5), and {beta_1} = 1 in Eqs. (2)–(3). By comparison, here we use {x_t} = 1.78, {n} = 2.4, {beta_1} = 0.9, and {beta_2} = 1. The bifurcation diagram in Fig. 1 indicates that these parameter values, along with (g_i,f_i,D) = 0, result in a system with a single stable well at {x} \approx 1.8, {y} \approx 0.35. A non-zero input I can then “shift” the bifurcation diagram of Fig. 1 so that there is a stable state with low-x, high-y for {beta_1} = 0.9.

Circuit Realization

The circuit of a single inhibitory gene [24,25] is shown in Fig. 2. The transistor current represents the rate of gene expression and the voltage {V_{out}} represents the concentration of expressed protein, {V_{in}} represents the concentration of repressor, and the {V_{bias}} adjusts the affinity of the repressor binding to the gene’s DNA. The Hill function inhibition in Eqs. (2)–(3) is accounted for by the dependence of the transistor current on repressor concentration voltage {V_{in}}. The synthetic genetic network shown in Fig. 3 is comprised of two individual gene circuits connected in a loop, each inhibiting the other. For the model in Eqs. (2)–(3), the encoding inputs {I_1} and {I_2} add to production of y which is accounted for in Fig. 3 by the two logic-driven transistors sourcing current to {V_y}. Initially parameters g_1 and g_2 are taken to be zero.

The circuit equations are obtained by applying Kirchoff’s laws to {V_x} and {V_y}, the voltages across the capacitors in Fig. 3 [24,25]. Multiplying both equations by {R_s} results in equations for {V_x} and {V_y},

\[ R_s C \frac{dV_x}{dt} = - \frac{R_s}{R_x} V_x + \frac{R_s}{R_x} V_{x,noise} + R_s i_t \]  

Figure 6. Time series for circuit measurements (upper graph) and simulations (lower) for different noise strengths showing AND/NAND LSR. Circuit shown in Fig. 3. Simulations are of Eqs. (2)–(3). Upper red and green traces indicate logic states of the encoding inputs {I_1}, and lower traces show the complementary outputs x(yellow) and y(blue). Panel (b) has noise level within the optimal range for displaying AND/NAND characteristics. Noise strengths in simulation and in circuit are: (a) D = 0.043 and V_{Nrms} = 200mV, (b) D = 0.107 and V_{Nrms} = 500mV, (c) D = 0.15 and V_{Nrms} = 700mV. Voltages and times for the circuit measurements have been converted to dimensionless quantities as described in the text.

doi:10.1371/journal.pone.0076032.g006
dependence of the transistor current circuit Eqs. (4)–(5) to the model Eqs. (2)–(3) and by adjusting without loss of continuity. The connections are found by relating the next section. Readers may go directly to Results and Discussion without loss of continuity. The connections are found by relating circuit Eqs. (4)–(5) to the model Eqs. (2)–(3) and by adjusting the dependence of the transistor current \( i_i \) on \( V_\text{in} \) in Fig. 2 to match the Hill function inhibition. The dimensionless state-variables \( (x, y) \) in Eqs. (2)–(3) are related to voltages \( V_x \) and \( V_y \) by:

\[
x = \frac{V_x}{V_\text{th}} \quad y = \frac{V_y}{V_\text{th}}
\]

where \( V_\text{th} \) corresponds to the repressor’s half-maximal inhibition binding constant \( K_i \). The maximal expression rate is

\[
\alpha_1 = \alpha_2 = \frac{i_{\text{max}} R}{V_\text{th}} = \frac{1.35}{0.76} = 1.78
\]

where the voltage \( i_{\text{max}} R \) is \((3\text{mA})(0.45\text{k} \Omega) = 1.35\text{V}\) and \( V_\text{th} = 0.76\text{V} \). Protein decay rates are \( \beta_1 = \frac{R_y}{R} = \frac{350}{520} = 0.69 \), and \( \beta_2 = 1 \). The Hill coefficient \( n \) comes from

\[
n = 1.17 G_1 G_{-2}
\]

where \( G_1 = -1.1 \) and \( G_{-2} = -3.3 \) are closed loop gains of \( U_1 \) and \( U_2 \) in Fig. 2 resulting in \( n = (1.17)(1.33)/1.78 = 2.4 \). The characteristic time is \( R_y C = (470\text{W})(2\times 10^{-8}\text{f}) = 9.4\mu\text{s} \), so the dimensionless time is \( t' = \frac{t}{\mu\text{s} C} \).

The high value \( I_{\text{ON}} \) of the encoding signals \( I_1, I_2 \) is given by

\[
I_{\text{ON}} = \frac{R_y \times (1\text{volt})}{R_1 V_\text{th}} = \frac{470}{R_1(0.76)} = \frac{618}{R_1} \tag{8}
\]

where \( I_{\text{ON}} \) is changed by varying \( R_1 \) in Fig. 3, i.e. \( R_1 = 10\text{k} \Omega \) gives \( I_{\text{ON}} = 0.062 \). A non-zero value of \( g_2 \) in Eq. 3 is achieved in the circuit by including a third current-sourcing transistor in Fig. 3 in the same way as the two encoding signal transistors, but with the emitter resistor labelled \( R_y \) and the input grounded so that the transistor provides a constant current \( i_g = (1\text{volt})/R_g \). \( g_2 \) is changed by varying \( R_y \) in the same way \( R_1 \) controls \( I_{\text{ON}} \). The non-zero \( g_2 \) adds a term \( R_y i_g \) to Eq. 5, where

\[
g_2 = \frac{R_y i_g}{V_\text{th}} = \frac{470}{R_y(0.76)} = \frac{618}{R_y} \tag{9}
\]

\( R_y = 10\text{k} \Omega \) gives \( g_2 = 0.062 \).

The \( V_{\text{noise}} \) terms in Eqs. (4)–(5) approximate white noise voltages. Each \( V_{\text{noise}} \) is characterized by its measured \( \text{rms} \) value \( V_\text{Nms} \) and bandwidth. \( V_\text{Nms} \) is controlled by changing the gain.
via the potentiometer in Fig. 4. Noise strength $D$ in Eqs. (2)–(3) is given by

$$D = \frac{V_{\text{Nrms}}}{V_{\text{th}} \sqrt{\gamma R C f_c}} = \frac{V_{\text{Nrms}}}{0.76 \sqrt{(9.4\mu s)(1.5MHz)}}$$

where $f_c = 1.5$ MHz is the cut-off frequency for the amplifier in Fig. 4 and $\gamma$ decreases from $\pi/2$ at low gain to $\pi/4$ at high gain (when the potentiometer is set to 20k$\Omega$ in Fig. 4).

**Circuit Analysis and Simulation**

Here we describe the circuit analysis and derive the connections between the model parameters used in Eqs. (2)–(3) and the circuit parameters. Further details are given in Refs. [24,25].

The single gene circuit in Fig. 2 is designed to reproduce the Hill function inhibition in Eqs. (2)–(3). The op-amp U1 is configured as a subtraction amplifier with gain $G_1 = -\frac{11}{10} = -1.1$. Replication of the Hill function behavior is achieved by allowing saturation of the output of the op-amp U2 and by having different unsaturated gains $G_{+2}$ and $G_{-2}$ for $V_{\text{in}} > V_{\text{th}}$ and $V_{\text{in}} < V_{\text{th}}$, respectively, due to the diodes in the feedback for U2. $G_{-2}$ is the gain of U2 when its output goes negative, in which case the diodes are not conducting, and therefore $G_{-2} = -\frac{3.3}{11} = -3.3$. $G_{+2}$ is a diminishing gain when the output of U2 becomes increasingly positive causing the diodes to go into conduction. An increasing repressor concentration corresponds to $V_{\text{in}}$ surpassing $V_{\text{th}}$ which causes the unsaturated output at U2 to change from a negative voltage of $G_1 G_{-2} (V_{\text{in}} - V_{\text{th}})$ to a positive voltage $G_1 G_{+2} (V_{\text{in}} - V_{\text{th}})$. The increasing voltage at the output of U2 turns the transistor off ($t = 0$) which corresponds to complete inhibition of protein expression. Maximal protein expression $y_{1,2}$ in Eqs. (2)–(3) corresponds to the maximum value of $t_1$, designated $t_{\text{max}}$. $t_1 = t_{\text{max}}$ occurs when $V_{\text{in}} = 0$ (no repressor) because the output of U2 saturates at $V_{\text{sat}} = -3.5$ V (for the LF412 op-amp supplied by ±5 V), resulting in a 0.65 V drop across the 220W and therefore $b_{\text{max}} = 3$ mA.

Circuit parameters for $x$ and $\beta$ are found by using $V_{\text{th}}$ to convert Eqs. (4)–(5) to a dimensionless form for comparison to Eqs. (2)–(3). The 0.45k$\Omega$ used for $R$ in Eq. (6) comes from the $R_p = 470W$ being nearly in parallel with the resistance (10k$\Omega$) at the input to the subtraction amplifier U1 for gene-$x$. The relation for Hill coefficient $n$ is found by adjusting the dependence of $t_1$ on $V_{\text{in}}$ to match the slope of the Hill function $1/(1+x^n)$ at $x = 1$ resulting in the relation [25]:

$$\frac{-f V_{\text{th}} G_1 G_{-2}}{f(5 - V_{\text{sat}}) - 0.6} = -\frac{n}{4}$$

In Fig. 2 the voltage divider fraction $f = 0.4/2.6 = 0.154$ and $V_{\text{sat}} = -3.5$ V. Using Eq. (6) in Eq. (11) yields Eq. (7).

Parameter $V_{\text{th}}$’s correspondence to binding affinity of repressor to DNA is seen by noting that Eqs. (2)–(3) are dimensionless,
meaning that in the process of going from chemical kinetic equations to Eqs. (2)–(3) the maximal expression rate $\chi$ has been scaled by the repressor’s inhibition binding constant $K_i$, and by a mRNA degradation rate $[21,25]$. From Eq. (6) it follows that $V_{in}$ is proportional to $K_i$ since $\chi$ is inversely proportional to $K_i$ due to the scaling. To find the relation between $V_{th}$ in Fig. 2 and $V_{in}$ we note that the Hill function equals 0.5 when $x = 1$. Therefore $i_i$ must be half its maximum value when $V_{in} = V_{th}$ which gives [25]

$$\frac{i_i}{i_{\text{max}}} = \frac{f(5 - G_1 - G_2(V_{in} - V_{\text{th}})) - 0.55}{f(5 - V_{\text{sat}}) - 0.6} = 0.5$$

Solving gives $V_{\text{th}} \approx V_{in} + \frac{1}{\mu i_{\text{max}}}$. Using $n = 2.4$ and $\chi = 1.78$ in Eqs. (6)–(7) gives: $G_1, G_2 = 3.65$, satisfied by $G_1 = -1.1$ and $G_2 = -3.3$; $V_{\text{th}} = 1.35/1.78 = 0.76$ V; and $V_{\text{th}} = 0.76 + 1/3.65 = 1.03$ V.

Comparing Eqs. (3) and (5) shows that the encoding signals $i_1, i_2$ are related to the transistor currents $i_{th}$ by:

$$i_{th} = \frac{R}{V_{th}}.$$ (12)

The encoding currents $i_{th}$ take on two possible values depending on whether their logic train input in Fig. 3 is high or low. When the input is high ($>4\text{V}$) the transistor is off so the current is zero. When the input is zero, the voltage divider consisting of the 4.7kΩ and 2.2kΩ produces one volt across the $R_1$ connected to the emitter of the $npn$ transistor creating current $i_{th} = (1\text{Vvolt})/R_1$. Equation (12) then gives Eq. (8) for $i_{th}$. Results of an analysis for a non-zero value of parameter $g_2$ are the same as for encoding signals $i_1, i_2$ and currents $i_{th}$ because $g_2$’s sourcing transistor is set up in the same way as the transistors in Fig. 3 for the encoding signals. Thus the non-zero value of $g_2$ is Eq. (9).

Here we show how to use simulations to predict the circuit results. In the process we find Eq. (10), the connection between circuit parameters and the noise strength $D$ in Eqs. (2)–(3). A standard Euler-Maruyama simulation of Eq. (2) is:

$$x_{t+1} - x_t = \left( \frac{2}{1 + \beta_1} - \beta_1 x_t + g_1 \right) \Delta t + D\sqrt{\Delta t} \eta(0,1)$$ (13)

where $\eta(0,1)$ is a unit variance zero mean normal random distribution. The noise circuit in Fig. 4 produces a measurable $rms$ voltage $V_{\text{N rms}}$ consisting of contributions from all the frequency components present in the noise. The variance of the noise is the integral of its spectral density function $SD(f)$ over frequency, and is obtained from a measurement of $V_{\text{N rms}}$:

$$V_{\text{N rms}} = \int SD(f) df.$$ (14)

Idealized white noise assumed in Eqs. (2)–(3) has a $SD(f)$ which is uniform over an infinite bandwidth. However, for the real noise from the 2-stage noise amplifier circuit in Fig. 4 each op-amp’s gain-bandwidth product produces a high frequency cut-off, $f_{c1}$ and $f_{c2}$. The resulting $SD(f)$ has the form

$$SD(f) = \frac{SD_0}{(1 + (f/f_{c1})^2)(1 + (f/f_{c2})^2)}$$ (15)

where $SD_0$ is a constant related to the strength of the noise. The OPA228 op-amp has a gain-bandwidth product 33 MHz, therefore the first stage in Fig. 4 with fixed gain $22 \times$ has cut-off, $f_{c1} = 33/22 = 1.5$ MHz. The second stage’s cut-off $f_{c2}$ varies from 3 to 1.5 MHz depending on the gain setting determined by the potentiometer in the feedback of the second stage op-amp.

The integration in Eqs. (14)–(15) gives

$$V_{\text{N rms}}^2 = \int_0^\infty \frac{SD_0}{(1 + (f/f_{c1})^2)(1 + (f/f_{c2})^2)} df$$ (16)

$$f_{c1}/f_{c2} \pi \frac{SD_0}{f_{c1} + f_{c2}}$$

There are two limiting cases for the integral: for small gain $f_{c2} \approx 33$ MHz $> f_{c1}$ giving $(\pi/2)f_{c1}SD_0$; and for large gain (potentiometer $\rightarrow 20$kΩ in Fig. 4) $f_{c2} \ll f_{c1}$ giving $\pi f_{c1} SD_0$. Thus the integral in Eq. (16) is $\int f_{c1}SD_0$ where $f_{c1} = 1.5$ MHz and $\pi$ varies from $\pi/2$ for small noise to $\pi$ for large noise. For frequencies within the noise bandwidth ($<1.5$ MHz) the amplitude spectral density ASD (units Volt Hz$^{-1/2}$) has a constant value given by

$$ASD_0 = \frac{SD_0}{\sqrt{\gamma f_{c1}}}$$ (17)

$ASD_0$ is a good approximation of the white noise strength provided that the Fig. 3 genetic circuit’s characteristic response rate $1/(RC)$ is much less than the noise bandwidth, meaning that $1/(RC) << 2\pi f_{c1}$. This condition is ensured since the $RC$ used here is $(470\Omega)(0.02\mu F) = 9.4\mu F$, and the bandwidth of the noise is $f_{c1} = 1.5$ MHz.

Figure 5 shows the measured frequency content from the noise circuit when the potentiometer at the second stage is set for gain $11 \times$ producing $V_{\text{N rms}} = 0.90$ V and $f_{c2} = 33/11 = 3$ MHz. In this case $f_{c2} = 2f_{c1}$ and $\gamma = \pi/3$. Figure 5 shows that the frequency content is relatively flat out to the cut-off near 1.5 MHz and therefore is a good approximation to white noise for the genetic network circuit. $V_{\text{N rms}} = 0.90$ V is on the high end of the noise amplitudes used here.

The circuit Eqs. (4)–(5) which need to be simulated are of the form

$$\frac{dV}{dt} = \frac{f(V)}{RC} + \frac{V_{\text{noise}}}{RC}$$ (18)

where $V_{\text{noise}}$ approximates a white noise voltage. $V_{\text{noise}}$ is characterized by its measured $rms$ value $V_{\text{N rms}}$ and bandwidth, and Eq. (17) gives the noise’s amplitude spectral density. The Euler-Maruyama simulation for Eq. (18) is

$$V_{t+1} - V_t = \frac{f(V)}{RC} \Delta t + \frac{ASD_0}{RC} \sqrt{\Delta t} \eta(0,1).$$ (19)

Using dimensionless time $\Delta t' = \Delta t/(RC)$ and the measured noise
amplitude $V_{\text{Nrms}}$ gives
\[ V_{i+1} - V_i = f(V_i)\Delta t + \frac{V_{\text{Nrms}}}{\sqrt{\gamma RC f_c}} \sqrt{\Delta t} \eta_i(0,1) \tag{20} \]

Normalizing by the voltage scale $V_{\text{th}}$ puts Eq. (20) in the form of Eq. (13) and gives the connection between circuit parameters and dimensionless noise amplitude $D$ shown in Eq. (10). For example, using $V_{\text{th}} = 0.76V$, $RC = 470W(0.02\mu F)$, $\gamma = \pi/2$, and $f_c = 1.5$ MHz gives $D = 0.28V_{\text{Nrms}}$.

Results and Discussion

Figure 6 shows simulations and circuit measurements for three values of noise using parameters $n = 2.4$, $x = 1.78$, $\beta_1 = 0.90$, $\beta_2 = 1$, $I_{\text{ON}} = 0.067$, and $g_1 = g_2 = 0$. It is apparent that for an optimal noise level (Fig. 6b) the circuit indeed performs the logic AND/NAND function, and that for the smaller (Fig. 6a) and optimal noise level (Fig. 6b) the circuit indeed performs the logic function.

In order to investigate the range of noise strengths which produce accurate logic response, and to find optimal values for the amplitude $I_{\text{ON}}$ of the small signal inputs $I_{1,2}$ we define an accuracy measure $a$,
\[ a = a_L \times a_H \tag{21} \]

where $a_L$ is the percent of time the $x,y$ outputs are correct when the AND operation of $I_1$ and $I_2$ is low, and $a_H$ is the percentage of time correct when the AND operation is high. This definition has the property that when the $x,y$ outputs do not respond at all, then $a = 0$ since $a_H = 0$ even though $a_L = 1$. The expectation then is that for no noise there should be no stochastic resonance response so that $a = 0$, and that for extreme noise each accuracy would approach 50% so that $a \rightarrow 0.25$. If $x,y$ respond immediately with no mistakes then $a = 1$. Figure 7 shows accuracy $a$ for simulations and circuit measurements as a function of noise strength for different values of encoding amplitude $I_{\text{ON}}$.

Figure 7a shows that for a small value of encoding amplitude, $I_{\text{ON}} = 0.051$ the network is not able to give a faithful logic response at any noise level. The response at low noise and at high noise are as predicted, $a = 0$ and $a \rightarrow 0.25$, respectively, but the peak of the window of stochastic resonance response is well below 1. Figure 7b shows a window of noise providing faithful response for $I_{\text{ON}} = 0.067$. The reason that the accuracy $a$ is slightly below 1 in the window is that the $x,y$ outputs do not respond immediately to the AND/NAND transitions. This time lag causes the percent of time with incorrect response to be non-zero and therefore $a_H$ and $a_L$ are less than 1. In principle an allowance for a time lag could be included in the calculation of $a$ if it were deemed necessary. However, such an added complication would not make the noise window any more apparent than it already is in Fig. 7. Figure 7c shows that at a high value, $I_{\text{ON}} = 0.10$, the outputs respond even with no noise, and the addition of noise only creates more errors. The relative shift between the simulation and circuit accuracies is due to assumptions made about the noise spectral density function and the integration in Eq. (16) leading to Eq. (20) which gives the connection between the measured noise amplitude $V_{\text{Nrms}}$ and dimensionless noise $D$. In the idealized case Eq. (16) finds $\gamma$ ranges from $\pi/2$ to $\pi/4$ in Eq. (20), with $\pi/2$ being appropriate for the noise levels used in Fig. 6. Adjusting the value of $\gamma$ can eliminate the relative shift, but there is nothing to be gained since the appearance of an optimal noise window for LSR at an appropriate value of $I_{\text{ON}}$ is already apparent.

One can also reconfigure the system to another set of logic functions, namely the fundamental OR/NOR logic, by simply including a non-zero value for $g_2$. The parameter $g_2$ effectively changes the relative position and depth of the wells of the bistable system, allowing the response to morph from AND/NAND to OR/NOR. For instance changing $g_2$ from 0 to 0.062 (with all other parameters unchanged) changes the bifurcation diagram from that in Fig. 1 to Fig. 8 showing that the system is now bistable for $\beta_1 = 0.9$. The result is that the system displays a clear OR and the complementary NOR response as shown by the simulation and circuit results in Fig. 9. The low noise case Fig. 9a shows that at this low noise level the system usually fails to respond. Figure 9a also shows the remaining states are reversed from the $g_2 = 0$ case in Fig. 6a. Figure 9b shows the OR/NOR response at a noise value within the window for LSR, and Fig. 6c shows errors when the noise is too large.

In summary, our results show that the dynamics of the two variables $x$ and $y$ with $g_2 = 0$, mirror AND and the complementary NAND gate characteristics. Further, when $g_2 \neq 0$, we obtain a clearly defined OR/NOR gate. Since $x$ is low when $y$ is high and vice-versa, the dynamics of the two variables always yield complement logical outputs, simultaneously. That is, if $x(t)$ operates as NAND/NOR, $y(t)$ will give AND/OR.

These results extend the scope and indicate the generality of the recently observed phenomena of Logical Stochastic Resonance through experimental verifications. Further, these observations may provide an understanding of the information processing capacity of synthetic genetic networks, with noise aiding logic patterns. It also may have potential applications in the design of biologically inspired gates with added capacity of reconfigurability of logic operations.

We have also demonstrated that the electronic circuit provides an additional tool for investigating dynamics of proposed genetic networks. The circuit measurements are complementary to numerical simulations, thereby giving indication of the robustness of a particular network design and potential for successful realization in a biological system.

Thus the results presented in this work suggest new directions in biomolecular computing, and indicate how robust computation may be occurring at the scale of regulatory and signalling pathways in individual cells. Design and engineering of such biologically inspired computing systems not only present new paradigms of computation, but can also potentially enhance our ability to study and control biological systems [26].

Author Contributions

Conceived and designed the experiments: EHH SKD JK SS. Performed the experiments: EHH. Analyzed the data: EHH. Wrote the paper: EHH SKD SS.
References

1. Mano MM (1993) Computer system architecture (3. ed.). Pearson Education, I-XIII, 1–524 pp.
2. Barter T (1991) Computer architecture and logic design. Computer science series. McGraw-Hill. URL http://books.google.com/books?id=b9dQAAAAMAAJ.
3. Bulsara A, Jacobs E, Zhou T, Moss F, Kiss L (1991) Stochastic resonance in a single neuron model: Theory and analog simulation. Journal of Theoretical Biology 152: 531–555.
4. Levin JE, Miller JP (1996) Broadband neural encoding in the cricket cereal sensory system enhanced by stochastic resonance. Nature 380: 165–166.
5. Gammaitoni L, Hanggi P, Jung P, Marchesoni F (1998) Stochastic resonance. Rev Mod Phys 70: 223–287.
6. Hanggi P (2002) Stochastic resonance in biology how noise can enhance detection of weak signals and help improve biological information processing. ChemPhysChem 3: 285–289.
7. Murali K, Sinha S, Ditto WL, Bulsara AR (2009) Reliable logic circuit elements that exploit nonlinearity in the presence of a noise floor. Phys Rev Lett 102: 104101.
8. Murali K, Rajamohamed I, Sinha S, Ditto WL, Bulsara AR (2009) Realization of reliable and exible logic gates using noisy nonlinear circuits. Applied Physics Letters 95: 194102.
9. Guerra DN, Bulsara AR, Ditto WL, Sinha S, Murali K, et al. (2010) A noise-assisted reprogrammable nanomechanical logic gate. Nano Letters 10: 1168–1171.
10. Worschech L, Hartmann F, Kim TY, Hoing S, Kamp M, et al. (2010) Universal and reconfigureable logic gates in a compact three-terminal resonant tunneling diode. Applied Physics Letters 96: 042112.
11. Fierens P, Ibez S, Perazzo R, Patterson G, Grosz D (2010) A memory device sustained by noise. Physics Letters A 374: 2207–2209.
12. Sinha S, Cruz JM, Bulsara AR, Ditto WL, Sinha S (2009) Exploiting the effect of noise on a chemical system to obtain logic gates. EPL (Europhysics Letters) 90: 060003.
13. Singh KP, Sinha S (2011) Enhancement of “logical” responses by noise in a bistable optical system. Phys Rev E 83: 046219.
14. Perrone S, Vilaseca R, Massler C (2012) Stochastic logic gate that exploits noise and polarization bistability in an optically injected vcsel. Opt Express 20: 22692–22699.
15. Ando H, Sinha S, Storm R, Aihara K (2011) Synthetic gene networks as potential flexible parallel logic gates. EPL (Europhysics Letters) 93: 50001.
16. Dari A, Kia B, Bulsara A, Ditto W (2011) Creating morphable logic gates using logical stochastic resonance in an engineered gene network. EPL (Europhysics Letters) 93: 18001.
17. Dari A, Kia B, Wang X, Bulsara AR, Ditto W (2011) Noise-aided computation within a synthetic gene network through morphable and robust logic gates. Phys Rev E 83: 041909.