Efficient and Reversible Electron Doping of Semiconductor-Enriched Single-Walled Carbon Nanotubes by Using Decamethylcobaltocene

Jian-Long Xu1,2, Rui-Xuan Dai1, Yan Xin3, Yi-Lin Sun1, Xian Li1, Yang-Xin Yu3, Lan Xiang4, Dan Xie1, Sui-Dong Wang2 & Tian-Ling Ren1

Single-walled carbon nanotubes (SWCNTs) offer great potential for field-effect transistors and integrated circuit applications due to their extraordinary electrical properties. To date, as-made SWCNT transistors are usually p-type in air, and it still remains challenging for realizing n-type devices. Herein, we present efficient and reversible electron doping of semiconductor-enriched single-walled carbon nanotubes (s-SWCNTs) by firstly utilizing decamethylcobaltocene (DMC) deposited by a simple spin-coating process at room temperature as an electron donor. A n-type transistor behavior with high on current, large $I_{on}/I_{off}$ ratio and excellent uniformity is obtained by surface charge transfer from the electron donor DMC to acceptor s-SWCNTs, which is further corroborated by the Raman spectra and the ab initio simulation results. The DMC dopant molecules could be reversibly removed by immersion in N, N-Dimethylformamide solvent, indicating its reversibility and providing another way to control the carrier concentration effectively as well as selective removal of surface dopants on demand. Furthermore, the n-type behaviors including threshold voltage, on current, field-effect mobility, contact resistances, etc. are well controllable by adjusting the surface doping concentration. This work paves the way to explore and obtain high-performance n-type nanotubes for future complementary CMOS circuit and system applications.

Single-walled carbon nanotubes (SWCNTs) are promising for post-silicon nanoelectronics especially for field-effect transistor applications on rigid and flexible substrates due to their extraordinary properties including high mobility, low costs, high transparency, excellent robustness and one-dimensional structure that can provide excellent performances at scaled transistor channel lengths both at the device and system level1–6. To obtain the most performance benefits from scaled carbon nanotube field-effect transistors (CNFETs), circuits and systems made from carbon nanotubes must be fulfilled via the complementary logic types utilizing both p- and n-type transistors rather than unipolar ones because unipolar logic circuits usually sustain high steady-state currents and thus resulting in significant steady-state power consumption during operation6,7. However, n-type doping of semiconductor-enriched SWCNTs (s-SWCNTs) has still remained challenging since the as-fabricated CNFETs are usually p-type in ambient due to the electron withdrawal effects by the absorbed oxygen on nanotube surface8. In this regard, surface charge transfer is a widely adopted and effective n-type doping technique through directly depositing electron donor materials onto nanotubes9. In this approach, by the choice of surface absorbed species with proper reduction potentials, the electron donation from dopants to nanotubes and thus n-doping effects of dopants on nanotubes can be induced. Notably, in comparison with conventional substitutional doping methods, the surface charge transfer method does not induce any structural defects in the lattices10. A range of electron donating molecules such as dihydronicotinamide adenine dinucleotide (NADH),

1Institute of Microelectronics, Tsinghua National Laboratory for Information Science and Technology (TNList), Tsinghua University, Beijing, 100084, China. 2Institute of Functional Nano & Soft Materials (FUNSOM), Jiangsu Key Laboratory for Carbon-based Functional Materials & Devices, Soochow University, Suzhou, 215123, Jiangsu Province, China. 3Laboratory of Chemical Engineering Thermodynamics, Department of Chemical Engineering, Tsinghua University, Beijing, 100084, China. 4Department of Chemical Engineering, Tsinghua University, Beijing, 100084, China. Correspondence and requests for materials should be addressed to J.-L.X. (email: xujianlong@suda.edu.cn) or D.X. (email: xiedan@tsinghua.edu.cn) or T.-L.R. (email: RenTL@tsinghua.edu.cn)

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benzyl viologens (BV), poly (ethylene imine) (PEI), hydrazine, etc. have been shown to be highly effective in donating electrons to nanotubes for n-type doping, which can be easily deposited by simple solution processing methods\textsuperscript{11–15}. Moreover, to achieve robust digital circuits with high immunity against the effects of electronic noise in the electronic systems, it is important and necessary to control the threshold voltage of transistors because the threshold voltage determines the input voltage at which the circuit switches between two logic states\textsuperscript{7}. If the threshold voltage cannot be exactly controlled during the fabrication process, the circuit might not work reliably due to the electrical noise that is always present in the electronic system\textsuperscript{7,16–18}. However, it still remains challenging for CNFET devices while there are only few works reported about tuning the threshold voltage of n-type CNFETs\textsuperscript{19}.

Here we demonstrate the efficient and reversible n-type doping of s-SWCNTs by adopting decamethylcobaltocene (DMC) as an efficient surface charge transfer electron donor. DMC doping was previously reported to be applied as efficient surface charge transfer electron dopants for organic host semiconductors and novel 2-dimensional (2D) materials including pentacene, copper phthalocyanine (CuPc), fullerenes and graphene due to its extremely low solid-state ionization energy (IE) of 3.3 eV and twelve $\pi$ electrons induced strong adhesion with doped material surface, demonstrating its stable electron donating characteristics and thus n-doping effects on them\textsuperscript{20–24}. The surface charge transfer based n-doping effects is much more preferred compared with the chemically modified methods\textsuperscript{25–28}, where the charge transfer process occurring in undoped semiconductors always needs complicated synthesis process. In this paper, we demonstrate the n-doping effects of DMC molecules on s-SWCNTs by a simple solution process. N-type CNFETs with high-performance transistor behaviors and excellent uniformity are obtained, which also show high stability, demonstrated by time-dependent electrical measurements. Electron transfer from DMC to s-SWCNTs generates an electron transfer complex, allowing for extensive charge transport characterization and practical CMOS circuits applications. A surface charge transfer model was proposed to be responsible for the above n-doping behaviors, confirmed by Raman spectroscopy and the ab initio simulations. Furthermore, the n-type transistor behaviors including threshold voltage, on current, field-effect mobility, contact resistances, etc. are well controllable by adjusting the DMC concentration. This work provides us a novel and effective way for fabricating high-performance n-type CNFETs, making them promising for future CMOS circuit and system applications.

**Results**

**Characterization of As-Fabricated P-type CNFETs.** The effects of DMC on the electrical properties of nanotubes were firstly examined by fabricating back-gated CNFETs based on random nanotube networks (as shown in Fig. 1a). The detailed fabrication process can be seen in the Methods section. Figure 1b presents the AFM image of the obtained nanotube network on SiO\textsubscript{2}/Si substrates, from which a densely packed nanotube network suitable for TFT applications can be clearly seen. The channel region was defined by photolithography and then oxygen plasma etching to remove the extra nanotubes. Finally, the wafers were annealed in argon atmosphere for 30 min at 200°C to remove the residual resist scum which may affect the following doping process. The fabricated 4-in. wafer composed of CNFETs with ranging channel widths and lengths is shown in Figure S1 and Fig. 1c presents the optical microscopy of a back-gated CNFET device with the channel length (20 $\mu$m) and width.
excellent gate modulation effects on the nanotube channel with a high on/off current ratio of 5 shown in Figs. 2b and 2c. The n-doping level is nondegenerate and still at a controllable level to allow for the spin-coating procedure is very efficient for n-doping of nanotubes, as indicated by the transfer and output curves withdrawal effects by the absorbed oxygen on nanotubes in air ambient consistent with the reported results. Properties with less electrical noise and linear characteristics at lower 

\[ V_{ds} \]

After exposure to air for 1, 2, 3, 4 and 5 weeks under a high humidity (40~80%) with no encapsulation (Figure S2). The air-stability properties of our doped n-type devices were also studied by measuring the transfer curves of the same device immediately after the doping treatment as well as after exposure to air for 1, 2, 3, 4 and 5 weeks under a high humidity (40~80%) with no encapsulation (Figure S2). After exposure to air at such a high humidity for 5 weeks, the doped CNFET device still exhibits n-type transistor behaviors with little on current decrease and original p-type behaviors gradually appearing. However, the on/off current ratio almost keeps constant and the field-effect electron mobility first decreases and then almost remains constant after exposure in air for 3 weeks, as shown in the inset figure of Figure S2. It indicates its relatively good air-stability with slow property degradation in air, however, the stability still needs to be improved by adopting a capping layer such as CYTOP, PMMA, etc. in future. Moreover, the device-to-device uniformity of the DMC doping technique is very critical for exploring its possible practical applications. The transistor characteristics of 35 doped CNFET devices in one wafer were measured and their transistor parameters such as threshold voltage, on/off current ratio, on current etc. are extracted and counted for further analyses. Figs 2(d)–(f) present the histograms of extracted \[ V_{th} \], \[ I_{on}/I_{off} \] and \[ V_{th} \] (at \[ V_{gs} = 20 \text{ V}, \ V_{ds} = 5 \text{ V} \] for our measured 35 doped CNFET devices, clearly

Figure 2. Structural and electrical characterization of back-gated CNFETs after DMC n-doping. (a) The schematic of the CNFET device coated with a DMC layer and the molecular structure of DMC with cobalt atom (blue), carbon atoms (gray) and hydrogen atoms (white). (b) Transfer curves and (c) output characteristics of a doped CNFET device after 5.0 wt.% with W/L = 100 μm/20 μm. Distribution of (d) \[ I_{ds} \] current at \[ V_{gs} = 20 \text{ V} \] and \[ V_{ds} = 5 \text{ V} \], (e) threshold voltage and (f) \[ I_{on}/I_{off} \] current ratio of 35 n-type doped CNFET devices.

Efficient Electron Doping Effects of DMC on s-SWCNTs. To n-dope s-SWCNTs, a DMC solution where DMC powders were dissolved in N, N-Dimethylformamide (DMF) solvent with a concentration of 5.0 wt. % was directly spin-coated onto nanotube network surface at room temperature to form a uniform doping layer. Figure 2a shows the DMC molecule structure and a schematic of a CNFET device chemically doped with DMC. As shown in Fig. 2a, a DMC molecule consists of a cobalt atom sandwiched between two methyl-substituted cyclopentadienyl groups and 19 valence electrons, and thus it has an extremely low solid-state IE of 3.3 eV, making it an optimal choice for an electron donor. For probing the doping effects, a wide range of doped CNFET devices were tested, where the prepared samples after doping process were kept in ambient air without any passivation process before, during and after electrical characterization. All the fabricated CNFET devices exhibit clear polar conversion from p-type to n-type charge transport characteristics after DMC layer deposition (the electrical transistor behaviors of all samples are recorded and counted in Fig. 2d–f) and our simple passivation process before, during and after electrical characterization. All the fabricated CNFET devices were tested, where the prepared samples after doping process were kept in ambient air without any passivation process before, during and after electrical characterization. All the fabricated CNFET devices exhibit clear polar conversion from p-type to n-type charge transport characteristics after DMC layer deposition (the electrical transistor behaviors of all samples are recorded and counted in Fig. 2d–f) and our simple passivation process before, during and after electrical characterization.
demonstrating the uniform device-to-device distribution of our DMC n-doping technique for CNFET devices. Thus, the obtained device-to-device uniformity of doped CNFET devices is yet sufficient for the applications of this n-doping method in practical circuit and system applications such as displays, sensors, wearable electronics, etc.30, 31.

Controllable Transistor Behaviors of n-type CNFETs by DMC doping. N-type electron donor DMC layer spin-coating is the most crucial step in this doping technique. In our n-doping process, the DMC/DMF solution was directly spin-coated onto the CNT channel region and thus the n-doping extent mainly depends on the amounts of DMC molecules deposited onto the CNT channel surface. Therefore, to further examine the n-doping effects of DMC surface functionalization on the electrical transport behaviors of CNFET devices, DMC/DMF solutions with different DMC concentrations ranging from 0.5 to 5.0 wt.% were spin-coated onto the channel surface and their electrical characteristics were then measured and analyzed. Figure 3a shows the transfer characteristics of the CNFET device on a logarithmic scale after spin-coating DMC/DMF solutions with different concentrations ranging from 0.5 to 5.0 wt.% at \( V_{d} = 5 \) V measured in air ambient and at RT. The drain voltage applied between the drain and source electrodes was fixed at 5 V for all samples during all electrical measurements. When the DMC concentration is 0.5 wt.%, the p-type behaviors almost disappear and the doped device convert from original p-type to n-type behaviors, proving that DMC has an excellent doping level on carbon nanotubes even at such a low concentration. A progressively heavier n-doping can be observed with increasing doping concentrations, reflected by an increasing \( I_{on} \) at \( V_{GS} = 20 \) V and diminishing current at \( V_{GS} = -20 \) V, as shown in Fig. 3b. Moreover, the n-doping level is still nondegenerate and the gate control over the channel conduction is still large when the DMC concentration increases to a high doping concentration (5 wt.%), displaying the large doping tenability via DMC concentration.

Figure 3. (a) The transfer curves (\( I_{ds} - V_{gs} \)) of the a CNFET device on a logarithmic scale after spin-coating DMC/DMF solutions with different concentrations ranging from 0.5 to 5.0 wt.% at \( V_{d} = 5 \) V. Inset shows the transfer curves on a linear scale. (b) The drain current value \( I_{on} \) at \( V_{gs} = 20 \) V and \( -20 \) V as a function of DMC concentration. (c) Threshold voltage value of the doped CNFET device as a function of DMC concentration. (d) The schematic energy band diagrams of the Au-SWCNT heterojunction exhibiting the electron barrier height and region width reduced by increasing DMC concentrations. (e) The ON/OFF current ratio \( (I_{on}/I_{off}) \) at \( V_{d} = 5 \) V and the field-effect electron mobility ratio \( (\mu_{FE} \text{ ratio} = \mu_{FE-DMC}/\mu_{FE-DMC-0.5wt.\%}) \) as a function of DMC concentrations. (f) Contact resistance extracted by using the gated transmission line method as a function of DMC concentrations.
in CNTs\textsuperscript{22,31}. When the doping concentration increases, more electrons will transfer from DMC to s-SWCNTs, leading to larger upward shift of the vacuum level and Fermi level and thus stronger n-doping effects. The larger upward shift of Fermi level will cause a smaller Schottky barrier width for electron injection with increase of doping concentration at Au/s-SWCNT interfaces, as shown in band diagrams (Fig. 3d). It provides an excellent and convenient way to obtain the n-type CNFET devices with the desired threshold voltage value by adjusting the doping concentration for carbon nanotube based CMOS circuits and electronic systems.

Moreover, the electron field-effect mobility also increases with the increase of doping concentration, as shown in Fig. 3e. The electron field-effect mobility increases rapidly at low doping concentration, however, it increases much more slowly at higher doping concentrations. The slower gain is likely to originate from the aggregation of dopants for high doping concentration, which limits the contact between the DMC dopant and underlying s-SWCNTs and perturbs the morphology of nanotube networks and the tube-to-tube contacts\textsuperscript{35}. Furthermore, as presented in Fig. 3e, the on/off current ratio of the doped samples almost remains constant at $5 \times 10^4$ for all doping concentrations. Therefore, DMC is confirmed to be an effective n-type dopant for carbon nanotubes with excellent gate control abilities, high on/off current ratio, large on current and accurate and continuous threshold voltage tuning of the n-type semiconducting behaviors by adjusting the doping concentration. In particular, as a result of the reduced electron injection barrier width, the contact resistance reduction is expected, confirmed by Fig. 3f where the contact resistance value is extracted from Fig. 3a by using the gated transmission line method\textsuperscript{34,35}.

**Discussions**

**Reversible Doping Behaviors and Surface Charge Transfer Induced Electron Doping Mechanisms.** From the above electrical characterizations, it can be seen that DMC is an effective electron donor for nanotubes and results in high-performance n-type s-SWCNTs and thus n-type CNFETs. The DMC molecule is composed of a cobalt atom sandwiched between two methyl-substituted cyclopentadienyl groups and owns 12 π electrons (6 from each substituted cyclopentadienyl ring)\textsuperscript{23,24}, resulting in strong π–π interactions and thus excellent adhesion effects between DMC molecule and s-SWCNTs\textsuperscript{24}. Given the strong reduction nature of DMC, a surface charge transfer based n-doping model is proposed where the neutral molecule (DMC\textsuperscript{0}) directly transfers electrons to the unoccupied π* states of carbon nanotubes (that is to say, to reduce the nanotubes) and eventually results in strong n-type doping effects due to its low reduction potentials compared to other accepted electron-donor organic and polymer donors, as illustrated in Figure 4a. This redox reaction may generate an electron transfer complex between DMC molecules and carbon nanotubes and thus can be represented as: DMC\textsuperscript{0} + s-SWCNT\textsuperscript{0} → DMC\textsuperscript{δ−} + s-SWCNT\textsuperscript{δ+}. The corresponding energy band diagram shown in Fig. 4b depicts the expected energy level offset between the conduction band of nanotubes and the reduction potential of DMC which shows the reduction of nanotubes by DMC molecules. It has been reported that the ionization energy (IE) and electron affinity (EA) of DMC is 3.30 eV and 2.06 eV, respectively, while the EA and Fermi level of semiconducting carbon nanotubes is 4.06 eV and close to 4.56 eV, respectively\textsuperscript{20,21}. Due to this energy level offset, s-SWCNT acts as an electron-acceptor material, and the DMC molecule can readily transfer electrons to nanotubes as the above redox reaction, resulting in n-type semiconducting nanotubes. Raman spectroscopy was then used to confirm our above electron donating hypothesis of DMC induced n-doping effects. Figure 4c presents the Raman spectra of pristine no-doped and DMC doped nanotube networks on SiO\textsubscript{2}/Si substrates. It can be seen that almost no obvious D bands can be clearly observed in all samples and the G/D band intensity ratio and wavelength remain almost unchanged, indicating that no structural defects are introduced in nanotube networks after doping since the G/D band intensity ratio reflects the formation of defects\textsuperscript{36}. Moreover, the doped s-SWCNT samples show a downshift of about 4.95 cm\textsuperscript{-1} in the G-peak compared with pristine nanotube sample, consistent with the spectrum of n-type nanotube networks by electron transfer\textsuperscript{37}, suggesting the n-doping effects of DMC molecules on semiconducting nanotubes.

Uniquely, the n-type DMC dopants could be reversibly removed from the nanotube network surface by immersion the doped sample in DMF solvents. After immersion in DMF for a period of time and drying by N\textsubscript{2} flow, the n-type transistor behaviors arising from DMC doping effects weakened over immersion time with the p-type behaviors gradually recovering, and finally returned to the original p-type behaviors, as shown in Fig. 4d. Here, after immersion in DMF for 10 min, the device exhibits some ambipolar transistor behaviors where the p- and n-type transistor behaviors both exist. With the increase of immersion time in DMF, the n-type current level decreases and the p-type current level increases, and eventually after immersion in DMF for 16 hours, the device almost returns back to the original state and the transistor almost behaves the same p-type behaviors with that before any doping processes, where the hole concentration increases and electron concentration decreases with increasing immersion time in DMF and finally almost reached back to the original state. After that, the DMC solution was spin-coated onto the nanotube network surface again and the device can go back to n-type, also as shown in Fig. 4d. Therefore, the DMC n-doping process on nanotube networks is reversible as indicated in the electrical characterizations. As mentioned before, the DMC molecule and s-SWCNT form an electron transfer complex (as shown in Fig. 3a) where DMC directly transfers electrons to s-SWCNTs and thus n-doping effects of DMC on nanotubes are obtained. When the doped sample was immersed in DMF, the DMC dopant molecule desorbed form the nanotube surface and dissolved in DMF, and the doped n-type CNFET devices were finally converted back to the original p-type behaviors after immersion in DMF for enough time to remove all the dopant molecules. It indicated that the n-doping process is a reversible process, as shown in Fig. 4e.

To further confirm it, besides electrical measurements, Raman spectroscopy is also adopted to examine the structural properties of nanotube networks after immersion in DMF. Fig. 4f presents the Raman spectra of nanotube networks on SiO\textsubscript{2}/Si substrates before doping, after DMC doping and after immersion in DMF for 16 hours, from which it can be clearly seen that immersion in DMF is not greatly detrimental to nanotube networks and can convert them back to the original states consisting of electrical and structural properties, confirmed by the above electrical and structural measurements and analyses. It is corresponding to the surface charge transfer
mechanisms. This reversible doping method shown above presents a unique feature of surface charge transfer doping mechanisms compared with conventional substitution doping process. Moreover, the dopants can be selectively removed by using proper solvents and controlling the immersion time in the solvents, which in principle presents one pathway towards patterned doping where only a section of the surface dopants can be wholly or partly selectively removed by using lithographic methods and thus forming planar structures such as p-n, p-n-p, n-p-n, p-n-n⁻, p-n-n⁺, etc. for optoelectronic device or other electronic device applications. In such devices, we can use the reversible doping method to modulate the n-type behaviors of doped carbon nanotubes in different channel regions according to the device function requirements of the optoelectronic devices.

**Ab Initio DFT Simulations of DMC-SWCNT Complexes.** Subsequently, ab initio density functional theory (DFT) simulations were performed to further investigate the n-doping effects of DMC molecules on s-SWCNTs. S-SWCNT with a diameter of 0.9402 nm is chosen as representative as shown in Fig. 5a. The simulation box contains an individual carbon nanotube with a single DMC molecule attached to it through van der
vaals (vdW) interactions. The ring of DMC parallels to the c axis of nanotube after the structure optimization, as shown in Fig. 5b. The band structures were calculated within the DMol3 package after the spin-unrestricted optimizations of both s-SWCNT and s-SWCNT-DMC systems37, 38. The generalized gradient-corrected Perdew-Burke-Ernzerhof (PBE-GGA) functional39, along with large Gaussian basis sets40, 41, is employed for the geometry optimization and property calculations. Here, PBE-GGA rather than BLYP functional is adopted because PBE-GGA is more suitable for long-range weak van der Waals (vdW) interactions over other GGAs while B3LYP and BLYP hybrid functional are usually used for chemical reactions40, 42. It is important to note that the investigation of DMC-CNT system requires careful treatment of the van der Waals (vdW) interactions between them. A dispersion correction scheme proposed by Grimme (DFT-D2) was used to describe the electronic exchange and correlation effects as well as the dispersion interactions43, where the incorporation of DFT-D2 scheme further improves the accuracy in evaluating weak interactions. DFT-D2 is much more accurate than DFT-D scheme and this simple approach works very well and gives reasonable results while the computation of C6 coefficient from first principles on the basis of a large database of such coefficients calculated accurately for any pair of atoms in the DFT-D3 scheme is usually complicated and time-consuming and the DFT-D3 scheme is always used to calculate the corrected energy values in a DFT-D manner44, 45. The 3 × 3 × 1 k points are sampled in the Brillouin Zone (BZ). The density mesh cutoff is 540 eV and the maximum displacement and the maximum energy is 0.005 Å and 1E-6 Ha for the geometry optimizations, respectively. The band structures (BS) of s-SWCNTs before and after DMC doping are presented in Fig. 5c. In pristine s-SWCNTs before doping, the Fermi energy level (donated by the dashed line) is located closer to the top of valence band (EV) compared to the bottom of conduction band (EC), indicating its intrinsic p-type behaviors. After doping, the Fermi energy level shifts upwards and closer to EC, demonstrating its n-type behaviors and the effective N-doing effects of DMC molecules on s-SWCNTs, which is consistent with the experimental results shown in Fig. 2.

In conclusion, we demonstrate an efficient and reversible n-type doping of semiconductor-enriched single-walled carbon nanotube networks with excellent transistor and uniformity performances by spin-coating DMC, an effective charge transfer donor, directly onto the nanotube network film surface. Effective and controllable n-doping levels of DMC on nanotubes and thus n-type CNFET devices have been achieved. This n-doping phenomenon is proposed to originate from the surface electron charge transfer between the dopant DMC molecule and s-SWCNTs to form an electron transfer complex, confirmed by the Raman spectroscopy and ab initio
DFT calculations. The n-doped sample can convert back to the original state by immersing it into DMF and the recovered p-type behaviors can be well tuned by changing the immersion time in DMF, indicating the reversibility properties of DMC electron doping process. The n-doping levels such as on current, threshold voltage, on/off ratio, mobility, contact resistance etc. can be well controlled by adjusting the DMC concentrations. To be the most important, the threshold voltage of doped n-type CNFET devices can be modulated by adjusting the spin-coated DMC solution concentration where it shows an almost linear relationship versus DMC concentration, which is attributed to the increasing doping levels induced upward shift of the Fermi level and thus the reduced Schottky barrier width for the electron injection. This provides us a new route for high-performance obtained n-type carbon nanotube and thus n-type CNFET devices, making it a promising candidate for future carbon nanotube based electronic circuit and system applications.

Method Materials. All the chemical materials were purchased and used without further purification after purchase. Poly-L-lysine (0.1% w/v in H$_2$O) solution and decamethylcobaltocene were purchased from Sigma-Aldrich. N, N-Dimethylformamide (DMF) was purchased from Sinopharm Chemical Reagent Co., Ltd. (China). The 98% semiconductor-enriched suspension was supplied by Nanointegris Inc. Deionized water used in these experiments was obtained by using a Milli-Q water system.

Device Fabrication. Back-gated CNFET devices were fabricated on 4-in. heavily p-doped silicon substrates which also function as the back gate electrode with a thermally grown 90-nm thick SiO$_2$ layer on the polished side as the gate dielectric. Photolithography was firstly adopted to pattern the source/drain (S/D) electrodes, followed by e-beam evaporation of Cr/Au (10/50 nm) and the following lift-off process to form S/D electrodes. Then Poly-L-lysine solution was drop-onto the SiO$_2$ surface for 5 min to functionalize the SiO$_2$ surface for nanotube network film deposition, followed by immersing the substrate in 98% semiconductor-enriched SWCNT suspension (Nanointegris Inc.) for 1 hour and a following rinse with DI water and isopropanol to remove the excess solution. The channel region was defined also by photolithography and then oxygen plasma etching to remove the extra nanotubes. Finally, the wafers were annealed in argon atmosphere for 30 min at 200°C to remove the residual resist scum which may affect the following doping process.

DMC n-doping process. DMC with different concentrations ranging from 0.5 wt.% to 5 wt.% was firstly totally dissolved into DMF without any residual at room temperature. Then, for n-type doping, DMC solution (different concentrations ranging from 0.5 wt.% to 5 wt.% in DMF) was directly spin-coated onto the wafers at room temperature in air ambient. After spin-coating, the sample was then annealed on a hotplate at 100°C for 10 min.

Structural Characterization. In order to examine the surface morphologies of the as-prepared nanotube network films, the morphology of the SWCNT network films on SiO$_2$/Si substrates were observed by atomic force microscopy (AFM, SPA 500, Seiko Instruments Inc.) with tapping mode. All Raman spectra for undoped, DMC n-doped nanotubes and DMF washed nanotubes were excited with the 514.5 nm line of a He-Ne laser and recorded using a Renishaw Invia Raman Microscope.

Electrical Characterization. All electrical characteristics of all fabricated CNFET devices in this paper were measured with an Agilent B1500A semiconductor parameter analyzer in ambient environment at room temperature.

The threshold voltage ($V_{TH}$) was extracted by finding the x-intercept points of the tangential lines in the linear region of $I_{DS}$ versus $V_{GS}$ curves (Figure S3). The field-effect mobility ($\mu_{FE}$) was calculated from the following equation:

$$\mu_{FE} = \frac{L}{W V_{DS} C_{ox}} \frac{\partial I_{DS}}{\partial V_{GS}}$$

where $L$ and $W$ are the length and width of the channel, $V_{DS}$ is the drain-source voltage and $C_{ox}$ is the gate oxide capacitance per unit area.

In the linear regime, the overall device resistance ($R_{on}$) can be expressed as the sum of the channel resistance ($r_{ch}L$) and contact resistance ($2R_{SD}$) according to

$$R_{on} = r_{ch}L + 2R_{SD} = \frac{L}{W \mu_{FE} C_{ox} (V_{GS} - V_{TH})} + 2R_{SD}$$

where $\mu_{FE}$ and $V_{TH}$ are the field-effect mobility and threshold voltage, respectively. Thus the contact resistance $2R_{SD}$ can be extracted from the above section.

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Author Contributions
J.-L.X. made and tested the samples, analyzed and simulated the results and wrote the manuscript. T.L.R. and D. X. oversaw all research phases, optimized the devices performance and revised the manuscript. R.X.D., Y.L.S. and X.L. participated in the sample preparation and measurement processes. S.D.W. analyzed the results and made some suggestions on the paper revisions. Y.X., Y.X.Y. and L.X. participated in and guided the simulation. All authors commented on the final manuscript.

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