Dynamic Frame Update Policy for UHF RFID Sensor Tag Collisions

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Abstract: The current growing demand for low-cost edge devices to bridge the physical–digital divide has triggered the growing scope of Radio Frequency Identification (RFID) technology research. Besides object identification, researchers have also examined the possibility of using RFID tags for low-power wireless sensing, localisation and activity inference. This paper focuses on passive UHF RFID sensing. An RFID system consists of a reader and various numbers of tags, which can incorporate different kinds of sensors. These sensor tags require fast anti-collision protocols to minimise the number of collisions with the other tags sharing the reader’s interrogation zone. Therefore, RFID application developers must be mindful of anti-collision protocols. Dynamic Frame Slotted Aloha (DFSA) anti-collision protocols have been used extensively in the literature because EPCglobal Class 1 Generation 2 (EPC C1G2), which is the current communication protocol standard in RFID, employs this strategy. Protocols under this category are distinguished by their policy for updating the transmission frame size. This paper analyses the frame size update policy of DFSA strategies to survey and classify the main state-of-the-art of DFSA protocols according to their policy. Consequently, this paper proposes a novel policy to lower the time to read one sensor data packet compared to existing strategies. Next, the novel anti-collision protocol Fuzzy Frame Slotted Aloha (FFSA) is presented, which applies this novel DFSA policy. The results of our simulation confirm that FFSA significantly decreases the sensor tag read time for a wide range of tag populations when compared to earlier DFSA protocols thanks to the proposed frame size update policy.

Keywords: Radio Frequency Identification (RFID); RFID sensor tag; EPC-global standard; anti-collision; tag estimation; frame update policy

1. Introduction

Traditionally, Radio Frequency Identification (RFID) technology applications focused on item identification, location, and authentication. In the past years, the growing interest in wireless sensors has also reached RFID, and it has been transformed to a technology for both identification and sensing applications. As a result, RFID has become a crucial element of the Internet of Things (IoT) platform. Industry alliances, such as the NFC forum (for HF RFID) and the RAIN RFID alliance (for UHF RFID), have been formed to motivate and promote these efforts. The use of RFID technology to sense our physical world has expanded tremendously in the last decade. This has enabled the sensing ability of RFID technology to gather information from real-world objects and seamlessly integrate this data within the IoT.

RFID applications using wireless sensors require a fast communication protocol to read the sensor’s data, especially with increasing tag populations. The main purpose of the protocol is for the
reader to first obtain the tag’s unique identification code, which is referred to as EPC, and then to receive the data from the sensors. The coexistence of several sensor tags sharing the reader’s interrogation zone provides RFID technology with great flexibility, but it does so at the expense of suffering from the tag collision problem. A collision occurs when two or more tags respond simultaneously to a reader command, which results in a waste of energy and an increase in the tag identification time and sensor data read time. Therefore, RFID systems require an anti-collision protocol to deal with tag collisions and to minimise their negative impact.

EPCglobal Class 1 Generation 2 (EPC C1G2) (ISO/IEC 18000-63) [1] is the standard currently adopted by RAIN, a fundamental component of the RFID market. This standard uses a Dynamic Frame Slotted Aloha (DFSA) protocol to handle the message collisions among RFID tags. A DFSA protocol is characterized by the strategy that it employs to update the transmission frame size alongside the identification and sensor data reading process. Most RFID manufacturers currently follow the EPC C1G2 standard. Consequently, this work focuses on DFSA protocols. In particular, we propose a novel frame update policy and the Fuzzy Frame Slotted Aloha (FFSA) protocol, which lower the time required for the reader to receive sensor data packets. FFSA meets EPC C1G2 requirements. Consequently, the proposed protocol can easily be deployed with current RFID technology infrastructure using commercial readers.

A great deal of effort has been paid to the problem of RFID sensor data collection. However, many existing solutions make the assumption that all tag EPCs in the system are known to the reader in advance [2–4]. However, this assumption is not true for most scenarios, where unexpected tags frequently appear [5]. Additionally, these solutions are not fully compliant with the EPC C1G2 standard, thus they cannot be implemented with existing commercial readers and sensor tags. For a protocol to be compliant with the EPC C1G2, it should employ the standard inventory and access commands to read the sensor data. Otherwise, commercial sensor tags [6] will not be able to interpret the commands and send a response back to the reader.

Several protocols have been presented in the literature that employ a DFSA strategy to address the collisions among tags’ messages and which are compliant with the EPC C1G, including Slot Counter [1], FuzzyQ [7], Chen14 [8], Eom [9], ILCM-SbS [10], ILCM-FbF [11], Chen16 [12], and SUBEB-Q [13]. Each protocol employs a different frame update strategy to receive the tag EPC and the sensor data, and it will be presented and compared in the next section. However, none of them has been designed with the focus of minimising the sensor data read time. This metric is critical given the increasing number of passive UHF RFID sensors sharing a common reader interrogation area.

**Novel Contributions over Prior Work**

The major novelty of this paper is that we decrease the average time to receive the EPC and read a sensor data packet from one tag when compared with existing recent DFSA strategies that are compliant with the current standard. Consequently, the FFSA protocol is presented, which meets EPC C1G2 requirements. The main contributions made in this work are as follows:

1. **An analysis and classification of the state-of-the-art DFSA tag anti-collision protocols according to their frame update policy.**
2. **A novel fast frame update policy for DFSA protocols.** This policy first applies fuzzy logic to select the value of the slot where the frame size is updated. It then calculates the frame size as a function of the estimated number of tags inside the reader interrogation zone and the duration of the different time slots of the RFID platform.
3. **We introduce the anti-collision Fuzzy Frame Slotted Aloha (FFSA) protocol, which applies the previous policy to lower the average time to read a sensor data packet from one tag compared with existing recent strategies.**

The rest of this paper is organised as follows. Section 2 analyses the frame update policy of DFSA protocols. Next, Section 3 presents the related work and classifies the main state-of-the-art DFSA
protocols according to their frame update policy. A novel frame update policy and the FFSA protocol are presented in Section 4. Section 5 provides the results of the performance evaluation followed by some of the limitations that we have identified. Finally, Section 6 concludes this paper and presents some recommendations for future work.

2. Analysis of Frame Update Policy of DFSA Protocols

In order to improve different metrics regarding the process of tag identification, several DFSA anti-collisions protocols have been studied in the literature. Each strategy employs a different approach to update the frame size, with the focus of improving different performance metrics. Establishing a clear classification of all DFSA protocols is not straightforward. The key feature that differentiates DFSA protocols is the strategy that they follow to update the frame size. This section establishes a novel approach to classify the main frame update policies employed by DFSA anti-collision protocols. This classification considers three different perspectives to update $L$, which respond to the following three questions: how is $L$ calculated? When is $L$ examined? And, when must a new frame be started? The classification of the main up-to-date policies is summarised in Table 1. The literals in this table will be defined in the next section.

| Operation          | $L$ calculation | $L$ examination | Frame break condition |
|--------------------|-----------------|-----------------|-----------------------|
| $f(Q)$             | $f(\hat{n})$    | $\text{LUT}(\hat{n})$ | $\text{FbF}$          |
| $\text{SbS}$       | $\text{PbP}$    | $\text{Different }L$ | $c_{\hat{n}>c_1}$      |
| $\text{LUT}(\hat{n})$ |                 |                 | $\text{Lower }t_{I_T}$ |
|                    |                 |                 | $\text{Lower }t_R$     |
|                    |                 |                 | $\text{EoF}$           |

2.1. Frame Size Calculation

The reader adjusts $L$ in each reading cycle according to the responses from the competing tags in each frame. Two main strategies can be found in the literature to set a value for the frame size in DFSA protocols: the first calculates $L$ as a function of the parameter $Q$, and the second sets $L$ as a function of the estimated number of tags $\hat{n}$. The parameter $Q$ is an integer value used in the EPC C1G2 to set $L$ as $L = 2^Q$.

1. Parameter $Q$, $f(Q)$: the frame size can be adjusted by controlling the number and types of the slots in each frame with the parameter $Q$, so that $Q$ increases when collisions are detected and decreases with increasing number of idle slots. Several approaches in the literature update $L$ by adjusting $Q$ [1,7,14–17].

2. Tag set size estimation: several works in the literature have addressed the tag estimation task to provide an optimal frame size according to the estimated number of tags. It is known that a DFSA protocol reaches its maximum slot efficiency, which is defined as the ratio between the number of tags and the total number of slots required to identify them, when the frame size is equal to the number of tags. Therefore, to maximise this metric, the reader should set the frame size equal to the estimated number of tags. However, this condition of setting $L = \hat{n}$ is only satisfied if the
reader assumes that the three types of slots have equal duration. However, the standard EPC C1G2 determines that each time slot has a different duration. Consequently, some approaches set the frame size according to $\hat{n}$ but assume unequal processing duration for each type of slot (single, collision, idle) [12,18].

Once the tag set size has been estimated, the next step is to calculate $L$ according to $\hat{n}$. Two main strategies to set $L$ as a function of $\hat{n}$ can be found in the literature, which is presented next.

- Continuous function of $\hat{n}$, $f(\hat{n})$: the first strategy is to set $L$ as a continuous function of $\hat{n}$. The reader analyses the information extracted from the tags’ responses and then sets $L$ as a function of these values. Several anti-collision protocols follow this strategy [9–12,19–24].

- Look-up table (LUT) according to $\hat{n}$, LUT($\hat{n}$): the second strategy is to set $L$ according to an LUT based on $\hat{n}$. The idea is to define different ranges of $\hat{n}$ and assign a different value of $L$ for each $\hat{n}$ range. Several approaches in the literature follow this strategy, including [8,13,25–28].

### 2.2. Frame Size Examination

This section answers the question related to when (and in which slot) $L$ must be examined, considering that an examination refers to a new calculation of $L$. DFSA algorithms update $L$ dynamically. Therefore, a strategy is defined to establish in which slot or slots within a frame the value of $L$ must be examined. Three main strategies can be found in the literature, as follows:

1. **Frame by Frame (FbF):** the simplest strategy is to only calculate $L$ at the last slot of each frame [9,11,20–24,26,27].

2. **Pointer by Pointer (PbP):** some protocols have defined some particular slots within the frame, referred to as the pointer $p$ in the present paper ($p < L$), where $L$ is examined to check its appropriateness [7,8,12,13,28]. These pointers are usually set as a fraction of the current frame size.

3. **Slot by Slot (SbS):** the third strategy is based on examining $L$ at every slot of the frame [1,10,14–17,19,25].

### 2.3. Frame Break Condition

This section presents the different policies followed by the reader to decide, after calculating $L$, whether a new frame must be started, or if the reader must proceed with the next slot. Six main strategies can be found in the literature.

1. **Different $L$:** several protocols start a new frame when the new value of $L$ differs from the current one [1,7,12,14,15,17,19,25,28].

2. **$L$ fits $n$ from an LUT, LUT($\hat{n}$):** some algorithms define an LUT based on $\hat{n}$ and $L$ [8,13] to check the appropriateness of $L$. First, the reader searches in the LUT for the corresponding value of $L$ for the previously obtained $\hat{n}$. Then, if this new value differs from the current one, a new frame is started. Otherwise, the reader proceeds to the next slot of the current frame.

3. **Higher expected number of successful slots, $c_s(n,L)$:** the authors in [10] define a policy to break the current frame and start a new one if the expected number of successful slots in the rest of the current frame $c_{s1}(n,L)$ is less than that expected in the new frame $c_{s2}(n,L)$. In other words, a new frame is started if $c_{s2}(n,L) > c_{s1}(n,L)$.

4. **Lower Identification Time, (lower $t_{IT}$):** the authors in [16] present a frame cancellation strategy to minimise the total expected time to identify a tag set.

5. **Lower sensor read time, (lower $t_R$):** this work presents a strategy where a new frame is started if the expected average time for reading one sensor packet $t_{kr}(n,L)$ in the new frame is lower than the one in the current frame.

6. **End of Frame, (EoF):** a new frame is started when the current frame has finished. This strategy is intrinsic to a DFSA-based anti-collision protocol and it is applied in all the protocols analysed in the present paper.
3. Related Work: Classification of Dfsa Protocols

In this section, we will present and classify some of the most relevant related work in DFSA protocols, including Slot Counter [1], FuzzyQ [7], Chen14 [8], Eom [9], ILCM-FbF [11], ILCM-SbS [10], Chen16 [12], and SUBEB-Q [13]. The performance evaluation of these protocols will be analysed and evaluated with detail in the next section, and will also be compared with the proposed solution FFSA.

The analysis performed in this work is based on the standard RFID wireless communication model, and it is shown in Figure 1. This figure shows the different reader and tags messages along with their corresponding duration meeting the EPC C1G2 requirements. A sequence of $L$ slots is referred to as a frame, where $L$ represents its size. The reader distinguishes between three different type of slots: idle (no tag respond), collision (two or more tags transmit a message simultaneously), and single read (the reader correctly receives the tag EPC during $T_s$ and one sensor data packet with during $T_p$). The duration of each slot is referred to as $T_1$, $T_2$, and $T_3$ respectively.

![Figure 1. Transmission model of EPC Class 1 Generation 2 (C1G2).](image)

Next, Table 2 presents a novel classification of the previous protocols, including the proposed FFSA. The classification is made according to the frame update policy followed by each protocol to identify a group of tags of size $n$.

| Type         | $L$ Calculation | $L$ Exam | Frame Break Condition |
|--------------|-----------------|----------|-----------------------|
| Slot Counter [1] | $f(Q)$         | $Q = Q_f + C$ | $L = 2^{round(Q_f)}$ | $SbS$ – different $L$ at slot or EoF |
| FuzzyQ [7]   | $f(Q)$         | $Q = Q \pm \Delta Q$ | $L = 2^{round(Q)}$ | PbP $L/9$ different $L$ at $p$ or EoF |
| Chen14 [8]   | LUT($\hat{n}$) | $\hat{n} = (c_s + 2.39c_k)p$ | LUT | PbP $L/4$ LUT($\hat{n}$) at $p$ or EoF |
| Eom [9]      | $f(\hat{n})$   | $\hat{n} = \gamma c_k + c_s$ | $L = 2^{round(\log_2(\hat{n}))}$ | PbF – $EoF$ |
| ILCM-FbF [11]| $f(\hat{n})$   | $\hat{n} = k_c + l$ | $L = 2^{round(\log_2(\hat{n}))}$ | PbF – $EoF$ |
| ILCM-SbS [10]| $f(\hat{n})$   | $\hat{n} = k_c + l$ | $L = 2^{round(\log_2(\hat{n}))}$ | $SbS$ – Higher $c_s(n, L)$ at slot or EoF |
| Chen16 [12]  | $f(\hat{n})$   | $\hat{n} = (c_s + 2.39c_k)p$ | $L = 2^{round(\log_2(\hat{n} / \rho))}$ | PbP $L/5$ different $L$ at $p$ or EoF |
| SUBEB-Q [13] | $f(\hat{n})$   | $\hat{n} = (c_s + 2.39c_k)p$ | LUT | PbP LUT LUT($\hat{n}$) at $p$ or EoF |
| FFSA         | $f(\hat{n})$   | MMSE estimator [26] | $L = 2^{round(\log_2(\hat{n} / \rho))}$ | PbP FRBS Lower $I_R(n, L)$ at $p$ or EoF |

4. The Proposed Frame Update Policy

This section introduces the novel fuzzy frame update policy. The arbitration of RFID communication is a stochastic process of unknown behaviour. Therefore, fuzzy logic is an efficient tool to model the process of identifying RFID tags. Fuzzy control for RFID anti-collision protocols was first introduced in [7], where a fuzzy system was used to give an intuitive value of the frame size. This work presents a Fuzzy Rule Based System (FRBS), which obtains the value of the pointer
slot $p$ to only accurately examine the frame size when appropriate. This solution is combined with a time-minimising function to update the value of $L$ at slot $p$. The resulting proposed policy lowers the average time required to read one sensor packet from one tag compared to existing strategies. The three parts of the proposed policy (frame size calculation, frame size examination, and frame break condition) are presented next.

4.1. Frame Size Calculation to Minimise $T_R(N, L)$

The first part of the policy sets the value of $L$ to minimise the expected time to receive one sensor data packet from one tag in a frame. For this purpose, the sensor data read time $t_R(n, L)$ is defined as the expected time to identify one tag among $n$ in a frame of size $L$ and read one sensor data packet:

$$t_R(n, L) = \frac{(T_s + T_p)c_s(n, L) + T_kc_k(n, L) + T_ic_i(n, L)}{c_s(n, L)}$$

where $c_s(n, L)$, $c_k(n, L)$, and $c_i(n, L)$ are defined as the expected value of the number of single, collision, and idle slots in a frame, respectively. The duration of the slots, $T_s$, $T_p$, $T_k$, and $T_i$, are set according to the standard

$$T_s = T_{\text{command}} + 2T_1 + 2T_2 + T_{RN16} + T_{ACK} + T_{EPC}, \quad (2)$$

$$T_p = T_{\text{reqRN}} + 2T_1 + 2T_2 + T_{\text{handle}} + T_{\text{Read}} + T_{\text{data}}, \quad (3)$$

$$T_k = T_{\text{command}} + T_1 + T_{RN16} + T_2, \quad (4)$$

and

$$T_i = T_{\text{command}} + T_1 + T_3, \quad (5)$$

where $T_{\text{command}}$ is the duration of the reader command $Q_c$, $QA$, or $QR$, referred as $T_{Qc}$, $T_{QA}$, and $T_{QR}$, respectively.

The parameters $T_{EPC}$ and $T_{RN16}$ correspond to the duration of the $EPC$ and RN16 tag messages, respectively. These two parameters are calculated as a function of the Tag-to-Reader synchronisation time $T_{\text{Preamble}_{TR}}$, the length of each parameter, and the tag data rate $DR_t$, calculated as

$$DR_t = 1/BLF. \quad (6)$$

The parameter $BLF$ refers to the Backscatter-link frequency. Thus,

$$T_{RN16} = T_{\text{Preamble}_{TR}} + 17\text{bits} / DR_t, \quad (7)$$

$$T_{EPC} = T_{\text{Preamble}_{TR}} + 129\text{bits} / DR_t, \quad (8)$$

$$T_{\text{handle}} = T_{\text{Preamble}_{TR}} + 38\text{bits} / DR_t \quad (9)$$

and

$$T_{\text{data}} = T_{\text{Preamble}_{TR}} + 118\text{bits} / DR_t. \quad (10)$$

The length of the sensor data packet $T_{\text{data}}$ is calculated by taking a commercial UHF RFID accelerometer sensor tag as a reference [6]. According to the sensor data sheet, each accelerometer data packet contains 10 bytes of data.

The reader transmits one $QA$ or $Qc$ command in the first slot of each frame. Then, it transmits consecutive $QR$ commands in the following slots of the frame until it reaches the last slot of the frame. Assuming a frame with sufficiently large $L$, $T_{\text{command}}=T_{QR}$ is applied in Equations (2), (4), and (5) when one frame is analysed.

The duration of the reader commands $Qc$, $QA$, $QR$, $\text{ReqRN}$, and $\text{ACK}$ are calculated as

$$T_{Qc} = T_{\text{FSync}_{RT}} + 22\text{bits} / DR_t, \quad (11)$$
\[ T_{QA} = T_{Preamble_{RT}} + 9 \text{bits} / DR_r, \]
\[ T_{QR} = T_{Preamble_{RT}} + 4 \text{bits} / DR_r, \]
\[ T_{reqRN} = T_{Preamble_{RT}} + 40 \text{bits} / DR_r, \]
and
\[ T_{ACK} = T_{Preamble_{RT}} + 18 \text{bits} / DR_r. \]

The duration of the Read command \( T_{Read} \) is calculated using a commercial UHF RFID accelerometer sensor tag as a reference [6]. Thus,
\[ T_{Read} = T_{Preamble_{RT}} + 62 \text{bits} / DR_r. \]

The parameters \( T_{FSyncRT} \) or \( T_{Preamble_{RT}} \) correspond to the Reader-to-Tag synchronisation time as defined in [1], and the reader data rate \( DR_r \) is obtained as
\[ DR_r = 1 / \left( \frac{T_{symbol_0} + T_{symbol_1}}{2} \right), \]
where \( T_{symbol_0} = T_{ari} \), and \( T_{symbol_1} = 1.5T_{ari} \). \( T_{ari} \) represents the reference time interval for a symbol-0 (FM0 symbol) transmission.

Next, the value of \( L \) minimising \( t_R(n, L) \) is obtained by evaluating an RFID system with \( n \) tags and one reader. In this system, we can apply a binomial distribution \( P_r(n, L) \) [9] to approximate the probability that \( r \) tags among \( n \) select one slot along a frame of size \( L \)
\[ P_r(n, L) = \binom{n}{r} \left( \frac{1}{L} \right)^r \left( 1 - \frac{1}{L} \right)^{n-r}. \]

Additionally, \( p_s(n, L) \), \( p_k(n, L) \), and \( p_i(n, L) \) correspond to the probabilities that only one tag, more than one tag or no tag, respectively, occupy a slot [7]. In order to obtain the expected number of idle, single, and collision slots in a frame with a size \( L \) sufficiently large, we can apply a Poisson distribution with mean \( \rho = n/L \) [9].

\[ c_i(n, L) \] is approximated with \( r = 0 \) in Equation (18) by
\[ c_i(n, L) = Lp_i(n, L) = L \left( 1 - \frac{1}{L} \right)^n \approx Le^{-\rho}. \]

\[ c_s(n, L) \] is approximated with \( r = 1 \) in Equation (18) by
\[ c_s(n, L) = L \frac{n}{L} \left( 1 - \frac{1}{L} \right)^{n-1} \approx L \rho \frac{n/\rho}{n/\rho - 1} e^{-\rho}. \]

Then, \( c_k(n, L) \) is approximated by
\[ c_k(n, L) = Lp_k(n, L) = L(1 - p_0 - p_1). \]

By substituting Equations (19), (20), and (21) into (1), and applying \( \frac{n/\rho}{n/\rho - 1} \approx 1 \), the following expression is obtained
\[ t_R(\rho) \approx \frac{(T_k + T_p)\rho e^{-\rho} + T_i e^{-\rho} + T_k(1 - (1 + \rho)e^{-\rho})}{\rho e^{-\rho}}. \]

Computing the derivative of \( t_R(\rho) \) in Equation (1) with respect to \( \rho \) yields
\[ \frac{dt_R(\rho)}{d\rho} = \frac{T_k(\rho(\rho - 1) + 1) - T_i}{\rho^2}. \]
Then, by posing \( \frac{dt_R(\rho)}{d\rho} = 0 \), we obtain the following equation

\[
e^\rho(\rho - 1) + 1 = \frac{T_i}{T_k}.
\]

(24)

By solving Equation (24), the value of \( \rho \) that minimises \( t_R(\rho) \) is obtained:

\[
\rho = 1 + W\left( \left( \frac{T_i}{T_k} - 1 \right)e^{-1} \right)
\]

(25)

where \( W(x) \) is the Lambert W-function. Finally, the optimal frame size which minimises \( t_R(n, L) \) is

\[
L = \frac{n}{\rho}
\]

(26)

where \( \rho \) is obtained from Equation (25).

The value of \( \rho \) in Equation (25) is evaluated and presented in Figure 2 as a function of \( T_i/T_k \). It can be appreciated that \( \rho \) decreases when the difference between \( T_i \) and \( T_k \) grows, which results in an increasing \( L \). In conclusion, a higher difference in the values of \( T_i \) and \( T_k \) (with \( T_i \leq T_k \)) will result in a higher \( L \). This result is coherent regarding the process of RFID tags identification and sensor data reading. If the duration of collision slots is much higher than that of idle slots, then it is necessary to increase \( L \) to reduce the number of collision slots. This occurs at the expense of an increase in the number of idle slots. However, because idle slots are much shorter than collision slots, this is an acceptable effect.

The previous analysis and Equation (26) demonstrate that the frame size calculation of the proposed policy is timing-aware. This means that the calculation is made as a function of the number of tags \( n \) and the timing parameters (ultimately the duration of the reader commands and tags responses) of the RFID scheme.

![Figure 2. Evaluation of \( \rho \) solution in Equation (25) for \( T_i/T_k \).](image)

4.2. Frame Size Examination: PbP

The second part of a frame update policy refers to the slot where \( L \) is examined. The FbF strategy is not efficient in the case of large frames filled with many collisions because the reader must wait until the frame has finished to update the frame size, which increases the identification time [11]. The SbS strategy involves the calculation of \( L \) at every single slot of the frame. As a consequence, one drawback of this solution is that it could overload a system with limited resources. Finally, the PbP strategy provides the flexibility of breaking the current frame before it ends, which maintains a low computational complexity in the reader. Therefore, the proposed policy applies a PbP strategy where the value of the pointer slot is dynamically updated using fuzzy logic.

The proposed policy applies a fuzzy rule-based system (FRBS) to adjust the value of the pointer efficiently. Consequently, the current \( L \) and the tag collision rate \( col_{...rate} \) are modelled as fuzzy sets to adaptively update the value of the pointer. A zeroth-order Takagi–Sugeno–Kang fuzzy system with
a complete AND-composed rule [29] is proposed. The membership functions that we have used to codify the input variables are trapezoidal (see Figure 3) and the t-norm minimum is used to implement the AND operator. Among the traditional shapes of membership functions (triangular, trapezoidal, Gaussian, generalized bell, and sigmoid), trapezoidal membership functions have been selected due to their representation simplicity, which allows faster calculations. The proposed system has two inputs, as follows:

- \( Q \): codifies the current value of this parameter which determines \( L \), where \( Q \in \mathbb{N} \) and \( 0 \leq Q \leq 20 \).
- \( \text{col\_rate} \): codifies the tag collision rate up to the current slot. This is defined by \( \text{col\_rate} = c_k / \text{slot\_index} \), and \( 0 \leq \text{col\_rate} \leq 1 \).

Additionally, the variable \( \text{slot\_index} \) represents the reader’s internal counter, which keeps track of the present slot in the current frame.

The output \( p \) represents the slot where \( L \) must be examined. Specific values for membership functions and consequents in the rule base have been adjusted experimentally. The rules were designed also experimentally, considering the typical behaviour of an RFID system: on the one hand, more collisions (higher \( \text{col\_rate} \)) require us to promptly examine \( L \) (smaller output \( p \)); while on the other hand, a smaller frame size (smaller \( Q \)) requires the examination of \( L \) in a later time slot (higher output \( p \)). The experimental values for the membership functions and the rules have been obtained by evaluating different ranges and selecting the one with the best performance in \( TR \).

Figure 4 shows the surface representation of the proposed FRBS that determines the output \( p \), normalised to \( L = 16 \). To illustrate an example, for the inputs \( Q = 10 \) and \( \text{col\_rate} = 0.3 \), the output is \( p = L / 9 \). Then, the new value of the pointer slot is \( p = \text{round}(2Q / 9) = \text{round}(20 / 9) = 114 \).

4.3. Frame Break Condition: Lower \( TR(N, L) \)

Finally, the last part of the policy determines the condition to break the current frame and start a new one. The expected average time to read one sensor data packet [6] among \( n \) sensor tags in the current frame of size \( L_c \) is obtained as
\[ t_{R_c} = t_R(n, L)|_{n=\hat{n}, L=L_c}. \]  

(27)

and the expected average time to read one sensor data packet among \( n \) sensor tags in the newly calculated frame of size \( L_n \) is

\[ t_{R_n} = t_R(n, L)|_{n=\hat{n}, L=L_n}. \]  

(28)

To lower the tag sensor data read time, a new frame will be started if the condition \( t_{R_n} < t_{R_c} \) is satisfied. Thus, at slot \( p \), the reader obtains \( t_{R_n} \) and \( t_{R_c} \) with Equations (27) and (28), assuming \( T_{\text{command}} = T_{QR} \), and then compares these values. Following this strategy, the reader guarantees that if a new frame is started at slot \( p \), then the expected average time required to read one sensor data packet will be reduced.

4.4. The Proposed Fuzzy Frame Slotted Aloha Protocol

The novel FFSA protocol is introduced in this work, which applies the previously presented DFSA policy: determines the frame size minimizing \( t_R(n, L) \) (Section 4.1), examines the frame size following a PbP strategy (Section 4.2), and starts a new frame with the condition to lower \( t_R(n, L) \) (Section 4.3). FFSA is compliant with the EPC C1G2 standard, meaning that it meets the specific communication timing requirements and uses power-of-two values for \( L \). As a consequence, this policy can be used to identify commercial sensor tags.

In order to calculate the frame size in Equation (26), FFSA applies the traditional Mean Minimum Square Error (MMSE) estimator [26] to calculate \( \hat{n} \) as

\[ \hat{n} = \min_n \left\{ \frac{p_i(n, L)L}{p_s(n, L)L} - \begin{pmatrix} c_i \\ c_s \\ c_k \end{pmatrix} \right\}. \]  

(29)

MMSE has been applied in FFSA due to its computational simplicity while providing a relatively low estimation time.

The pseudocode of FFSA is presented in Algorithm 1. Initially, the reader sets the value of \( \rho \) with Equation (25) according to the RFID system timing parameters, and starts the identification procedure by broadcasting \( Q_c \). Each tag selects a slot in the frame to transmit its \( RN_{16} \), and the reader updates the variables \( c_s, c_k, \) and \( c_l \) accordingly. When the reader reaches the last slot of the frame, the remaining tag population size is estimated with Equation (29). Then a new frame is started by broadcasting \( QA \), specifying the new frame size as \( Q_n = \log_2((\hat{n} - c_l)/\rho), L_n = 2^{\text{round}(Q_n)} \). At every slot, \( \text{col\_rate} \) is calculated and \( p \) is set as the current slot if \( \text{col\_rate}=1 \). If the current slot is a pointer, the reader calculates \( \hat{n} \) with Equation (29) and sets \( L_n \) with. Then, it obtains \( t_{R_c} \) and \( t_{R_n} \) with Equations (27) and (28). If the condition \( t_{R_n} < t_{R_c} \) is satisfied, a new frame is started and \( p \) is updated with the FRBS. Otherwise, the reader broadcasts \( QR \) to proceed to the next slot. The sensor tags reading process ends when there are no collision slots in the current frame and the frame is terminated.
Algorithm 1: Pseudocode of Fuzzy Frame Slotted Aloha (FFSA) protocol, reader operation.

1: Initialization: \( L_c = 16 \), \( \text{slot\_index} = 1 \), calculate \( \rho \) with Equation (25)
2: Broadcast \( Q_c \)
3: while 1 do
4: read slot and update \( c_i, c_s, c_k \)
5: if \( \text{slot\_index} = L_c \) and \( c_k = 0 \) then
6: break
7: end if
8: if \( \text{slot\_index} = L_c \) then
9: \( \hat{n} = \text{MMSE}(c_s, c_k, c_i) \)
10: \( Q_n = \log_2((\hat{n} - c_s)/\rho) \), \( L_n = 2^{\text{round}(Q_n)} \), \( L_c = L_n \)
11: broadcast QA
12: else
13: \( \text{col\_rate} = c_k / \text{slot\_index} \)
14: if \( \text{col\_rate} = 1 \) then
15: \( p = \text{slot\_index} \)
16: end if
17: if \( \text{slot\_index} = p \) then
18: \( \hat{n} = \text{MMSE}(c_s, c_k, c_i) \)
19: \( Q_n = \log_2((\hat{n} - c_s)/\rho) \), \( L_n = 2^{\text{round}(Q_n)} \)
20: if \( t_{SR} < t_{SR}^* \) then
21: \( p = \text{FRBS}(\text{col\_rate}, Q_n) \), \( L_c = L_n \)
22: broadcast QA
23: else
24: \( \text{slot\_index} = \text{slot\_index} + 1 \)
25: broadcast QR
26: end if
27: else
28: \( \text{slot\_index} = \text{slot\_index} + 1 \)
29: broadcast QR
30: end if
31: end if
32: end while

5. Performance Evaluation

This section evaluates the performance of FFSA in terms of the average time to read one sensor data packet from one tag \( t_R \). This metric is calculated as the total sensor read time divided by the total number of tags \( n \) in one inventory round:

\[
t_R = \frac{(T_s + T_p)c_s + T_k c_k + T_i c_i}{n}.
\]  \hspace{1cm} (30)

In one inventory round, the variables \( c_s, c_k, \) and \( c_i \) are the total number of single, collision, and idle slot, respectively. The value of \( T_{\text{command}} \) in Equations (2), (4), and (5) will vary depending on the slot position within a frame:

- First slot of the inventory round: \( T_{\text{command}} = T_{Qc} \).
- First slot of the frame: \( T_{\text{command}} = T_{QA} \).
- None of the above: \( T_{\text{command}} = T_{QR} \).

Table 3 summarizes the most relevant variables covered in this work. For each scenario, \( t_R \) is evaluated as a function of the control variable indicated with *, \( n \) in S1 and BLF in S2. BLF is varied from 40 kbps (the minimum value allowed by the standard) to 640 kbps (maximum). S2 represents a special case because BLF also influences \( T_{ari} \), which represents the reference time interval for a data-0 transmission, and affects \( T_{Rcal}, T_{Rcal}, T_1, \) and \( T_2 \). These parameters are also modified every time that BLF changes during the simulation. In both scenarios, the initial \( L \) is set to 16. In FFSA, the initial value for \( p \) is set to eight and this value has been obtained experimentally. Table 4 shows the parameter values that we have employed.
Table 3. Main parameters used in the frame update analysis of this work.

| Parameter | Description |
|-----------|-------------|
| \(n\)     | Total number of tags |
| \(L\)     | Transmission frame size |
| \(\hat{n}\) | Estimated number of tags |
| \(T_1, T_2, T_3\) | Link-timing parameters |
| \(T_i, T_{sp}, T_k\) | Duration of idle, single read, and collision slots |
| \(T_{Qc}, T_{QA}, T_{QR}, T_{ACK}, T_{req}, T_{Read}\) | Reader commands duration |
| \(T_{RN16}, T_{EPC}, T_{handler}, T_{data}\) | Tags messages duration |
| \(c_i, c_s, c_k\) | Number of idle, single, and collision slots in one frame |
| \(c_i, c_s, c_k\) | Probability that \(r\) among \(n\) tags occupies a slot in a frame of size \(L\) |
| \(p_i(n, L), p_s(n, L), p_k(n, L)\) | Probability of idle, single, and collision slot in a frame of size \(L\) |
| \(c_i(n, L), c_s(n, L), c_k(n, L)\) | Expected value of the number of idle, single, and collision slots in one frame |
| \(t_R\) | Time to read one sensor data packet from one tag |
| \(t_R(n, L)\) | Expected time to read one sensor data packet from one tag among \(n\) in a frame of size \(L\) |

Next, the protocols presented in Section 3 are evaluated and compared with FFSA for different performance metrics. A scenario with one reader and a varying number of tags has been evaluated with Matlab R2019, where the tags are uniformly distributed. The simulation responses have been averaged over 1000 iterations to ensure accuracy in the results. The performance evaluation followed in this work focuses on the media access control layer, ignoring the physical layer effects (assuming no capture effect and a non-impaired channel). This approach is widely accepted and incorporated by several studies in the related literature [8,10–12]. Our evaluation is performed for one inventory round, which is defined as the period of time that begins when the reader transmits the initial command \(Q_c\) and which ends when the reader interrupts the reading process and the tags lose their state.

Table 4. Parameters used in the simulations. * indicates the control variable.

| Scenario | S1 | S2 |
|----------|----|----|
| \(n\)   | [64–8192] tags * | [64–8192] tags |
| BLF      | 40 kbps | [40–640] kbps * |
| \(Tari\) | 6.25 \(\mu\)s | 6.25 \(\mu\)s |
| \(RTcal\) | 15.63 \(\mu\)s | [15.63–62.50] \(\mu\)s |
| \(TReal\) | 17.34 \(\mu\)s | [17.34–69.38] \(\mu\)s |
| \(T_1\) | 24.50 \(\mu\)s | [16.06–24.50] \(\mu\)s |
| \(T_2\) | 375.50 \(\mu\)s | [23.44–375.50] \(\mu\)s |
| \(T_3\) | 15.63 \(\mu\)s | 15.63 \(\mu\)s |

5.1. Impact of the Number of Tags in S1

This section compares the selected protocols in terms of \(t_R\) of Equation (30) by varying the number of tags \(n\) from 64 to 8192 (see Table 4). Additionally, \(c_i\) and \(c_s\) per tag are measured because \(t_R\) is mostly influenced by them. The results of \(t_R\) evaluation are shown in Figure 5. The average percentage improvement of FFSA compared to the rest of the protocols in terms of \(t_R\) ranges from 3% to 9% in S1. This improvement will be more notable (above 9%) for shorter sensor data length (lower \(T_{data}\) )
Most protocols show a quasi-constant $t_R$ for $n$ up to 2048 in Figure 5. FFSA requires the lowest average time read one sensor data packet from one tag. The strategy Chen14 shows an increasing $t_R$ for $n > 2048$ because it limits the frame size to 1024 when $n$ is greater than 710. FuzzyQ also presents a peak at $n = 2048$, because the value of the $Q$ parameter is upper-bounded. The improvement in FFSA comes from the reduction in $c_k$ at the expense of an increase in $c_i$, as can be appreciated in Figure 6a,b, respectively. Because the duration of an idle slot (Equation (5)) is shorter than that of a collision slot (Equation (4)), the reduction in $c_k$ leads to a lower $t_R$ for FFSA. The strategies ILCM-FbF and Chen14 present the highest $c_k$, leading to the highest $t_R$.

Figure 5. Evaluation results of the average time to read one sensor data from one tag in one frame in S1.

Figure 6. Evaluation results of $c_k$ per tag (a) and $c_i$ per tag (b) in S1.

5.2. Impact of the Tag Backscatter Link Frequency in S2

This section compares the selected protocols in terms of $t_R$ (Equation (30)), while varying the tag Backscatter Link Frequency $BLF$. Therefore, the previous protocols are evaluated by varying $BLF$ from 40 to 640 kbps; minimum and maximum values specified in the current standard. $Tari$ is set to its minimum value 6.25 µs. The simulation results are averaged for $n$ from 64 to 8192, and are shown in Table 5. The value of $\rho$ employed by FFSA is also presented, which has been obtained with Equation (25).
Table 5. Effect of BLF and ρ on the protocols’ performance in terms of $t_R$ in S2. Quantities in bold represent the best results among the protocols in the comparison. * Indicates the control variable.

| $t_R$ (ms) | BLF * (kbps) | 40  | 80  | 124 | 274 | 640 |
|------------|--------------|-----|-----|-----|-----|-----|
| ρ          |              | 0.25| 0.32| 0.39| 0.53| 0.69|
| FFSA       | 35.14        | 18.30| 12.30| 6.30| 3.46|
| SUBEP-Q    | 36.84        | 19.04| 12.73| 6.46| 3.50|
| Chen16     | 36.74        | 18.34| 12.32| 6.32| 3.48|
| FuzzyQ     | 36.52        | 18.87| 12.64| 6.43| 3.49|
| Chen14     | 38.01        | 19.63| 13.13| 6.65| 3.59|
| Eom        | 36.97        | 19.10| 12.78| 6.48| 3.51|
| ILCM-SbS   | 36.05        | 18.66| 12.50| 6.38| 3.48|
| ILCM-FbF   | 37.57        | 19.40| 12.98| 6.58| 3.56|
| Slot Counter | 37.12      | 19.20| 12.85| 6.53| 3.55|

All of the protocols present a decreasing $t_R$ with increasing BLF. For the highest values of BLF, all of the protocols present a similar behaviour and FFSA does not introduce a significant performance improvement. This occurs because the value of ρ (see Table 5) takes a significantly higher value, which causes a larger number of collision slots. As BLF decreases, FFSA shows a significant reduction in $t_R$ in relation to the prior protocols.

To analyse the previous results, $c_{k_T}$ and $c_{i_T}$ per tag are measured as functions of BLF and averaged for all the tag set sizes $n$ in S2, and the simulation results are shown in Figure 7. When BLF gets close to its upper limit, the increase in $c_{i_T}$ of FFSA is not compensated by the small reduction in $c_{k_T}$ in relation to the prior protocols, which limits the performance improvement of the proposed protocol. On the other hand, while the prior protocols present a quasi-constant $c_{k_T}$ with decreasing BLF, FFSA presents a notably decreasing $c_{k_T}$, which is reflected in a reduction in $t_R$ in relation to the prior protocols. Although for BLF > 80 kbps Chen16 behaves similarly to FFSA, the improvement introduced by FFSA becomes notably clear when BLF gets close to its upper or lower limit. This occurs because as BLF gets closer to its lower bound, Chen16 results in a low value of $y$ ($y$ is used by Chen16 in its algorithm to obtain $L$), which leads to an increasing $c_{k_T}$ and decreasing $c_{i_T}$.

![Figure 7](image_url). Evaluation results of $c_{k_T}$ per tag (a) and $c_{i_T}$ per tag (b) in S2.
5.3. Discussion

The previous section evaluated the performance of FFSA in terms of $t_R$ (Equation (30)). To demonstrate the benefits of the proposed protocol, its performance was compared with several related works presented in Section 3. In terms of the sensor data read time, the main parameter evaluated in this work, FFSA presents the lowest $t_R$ for most of the values of $n$ and BLF evaluated. The parameter BLF was selected as a control variable because it is related to the tag data rate. The two scenarios evaluated in this work consider that tags use Miller modulation with $M = 4$. The relationship between the tag BLF and the tag data rate is $DR_t = BLF / M$. Thus, a higher BLF results in a faster tag, and vice versa. Consequently, the time to identify one tag $t_R$ is lower for higher BLF values. This effect is appreciated in Table 5. FFSA analyses this characteristic and takes into consideration the value of BLF to adjust $L$ according to Equation (25). Therefore, FFSA lowers the sensor read time of the comparative protocols for a wide range of tag data rate configurations. In conclusion, the savings in the tag sensor data read time of FFSA is substantial for most of the range of BLF and $n$, which confirms that the proposed protocol is a time saving procedure in S1 and S2.

5.4. Identified Limitations

The protocols performance evaluation analysed in this work assumed an ideal communication channel, because it focused on the media access control layer. However, in a real scenario for passive RFID systems, the capture effect is typically present [30]. The capture effect occurs when the reader successfully resolves one tag reply in a collided slot. This effect could benefit the performance of FFSA because fewer collided slots and more single slots would occur, decreasing $t_R$. However, there is a negative impact of this effect over FFSA performance. The capture effect may hide some tags, which provides erroneous information to the tag estimator and increases the estimation error. Thus, the updated $L$ value may not be appropriate, which negatively affects $t_R$. A study of the capture effect on $t_R$ and an evaluation of FFSA taking this effect into account is recommended for future work.

6. Conclusions

A comprehensive survey and classification of the frame update policies for RFID DFSA anti-collision protocols has been presented. In general, this policy can be divided into three parts: $L$ update, $L$ calculation, and frame break condition. Then, several state-of-the-art DFSA anti-collision protocols have been analysed and classified according to this policy. Finally, a novel frame update policy has been proposed. This results in the Fuzzy Frame Slotted Aloha (FFSA) protocol, which is a fast DFSA anti-collision protocol and is compliant with the current UHF RFID standard. With a significant improvement in the sensor data read time in relation to the current anti-collision protocols, FFSA is a suitable candidate where low sensor data read time is sought in UHF RFID systems that require a varying number of sensor tags.

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