Energy-Efficient Resource Allocation for Application Including Dependent Tasks in Mobile Edge Computing

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Abstract

This paper studies a single-user Mobile Edge Computing (MEC) system where mobile device (MD) includes an application consisting of multiple computation components or tasks with dependencies. MD can offload part of each computation-intensive latency-sensitive task to the AP integrated with MEC server. In order to accomplish the application faultlessly, we calculate out the optimal task offloading strategy in a time-division manner for a predetermined execution order under the constraints of limited computation and communication resources. The problem is formulated as an optimization problem that can minimize the energy consumption of mobile device while satisfying the constraints of computation tasks and mobile device resources. The optimization problem is equivalently transformed into solving a nonlinear equation with a linear inequality constraint by leveraging the Lagrange Multiplier method. And the proposed dual Bi-Section Search algorithm Bi-JOTD can efficiently solve the nonlinear equation. In the outer Bi-Section Search, the proposed algorithm searches for the optimal Lagrangian multiplier variable between the lower and upper boundaries. The inner Bi-Section Search achieves the Lagrangian multiplier vector corresponding to a given variable receiving from the outer layer. Numerical results demonstrate that the proposed algorithm has significant performance improvement than other baselines. The novel scheme not only reduces the difficulty of problem solving, but also obtains less energy consumption and better performance.

Keywords: Mobile edge computing, computation offloading, task dependency, optimization problem, convex optimization

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1. Introduction

With generations of mobile devices launched by mobile device providers, users focus on implementing more complex applications on their MDs. More and more emerging applications (i.e., online game, augmented reality, video optimization/acceleration, real-time monitoring, face recognition) are being integrated to form more powerful mobile applications [1,2]. Those powerful mobile applications consist of multiple computation tasks with dependencies. In order to accomplish every computation task, the computation and communication resources of MD will be heavily occupied. However, MD's finite battery life and limited computation capacity pose significant challenges on accommodating the resource demand of those applications [3]. Fortunately, offloading computation tasks partly to Mobile Cloud Computing (MCC) [4] provides a promising technique to the elastic scaling of the capability of MDs. However, driven by the vision of 5G communications, the inherent limitation of MCC, i.e., the long transmission distance from MD to MCC, will lead to exceedingly long latency for mobile applications [3]. Moreover, there still exists many urgent issues to solve, such as increasing demand for high bandwidth, decreasing the energy consumption, and high quality of experience. Mobile Edge Computing (MEC) [6] can address those limitation by offloading computation tasks to the near-user MEC server instead of the remote cloud center [7]. indicated that MEC would play an significant role in 5G, the next generation mobile network. MEC [8,9] is regraded as a brilliant solution to overcome these difficulties.

By combining with partial computation offloading, the computation task can be divided into two parts, which are executed locally and on the MEC server. [10] deeply studied the computation offloading scheme with the two protocols TDMA and OFDMA in a multiuser Mobile Edge Computing Offloading system by joint optimization the offloading ration, computation and communication resources, and formulated the two computation offloading problem as a convex optimization problem and a mixed-integer problem, respectively. Chen et al. [11] studied a multiuser computational offloading problem in a MEC system under the constraints of communication and computation resources, and designed a distributed computation algorithm which could obtain the Nash equilibrium by using the game theoretic. Chen et al. [12] proposed a decentralized computation offloading algorithm which could achieve a Nash equilibrium in a multiple mobile devices MCC system. [13] proposed a computation offloading framework in a multiple wireless access points MEC system by optimizing the chosen edge server, the CPU frequency and computation and communication resources to trade off the energy consumption and execution time of mobile device including multiple independent tasks. [14,15] all carried out research for the purpose of maximizing the computation rate, but the backgrounds of the proposed problem and the strategies and measures show were different. [14] jointly optimized the transmission power at the access point, the task computation mode (local computing or computation offloading) and time allocation to tasks, and derived the optimal solution by using the convex optimization techniques. In order to prolong the standby time of MD, wireless powered transfer (WPT) and MEC had been integrated together. [15] jointly optimized the computation mode selection (local computing or edge computing), the time allocated to wireless power transfer (WPT) and task offloading in a WPT-assisted multi-MD MEC system. And a joint optimization algorithm based on ADMM decomposition technique was proposed to tackle this problem. In [16], the proposed dynamic computation offloading algorithm achieved the multi-objective optimization by jointly optimizing the computation offloading ratio, the CPU
frequency allocated to local execution, and the transmission power for computation offloading under the MEC system with an energy harvesting technique. In the following paper[17], MDs not only could offload their jobs to the MEC server, but also could request computation resource from others MD in the same MEC system. Meanwhile, all the MDs obtained energy from the AP by using WPT technique. [17] aimed at minimizing the whole AP's energy transmitted under the constraints of communication and computation resources, the deadline and the power transmission. [18,19] also gave a study on computation offloading. Although these papers are all about independent task with MD as shown previously, there is no dependency relationship among tasks. As the application becomes increasingly complex, multiple tasks, functions or applications are incorporated into an integrated application. Thus, an in-depth study of offloading computation tasks with dependencies is worthwhile.

Coarse-granularity based task offloading policy is used to provide an optimal solution for tasks with dependencies. [20] proposed a collaborative task computation algorithm to prolong the standby time of mobile devices for the application model in the linear topology. [21] provided a comprehensive computation offloading algorithm for computationally intense applications with multiple subtasks to determine which subtasks should be computed in the MD or cloud. [22] aimed to trade-off the energy consumption of MD and latency for application by finding the optimal assignments of tasks executed on local or remote devices. [23] achieved the trade-offs between the energy cost and latency by optimizing the power allocation and the offloading decisions for an application modeled as directed acyclic graph(DAG). [24,25] also obtained an overall solution by designing a joint scheduling offloading policy for the application with sequential task dependencies. [26] divided the application into a non-offloadable task and multi-offloadable tasks, and a low complexity sub-optimal algorithm was proposed to decide which offloadable task should be transmitted to MEC server. For an application including multiple subtasks depended on each other, [27] proposed a heuristic algorithm based on particle swarm optimizer (PSO) to solve the 0-1 programming problem, where 0 represented local computing and 1 represented edge computing.

Though these studies improve the performance of mobile device by using computation offloading, most of those researches focus on the optimal scheduling order. Meanwhile, the transmission power and the CPU frequency allocated to tasks are fixed. Therefore, we make a study on the computation offloading in the mobile application which includes multiple computational intensive dependent tasks for a single MD MEC system, and propose an algorithm that minimizes the energy cost and improves performance by jointly optimizing the offloading ratio, CPU frequency, the transmission power and the transmission time. [28] compared the performance of offloading the dependent tasks from MD to a remote cloud center or an edge server and made the conclusion that offloading to an edge server is a promising technique in providing better performance than a remote cloud server. [29] took use of offloading strategy which migrated tasks in the task graph to the nearby wearable or mobile device through the available wireless communication interface such as Bluetooth or Wi-Fi. Experimental conclusions of [29] demonstrated that the scheduling policy could achieve the two objectives of extending battery lifetime and enhancing performance. At present, there are not many works related to task graph on MEC system, most of them are sub-task granularity and these works aim at different objective. As far as we know, there is no related work with the goal of minimizing energy consumption of MD by dividing the input data of all the subtasks on task graph bit by bit, which is able to guarantee the higher resource utilization and less energy consumption. Accordingly, this paper aims to minimize the energy consumption of task graph by jointly optimizing the resource allocation strategy.
In this paper, we study a single MD MEC system where MD executes a mobile application made up of multiple computation tasks. The objective of the paper is to minimize the energy consumption of MD with energy-saving resource allocation. In this paper, we model the complex mobile application consisting of multiple interdependent tasks as a DAG task-graph, and develop an energy-efficient computation offloading algorithm by joint optimization the communication and computation resources. Our main contributions are generalized as follows.

Firstly, in term of system model, most the computation offloading algorithms do not take into account the computation results feedback, and partial computation offloading for tasks with dependencies is task granularity rather than dividing the input data bit by bit. Therefore, we propose a system model of partial computation offloading for a application consisting of multiple tasks with dependencies, which divides the computation data by bitwise and integrates results feedback.

Secondly, in term of problem formulation, for tasks with dependencies, most studies are aimed at achieving the objective of saving energy or reducing latency by optimizing scheduling strategy with fixed transmission power and local computation capacity. However, this paper uses the DVS technique optimize the CPU frequency, and achieves the optimal transmission power. The corresponding time slots for the local computing, uploading the offloading bits and computation results feedback are allocated with a time division mechanism. Therefore, the problem is formulated as an optimization problem that minimizes the MD’s energy consumption with the constraints of the delay, the computation and communication capacity.

Finally, in term of the optimal solution, convex optimization techniques and the Lagrange Multiplier method are used to simplify the problem, and the original non-convex optimization problem is transformed into a univariate nonlinear equation, which accelerates the problem solving. Simulation results demonstrate that the proposed algorithm not only saves energy consumption, but also prolongs the standby time of MD.

The rest of this paper is organized as follows. In Section II, we expounds the system model. The problem is formulated and the algorithm for the problem is proposed in Section III and its performance evaluation and simulation results are analysed and shown in Section IV. The conclusion is given in Section V.

2. System Model

As shown in Fig. 1, we consider a basic two-node MEC system that consists of single MD and one AP node integrated with a MEC server. In order to reduce the impact of multiple wireless channel creation, frequent requests for the MEC server resources, etc. on task offloading, we conduct the study in the single MEC server scenario. Both of the two nodes are equipped with one single antenna. This system provides the simplex mode. That is to say, the two processes, offloading input data or receiving computation results, can not be executed through the wireless channel at the same time. The MEC server not only provides the same execution environment but has more sufficient resources than the MD. Therefore, the MEC server can accomplish the computation tasks of application more efficient than the MD. The distance is defined as \( d \) between MD and AP, and the bandwidth is denoted as \( B \).
2.1 Computation Application Model

We assume that the MD has an application that is composed of $N$ computation-intensive tasks (See Fig. 1, with an example where $N = 6$). Each task can be offloaded to MEC server by using partial computation offloading technique. This application model can be described by using Directed Acyclic Graph (DAG) $G = (V, E)$ where the node $v_j$ in the node set $V$ denotes a task in the application, the edge $(v_i, v_j)$ represents the dependency from task $v_i$ to $v_j$ and the edge set $E$ of the DAG denotes the set of dependencies. $Set_i$ represents the in-degree set of task $i$. Task $i$ can not start executing until tasks belonging to $Set_i$ are completed. To describe the parametric context of each task, a two-tuple $(I_i, \eta_i)$ is defined, where $i = 1, ..., N$. Accordingly, $I_i$ is the size of task $i$ input data which includes the existing data $l_i$ and the computation results transmitted from tasks in $Set_i$. According to [10,17,19,20], we learn that there is a linear relationship between the computation result and the input data for the computation task. $\eta_i$ is the ratio of the output data to the input data for task $i$. Task $i$ may need the computation results from related tasks if $Set_i$ is not null. As such, the total input data size $I_i$ is $I_i = l_i + \sum_{k \in Set_i} \eta_i l_i$ while $Set_i$ is non-null. If $Set_i$ is an empty set, $I_i$ is equal to $l_i$. We use $D$, a N*N matrix, represent the task graph.

In this paper, we focus on the scenarios where the MD has finite computing capability. In order to accomplish the application, offloading parts of the input data to the MEC server is necessary. We assume that all tasks of the application are executed in a predetermined scheduling policy which results from Breadth-First Traversal (BFT). For example, we can get the scheduling policy (1,2,3,4,5,6 ) in Fig. 1. And then, we realize our objective which minimizes the energy consumption of the MD. Meanwhile, with the given scheduling strategy, we assume that both local computing and computation offloading of the current task begin simultaneously, which ensures that its dependent tasks have completed correctly. $\lambda_i$ is the ratio of the offloading data to the input data for task $i$, so we can get the constraint of $\lambda_i$:

$$0 \leq \lambda_i \leq 1, \forall i \in \{1, ..., N\}.$$  \hspace{1cm} (C1)

where $\lambda = \{\lambda_1, ..., \lambda_N\}$ denotes the offloading policy of the tasks graph.
2.2 Communication Model

For each computational intensive task \( i \in \mathcal{V} \), its input bits \( I_i \) is split into two parts \( \lambda_i I_i \geq 0 \) and \( (1 - \lambda_i)I_i \geq 0 \) bits, which denote the number of bits offloaded to the MEC server through AP and computed locally, respectively. MD receives the offloading computing results from the MEC server. We introduce the TDMA protocol into the system to avoid any interference among tasks. Because of MD and AP equipped with one antenna, the wireless channel can execute only one process during each time slot. As shown in Fig. 2, the protocol divides the whole time \( T \) into \( 2N \) time slots, and each task contains two time slots \((t_i^u, t_i^d)\).

![Fig. 2. The TDMA protocol for multi-task computation offloading in an application](image)

1) Computation Offloading from MD to the AP

In the \( t_i^u \) time slot, task \( i \) offloads \( \lambda_i I_i \) input bits to the AP with the transmission power \( P_i^t \geq 0 \). We define \( h^2 \) as the channel power gain between MD and AP. Next, the achievable data rate (bits/sec) for uploading offloading input data from the MD to the AP is defined as

\[
r_i^u = B \log_2 \left( 1 + \frac{P_i^t h^2}{N_0} \right)
\]

where \( N_0 \) means the additive white Gaussian noise at the receiver of the AP. For simplifying the computation, we define a function \( g(x) = \frac{N_0}{h^2} \left( \frac{2^x}{x} - 1 \right), x > 0 \). According to (1), we can get the expression of \( P_i^t \) with the variable \( r_i^u \). That is to say, \( P_i^t = \frac{N_0}{{h^2}} \left( 2^{r_i^u} - 1 \right) \). After we get the offloading bits \( \lambda_i I_i \) and the uploading time \( t_i^u \), \( r_i^u \) can be represented as \( r_i^u = \frac{\lambda_i I_i}{t_i^u} \). So \( P_i^t \) can be re-defined as followed:

\[
P_i^t = \frac{N_0}{h^2} \left( 2^{\frac{\lambda_i I_i}{t_i^u}} - 1 \right) = g(\frac{\lambda_i I_i}{t_i^u})
\]
\[ P_i^u = P_i^e + \kappa_i P_i^d \]  
(3)

where \( \kappa_i \) denotes the linear growth and is a constant coefficient without unit. According to (3), the energy consumption of uploading data at MD in time slot \( t_i^u \) is expressed as

\[ E_i^u = P_i^u t_i^u = (g \left( \frac{I_i \lambda_i}{t_i^u} \right) + \kappa_i P_i^d) t_i^u \]  
(4)

2) Downloading the Computation Results from the AP to the MD

Considering that the MD needs to receive the computation results from AP, so the delay cannot be ignored. The computation results of task \( i \) are \( \eta_i I_i \). And then, the achievable data rate for receiving computation results from the AP to the MD is expressed by

\[ r_d = B \log_2 \left( 1 + \frac{P_t h^2}{N_0} \right) \]  
(5)

where \( P_t \) denotes the transmission power of the AP, which is a constant. Therefore, \( r_d \) is a constant. Meanwhile, we have known the amount of the computation results \( \eta_i \lambda_i I_i \). Ultimately, combining (5), we can get the representation as follows:

\[ t_i^d = \frac{\eta_i \lambda_i I_i}{r_d} \]  
(6)

According to [32,33], the MD power consumption \( P_i^d \) increases linearly with the download data rate \( r_d \). So we give the formula of \( P_i^d \) as follows.

\[ P_i^d = P_i^e + \kappa_d r_d \]  
(7)

where \( P_i^e \) is similar to the previous definition \( P_i^e \). However, \( P_i^d \) is affected by converting from analog signal to digital signal, unpackaging, and so on. \( \kappa_d \) represents the linearly increasing coefficient. And then, we get the equation of calculating the energy consumed by receiving the computation results from AP in the time slot \( t_i^d \). By merging (6) and (7), \( E_i^d \) is expressed as the following equation.

\[ E_i^d = P_i^d t_i^d = (P_i^e + \kappa_d r_d) \frac{\eta_i \lambda_i I_i}{r_d} = \left( \frac{P_i^e}{r_d} + \kappa_d \right) \eta_i \lambda_i \]  
(8)

2.3 Computing Model

1) Local Computing:

\[ (1 - \lambda_i)I_i \]  
input data of task \( i \) is executed on the mobile device(MD) within a duration \( t_i^l \). \( C \) denotes the number of CPU cycles required for computing 1-bit of input data at MD, and \( f_i \) is the CPU frequency allocated to task \( i \), i.e. \( f_i = \frac{(1 - \lambda_i)I_i C}{t_i^l} \). In practical application, the CPU frequency of MD has a maximum value, denoted by \( f_{\text{max}} \). So \( f_i \) is capped by \( f_{\text{max}} \), i.e.

\[ \frac{(1 - \lambda_i)I_i C}{t_i^l} \leq f_{\text{max}}, \forall i \]  
(C2)
Also, the computation latency constraint $t_i^l$ for local computing of task $i$ must be satisfied as follows:

$$t_i^l \geq 0.$$  \hfill (9)

Then, the whole local computation tasks must be accomplished before the deadline, i.e.

$$\sum_{i=1}^{N} t_i^l \leq T \quad \text{(C3)}$$

The energy consumption of task $i$ for local computing is

$$E_i^l = \kappa_i f_i^l \tau_i^l = \kappa_i \frac{(1-\lambda_i)^3 I_i^l C_i^3}{(t_i^l)^3}, \forall i$$  \hfill (10)

where $\kappa_i$ means the effective capacitance coefficient which has relation with the chip structure of MD.

According to Fig. 2, for task $i$, the time that MD starts to transmit input data through wireless channel is simultaneous with starting local execution. Besides, according to (10), the bigger $t_i^l$ is, the smaller $E_i^l$ is. So we can infer that CPU is not in idle state between the end of the previous task and the beginning of the next task, which means that local execution lasts the whole time period $t_i^l$. Consequently, the wireless channel occupation time and local computation time should meet the following time constraint for task $i$.

$$t_i^a + t_i^c \leq t_i^l, \forall i.$$  \hfill (11)

Replacing $t_i^c$ with (6), we can rewrite the former time constraint as below.

$$t_i^a + \eta \frac{\lambda_i L_i}{r_d} \leq t_i^l, \forall i.$$  \hfill (C4)

2) Edge-Computation: We assume that the network and computational resources of the MEC server are infinite. Owing to the sufficient computation capability of the MEC server, the delay of receiving offloading data, the time spending on accomplishing the offloaded computation task, and the delay of sending computation results are relatively small and negligible at the MEC server. Therefore, we assume that MD immediately receives computation result from the AP when the offloading data transmission is completed. Since the objective is to minimize the energy consumption of mobile device, the energy consumption of MEC server is negligible.

### 3. Problem Formulation and Optimal Solution

In this section, we propose a feasible computation offloading scheme to the application modeled as task graph. In order to minimize the energy consumption of MD, the CPU frequency and the transmission power are optimized for local computing and computation offloading, respectively. The solution process of the optimal computation offloading scheme is shown below.

#### 3.1 Problem Formulation

Under the system model above mentioned, we give a trade-off model between the energy consumption of local computing and the energy cost of communication among the AP and the MD to obtain the total minimum energy consumption of the MD. Meanwhile, the trade-off
model must satisfy the delay constraint, the limited computation and communication resources. From (4), (8) and (10), we can come to the conclusion that all the energy consumption of MD is impacted by local computing, computation offloading and receiving computation results. Learning from the former section, the local energy consumption is influenced by the locally executed bits \((1 - \lambda_i)I_i\) and its executed time \(t'_i\), and the energy consumed by communication between the AP and the MD is impacted by the time spending on uploading the offloading bits \(t''_i\) and the offloading bits \(\lambda_i I_i\). Then, Let \(\lambda = \{\lambda_1, \ldots, \lambda_N\}\), \(t'' = \{t''_1, \ldots, t''_n\}\), \(t' = \{t'_1, \ldots, t'_n\}\), and these three vectors determine how much energy the application will consume. Mathematically, the delay-constrained energy minimization optimization problem is formulated as

\[
\mathcal{P} : \min_{(\lambda, r, f)} \sum_{i=1}^{N} E''_i + E'_i + E^i
\]

s.t. \(C1, C2, C3, C4, C5 : t''_i, t'_i > 0 \quad \forall i\)

Now we substitute all related equations into problem \(\mathcal{P}\), and \(\mathcal{P}\) is represented as follows.

\[
\mathcal{P}_1 : \min_{(\lambda, r, f)} \sum_{i=1}^{N} t'_i \left(P'_i + \frac{\alpha}{r} (e^{\frac{\alpha}{r}} - 1) + \left(\frac{P'_d}{r_d} + \frac{\alpha}{r_d}\right)\eta I_i \lambda_i + \kappa_i \frac{(1 - \lambda_i) I_i C}{(t'_i)^2}\right)
\]

s.t. \(\lambda \leq 1 \quad \forall i\)

\[C2 : \frac{(1 - \lambda_i) I_i C}{t'_i} \leq f_{max} \quad \forall i\]

\[C3 : \sum_{i=1}^{N} t'_i \leq T\]

\[C4 : t''_i + \frac{\eta I_i \lambda_i}{r_d} \leq t'_i \quad \forall i\]

\[C5 : t''_i, t'_i > 0 \quad \forall i\]

In Problem \(\mathcal{P}_1\), \(C1\) denotes the constraint of the offloading ratio, \(\lambda\) is the maximum CPU frequency constraint for each task of the application, and \(C3\) is the deadline constraint. \(C4\) guarantees that the time cost by local computing must be greater than or equal to the time taken by the data transmission and receiving computation results for task \(i\). Note that \(\mathcal{P}_1\) is non-convex in general because of the coupling of \(\lambda_i\) and \(t'_i\) in \(C2\). Therefore, we need convert \(\mathcal{P}_1\) into a convex problem[34] with some measures, and then give the optimal solution.

### 3.2 Optimal Solution To \(\mathcal{P}_1\)

This subsection gives the optimal solution of \(\mathcal{P}_1\). To achieve this goal, firstly, we transform \(C2\) into a convex constraint by multiplying both sides of inequality \(C2\) with \(t'_i\). The constraint \(C2\) is converted to \(C2'\).

\[
(1 - \lambda_i) I_i C - f_{max} t'_i \leq 0, \forall i.
\]

(\(C2'\))

Then, \(\mathcal{P}_1\) is reformulated as follows.
\[\mathcal{P}_2 : \min_{(\lambda, t', \alpha, \beta, \gamma)} \sum_{i=1}^{N} t_i^* (P_i^* + \kappa_i \frac{I_i \ln 2 \lambda_i}{h^2} (e^{\frac{h^2}{B \varepsilon^2}} - 1)) + (\frac{P_d}{r_d} + \kappa_d) \eta I_i \lambda_i + \kappa_i (\frac{(1 - \lambda_i) I_i C}{t_i^*})^3 \]

\[\text{s.t. C1, C3, C4, C5} \]

\[C2': (1 - \lambda_i) I_i C - f_{\text{max}} t_i^* \leq 0 \forall i \]

Meanwhile, the function \(\frac{1 - \lambda_i}{t_i^*}\) is convex with respect to \(0 < \lambda_i < 1\) and \(t_i^* > 0\) \([35]\), and hence the equation \(\frac{\kappa_i ((1 - \lambda_i) I_i C)^3}{(t_i^*)^3}\) in the objective function is jointly convex with respect to \(0 < \lambda_i < 1\) and \(0 < t_i^* < T\). Moreover, the following lemma demonstrates the evidence that Problem \(\mathcal{P}_2\) is a convex problem.

**Lemma 1** Problem \(\mathcal{P}_2\) is a convex optimization problem.

**Proof:** Because \(g(x)\) is an exponential function and a convex function, its perspective function\([13]\), \(t_i^* g(t_i^*)\) is still convex. Also, \(\frac{\kappa_i ((1 - \lambda_i) I_i C)^3}{(t_i^*)^3}\) is jointly convex function\([35]\).

Thus, the objective function which is the summation of a set of convex functions, holds the convexity. All constraints are the linear convex function. Consequently, we get the result of Lemma 1.

Assume that Problem \(\mathcal{P}_2\) is feasible, we use the Lagrange Multiplier method to simply the problem solving. We define \(\alpha_i \geq 0, \beta_i \geq 0, \gamma_i \geq 0\) as the Lagrangian multipliers for C1, C2', C4, respectively. \(\mu\) is the Lagrange multiplier for C3. Therefore, the partial Lagrangian function of \(\mathcal{P}_2\) is given by

\[\mathcal{L}(\lambda, t', \alpha, \beta, \gamma, \mu) = \sum_{i=1}^{N} t_i^* (P_i^* + \kappa_i \frac{I_i \ln 2 \lambda_i}{h^2} (e^{\frac{h^2}{B \varepsilon^2}} - 1)) + (\frac{P_d}{r_d} + \kappa_d) \eta I_i \lambda_i + \kappa_i (\frac{(1 - \lambda_i) I_i C}{t_i^*})^3 \]

\[+ \sum_{i=1}^{N} \alpha_i (\lambda_i - 1) + \sum_{i=1}^{N} \beta_i ((1 - \lambda_i) I_i C - f_{\text{max}} t_i^*) + \sum_{i=1}^{N} \gamma_i ((t_i^*)_n - t_i^*) \]

\[+ \mu(T - T) \]

(14)

Let \(\{\lambda^*_i, t'^*_i, t''_i\}\) denote the optimal solution of \(\mathcal{P}_2\). The optimal solution always satisfies all constraints and has the minimum energy consumption of MD. By applying KKT conditions, the following necessary and sufficient conditions must be true.

\[\frac{\partial L}{\partial \lambda_i^*} = \kappa_i I_i \frac{\ln 2 I_i \lambda_i}{h^2} + (\frac{P_d}{r_d} + \kappa_d) \eta I_i - \frac{3 \kappa_i (1 - \lambda_i^*)^3 I_i C^3}{(t_i^*)_n^3} + \alpha_i^* - \beta_i^* I_i C + \gamma_i^* \eta I_i = 0 \quad (a)\]

\[\frac{\partial L}{\partial t_i^*} = P_i^* + \kappa_i \frac{I_i \ln 2 \lambda_i}{h^2} (e^{\frac{h^2}{B \varepsilon^2}} - 1) - \kappa_i \frac{I_i \ln 2 \lambda_i}{h^2} - \beta_i^* f_{\text{max}} - \gamma_i^* \mu^* = 0 \quad (b)\]

\[\frac{\partial L}{\partial t''_i} = -\frac{2 \kappa_i (1 - \lambda_i^*)^3 I_i C^3}{(t_i^*)_n^3} - \beta_i^* f_{\text{max}} - \gamma_i^* \mu^* = 0 \quad (c)\]

\[\alpha_i^* (\lambda_i^* - 1) = 0, \forall i \quad (d)\]
According to (b), we can get Lemma 2 which describes the relations among \( \{l_i^*, t_i^*, \gamma_i^*\} \).

**Lemma 2**
The optimal \( \{l_i^*, t_i^*, \gamma_i^*\} \) satisfies

\[
\frac{\lambda^*_i}{t_i^*} = \frac{B}{I_i \ln 2} (1 + W\left(\frac{-1}{e} + \frac{(P_i^e + \gamma_i^*)h_i^2}{\kappa_i e N_0}\right))\forall i,
\]

where \( W(x) \) defines the Lambert-W function, i.e. \( x = W(e^x) \).

**Proof:** With the help of (b), we have

\[
\frac{I_i \ln 2 \cdot \lambda^*_i}{B \cdot t_i^*} = \frac{I_i \ln 2 \cdot \lambda^*_i}{B \cdot t_i^*} - 1 = e^{1 + (P_i^e + \gamma_i^*)h_i^2} = W\left(\frac{-1}{e} + \frac{(P_i^e + \gamma_i^*)h_i^2}{\kappa_i e N_0}\right).
\]

According to the Lambert-W function, i.e. \( x = W(e^x) \), which is the inverse function of \( f(x) = xe^x \). We can straightforwardly infer that \( \frac{I_i \ln 2 \cdot \lambda^*_i}{B \cdot t_i^*} = W\left(\frac{-1}{e} + \frac{(P_i^e + \gamma_i^*)h_i^2}{\kappa_i e N_0}\right) \),

which leads to the result in Lemma 2 with some simple operations.

With some operations on (c), we can get the following expression.

\[
\mu^* = \frac{2\kappa_i (1 - \lambda^*_i) I_i C^3}{(t_i^*)^3} + \beta_i^* f_{\text{max}} + \gamma_i^*, \quad \forall i.
\]

Therefore, we can get the result \( \mu^* > 0 \). Combining with (g), the summation of \( t_i^* \) is identically equal to \( T \), i.e.

\[
\sum_{i=1}^{N} t_i^* = T
\]

which conforms to the monotonically decreasing property of the objective function respect to \( t_i^* \). According to (a), (c), (d), we can get the following Lemma 3. In order to reduce the complexity of interpretation, we firstly define

\[
\phi_i(\gamma_i) = 1 + W\left(\frac{-1}{e} + \frac{(P_i^e + \gamma_i^*)h_i^2}{\kappa_i e N_0}\right).
\]

**Lemma 3**
The optimal \( \{l_i^*, t_i^*, \gamma_i^*\} \) satisfies

\[
1 - \lambda^*_i = \frac{\kappa_i N_0 \ln 2}{Bh_i^2} e^\phi_i(\gamma_i) + \frac{\eta_i I_i}{r_d} + \frac{(P_i^e + \kappa_i d_i^2) \eta_i}{r_d \gamma_i^2} + \frac{1}{3\kappa_i^2 C_i^3} \gamma_i^2.
\]

The ratio is influenced by the i-th task parameters of \( \{\eta_i, I_i\} \) and the lagrangian multiplier \( \gamma_i \).

**Proof:** At first, according to (d), because not all the input data is transmitted to the MEC server, we obtain \( \alpha_i = 0 \). The result makes sure that the local energy cost is not the maximum. And then, to ensure (c) true, since the CPU frequency \( f_i \) allocated to task \( i \) always less than \( f_{\text{max}} \), we obtain \( \beta_i^* = 0 \). Plugging \( \beta_i^* = 0 \) into (c), and making some simple manipulations, we obtain the following result.
\[ \mu^* = 2\kappa_i (1 - \lambda_i^*) I_i^1 C^i \left( t_i^* \right)^3 + \gamma_i^* \quad \forall i. \]  

(19)

At last, we substitute (17), \( \alpha_i^* = 0, \beta_i^* = 0 \) into (a). The Lemma 3 is inferred.

We know \( f_i = \frac{(1 - \lambda_i I_i^1 C^i)}{t_i} \leq f_{\text{max}} \). Meanwhile, plugging (18) into \( f_i \), we obtain an inequality about \( \gamma_i^* \). We introduce the function \( F_i(\gamma_i^*) \) for replacing the ratio \( \frac{1 - \lambda_i^*}{t_i^*} \), i.e.

\[ F_i(\gamma_i^*) = \frac{1 - \lambda_i^*}{t_i^*}. \]

Correspondingly, the range of \( \gamma_i^* \) can be described as Proposition 1.

**Proposition 1** \( F_i(\gamma_i^*) \) is a monotonically increasing function when \( \gamma_i^* > 0 \). The one and only \( \gamma_i^* \) satisfies \( F_i(\gamma_i^*) = f_{\text{max}} \) existed. The value is defined as \( \gamma_i^{(0)} \), and \( \gamma_i^* \leq \gamma_i^{(0)} \).

**Proof:** \( \varphi_i(\gamma_i) \) is an increasing function with \( \gamma_i \geq 0 \) because \( \varphi_i(\gamma_i) \) is an increasing function with \( \gamma_i \geq 0 \). Meanwhile, \( \frac{\eta_i}{r_i} \gamma_i \) is a linear function. Because \( F_i(\gamma_i) \) is the square root of the sum of an increasing function, we infer that \( F_i(\gamma_i) \) is an increasing function with \( \gamma_i \geq 0 \). \( \gamma_i \to +\infty \) and \( F_i(\gamma_i) \to +\infty \). So we can get the unique \( \gamma_i^{(0)} \) by using the Bi-Section Search method. The Proposition 1 is proved.

\[ \mu^* = \gamma_i^* + 2\kappa_i (F_i(\gamma_i^*))^3. \]  

(20)

Now, substituting (18) into (19), we get (20). Learning from (18), we acknowledge that \( \mu \) is associated with \( \gamma_i^* \), \( \eta_i \), and the performance parameters of SD. Although the ratio of (18) changes over \( \eta_i, I_i, \gamma_i^* \), the lagrangian multiplier \( \mu \) is fixed. Meanwhile, \( \mu \) is a scalar rather than a vector. Therefore, the lagrangian multiplier \( \gamma_i^* \) should satisfy \( \gamma_i^* > 0 \). In order to meet the condition (f), we get the result \( t_i^* + \frac{\eta_i I_i^1 C^i}{r_i} = t_i^* \) because of \( \gamma_i^* > 0 \). Accordingly, we obtain Lemma 4 by using the above derivation results, which denotes that the local execution time \( t_i^* \) can be expressed as a function with a single variable lagrangian multiplier \( \mu^* \).

**Lemma 4** The optimal \( t_i^* \) can be expressed as

\[ t_i^* = Q_i(\mu^*) = \frac{1}{r_i^{-3} + \frac{r_i^2}{I_i \eta_i} \ln 2 + B I_i \eta_i \varphi_i(\psi_i^{-1}(\mu^*))) + \frac{(\mu^* - \psi_i^{-1}(\mu^*))^{-1} + I_i C (2\kappa_i)^3}{2}, \]  

(21)

\( Q_i(\mu) \) is monotonically decreasing with \( \mu' \leq \mu^* \leq \mu'' \) if \( \sum Q_i(\mu') > T \) or \( \sum Q_i(\mu') < T \), \( \lambda^* \), \( t^* \), and \( t'' \) do not exist. If \( T < \sum Q_i(\mu') \) and \( \sum Q_i(\mu') < T \), \( \lambda^* \), \( t^* \), and \( t'' \) are unique.

**Proof:** Firstly, \( \mu = \psi_i(\gamma_i) \) is introduced to simplify the derivation and computation, i.e.

\[ \psi_i(\gamma_i) = \gamma_i + 2\kappa_i F_i^3(\gamma_i) \quad \psi_i'(\gamma_i) = 1 + 6\kappa_i F_i^2(\gamma_i) F_i'(\gamma_i) \]

\( F_i'(\gamma_i) \) denotes its derivation. Learning from Proposition 1, we can get \( \psi_i(\gamma_i) > 1 \) because \( F_i(\gamma_i) \) is a monotonically increasing function \( F_i'(\gamma_i) > 0 \). Consequently, \( \psi_i(\gamma_i) \) is a monotonically increasing function with \( \gamma_i > 0 \). Given
the value of $\mu$, we can get a unique $\gamma_i$, and vice versa. The inverse function of $\psi_i(\gamma_i)$ is denoted as $\gamma_i = \psi_i^{-1}(\mu)$, and $F_i(\gamma_i)$ can be rewritten as $F_i(\gamma_i)=\left(\psi_i(\gamma_i) - \gamma_i\right)\frac{1}{2\kappa_i}$. As we all know, the derivative of the original function is equal to the inverse of the derivative of its inverse function in calculus. Hence, $0 < (\psi_i^{-1})'(\mu) = \frac{1}{\psi_i'(\gamma_i)} < 1$, where $\mu = \psi_i(\gamma_i)$. Learning from Lemma 2 and Lemma 3, we can get the expression of $t_i^*$ by solving the simultaneous equation (22). We use $\psi_i^{-1}(\mu)$ replace the Lagrangian multiplier $\gamma_i$ in (23), and we can use a function $Q_i(\mu^*)$ only associated with $\mu^*$ represent $t_i^*$, which is (21).

$$t_i^* = \frac{1}{r_d} - \frac{r_d \ln 2}{I_i \eta_i} + \frac{F_i(\gamma_i)}{I_i C(2\kappa_i)^{\frac{1}{3}}}$$  

(15),(18)

Secondly, based on the property of Lambert-W function $\phi_i' > 0$, and $0 < (\psi_i^{-1})' < 1$, the derived function $Q_i'(\mu) < 0$, and $Q_i(\mu)$ is $\mu$’s rigid monotony decrease by degrees function.

$$Q_i'(\mu) = \frac{BL \rho_i^2 \ln 2 \phi_i(\psi_i^{-1}(\mu))}{(I_i \eta_i \ln 2 + BL \eta_i \phi(\psi_i^{-1}(\mu)))^2} + \frac{1}{3I_i C(2\kappa_i)^{\frac{1}{3}}(\mu - \psi_i^{-1}(\mu))^2} < 0$$  

(24)

Thirdly, we get the interval range of $\gamma_i$ as $0 \leq \gamma_i \leq \gamma_i^{(0)}$ according to Proposition 1. And, (20) indicates that $\mu$ is a monotonically increasing function with $\gamma_i$ . We define $\mu_i^{(0)} = \max \{\psi_i(0)\}$ and $\mu_i^{(0)} = \min \{\psi_i(\gamma_i^{(0)})\}$, and the numeric zone of $\mu^*$ is displayed as follows.

$$\mu_i^{(0)} \leq \mu^* \leq \mu_i^{(0)}.$$  

(25)

Meanwhile, according to (21), $t_i^*$ is monotonically decreasing with $\mu^* \geq 0$. What’s more, $0 \leq t_i^* \leq T$. By solving $t_i^* = T$, we can get a unique value of $\mu$, defined as $\mu_i^{(0)}$. Let $\mu_i = \max \{\mu_i^{(0)}, \mu_i^{(0)}\}$ and $\mu_i = \mu_i^{(0)}$. The restriction of $\mu^*$ is rewritten as

$$\mu_i \leq \mu^* \leq \mu_i.$$  

(26)

By using the well studied methods, i.e. Bi-Section Search, Newton Method, solve $\mu = \psi_i(\gamma_i), \forall i$, the numeric zone of $\gamma_i, \forall i$ can be achieved with the given lower and upper limits of $\mu$.

Ultimately, by judging the value of $\sum Q_i(\mu)$, i.e. $\sum t_i^*$, we can determine whether problem $\mathcal{P}_2$ exists the optimal solution $\lambda^*$, $t_i^*$, and $t_i^*$ or not. If $\sum Q_i(\mu^*) > T$ or $\sum Q_i(\mu^*) < T$, it
does not exist the optimal solution. If $T < \sum Q(\mu^i)$ and $\sum Q(\mu^i) < T$, the unique optimal solution can be achieved by solving (16). Lemma 4 is proved now.

Accordingly, problem $\mathcal{P}_2$, a multiple variables non-linear optimization problem with multi-constraint, is equivalently transferred to a non-linear equation with a linear inequality constraint. The non-linear equation is expressed as follows:

$$\mathcal{P}_3 : \sum_{i=1}^{N} Q(\mu) = T$$

subject to $\mu^i \leq \mu \leq \mu^*$

(27)

Since $t^i$ is a monotonic decreasing function respect to $\mu$, $\sum t^i$ inherits the descending characteristic. Therefore, there exists a unique $\mu$ to $\mathcal{P}_3$. Then, we propose our algorithm Bi-JOTD to calculate the optimal solution to $\mathcal{P}_3$. The solution is the optimal offloading strategy for $\mathcal{P}_2$, too. The proposed optimization method not only successfully avoids solving a nonlinear convex optimization problem with high dimensions and high orders, but also overwhelmingly decreases on the computational complexity and the computation time to acquire the optimal strategy. The following pseudo-code gives the accurate processes to solve the non-linear equation with a linear inequality constraint.

**Algorithm 1** Bi-inner: Bi-section algorithm for achieving the approximate quantity $\gamma$ satisfied $\mu = \nu(\gamma)$ with $\mu$ known.

Input: the value of lagrange multiplier $\mu$, $\delta_0 \leftarrow 1 \times 10^{-8}$

1. $LB \leftarrow \gamma_{LB}$, $UB \leftarrow \gamma_{UB}$
2. repeat
3. $\gamma_{\text{mid}} \leftarrow \frac{UB + LB}{2}$
4. if $(\gamma_{LB} - \mu > 0)$ then
5. $UB \leftarrow \gamma_{\text{mid}}$
6. else
7. $LB \leftarrow \gamma_{\text{mid}}$
8. end if
9. until $(UB - LB) \leq \delta_0$;
10. return $\gamma_{\text{mid}}$

Algorithm 1 calculates the approximate multiplier $\gamma$ which satisfies (19) with given $\mu$. Bi-JOTD computes the multiplier $\mu$ among the linear constraint. After Lagrangian multipliers $\gamma^i$ and $\mu^*$ are achieved, we can use (15), (18), (21) obtain the final optimal resource allocation strategy $\lambda^*$, $t^*$, and $u^*$.

**Algorithm 2** Bi-JOTD: achieving the optimal lagrange multiplier $\mu^*$ satisfied $\mathcal{P}_3$

Input: $N$, $\eta$, $D_I$, $D_T$, $T_I^m$, $T_T^m$, $T_D^m$, $d$, $h_I$, $h_T$, $h_D$, $\delta_1$, $\delta_2$, $\delta_3$

1. $I \leftarrow \{I_1, \ldots, I_N\}$ (the whole input data of each task in the task graph)
2. $I_I \leftarrow I + \sum_{j=1}^{N} I_j h_I D_{Ij}$
3. $LB_0 \leftarrow \mu_I, UB_0 \leftarrow \mu^*$
4. repeat
5. $\bar{\mu} \leftarrow \frac{UB_0 + LB_0}{2}$
6. for each $i \in \{1, N\}$ do
7. $\gamma_i \leftarrow Bi-IInner(\bar{\mu})$
8. end for
9. if $(\sum_{i=1}^{N} I_i(\gamma_i) - T > 0)$ then
10. $LB_0 \leftarrow \bar{\mu}$
11. else
12. $UB_0 \leftarrow \bar{\mu}$
13. end if
14. until $(UB_0 - LB_0) \leq \delta_1$;
15. $\mu^* \leftarrow \bar{\mu}$
16. for each $i \in \{1, N\}$ do
17. $\gamma^*_i \leftarrow Bi-IInner(\bar{\mu})$
18. Calculate $t^*_i$ using $\mu^*$, $\gamma^*_i$ and (21);
19. Calculate $\lambda^*_i$ using $t^*_i$, $\gamma^*_i$ and (18);
20. Calculate $u^*_i$ using $\lambda^*_i$, $\gamma^*_i$ and (15);
21. end for
22. return $\mu^*, \lambda^*, t^*, u^*$.
Through analyzing the proposed algorithm Bi-JOTD, the complexity of Bi-JOTD is associated with the iteration number of inner and outer Bi-Section Search algorithms. We define \( C_{\text{inner}} \) and \( C_{\text{outer}} \) as the complexity of inner and outer algorithms, respectively. Therefore, the complexity of Bi-JOTD algorithm is \( \mathcal{O}(N C_{\text{inner}} C_{\text{outer}}) \). Compared with the original Problem \( \mathcal{P} \), our proposed algorithm can greatly reduce the complexity.

4. Simulation Results

In this section, the simulation results of the proposed computation offloading scheme through joint optimization time allocation and dependency tasks is presented. The scheme is named as Bi-JOTD in the simulation results. Based on \([16,23,24]\), the simulation parameters are set as follows. We assume that the channel reciprocity holds for the downlink and uplink, and thus \( h_u^2 = h_d^2 = h^2 \). The channel power gain is modeled as \( h^2 = 10^{-3} d^{-\zeta} \Phi \) \([24]\), where \( \Phi \) stands for the short-term fading which obeys the Rayleigh fading. For distance \( d \) in meters with the same path-loss exponent \( \zeta \), a 30dB average signal power attenuation is assumed for all the channels at reference of 1m. Moreover, the input data size and the ratio of the computation results follow the uniform distribution with \( I_i \in [10,100]KB \) and \( \eta_i \in [0.01,0.1] \), separately. The other detailed parameters are listed in Table 1.

| Parameter                          | Value            |
|------------------------------------|------------------|
| Bandwidth \( B \)                  | 5 Mbps           |
| Deadline \( T \)                   | 1s               |
| Circuit powers \( P_r^c \) and \( P_d^c \) | 5.0*10^{-4}W    |
| Distance between the MD and the MEC server \( d \) | 100m             |
| Path-loss exponent \( \zeta \)     | 1.7              |
| White Gaussian channel noise \( N_0 \) | 10^{-9}W        |
| MD’s maximum CPU frequency \( f_{\text{max}} \) | 1.0GHz           |
| The linear growth coefficient \( \kappa_l \) | 1.0              |
| The effective capacitance coefficient \( \kappa_e \) | 10^{-27}          |
| The power consumption of receiving per bit \( \kappa_d \) | 2.86*10^{-7} W/bit |
| The required number of CPU cycles per bit \( C \) | 800 cycles/bit |

Meanwhile, in order to comparison, the following five baselines\([5,16,17,23,24,25,26]\), are displayed.

1) \( \lambda = 0.4 \): The computation input data are divided into two parts for each task. The ratio \( \lambda_i = 0.4 \) of input data is accomplished in the MEC server, and the rest of input data is completed in MD.

2) Task: In the task-graph representing the mobile application, some tasks are offloaded to MEC server, and the rest of tasks execute on the mobile device.

3) \( \lambda = 0.8 \): The offloading ratio of computation data transmitted to MEC server is set as \( \lambda_i = 0.8 \).
4) Ave-JOTD: The offloading ratio $\lambda_i$ is set as the average of suboptimal solution $\lambda^*_i$, i.e. $\lambda_i = \sum \lambda^*_i$.

5) PSO-JOTD: The suboptimal task offloading scheme is achieved by using particle swarm optimizer (PSO).

4.1 The Validation of the Optimal Solution

In this subsection, we verify the correctness and superiority of the proposed algorithm. The energy consumption of the approximate solution is close to the cost of the exact solution in a tolerable accuracy range, which compares the simulation results of different block input data $I (KB)$ and the ratio $\eta$ at the same deadline $T = 1.0 s$ in Fig. 3 and Fig. 4, respectively.

![Fig. 3. The energy consumption with different $I$ versus $T$](image1)

![Fig. 4. The energy consumption with different $\eta$ versus $T$](image2)
By comparing with the energy consumption between the global search with the default step size of 0.001 among the fixed range of offloading ratio $\lambda$ and the proposed optimization algorithm, the energy consumption of the proposed approximate solution is minimum. The simulation results of Fig. 3 and Fig. 4 provide the evidence that problem $\mathcal{P}_2$ is a convex optimization problem. Meanwhile, the process of solving $\mathcal{P}_2$ is correct.

4.2 The Simulation Results for Task Graph

This subsection evaluates the energy consumption of the proposed Bi-JOTD algorithm and other five baselines, which spends on computing and communicating versus the deadline $T$. 

Fig. 5. The energy consumption with fixed $I$ and random $\eta$ versus $T$

Fig. 6. The energy consumption with fixed $\eta$ and random $I$ versus $T$
Fig. 5 makes the comparison simulation of energy consumption with a vector \((I, \eta)\), where \(I\) is fixed and \(\eta\) is random, and Fig. 6 gives a simulation with random \(I\) and fixed \(\eta\). The proposed Bi-JOTD algorithm is obviously superior to the other baselines. Specifically, the energy consumption of the proposed algorithm is always less than other baselines, which further displays the advantages of offloading the computation tasks to the MEC server. The minimum energy cost has decreased trend with the increase of \(T\). Therefore, Bi-JOTD algorithm has some advantages in solving multi-task application. Then, making a comparison test between Bi-JOTD and Ave-JOTD, we can see that the energy cost of Bi-JOTD is less than Ave-JOTD with the increase of \(T\). It can be concluded that task's result feedback has a great influence on the ratio of offloading input data. Hence, we should comprehensively take task's feedback into account in the task graph to solve the objective of the formulated problem.

Fig. 7. The succeeding assignment ratios with random \(\eta\) and \(I\) versus \(T\)

Fig. 8. shows the relationship of the minimum energy consumption of MD, the input data \(I\) and the ratio of the computation results \(\eta\)
The figure simulated in Fig. 7 is based on 300 times simulated testing, in which \( I, \eta \) are randomly selected on the basis of the above assumptions in every implementation, modeling the actual computation offloading scenario. We simulate the resource allocation, and the statistics of success ratios are brought in Fig. 7. The success ratios of all the schemes increase with \( T \), as we expected. Besides, the success ratio of Bi-JOTD grows faster than others. Bi-JOTD can achieve a high success ratio even with low latency \( T \). Meanwhile, as time goes by, the success ratio of Bi-JOTD reaches 100% and remains stable. Compared with other baselines, our proposed algorithm has more advantages in a high success ratio. Fig. 8 shows the relationship of the minimum energy consumption of MD, the input data and the ratio of computation results. The energy consumption is achieved by using our proposed algorithm, and \( T = 1.5s \). According to Fig. 8, while setting the value of \( \eta \), the energy consumption increases significantly as the input data increases. When setting the value of the input data \( I \), the energy consumption also shows an increasing trend with the increase of \( \eta \), but the growth is not very obvious. In particular, the application cannot be accomplished while \( I = 100KB \) and \( \eta = 0.1 \). Integrated with the previous simulation results and the optimization processes, the proposed algorithm not only has a great advantage in energy consumption, but also has a good performance on the executability of the computation offloading strategy. Ultimately, combining all figures and the equivalence of problem \( P_1, P_2, \) and \( P_3 \), we can get the conclusion that our proposed algorithm not only can obtain higher efficiency, less energy consumption and better performance, but also can be calculated more easily than others.

5. Conclusion

This work studies a computation offloading scheme for an application including multiple computation components with dependencies under a single-user MEC system. MEC server assists MD in completing its computation-intensive latency-critical application. The application is modeled as a DAG task-graph. By jointly optimizing the offloading ratio, CPU frequency, transmission power and transmission time, the objective that minimizes the energy consumption of MD is achieved. The problem is formulated as an optimization problem. The nonlinear equation with a linear inequality is obtained by using the Lagrange Multiplier method and convex optimization techniques. A double Bi-Section Search algorithm is proposed to solve the transformed nonlinear equation. Simulation results reveal that the proposed strategy greatly reduces the energy consumption while compared with the used baselines. The proposed computation offloading scheme not only reduces the difficulty of problem solving, but also prolongs the MD’s standby time and achieves better performance. Meanwhile, we will extend our study of using partial computation offloading strategy to deal with the application including dependent tasks to the multiple mobile devices multiple MEC servers scenario.

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