A High-Voltage High-Frequency Subnanosecond Pulse Generator Based on Gallium Arsenide Drift Step-Recovery Diodes

A. V. Rozhkov*,

a Ioffe Physical Technical Institute, Russian Academy of Sciences, St. Petersburg, 194021 Russia

*e-mail: rozh@hv.ioffe.rssi.ru

Received February 5, 2021; revised March 3, 2021; accepted March 16, 2021

Abstract—The prospect of using high-voltage GaAs drift step-recovery diodes for the formation of subnanosecond pulses is shown. The electrical circuit of a generator is described, which provides (with a total efficiency of at least 25%) the formation of pulses at a load of 50 Ω with an amplitude of up to 550 V, a voltage rise time of 0.43 ns, a full width at half maximum of 0.73 ns, and a repetition frequency of up to 200 kHz.

INTRODUCTION
Silicon drift step recovery diodes (DSRDs) are used to generate nanosecond high-voltage pulses in a wide switched-power range. The nanosecond recovery of the blocking ability of the DSRD semiconductor structure is provided both by the design features of the structure itself and by the parameters of the modulation mode [1]. In the developed high-voltage generators based on an assembly of several series-connected Si DSRDs, when the pulse amplitude exceeds 2 kV, the voltage rise time is at least 1.5 ns, and the pulse FWHM duration (FWHM is the full width at half-maximum) reaches 2–3 ns [2, 3]. High-voltage pulse generators with an amplitude of ~500 V, a voltage rise time of 0.5–0.7 ns, and FWHM = (1.5–1.7) ns are manufactured on the basis of epitaxial Si DSRDs [4, 5].

The results of the first experimental studies of GaAs-based DSRDs [6] showed a number of fundamental and constructive advantages of structures manufactured on the basis of semiconductor materials, which have a larger energy band gap compared to Si and a higher mobility of charge carriers. The objective of this study was to experimentally confirm the prospects of using GaAs DSRDs for generating subnanosecond high-voltage pulses.

THE EXPERIMENTAL RESULTS
Our investigation was performed using GaAs-p⁺—p—i—n—n⁺ structures that were manufactured using the method of liquid-phase epitaxy from a limited volume of a solution—melt of gallium arsenide in gallium. The diode structures had a distribution profile of the impurity concentration in the p⁺, i, and n regions that was characteristic of DSRDs. The concentration gradient in the base regions was as high as two orders of magnitude with a residual-impurity concentration of at most 5 × 10¹⁴ cm⁻³ in the ith region and concentrations of the acceptor and donor impurities at levels of ~2 × 10¹⁶ cm⁻³ at the interface of the p—p⁺ region and ~5 × 10¹⁵ cm⁻³ at the interface of the n—n⁺ region.

The thicknesses W of the p, i, and n regions of the diode structures changed depending on the technological modes and were within the following ranges: W_p = (20–30) μm, W_i + W_n = (30–40) μm. The diode area was ~0.8 mm² and was limited to the diameter of the mesa structures, which were manufactured via chemical etching. The lifetimes of nonequilibrium charge carriers in the n base (τ_p) that were measured by the Lax method did not exceed 100 ns. The maximum blocking diode voltage U_b ~ 500 V was comparable with U_b of DSRDs that were based on Si with a resistivity of 5 Ω cm [2]. The capacitance of GaAs DSRDs was 7–4 pF for reverse voltages in the range of 200–500 V.

When developing the generator design, a circuit-design solution was used to ensure an extremely high operating frequency, which allowed minimization of the heat release on a single active element of the circuit. Figure 1 shows a diagram of a push—pull generator in which the duration and amplitude of the forward (J_f) and reverse (J_r) current pulses were formed using two opposite-parallel L—C circuits. TTL logic chips and IXDD404SIA drivers were used to generate control signals I_1, I_2.

The circuit operates in the following way. When a control pulse I_1 is fed to the gate of field-effect transistors FET_1 and FET_3 during the first oscillation half-period of symmetric L_4—C_i and L_3—C_3 circuits, the
DSRD. At the moment when the current direction changes upon application of a control pulse $I_1$ to the gates of the $FET_1$ and $FET_4$ FETs, the $L_1–C_1$ and $L_3–C_3$ circuits of the reverse current $J_r = 2J_{\text{max}}$ are enabled. The currents $J_{\text{max}}$ and $J_{\text{max}}$ are limited by the characteristic impedance of the circuits: $p_1 = (L_1/C_1)^{1/2} = (L_3/C_3)^{1/2}$, $p_2 = (L_2/C_2)^{1/2} = (L_4/C_4)^{1/2}$, the resistance of the DSRD base region, and the resistance of the $FET_1–FET_4$ transistors. The minimum rise time of the forward and reverse currents was $t_r = 7$ ns, which was limited by the operation time of the FETs.

In this research, the conditions for rapid recovery of the blocking ability of the DSRDs were determined, when, at a chosen rate and duration of the accumulation and dispersal of nonequilibrium charge carriers, in accordance with [1, 6], the necessary synchronicity of the moment of depletion of the plasma charge in the diode base and the moment of formation of the $p–n$ junction space charge region was observed. A DS06102A oscilloscope (Agilent Technologies) provided recording of the transient characteristics with a time resolution of $\sim 200$ ps. Two 20-dB attenuators were used in the measuring path. CSV-format text files were used to save and further process the numeric data.

Figure 2 shows the oscillogram of the diode switching process until the moment of a sharp voltage increase. In the mode of the formation of subnanosecond pulses, the forward-current duration was $t_f \leq 20$ ns. At a voltage of $U = 40$ V, the maximum value of the accumulated charge in the diode base reached 120 nC. At the specified forward-current durations, there was no diffusion modulation of the weakly doped diode base.

The thickness of the plasma diffusion layer $L_D = (D_t t_f)^{1/2}$ at the values of the diffusion coefficient $D = 20 \text{ cm}^2/\text{s}$ that are characteristic of GaAs was at most 6 μm. The residual resistance was $\sim 1$ Ω. The delay time of the control signal $I_1$ relative to $I_1$ was selected so that the reverse current was interrupted at the maximum of its amplitude $J_r$. As a result of the current interruption in the DSRD, the energy that was stored in the $L_2$ and $L_3$ inductances of the reverse-current circuits was transferred within a time of $\sim L_3/(2p)$ to a matched transmission line with the characteristic impedance $\rho = R$ and then to a load $R = 50$ Ω. In this case, the pulse amplitude across the load reached $U_m = 2J_{\text{max}}^2\rho$.

The oscillogram of a generator output pulse (see Fig. 3) indicates that the achieved rise time $t_r$ and fall time $t_f$ have record-low values for drift diodes: $t_r = 0.43$ ns and $t_f = 0.53$ ns for an FWHM duration of 0.72 ns. The spread of the delay time of the output-pulse formation relative to the moment of initiation of the control pulse $I_1$ did not exceed the value that was set by the synchronization stability of the measuring circuit ($\sim 30$ ps). The used electrical circuit of the generator and the dynamic characteristics of the FETs provided generation of high-voltage subnanosecond pulses at frequencies of up to 200 kHz (the pulse repetition period was $T = 5$ μs). At the same time, the amplitude of the voltage generated at the load was 14 times higher than the supply voltage of the generator circuit.

In the absence of radiators and air ventilation of the active circuit components, the total volume occupied by the circuit components of the push–pull generator with two opposite-parallel $L–C$ circuits was limited to dimensions of $30 \times 30 \times 50$ mm. Estimates show that the main heat release occurred at the stage of the voltage rise and fall at the active elements of the circuit. With the comparability of the total dynamic losses at the $FET_1–FET_4$ transistors and a single GaAs-based DSRD, the operating frequency of the generator was limited by the capabilities of the transistor switches and the value of the dissipated power in the diode structure, in which the current density reached 1.5 kA/cm².
Table 1. The main pulse parameters of the generator depending on the operating frequency

| $f$, kHz | $U_m$, V | $I_{sup}$, mA | FWHM, ns | $t_r$, ns |
|---------|----------|---------------|----------|----------|
| 300     | 0.43     | 0.76          | 400      | 19       | 110     | 27       |
| 400     | 0.43     | 0.75          | 250      | 26       | 85      | 27       |
| 500     | 0.42     | 0.73          | 200      | 34       | 84      | 25       |

CONCLUSIONS

The efficiency of using GaAs-based DSRDs for generating subnanosecond high-voltage pulses at frequencies of several hundred kilohertz has been shown experimentally. It was found that the efficiency of the generator in the entire frequency range is at least 25%. According to a number of parameters, the advantage of GaAs structures with a larger band gap and a higher mobility of charge carriers in comparison to Si structures has been confirmed. The current density of GaAs DSRDs is more than 10 times higher than the optimal current density of Si DSRDs. The voltage rise time and the FWHM pulse duration for GaAs DSRDs are significantly shorter than the values of the corresponding parameters for Si DSRDs. The latter provides certain advantages when using GaAs DSRDs in short-pulse modulators in ultrawideband electronics systems, since a decrease in the duration of strobe pulses leads to an increase in the overall noise immunity. The existing interest in the development of such devices determines the need for further research aimed at increasing the amplitude of output pulses in generators that are based on assemblies of several sequentially connected GaAs DSRDs.

REFERENCES

1. Grekhov, I.V. and Mesyats, G.A., Usp. Fiz. Nauk, 2005, vol. 175, no. 7, p. 735.
2. Voronkov, V.B., Grekhov, I.V., Kozlov, A.K., Korotkov, S.V., and Stepanyants, A.L., Instrum. Exp. Tech., 2007, vol. 50, no. 3, p. 353. https://doi.org/10.1134/S0020441207030098
3. Merensky, L.M., Kardo-Sysoev, A.F., Shmilovitch, D., and Kesar, A.S., IEEE Trans. Plasma Sci., 2013, vol. 41, no. 11, p. 3138. https://doi.org/10.1049/el.2013.2129
4. Merensky, L.M., Kesar, A.S., and Kardo-Sysoev, A.F., Proc. IEEE Int. Conference on Microwaves, Communications, Antennas and Electronic Systems (COMCAS 2013), Tel Aviv, October 21–23, 2013, 978-1-4673-5756-2/13.
5. Kesar, A.S., Sharabani, Y., Shafir, I., Zoran, Sh., and Sher, A., IEEE Trans. Plasma Sci., 2016, vol. 44, no. 10, p. 2424. https://doi.org/10.1109/TPS.2016.2605744
6. Rozhkov, A.V. and Kozlov, V.A., Semiconductors, 2003, vol. 37, no. 12, p. 1425.

Translated by A. Seferov