New scheme to eliminate power loss of start-up resistor for low standby power consumption

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Abstract: In the initial start-up period of power converter, there is no voltage supply from auxiliary winding to pulse-width-modulation (PWM) IC. Therefore, a typical method is connected with a start-up resistor (R_start) between bridge rectifier and IC controller for initial voltage supply. However, the R_start would continue to consume energy even if the auxiliary voltage has been set up. The proposed scheme uses depletion-type NMOS to connect with R_start. The start-up mechanism can be guaranteed without any additional voltage, and naturally cut off the R_start to minimize power dissipation once the voltage of PWM IC is supplied from auxiliary winding. As a result, the outstanding standby power performance can be implemented and comply the power regulation of 5 star standards of mobile charger. Experimental results demonstrate the standby power consumption only 28.8 mW.

Keywords: start-up resistor, standby power, depletion-type NMOS

Classification: Electron devices, circuits and modules

References

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1 Introduction

The AC-DC power converter [1, 2] is used in a lot of applications includes charger for mobile phone, adaptor for notebook computer and many consumer electronics, etc. By AC-DC power converter, the AC is converted into DC to supply energy for load. A typical AC-DC converter is controlled by PWM IC. Many start-up mechanisms are employed to create start-up power rail for PWM IC. A common method is using start-up resistor. Firstly, as shown in Fig. 1(a) [3], the AC input voltage is rectified by a bridge rectifier and the start-up resistor is connected with the line source and VCC pin of PWM IC. After the first stable switching, the voltage supply comes from auxiliary winding. Another start-up scheme is shown in Fig. 1(b) [4], it is not supplied from auxiliary winding. The start-up resistor always transfers voltage supply for control chip. However, using start-up resistor would continue consuming power in whole operation duration. This power dissipation also slightly affect the efficiency, especially in light load or standby power condition [5]. Furthermore, due to the progress of manufacture technology, the ultrahigh voltage process can be embedded into IC as shown in Fig. 1(c). The PWM IC directly connects with line source, which is able to sustain from 600 V to 700 V [6, 7, 8]. Then, the embedded ultrahigh voltage process of PWM IC regulates the line voltage to step down for supplying power to IC. However, the cost of PWM IC adopts ultrahigh voltage process is very expensive. The static power consumption still exists because of the large voltage drop between line source and VCC pin of IC. Moreover, the no-load power consumption in 5 star standards of mobile charger is under 30 mW [9]. In low rating power converter, the standby power loss from start-up resistor is a major proportion. For complying with the power regulations, we need some solutions to eliminate the power loss of start-up resistor with a simple and low cost scheme once the auxiliary voltage has been successful built particularly in low power application.

![Fig. 1. PWM IC start-up mechanisms: (a) Start-up resistor and auxiliary winding. (b) Start-up with resistor. (c) Embedded high-voltage start-up pin.](image-url)
2 Scheme description

Fig. 2 shows the proposed scheme, which uses depletion-type NMOS to remove start-up resistor. The whole scheme is composed of one start-up resistor (R\text{start}), one depletion-type NMOS (Q\textsubscript{1}), one bipolar device (Q\textsubscript{2}), two current limit resistor (R\textsubscript{1}, R\textsubscript{2}), two diode (D\textsubscript{1}, D\textsubscript{2}), two hold-up capacitor (C\textsubscript{1}, C\textsubscript{2}), one auxiliary winding (N\textsubscript{aux}), one primary winding (N\textsubscript{1}), one main switch (SW), one current sensor resistor (R\textsubscript{S}), and one PWM IC.

![Diagram of the proposed scheme](image)

**Fig. 2.** Proposed remove start-up resistor scheme.

The example and value of start-up resistor can be determined as equation (1). The power dissipation of start-up resistor is calculated as equation (2).

\[
R_{\text{start}} \leq \sqrt{2} \cdot \frac{V_{\text{ac rms, min}} - V_{\text{th, ic}}}{I_{\text{ic}}} = \frac{\sqrt{2} \cdot 90 \text{ V} - 13 \text{ V}}{1 \text{ mA}} \approx 114 \text{ k}\Omega
\]

\[
P_{R_{\text{start}}} = \frac{V_{\text{ac rms}}^2}{R_{\text{start}}} = \frac{230^2 \text{ V}\text{rms}}{100 \text{ k}\Omega} \approx 529 \text{ mW}
\]

where $V_{\text{ac rms, min}}$ is the minimum of root mean square of AC input voltage. $V_{\text{th, ic}}$ is the turn-on threshold voltage of PWM IC. $I_{\text{ic}}$ is the driving current of IC.

As shown in Fig. 3(a), the initial start-up mechanism is supplied from R\text{start} and Q\textsubscript{1} (normally closed) to charge C\textsubscript{1} for supplying PWM IC. The IC initial sink little current (typically 20 \text{ \mu A}), when the $V_{C1}$ reaches $V_{\text{th, ic}}$ (13 \text{ V}), IC starts to pulse (duty) and its consumption increases (typically 1 \text{ mA}). Owing to the main switch (SW) is controlled by duty therefore $V_{C2}$ is build up gradually. At the same time, the peripheral components (Q\textsubscript{2}, D\textsubscript{2}, D\textsubscript{1}) are not activated due to the shortage of adequate $V_{C2}$.

![Diagrams of start-up mechanism](image)

**Fig. 3.** Start-up mechanism of proposed scheme.

In proposed case, when the $V_{C2}$ reaches about 5~6 \text{ V}, the Q\textsubscript{2} is turned on. As a result, the gate pin (G) of Q\textsubscript{1} is 0 \text{ V}. The source pin (S) of Q\textsubscript{1} is higher than 5 \text{ V}. The
reverse-bias voltage \((V_{GS} \geq -5 \text{ V})\) would turn off \(Q_1\), thus the \(C_1\) solely delivers energy to the IC. After about 5 ms, the \(V_{C2}\) reaches 13 V and via diode takes over for supply the IC. As long as the \(V_{C1}\) do not drops below a level called under voltage lock-out (UVLO), (typically 8 V), the IC will continue to send duty and the whole start-up mechanism will be achieved. Finally the power loss from start-up resistor is removed in normal operation interval as shown in Fig. 3(b).

The \(C_1\) selection depends on the hold-up time needed from \(C_1\) supply IC until the auxiliary voltage takes over, in proposed case is:

\[
C_1 = \frac{IT}{\Delta V} = \frac{I_{IC} \times t_{hold,\text{up}}}{V_{th,\text{IC}} - UVLO} = \frac{1 \text{ mA} \times 10 \text{ ms}}{13 \text{ V} - 8 \text{ V}} \approx 2 \mu\text{F}, \text{ choose 2.2} \mu\text{F}.
\]

The \(C_2\) selection is depend on the ripple of \(V_{C2}\), in proposed case is:

\[
C_2 = \frac{IT}{\Delta V} = \frac{I_{IC} \times t_{V_{AUX}}}{V_{C2,\text{ripple}}} = \frac{1 \text{ mA} \times 10 \text{ ms}}{1 \text{ V}} \approx 10 \mu\text{F}, \text{ choose 10} \mu\text{F}.
\]

In proposed case, in order to compare with conventional remove start-up resistor method [10] shown as Fig. 4, therefore \(R_1\) be select as 470 k\(\Omega\). Actually, the better design should \(Q_2\) be turned on when \(V_{C2} > 9 \text{ V}\) (UVLO + forward voltage of diode), we can replace proposed \(R_1\) as a 9 V Zener diode for ensure \(V_{C1} > \text{UVLO}\) or design the base current of \(Q_2\) is activated when \(V_{C2} > 9 \text{ V}\).

Proposed scheme looks like simple, but please consider there is no voltage can be supplied from \(V_{AUX}\) at initial operation. If \(Q_1\) is substituted by MOSFET or transistor (normally open), then the power converter of Fig. 2 can’t work in the initial start-up period without supply from power rail. Compare with Fig. 4. The initial voltage is supplied through \(R_{\text{start}}\) and bipolar transistor (\(Q_1\)) as Fig. 4(a). Once the auxiliary voltage (\(N_{AUX}\)) has been established, the \(Q_1\) is turned off by \(Q_2\) as Fig. 4(b). But the power loss becomes \(R_1\) to ground. In 230 Vrms condition, the power loss from \(R_1\) is about \(230^2 \text{ V} \div 470 \text{ k}\Omega = 112.5 \text{ mW}\). This power loss can’t meet the standby power regulation in 5 star standards of mobile charger (must under 30 mW). In proposed scheme, the power loss \((230^2 \text{ V} \div 100 \text{ k}\Omega = 529 \text{ mW})\) from \(R_{\text{start}}\) is completely removed. Even if considering other standby power consumptions from PWM IC, bridge rectifier, magnetic component, etc., the standby power consumption of whole power converter still can meet the regulation.

![Fig. 4. Conventional remove start-up resistor method.](image-url)
3 Experimental verification

To verify the feasibility of the proposed remove start-up resistor scheme, a prototype converter as shown in Fig. 2 has been constructed in the laboratory. The measured experimental results with the following parameters:

1) AC input voltage: 230 V<sub>rms</sub>
2) Start-up resistor: 100 kΩ
3) Hold-up capacitor (C<sub>1</sub>), (C<sub>2</sub>): 2.2 µF, 10 µF
4) PWM IC turn-on threshold voltage: 13 V
5) Rated output power of AC-DC converter: 7.5 W (focus for mobile phone charger application)

Fig. 5 shows the power-on sequence from initial voltage to the steady value of 15 V. After the power-on sequence, PWM IC is supplied from the auxiliary winding (actually the voltage of auxiliary winding V<sub>AUX</sub> is approximate rectangle waveform of positive and negative half-cycle, and converts by diode D<sub>1</sub> to direct current and keeps about 15 V in hold-up capacitor C<sub>2</sub>). The waveforms show the voltage of V<sub>C2</sub> is from 0 V rising to 15 V. The voltage in gate pin of depletion-type NMOS (Q<sub>1</sub>) falls to 0 V and the voltage in source pin of Q<sub>1</sub> rises up to 15 V. Therefore, the voltage (V<sub>GS</sub>) of Q<sub>1</sub> is −15 V and then more ensure to be turned off. The startup time is below 25 ms due to the start-up resistor in proposed scheme is only 100 kΩ, which can supply higher current than traditional start-up resistor (1 MΩ). This concludes the proposed scheme to be particularly suitable for quick start of the charger application. After power start-up mechanism, the AC-DC converter is fully functional and regulated, even if the start-up resistor is already disconnected. On the other hand, the depletion-type NMOS is completely turned off. Consequently, there is no any power loss or leakage current from the start-up resistor (R<sub>start loss</sub> $230^2 V / 100 \text{k}Ω = 529 \text{mW}$ is completely removed). In the end, the overall efficiency is slightly improved. From views on the overall result, the standby power consumption of whole power converter is only 28.8 mW (when rated output power is 7.5 W) in 230 Vrms condition as captured in Fig. 6. And this power consumption can comply the standby power regulation in 5 star standards of mobile charger.

Summarize the proposed technique for remove the start-up resistor is quite simple and low cost (depletion-type NMOS cost below USD$ 0.02). The further specification of depletion-type NMOS is listed in Table I.

| Table I. Key parameters of the proposed depletion-type NMOS |
|-----------------------------------------------|
| V<sub>DS_max</sub> = 600 V | I<sub>D_max</sub> = 18.5 mA |
| V<sub>DG_max</sub> = 600 V | I<sub>D_peak</sub> = 74 mA |
| V<sub>GS_max</sub> = ±20 V | V<sub>GS_breakdown</sub> = −5 V |
| C<sub>iss</sub> = 9.4 pF | R<sub>ds_on</sub> = 600 Ω |
4 Conclusion

Using depletion-type NMOS technique can ensure the initial voltage supply by start-up resistor during the power-on period. Owing to the depletion-type NMOS is normally turned on without any additional auxiliary voltage. Besides, by proposed scheme, the depletion-type NMOS can be fully shut down to eliminate power loss from start-up resistor for meet the standby power regulation, especially in no load or standby power condition when the auxiliary voltage has been successfully built. Experimental results show that the standby power consumption is only 28.8 mW. This result can meet the 5 star standards of mobile charger regulation with a simple and low cost solution.

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