MPBUS: MODULAR POWER BUS FOR SPACE VEHICLES

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ABSTRACT
Cost reduction in the access to space is leading the space race in the launchers market. Launcher’s companies are looking for a more efficient and economical way to place satellites in orbit.

MPBUS project proposes a decentralized power bus distribution with jettisonable smart batteries combined with the use of PLC technology, data over DC, achieving overall weight reduction and harness simplification, both goals in line with Next Generation Launchers (NGL) targets.

Two different bus interfaces have been developed, Battery Front End (BFE) in charge to connect power sources equipment to the power bus, and Device Front End (DFE) in charge to connect and protect power demanding equipment to the power bus. Same power wire is used for transmitting data for power management and telemetry, reducing overall data harness.

Panel demonstrator prototype integrates these front ends to functionally test the application of MPBUS concepts to the combination of both buses, unregulated 28V Power Bus and Telemetry Subsystem, from VEGA launcher.

Environmental tests have been performed to MPBUS devices successfully.

ABBREVIATIONS AND ACRONYMS
ADC Analogue Digital Converter
AFE Analog Front End
AIT Assembly & Integration & Testing
AVUM Attitude and Vernier Upper Module
BER Bit Error Rate
BFE Battery Front End
CE-I Conducted Emission Current Transient
DC Direct Current
DFE Device Front End

DPA Decentralized Power Bus Architecture
ECC Error Correction Codes
EGSE Electrical Ground Support Equipment
EMC Electro Magnetic Compatibility
FPGA Field Programmable Gate Array
LCL Latch Current Limiter
LDO Low Drop Out
MFU Multi Function Unit
NGL Next Generation Launcher
OBC On Board Computer
PLC Power Line Communication
SPI Serial Peripheral Interface
TID Total Ionizing Dose
TRL Technology Readiness Level
VEGA Vettore Europeo di Generazione Avanzata

1. INTRODUCTION
While the PLC technology is widely used and accepted in the industrial sector, its application for space is still in development stages, [1], [2]. However the interest for this technology and their application for launchers is raising due to adequate matching to several NGL requirements, such as mass reduction, harness optimisation and AIT cost reduction [3].

The MPBUS project has developed a new decentralized power bus architecture (DPA) applied to launchers, introducing the Smart Battery concept, combining distributed batteries with Battery Front Ends (BFE) connected to the power bus along the launcher, allowing the use of multi-power sources along the power bus and to maintain power management network permanently monitored using PLC over same DC power wires as communication path.

Smart Batteries can be jettisoned following the launcher’s stages separation schedule and MPBUS can adapt to the new power bus layout after every stage separation.
MPBUS can also manage, with DFEs, the protection and connection/disconnection of the different loads to/from the power bus.

The MPBUS PLC maximum rate is 3Mbps, the power network management consumes only 5 Kbps, so there are almost 3 Mbps to be used as data payload coming from other spacecraft subsystem. In MPBUS prototype, Telemetry subsystem data, non-critical subsystem sited along all the launcher’s length, has been used as payload data in PLC. Every Telemetry node is connected to the MPBUS through a DFE to be both powered and data connected using the power wires.

MPBUS combination of DPA and PLC applied to launcher typology achieve several advantages vs traditional power distribution (point to point), as power harness reduction and elimination of data wires for payload subsystem (Telemetry) data transmission.

2. DESIGN

The MPBUS is based in a bus topology with one master node and slave nodes. Communication is based on a Token Pass technique managed by the master node with limited access time to the bus for every slave node, accessing to the bus sequentially according to a unique address. Every message in the bus is broadcast to the rest of nodes and each node process it accordingly.

The current implementation of the MPBUS in the final prototype allows one master and up to 16 slaves, but there is no theoretical limitation for maximum amount of nodes into the bus. The protocol is composed of two different types of frames: master and slave. The master frame is a broadcast message received for all nodes with a command for a single node, telemetry configuration for all nodes and telemetry data from the master node. The slave frame is a broadcast message with information to maintain the token pass, answer to master commands and telemetry data exchange. Figure 3 shows a typical MPBUS communication oscilloscope screenshot, where nodes response can be observed as a carrier frequency over 28 Vdc.

Distributed power is achieved by connecting/disconnecting the batteries to the two wires power bus through the BFE interfaces. Make-before-break technique is used for switching on/off batteries to the bus, two batteries can be connected to the bus at the same time due to the use of a protective diode in each BFE, ensuring that there is always a battery supplying energy to the bus, eliminating voltage gaps in power source transitions.

Different loads or energy demanding devices can be powered/unpowered when connected/disconnected to the bus through DFE interfaces. Each DFE has its own protections, based in Latch Current Limiter (LCL) circuit, following [4] specification.
For the MPBUS final prototype, both BFEs and DFEs were designed according to VEGA launcher specifications [5].

2.1 Battery Front End

The Battery Front End is the interface that allows the connection/disconnection of a battery to/from MPBUS. The BFE electronics is always powered from its own battery and it is composed of the following main modules: Power Source Management, Power Supply, Control Logic, Communications and Acquisition.

The Power Source Management is the circuit that connects/disconnects the battery to/from MPBUS. It is composed by: switching circuit, on/off sensor and current sensor.

The Power Supply is connected directly to the battery and provides the different voltage levels needed by the front end components: 3.3 Vdc and 1.5Vdc. It is composed by a protection circuit, DC/DC converters and LDOs regulators.

The Control Logic subsystem, based in a Microsemi FPGA ProAsic3 A3P600, is the brain of the interface and manages the different functions of the module (connection/disconnection of the battery, communications and acquisition).

The Communications subsystem, the DCB3M modem, is the PLC core and it has been developed using a Microsemi FPGA SmartFusion2 M2S050. It is connected to the MPBUS and powered from the Power Supply module.

The DCB3M modem is based in a phase modulated (QPSK) narrow band carrier that protects the data with several levels of error correction codes. In addition, unique signalling technology allows arbitration over noisy and severely attenuating channels.

The Communications module has a selectable carrier frequency channel (13MHz to 30 MHz in steps of 100kHz), a selectable ECC level, resulting in an effective transmission rate from 0.9Mbps up to 3Mbps. This module is managed through a SPI channel by the Control Logic and provides the ability to select the operating carrier frequency. A software tool has been developed to sweep the entire frequency range and map the attenuation between the nodes of the final prototype as function of the carrier frequency in order to find the channels with the lowest signal attenuation.

A study about the integration of the Communication module into an ASIC has been performed in order to assess its feasibility.

The Acquisition system acquires relevant data from the power network (battery voltage, bus voltage, current, battery cells temperature…), subsystems and external sensors. All this data is managed by the Control Logic and sent through the MPBUS by the Communication module.

For the MPBUS final prototype, the BFE has being designed to work under three different operating modes:
### 2.2 Device Front End

The Device Front End (DFE) is the interface that protects the MPBUS from load device anomalies and vice versa. DFE allows the control of the connection/disconnection of a load to/from MPBUS. The electronics of this front end is always powered from the MPBUS and it is composed of the following main modules: Load Management, Power Supply, Control Logic, Communications and Acquisition.

The Load Management is the circuit that connects/disconnects the device to/from MPBUS and it has a protection built-in LCL. It is composed by: protection circuit, switching circuit, on/off sensor and current sensor.

The Power Supply is connected directly to the MPBUS and provides the different voltage levels needed by the front end: +5V, -5V, 3.3V, and 1.5V. It is composed by a protection circuit, DC/DC converter and LDOs.

The hardware of the Control Logic is similar to the one in the BFE but with different firmware. Rest of DFE are equal than those than BFE.

For the MPBUS final prototype, the DFE has being designed to work under the three different operating modes as for the BFE.

### 3. MPBUS PROTOTYPE

To assess the MPBUS, two different prototypes have been manufactured. First proto was in charge to demonstrate basic front ends functionalities and served as basis for the final proto.
EGSE subsystem, external to the panel, simulates the VEGA connection to ground equipment allowing to command and control the MPBUS from ground prior to lift off phase. The EGSE part is composed by one special MPBUS node developed as a support equipment that works as a gateway between the PC used as a control unit in the EGSE and the MPBUS panel demonstrator.

There are 3 different power supplies (28V) in the final prototype, one external power supply at the EGSE simulating launcher powering when at ground phase, BAT2 node at interstage 2/3 powered by external power supply and BAT1 node at upperstage powered from Li-ion battery.

There are 7 different nodes simulating VEGA avionics devices, each one is capable to simulate load energy consumption for up to five VEGA devices and it can handle the switching of every of these loads separately.

BAT1 and BAT2 BFE and TVC3 DFE have been designed and manufactured using components with space equivalent. Rest of nodes have been designed and implemented based on industrial components for cost and time constraints.

The MPBUS harness is a two wire shielded twister PTFE insulated AWG 12 cable, selected following [6] and DB15 3W3 connectors with central pin not connected. Auxiliary industrial DB9 connectors and cables, out of the MPBUS core scope, are used as ground-launcher link wires for simulating separator’s signal and wired telemetry link between UCTM and EGSE.

3.1 Battery nodes

Real VEGA launcher has two batteries [7], 55V and 28V, placed at the upperstage (AVUM). The 28V battery is made of Li-ion SAFT MP176065 cells in a 8S8P configuration. This battery powers the unregulated 28V bus, which powers several equipment as OBC, UCTM, UCATs, since the EGSE umbilical disconnection at launch pad to the end of mission. When AVUM starts its programmed payload orbit injection maneuver, the 28V battery capacity is drained about 25%. MPBUS proposes to split the original 28V 8S8P battery into two, one with a 28V 8S2P (BAT2) configuration placed at intersatge 2/3 in charge to power the bus from the EGSE umbilical disconnection to just before stage 3 separation and another battery 28V 8S6P (BAT1) in charge to power the AVUM after stage 3 separation. This battery distribution reduces the inert AVUM mass allowing to increase the thrust-to-weight ratio for this last stage.

Battery nodes have been designed in a two floor layout aluminum enclosure, upper floor for BAT2 accommodates 28V 8S2P Li-ion battery and BFE board is placed at the bottom floor.

BAT2 has a NTC sensor for temperature monitoring, this analog signal is wired to the BFE where the Acquisition module sample and convert it and the Control module send it to the simulated OBC through MPBUS by the Communication module.
Mechanized openings in the aluminum enclosure are available for connectors to distribute power to the several MPBUS branches.

3.2 Device nodes

In a similar manner as with battery nodes, a front end for devices that demands energy from the MPBUS has been developed.

For MPBUS prototype 7 simulated avionics devices have been implemented onto the panel demonstrator: OBC, TLM Controller, TLM, SIMx5, SIMx2, TVC3 and TVC1. The different DFEs have loads to simulate the real consumption of the devices in the VEGA launcher. The consumptions simulated for the TVCs are the one related to the electronic control, not to the TVCs electrical actuators. The SIMx5 and SIMx2 simulated the loads of several devices of VEGA.

The system is managed by the EGSE node at the beginning of the mission simulation. Once the EGSE is removed, the OBC becomes the master of the MPBUS.

The TLM Controller is the node that collects all data sent in the MPBUS and resend them to the EGSE station for post analysis.

Rest of devices just simulates the energy consumption and send their telemetry data.

TVC3 DFE has been designed and manufactured using components with space equivalent. A LCL class 3 circuit has been implemented. Rest of DFEs are developed with industrial components.

4. TESTS RESULTS

The BFE and DFE have been functionally tested into the panel demonstrator at NASP facilities and environmental tests over single nodes have been performed at CSL facilities to assess their compatibilities with VEGA launcher specifications.

4.1 Functional Tests

Three main functional tests have been performed: (i) mission test, (ii) transfer rate and (iii) word error rate. A specific testing software has been developed to manage different missions, collect and analyse data to extract test results.

The MPBUS final prototype was functionally tested following a simulated typical VEGA mission, covering from previous lift off moments to end of mission phase. During the mission, device and battery nodes were communicating through the MPBUS and executed their respective connection/disconnection in a correct sequence to perform the mission. Launcher stages were sequentially jettisoned and the nodes were connected and disconnected until the last stage remained powered from its own BFE. MPBUS communications, operation modes of every node and different power distribution scenarios, up to 23A, were achieved successfully.

Transfer rate tests was performed with all MPBUS nodes connected configured with Telemetry mode on, maximum sample rate and hard ECC. Average transfer rate obtained was 0,90 Mbps, almost matching with real VEGA Telemetry maximum rate 1 Mbps [8].

Word error rate tests has been performed over all possible MPBUS layout configurations after every stage separation. TLM Controller node role was to sniffer all MPBUS communications frames and send them to EGSE equipment for data post processing. Two
different errors, CRC and timeout, have been recorded. MPBUS fails VEGA word rate restrictive specifications [9]. The MPBUS measured byte error rate is 2.35E-7, showing MPBUS potential feasibility at this early development stage.

4.2 Environmental Tests

These tests were performed at CSL facilities to check BFE and DFE behaviour in VEGA/Ariane space environment in terms of vacuum, temperature, radiation and EMC.

4.2.1 Radiation test

The most critical radiation environment of a launcher is a flux of proton and neutron during 30 minutes of mission [10]. It has been established that in nominal solar conditions, the heavy ion aggression does not affect the success of a mission [11]. Electronic components from front ends selected for protons and neutrons testing were both FPGA, the ProAsic3 A3P600 and the SmartFusion2 M2S050 from Microsemi. 16 specimens each model has been tested.

There are mainly two types of effects induced by the radiation in electronic components: Total Ionizing Dose (TID) Effects and Single Event Effects (SEE). As SEE have to be managed at design level, tests focused on TID effects. Neutron dose can be converted to total ionizing dose equivalent value [12].

TID influence can be assessed by measuring the change in gate propagation delay [13]. FPGA design involving large number of serial inverter gates was developed and propagation delay was measured on test specimens before and after radiation.

5.2.1.1 Protons

The estimated fluence at 50 MeV p+ for Ariane5 is around 1.E7 p+/cm² [14] for a 30 minutes mission. Four FPGA specimens for both models were tested with this fluence and energy (30 minutes irradiation). There were another specimen batch with four specimens each model tested with 3 times the fluence (three times the flux with same 30 minutes irradiation time).

Results manifest that there is no significant change in the propagation delay.

5.2.1.2 Neutrons

The test was done at a flux of 1 n/cm²* s (corresponding to the mean atmospheric environment [14]) and 3 n/cm²* s. The irradiation time was 30 min as the typical Ariane5 launcher mission. The selected source (Am-Be: ISO 8529-2 Reference neutron radiations) has a (theoretical) spectrum between 1-10 MeV with a peak around 5 MeV. The flux of 1 n/cm²* s is obtained by adapting the distance between the source and the target, thanks to the 1/r² effect. The total fluence after 30 minutes is then 1800 n/cm².

As in protons test, four FPGA specimens from both models were tested with nominal dose and another four specimens from both models were tested with 3 times the dose during 30 minutes.

Results manifest that there is no significant change in the propagation delay.

4.2.2 Thermal Vacuum test

The MPBUS BFE has been tested into a vacuum chamber. Four thermal cycles between -10°C and +60°C at a pressure of less than 10⁻⁵ mbar were performed while BFE performed power connection/disconnection procedures and communication with another MPBUS nodes placing outside vacuum chamber.

No error occurs in the range of the test.

4.2.3 EMC test

BFE and DFE have been tested at the University of Liége Laboratory in a semi-anechoic chamber for EMC, following [15] and [16] procedures, to test the devices in radiative/conductive emission and susceptibility.

Except the transient current that exceeded the limit at the CE-I test, the MPBUS passed all tests.

5. CONCLUSION

The MPBUS solution has demonstrated the benefits of combining decentralized power distribution and PLC, achieving mass reduction, harness optimisation, complexity, risk and AIT cost reduction.

Communication over power wires has been established by MPBUS nodes despite to the complex environment prototype: stages separation, connectors and separator’s interfaces, bus branches and wire length.

Most of the functional and environmental tests based on VEGA’s specifications were successful, even at current MPBUS TRL 4, showing great potential for next development phases.

Lack of specific standards for spacecrafts using PLC technology has been detected. MPBUS developments can help to settle future standardization about this topic.
6. ACKNOWLEDGMENTS

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