Memristors with Nociceptor Characteristics Using Threshold Switching of Pt/HfO$_2$/TaO$_x$/TaN Devices

Minsu Park †, Beomki Jeon †, Jongmin Park © and Sungjun Kim *

Division of Electronics and Electrical Engineering, Dongguk University, Seoul 04620, Republic of Korea
* Correspondence: sungjun@dongguk.edu
† These authors contributed equally to this work.

Abstract: As artificial intelligence technology advances, it is necessary to imitate various biological functions to complete more complex tasks. Among them, studies have been reported on the nociceptor, a critical receptor of sensory neurons that can detect harmful stimuli. Although a complex CMOS circuit is required to electrically realize a nociceptor, a memristor with threshold switching characteristics can implement the nociceptor as a single device. Here, we suggest a memristor with a Pt/HfO$_2$/TaO$_x$/TaN bilayer structure. This device can mimic the characteristics of a nociceptor including the threshold, relaxation, allodynia, and hyperalgesia. Additionally, we contrast different electrical properties according to the thickness of the HfO$_2$ layer. Moreover, Pt/HfO$_2$/TaO$_x$/TaN with a 3 nm thick HfO$_2$ layer has a stable endurance of 1000 cycles and controllable threshold switching characteristics. Finally, this study emphasizes the importance of the material selection and fabrication method in the memristor by comparing Pt/HfO$_2$/TaO$_x$/TaN with Pt/TaO$_x$/TaN, which has insufficient performance to be used as a nociceptor.

Keywords: RRAM; nociceptor; threshold switching; high-k

1. Introduction

Among several next-generation memories, resistive random-access memory (RRAM) operates with the resistance state proposed by Chua [1]. Memristors based on RRAM have been extensively studied in various fields such as information processing, material engineering, structural integration, fabrication process, etc. [2–4]. These studies focus on filling the gap between storage class memory and traditional memory technologies (flash memory and dynamic random-access memory) and implementing a system for neuromorphic applications and non-Von Neumann computing [5–7]. The work to implement neuromorphic computing, which has a human learning algorithm and various thinking skills, has also been actively researched by mimicking brain synapse functions such as spike-time-dependent plasticity (STDP), paired-pulse facilitation (PPF), long-term potentiation (LTP), and long-term depression (LTD) [8–12]. These synaptic characteristics functionally operate based on resistive switching (RS) and are even capable of implementing threshold switching (TS). In particular, TS memristors can react according to external stimuli within a specific range. Due to these characteristics, they can be used in the field of artificial neural networks [13–16]. For example, biological properties can be conjugated to future research such as intelligent sensory systems and humanoid robots [17]. Additionally, in space research and science, the demand for future space suits that function as human skin has continued to increase among people with acquired neurological disorders [18–20].

The sensory system needs the ability to detect and efficiently recognize unwanted external pressure, force, mechanical strength, temperature, and danger signals. There are several receptors in the human biometric system, and a nociceptor is one of the most important and essential receptors that generates pain signals to make the body avoid dangers [21–23]. The nociceptor operates on an external signal in the following way.
When nerve cells are not excited, they show “no adaptation” and “relaxation” behaviors. Conversely, “alldynia” and “hyperalgesia” behaviors are exhibited in the excited state. Here, TS is the key to making an artificial nociceptor. TS does not respond to the signals below a certain value but reacts strongly when the boundary value is exceeded. When the external stimulus beyond the threshold disappears, the nociceptor slowly returns to an unexcited state. In addition, the nociceptors increase the response degree with the signal persistence or injured nociceptors respond to a much lower threshold value. The principle of this system defends the body from external and strong stimuli. For example, it is typical to show a sensitive reaction even if low stimuli are applied to the damaged skin.

When the nociceptor feels a strong stimulus beyond the threshold, it reaches an excited state and reacts to the stimulus. In an excited state, “alldynia” and “hyperalgesia” are indicators of the degree of stimulus versus the reaction. First, “alldynia” is the act of generating a reaction signal at a value less than the threshold value in an unexcited state, and “hyperalgesia” is the act of generating an excessively strong signal compared to the degree of reaction in an unexcited state above the threshold value. To simulate a nociceptor, a complementary metal–oxide–semiconductor (CMOS) sensor is required but has the disadvantages of increasing power consumption and circuit complexity. Therefore, if a memristor using RRAM with TS characteristics follows nociceptor characteristics, it will be possible to maximize advantages such as a simple MIM structure and a power reduction [13,24–27].

In this study, Pt/TaO$_x$/TaN (PTT) and Pt/HfO$_2$/TaO$_x$/TaN (PHTT) were investigated. The thickness of HfO$_2$ was varied into 3 nm (PHTT3), 5 nm (PHTT5), and 7 nm (PHTT7) to identify the four critical characteristics (“no adaptation”, “relaxation”, “alldynia”, and “hyperalgesia”) mentioned above. It was confirmed by transmission electron microscope (TEM). HfO$_2$, not only well known as a high-k material but also as an emerging material in the field of RRAM, was used, and TaO$_x$ with stable switching characteristics was also deposited. When these two overlapped materials were sandwiched by the Pt and TaN, it showed the I–V curve of TS at a low current level. The previous studies focused on implementing self-rectifying characteristics using the TS feature of TaO$_x$ and HfO$_2$ [28,29]. However, where and how this property can be applied and the effects of the thickness are not described in detail. Thus, we focused on this part to conduct our research. In the case of PTT and PHTT, they were selected as the control group to check how each insulating layer mimicked the nociceptor function. We investigated the I–V curve of PTT and PHTT and the retention characteristic. Furthermore, we proposed a charge-trapping and de-trapping mechanism to explain the TS behaviors. Eventually, we experimentally demonstrated that PHTT has four nociceptive properties. We also anticipate that this device can be applied to artificial intelligence fields such as humanoid robots as nociceptors in the future.

2. Experiments

RRAM samples were fabricated using deposition techniques such as sputtering, atomic layer deposition (ALD), and e-beam evaporator. For the Pt/HfO$_2$/TaO$_x$/TaN device, a TaN (100 nm) with a sheet resistance of 60 $\Omega$/sq was deposited for use as the bottom electrode with DC sputtering (AMAT, ENDURA 5500) on the 20 mm x 20 mm SiO$_2$/Si substrate. Then, 20 nm thick TaO$_x$ and 3 nm thick HfO$_2$ layers were deposited sequentially via pulsed DC reactive sputtering and ALD, respectively. Pulsed DC reactive sputtering of the TaO$_x$ film was performed at room temperature. The gas flow rate of Ar and O$_2$ were set to 8 sccm and 12 sccm, respectively, the base pressure of $1.6 \times 10^{-6}$ Torr was used, and the deposition pressure was 1 mTorr. The 500 W of DC power (pulsed DC, 50 kHz) was applied. HfO$_2$ film with the ALD process was deposited using TEMAHf and H$_2$O at the stage temperature of 280 $^\circ$C using a thermal ALD process (CN1, Atomic premium). One cycle for ALD HfO$_2$ is composed of TEMAHf (0.5 s)-purge (35 s)-H$_2$O (0.3 s)-purge (35 s). In total, 29 cycles, 49 cycles, and 68 cycles were performed for 3 nm (PHTT3), 5 nm (PHTT5), and 7 nm (PHTT7) of HfO$_2$ layers, respectively. Hf and Ta is entirely oxidized for the HfO$_2$ and TaO$_x$ film. The results of stoichiometry are shown from previous studies in which the device is
deposited under the same conditions as PHTT [26]. Finally, Pt (100 nm) was deposited as the top electrode using an e-beam evaporator (ULTEC, FR-EB20). Patterns were engraved utilizing the shadow mask filled with a 100 μm diameter cell. The electrical characteristics of each cell were measured in the DC mode using a Keithley 4200-SCS semiconductor parameter analyzer and in the pulse mode using a 4225-PMU ultrafast module.

3. Results and Discussions

Using the TEM analysis, we first identified the structure of the PHTT device as shown in Figure 1. It was confirmed that two insulator layers, TaOx and HfO2, were deposited between the top and bottom electrodes by 3 nm and 15 nm, respectively. Other devices, PTT, PHTT5, and PHTT7, were fabricated under the same fabrication condition. To measure the electrical characteristics and response to stimulus, an external bias provoked from the module was applied to the top electrode (Pt), while the bottom electrode (TaN) was grounded.

![Cross-sectional TEM analysis and schematic of the Pt/HfO2/TaOx/TaN (PHTT3) device.](image)

Figure 1.

Figure 2 shows a nociceptor system of the human body and TS memristor artificially operating it [30]. In our body, nociceptors are located anywhere at the end of the neuron sensor. The nociceptor recognizes the external stimulus and compares its magnitude with a threshold value to determine whether to generate an electrical signal and send the signal to the brain through the spinal cord. The response to stimuli allows our bodies to defend against dangerous things. If there is no receptor system, we will inevitably be exposed to danger and be defenseless. Similarly, when an electrical pulse is applied to an artificial memristor mimicking a nociceptor, a response occurs according to the pulse amplitude. If the pulse amplitude is greater than the threshold voltage, the memristor is turned on and the output current is generated. This process is described in Figure 2a,b, respectively. The opposite situation operates on a similar principle, but in this case, the memristor is turned off and the output current is not generated. Memristors receiving stimuli above the threshold value begin to be relaxed immediately to correspond to the next stimulus.

I–V characteristics of the threshold switching are shown in Figure 3a–d. Each I–V curve shows 100 cycles; the red line shows the 50th curve of positive TS property. A 0.05 V step per sweep and voltage sequence for returning to 0 V though Vset (4/6/8/12 V) and Vreset (−4 V) were given to each device in the condition of current compliance (Icc) of about 1 μA. The electroforming process was not shown in the TS characteristics due to the current limitation. Switching direction is marked with the numbers from 1 to 4 in Figure 3a, which shows the I–V curve of PTT and a small Vset of approximately 3.7 V. With increasing voltage from 0 V to 4 V, a set process that gradually switched from the high-resistance state (HRS) and low-resistance state (LRS) occurred. Interestingly, after going through a set process, it returned to the HRS even if the device did not experience the reset process. Nevertheless, the reason for the negative voltage was to keep the interval between the DC cycle and cycle constant. PHTT3 in Figure 3b has a Vset of about 4.5 V, which is greater than PTT. The Vset in Figure 3c,d increases gradually as the thickness of HfO2 becomes thicker
and the non-uniformity of the I–V curve becomes severe. Among the four devices, PHTT3 shows the most uniform performance, a low $V_{set}$, and the high on-off ratio of ≈21. Thus, we decided to use PHTT3 for the realization of nociceptor function in further study.

![Figure 2](image)

**Figure 2.** Nociceptor system in a human body and artificial nociceptor circuit using a TS memristor. (a) When external stimuli are introduced through the human skin, nociceptor determines the magnitude of the signal above the threshold and creates a potential to send information to the spinal cord. (b) Schematic in which an electrical pulse is applied to the sensor. A pulse higher than the threshold value is applied, the memristor is turned on and an output current is generated.

![Figure 3](image)

**Figure 3.** I–V characteristics of four devices: (a) PTT, (b) PHTT3, (c) PHTT5, (d) PHTT7. Schemes of the device are shown as the inset.

Figure 4 shows the band diagram schematic of the PHTT device. The conduction band offset of the top and bottom electrode with the insulating layer is shown. As a result of calculating the work function of each material from the zero bias in Figure 4a, the energy barrier between Pt and HfO$_2$ is 5.35 eV, and between TaO$_x$ and TaN it is 0.2 eV [31–33]. The detailed switching process can be described in the following description. The deep
and shallow trap sites exist in the insulating layer of HfO$_2$. When these traps are not filled with electrons, the device remains in the HRS. Figure 4b illustrates when a positive voltage is applied to the Pt top electrode so that the trap energy level becomes lower than the Fermi level of the TaN bottom electrode [24]. The trap sites in HfO$_2$ gradually accumulate with electrons injected from TaN and the PHTT device begins to switch from HRS to LRS. Moreover, depending on the magnitude of $I_{CC}$, it is possible to control the number of electrons being filled in the trap. When the externally applied voltage is removed, the energy band returns to its original state in the zero bias. Contrary to the previous state, the energy level in HfO$_2$ is higher than the Fermi level in TaN, resulting in de-trapping in which electrons naturally flow to the TaN bottom electrode. Therefore, PHTT returns from LRS to HRS with the removal of positive voltage. Figure 4c shows the condition of the negative bias; electron de-trapping is further accelerated and blocks the electron injection through the Pt top electrode. Since the trap energy level rises, the de-trapping speed of electrons is also increased. PHTT5 and PHTT7 have thicker HfO$_2$ than PHTT3, and a greater number of traps are located in the insulating layer. As confirmed in Figure 3, this may be the reason for the increase in the $V_{set}$ value.

Figure 4. Band diagram schematic of PHTT device. (a) Zero bias condition. (b) Positive bias condition. (c) Negative bias condition.

Figure 5a shows different TS behaviors at various $I_{CC}$ values. The PHTT3 device shows a large range of TS characteristics depending on $I_{CC}$ from a minimum of 10 nA to a maximum of 5 µA, showing that our device not only operated at a low $I_{CC}$ but was also driven at a high $I_{CC}$. The endurance test with 1000 cycles was checked as shown in Figure 5b. Endurance was measured by applying a DC voltage of 6 V (SET), reading through a DC voltage of 3.5 V, applying a DC voltage of −4 V (RESET), and reading at 3.5 V. The resistance values were extracted from the read voltage of 3.5 V, and stable endurance was achieved compared to the previously reported work [28]. Next, we experimentally confirmed the characteristics of the nociceptors, called “allodynia” and “hyperalgesia”. Figure 5c schematically indicates the signal responses of the nociceptors in damaged and normal states, and “allodynia” and “hyperalgesia” behaviors are separated by the threshold value. After the injury, the nociceptor must show the enhanced response at a reduced threshold as presented by vertical and horizontal arrows. Figure 5d expresses the current response under the different voltages (pulse amplitude: 4.5 V ~ 8.5 V / step: 1 V). The width of the pulse was fixed at 0.3 ms. First, a current of 75 µA flowed at a pulse amplitude of 4.5 V without damage. As the pulse amplitude increased, the output current reached a maximum of 150 µA. Second, the red line was damaged with a strong pulse of 9.2 V before pulse measurement, and then the current response was measured. The current raised from 120 µA to 170 µA as the voltage increased. Lastly, because of the strong injury (9.4 V pulse),
the current increased rapidly by 10 pulses of 8.5 V amplitude. Similar results were derived through measurements in PHTT5 and PHTT7. As shown in Figure 5e, the voltage was applied from 7 V to 9 V to show the current responses. Figure 5f shows the behavior of “hyperalgesia” and “allodynia”; only the magnitude of the injured voltage is different from Figure 5d. The thickness of the insulating layer was thicker than that of the PHTT3, so a larger voltage was applied to give damage. Through Figure 5d–f, we confirmed that the device reduces the threshold of their external stimuli, generating a current response in previous harmless stimuli (called “allodynia”) and increasing their response to normally innocuous stimuli (called “hyperalgesia”). From these results, it can be suggested that the PHTT device is sufficient to use as the nociceptor characteristics.

![Figure 5](image)

**Figure 5.** (a) TS characteristics of PHTT3 at different \( V_{CC} \). (b) DC endurance test of PHTT3 went up to \( \approx 10^3 \) times. Orange dots and pink dots refer to LRS current and HRS current, respectively. (c) Typical stimulus versus response relation in the nociceptor in the normal and damaged states. The arrows represent “allodynia” and “hyperalgesia” characteristics. (d) Current response according to the voltage in the PHTT3 with and without an injury. (e) Current responsivity according to different voltage at PHTT5. (f) Experimentally measured current of the PHTT7 before and after the pulse injury, showing the appearance of “allodynia” and “hyperalgesia” characteristics.

PTT also conducted experiments to check the current response by applying pulses to see if they show the characteristics of memristor nociceptors such as PHTT. Additionally, we experimented with two repetitive pulses rather than a single set of pulses. In Figure 6a, stair-shaped voltages (1 V, 2 V, 3 V, 4 V, 5 V, 5.5 V, 6 V, and 6.5 V) were applied, and after 0.4 s, the same pulses were applied immediately without a relaxation time. PTT did not respond to 4 V with a pulse width of 3 ms, and then a current flowed from a voltage higher than 4 V. The current is slightly increased compared with the current flowing in the first pulse (black circle mark). This corresponds to the “allodynia” and “hyperalgesia” characteristics of the nociceptor, respectively. Here we went a step further, adding two variable factors to prevent the current increase. As shown in Figure 6b, negative voltage (−4 V) was inserted between pulses, and instead of a negative voltage, a sufficiently long time of 160 \( \mu \)s was applied in a way that did not affect the device, as shown in Figure 6c. This suggests that in PTT, “no adaptation” and “relaxation” depend on the negative voltage or the relaxation time. Finally, we checked the current response according to the voltage when the nociceptor function was not fully broken. Before pulse measurement, a breakdown state, in which
After that, Figure 6d shows the characteristics of the current flowing in response to each voltage. Thus, PTT in the breakdown state is not appropriate for the nociceptor memristor at all.

**Figure 6.** Current response with stair-shaped voltage in PTT. (a) Different amplitudes (1 V, 2 V, 3 V, 4 V, 5 V, 5.5 V, 6 V, and 6.5 V) are applied with a pulse width of 3 ms. (b) Negative voltage (−4 V) is applied between stepped pulses. (c) Sufficiently long relaxation time of 160 µs and 0.5 V amplitude is applied between stepped pulses so as to not affect the current response. (d) Voltage (1 V, 2 V, 3 V, 4 V, 5 V, 6 V, 7 V, and 7.5 V) is applied in PTT that does not function as a nociceptor memristor.

**4. Conclusions**

We successfully fabricated an artificial nociceptor of PTT and PHTT devices. Four properties were experimentally demonstrated to show “no adaptation”, “relaxation”, “alldynia”, and “hyperalgesia”. In addition, we analyzed the functional operation of the nociceptors using the I–V curve and pulse voltage of PTT, PHTT3, PHTT5, and PHTT7 devices. In addition, the experiment that varied the thickness confirmed that PHTT3 could realize the most optimal artificial nociceptor concept in various aspects such as V_{set}, DC switching stability, and current response by external pulse voltage. In accordance with the previous results, it can be strongly argued that the materials and structures using PHTT are suitable for realizing artificial nociceptors in the future.

**Author Contributions:** Writing—original draft, M.P. and B.J.; Writing—review & editing, Data curation, J.P.; S.K.; Supervision, S.K. All authors have read and agreed to the published version of the manuscript.

**Funding:** This work was supported in part by the National Research Foundation of Korea (NRF), a grant funded by the Korean government (MSIP) under Grant 2021R1C1C1004422, and a Korea Institute of Energy Technology Evaluation and Planning (KETEP) grant funded by the Korea government (MOTIE) under Grant 2022400000020.

**Data Availability Statement:** Not applicable.

**Conflicts of Interest:** The authors declare no conflict of interest.
References

1. Chua, L.O. Memristor—The Missing Circuit Element. *IEEE Trans. Circuit Theory* **2020**, *18*, 507–519. [CrossRef]
2. Waser, R.; Dittmann, R.; Staikov, C.; Szot, K. Redox-based resistive switching memories—Nanoionic mechanisms, prospects, and challenges. *Adv. Mater.* **2009**, *21*, 2632–2663. [CrossRef]
3. Hwang, C.S. Prospective of Semiconductor Memory Devices: From Memory System to Materials. *Adv. Electron. Mater.* **2015**, *1*, 1400056. [CrossRef]
4. Kim, K.M.; Jeong, D.S.; Hwang, C.S. Nanofilamentary resistive switching in binary oxide system; a review on the present status and outlook. *Nanotechnology* **2011**, *22*, 254002. [CrossRef] [PubMed]
5. Hong, X.L.; Loy, D.J.J.; Dananjaya, P.A.; Tan, F.; Ng, C.M.; Lew, W.S. Oxide-based RRAM materials for neuromorphic computing. *J. Mater. Sci.* **2018**, *53*, 8720–8746. [CrossRef]
6. Park, S.; Kim, H.; Choo, M.; Noh, J.; Sheri, A.; Jung, S.; Seo, K.; Park, J.; Kim, S.; Lee, W.; et al. RRMB-based synapse for neuromorphic system with pattern recognition function. In Proceedings of the 2012 International Electron Devices Meeting, San Francisco, CA, USA, 10–13 December 2012; pp. 10.2.1–10.2.4.
7. Wu, H.; Yao, P.; Gao, B.; Wu, W.; Zhang, Q.; Zhang, W.; Deng, N.; Wu, D.; Wong, H.S.P.; Yu, S.; et al. Negative-Capacitance FinFET Inverter, Ring Oscillator, SRAM Cell, and FTr Technical Digest—International Electron Devices Meeting. In Proceedings of the 2018 IEEE International Electron Devices Meeting (IEDM), San Francisco, CA, USA, 1–5 December 2018; pp. 11.5.1–11.5.4.
8. Wang, Z.; Joshi, S.; Savel’Ev, S.; Song, W.; Midya, R.; Li, Y.; Rao, M.; Yan, P.; Asapu, S.; Zhuo, Y.; et al. Fully memristive neural networks for pattern classification with unsupervised learning. *Nat. Electron.* **2018**, *1*, 137–145. [CrossRef]
9. Park, M.; Park, J.; Kim, S.J. Compatible switching mechanisms in Ni/SiOx/ITO and application to neuromorphic systems. *Alloys Compd.* **2022**, *903*, 163870. [CrossRef]
10. Park, J.; Ryu, H.; Kim, S. Nonideal resistive and synaptic characteristics in Ag/ZnO/TiN device for neuromorphic system. *Sci. Rep.* **2021**, *11*, 16601. [CrossRef]
11. Yang, J.; Cho, H.; Ryu, H.; Ismail, M.; Mahata, C.; Kim, S. Tunable synaptic characteristics of a Ti/TiO2/Si memory device for reservoir computing. *ACS Appl. Mater. Interfaces* **2021**, *13*, 33244–33252. [CrossRef] [PubMed]
12. Wang, Z.Q.; Xu, H.Y.; Li, X.H.; Yu, H.; Liu, Y.C.; Zhu, X.J. Memristors: Synaptic Learning and Memory Functions Achieved Using Oxygen Ion Migration/Diffusion in an Amorphous InGaZnO Memristor. *Adv. Funct. Mater.* **2012**, *22*, 2758. [CrossRef]
13. Yoon, J.H.; Wang, Z.; Kim, K.M.; Wu, H.; Ravichandran, V.; Xia, Q.; Hwang, C.S.; Yang, J.J. An artificial nociceptor based on a diffusive memristor. *Nat. Commun.* **2018**, *9*, 417. [CrossRef] [PubMed]
14. Li, H.Y.; di Huang, X.; Yuan, J.H.; Lu, Y.F.; Fan, T.Q.; Li, Y.; Xue, K.H.; He, Y.H.; Xu, M.; Tong, H.; et al. Controlled Memory and Threshold Switching Behaviors in a Heterogeneous Memristor for Neuromorphic Computing. *Adv. Electron. Mater.* **2020**, *6*, 2000309. [CrossRef]
15. Lu, Y.F.; Li, Y.; Li, H.; Wan, T.Q.; Huang, X.; He, Y.H.; Miao, X. Low-power artificial neurons based on Ag/TiN/HfAlOx/Pt threshold switching memristor for neuromorphic computing. *IEEE Electron Device Lett.* **2020**, *41*, 1245–1248. [CrossRef]
16. Kim, D.; Jeon, B.; Lee, Y.; Kim, D.; Cho, Y.; Kim, S. Prospects and applications of volatile memristors. *Appl. Phys. Lett.* **2022**, *121*, 010501. [CrossRef]
17. John, R.A.; Tiwari, N.; Patdillah, M.I.B.; Kulkarni, M.R.; Tiwari, N.; Basu, J.; Bose, S.K.; Ankit; Yu, C.J.; Nirmal, A.; et al. Self healable neuromorphic memristor elements for decentralized sensory signal processing in robotics. *Nat. Commun.* **2020**, *11*, 1–12. [CrossRef]
18. Anderson, A. Understanding Human-Space Suit Interaction to Prevent Injury During Extravehicular Activity. Ph.D. Thesis, Massachusetts Institute of Technology, Cambridge, MA, USA, 2014.
19. Park, H.L.; Lee, Y.; Kim, N.; Seo, D.G.; Go, G.T.; Lee, T.W. Flexible Neuromorphic Electronics for Computing, Soft Robotics, and Neuroprosthetics. *Adv. Mater.* **2020**, *32*, 1903558. [CrossRef] [PubMed]
20. Stirling, L.; Arezes, P.; Anderson, A. Implications of Space Suit Injury Risk for Developing Computational Performance Models. *Av. Aerosp. Med. Hum. Perform.* **2019**, *90*, 553–565. [CrossRef]
21. Espinoza, S.C. Alteraciones conductuales por deprivación Visuo-kinestésica en ratas. *Rev. Psicol.* **1993**, *4*, 53–54. [CrossRef]
22. Woolf, C.J.; Ma, Q. Nociceptors—Nociceptor stimulus detectors. *Neuron* **2007**, *55*, 353–364. [CrossRef] [PubMed]
23. Dubin, A.E.; Patapoutian, A.J. Nociceptors: The sensors of the pain pathway. *Clin. Invest.* **2010**, *120*, 3760–3772. [CrossRef] [PubMed]
24. Kim, Y.; Kwon, Y.J.; Kwon, D.E.; Yoon, K.J.; Yoon, J.H.; Yoo, S.; Kim, H.J.; Park, T.H.; Han, J.W.; Kim, K.M.; et al. Nociceptive memristor. *Adv. Mater.* **2018**, *30*, 1704320. [CrossRef] [PubMed]
25. Liu, L.; Zhao, J.; Cao, G.; Zheng, S.; Yan, X. A Memristor-Based Silicon Carbide for Artificial Nociceptor and Neuromorphic Computing. *Adv. Mater. Technol.* **2020**, *6*, 2100373. [CrossRef]
26. John, R.A.; Yantara, N.; Ng, S.E.; Patdillah, M.I.B.; Kulkarni, M.R.; Jamaludin, N.F.; Basu, J.; Ankit, S.; Mahaisalkar, G.; Basu, A.; et al. Diffusive and drift halide perovskite memristive barriosters as nociceptive and synaptic emulators for neuromorphic computing. *Adv. Mater.* **2021**, *33*, 2007851. [CrossRef]
27. Kumar, M.; Kim, H.S.; Kim, J. A Highly Transparent Artificial Photonic Nociceptor. *Adv. Mater.* **2018**, *10*, 34370–34376. [CrossRef] [PubMed]
28. Ryu, H.; Kim, S. Implementation of a reservoir computing system using the short-term effects of Pt/HfO2/TaOx/TiN memristors with self-rectification. *Chaos Solitons Fractals* **2021**, *150*, 111223. [CrossRef]
29. Ryu, H.; Kim, S. Self-Rectifying Resistive Switching and Short-Term Memory Characteristics in Pt/HfO$_2$/TaO$_x$/TiN Artificial Synaptic Device. *Nanomaterials* 2020, 10, 2159. [CrossRef] [PubMed]

30. Mendell, L.M. Computational functions of neurons and circuits signaling injury: Relationship to pain behavior. *Proc. Natl. Acad. Sci. USA* 2011, 108, 15596–15601. [CrossRef] [PubMed]

31. Zhang, W.; Kong, J.Z.; Cao, Z.Y.; Li, A.D.; Wang, L.G.; Wang, L.; Zhu, X.; Li, Y.; Cao, Q.; Wu, D. Bipolar resistive switching characteristics of HfO$_2$/TiO$_2$/HfO$_2$ trilayer-structure RRAM devices on Pt and TiN-coated substrates fabricated by atomic layer deposition. *Nanoscale Res. Lett.* 2017, 12, 393. [CrossRef]

32. Yun, M.J.; Lee, D.; Kim, S.; Wenger, C.; Kim, H.D. A nonlinear resistive switching behaviors of Ni/HfO$_2$/TiN memory structures for self-rectifying resistive switching memory. *Mater. Charact.* 2021, 182, 111578. [CrossRef]

33. Yong, Z.; Persson, K.-M.; Ram, M.S.; D’Acunto, G.; Liu, Y.; Benter, S.; Pan, J.; Li, Z.; Borg, M.; Mikkelsen, A.; et al. Tuning oxygen vacancies and resistive switching properties in ultra-thin HfO$_2$ RRAM via TiN bottom electrode and interface engineering. *Appl. Surf. Sci.* 2021, 551, 149386. [CrossRef]