Common Mode Noise Reduction for an LLC Resonant Converter by Using Passive Noise Cancellation

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Abstract

This paper describes the application of a passive noise cancellation method to a prototype inductor-inductor-capacitor (LLC) resonant converter by placing a compensation winding in a transformer to reduce common mode noise. The connection method for the compensation winding is investigated. A circuit analysis is implemented for the displacement currents between the primary and secondary windings in the transformer. The analyzed displacement currents are verified by performing a circuit simulation and a proper compensation winding connection that reduces common mode noise is found. The measurement results show that common mode noise is reduced effectively up to 20 dB in the 1 to 7 MHz frequency region for the prototype LLC resonant converter by using the proposed passive noise cancellation method.

Key Words: Common Mode Noise, Electromagnetic Compatibility, LLC Resonant Converter, Passive Noise Cancellation.

1. INTRODUCTION

Switch mode power supplies (SMPS) that use converters are commonly used in today’s electronic products and industrial appliances. However, these converters are controlled by pulse width modulation (PWM), which means that electrical noise is generated due to rising and falling transients, and a common mode (CM) noise path is formed by the parasitic couplings in the circuit board. CM noise degrades system performance and may violate the regulations for conducted emissions.

A passive noise cancellation method has been proposed to reduce CM noise [1–3]. This method places a compensation winding in the transformer, thereby effectively reducing CM noise for buck, boost, buck-boost, and flyback converters.

The passive cancellation method can be applied to an inductor-inductor-capacitor (LLC) resonant converter as well. However, a direct application of the compensation winding used in other converters is not available, since the winding method in the LLC resonant converter is designed to obtain a large leakage inductance [4]. Therefore, a better approach needs to be considered when applying the passive noise cancellation method to an LLC resonant converter.

In this paper, we analyze the passive noise cancellation method for application to the LLC resonant converter. For the design of the compensation winding, we investigate the displacement currents flowing through the winding-to-winding capacitances in the transformer. By using circuit analysis...
and simulating the displacement currents, we propose a feasible connection method that can reduce CM noise. A prototype half-bridge LLC resonant converter is designed by applying the proposed connection method. The observed measurement results confirm that the passive cancellation method can effectively reduce CM noise for the LLC resonant converter.

II. METHODOLOGY

The circuit diagram that includes a compensation winding to reduce CM noise for a single-output half-bridge LLC resonant converter is shown in Fig. 1. The most dominant CM noise path is through the primary-to-secondary winding capacitance \( (C_{ps}) \) in the transformer because the system uses separate grounds. The addition of a compensation winding that has the same amount of parasitic capacitance \( C_{cs} \) as \( C_{ps} \) can cancel the CM noise because the compensation winding generates the same magnitude and anti-polarity parasitic current against the CM noise current.

In this paper, we apply the compensation winding to the transformer shown in Fig. 2, which can be used for the dual-output half-bridge LLC resonant converter. The transformer is for dual-output, so windings A and B constitute one of the outputs, and the other windings C and D are for the other output. Normally, the primary and the secondary windings overlap in the transformer to minimize leakage inductance. However, in the transformer shown in the figure, the primary and secondary windings are separated to obtain a large and controllable value of leakage inductance because leakage inductance is used as the resonant-tank series inductance [4]. By overlapping with the primary winding through other pins, the compensation winding can be connected to the transformer. Therefore, the method to match the parasitic capacitances \( (C_{cs} \text{ and } C_{ps}) \) should differ from the method used in conventional transformers because of the different winding structure.

Cross sections of the upper sides of the transformers with compensation windings are described in Fig. 3. The traditional overlapped winding transformer shown in Fig. 3(a) requires the connection of an additional capacitor to the compensation winding [1, 2], or adjustment of the compensation winding turn number [3], because \( C_{cs} \) has a different value from \( C_{ps} \) due to their different distances from the secondary winding. However, in the transformer for the LLC resonant converter, \( C_{cs} \) can be set at almost the same value as \( C_{ps} \) as long as the compensation winding has the same number of turns and the layer arrangement with the primary winding is as shown in Fig. 3(b). As \( C_{cs} \) can be

![Fig. 1. A half-bridge inductor-inductor-capacitor (LLC) resonant converter with a compensation winding.](image1)

![Fig. 2. A transformer for a dual-output half-bridge inductor-inductor-capacitor (LLC) resonant converter.](image2)

![Fig. 3. (a) Parasitic capacitances of overlapped windings in a conventional transformer. (b) Parasitic capacitances of an inductor-inductor-capacitor (LLC) resonant converter transformer.](image3)
Fig. 4. Displacement currents between the primary and secondary windings, and noise voltages along the windings.

adjusted by the winding method only, it does not need any additional capacitor. That is, the end of the compensation winding remains open.

The compensation winding is added as an open circuit; therefore, it does not affect normal converter operation. This winding only produces a path for high frequency harmonics, which are generated by the rising and falling transients in the switches through the parasitic capacitance between the compensation winding and the secondary winding.

III. DISPLACEMENT OF CURRENT AND NOISE VOLTAGE

A description of the displacement currents in the transformer is shown in Fig. 4, where the node connected directly to the switches is named ‘noisy node’ and the other side is named ‘quiet node.’ The induced noise voltages along the primary and the secondary windings are indicated in the figure. We can see that the highest noise voltage is induced on the noisy node, and the noise voltage has the lowest value on the quiet node.

The compensation winding can be connected to the pri-
mary winding in four ways, as shown in Fig. 5. The windings are constituted for a half-bridge and single output type. The single-output analysis can be applied to the dual-output case by a simple extension that includes additional windings in parallel. The connection methods are classified according to the connected node and the polarity of the compensation winding. With the induced noise voltage on all nodes, including the secondary winding, the displacement currents flow from the noisy and the quiet nodes to the earth ground. For the quiet node and the anti-polarity connection type, which are shown in Fig. 5(a), the displacement currents can be calculated as follows:

\[ i_{ps} = C_p \left( \frac{\Delta V}{dt} - \frac{\Delta V'}{dt} + \frac{\Delta V}{dt} - \frac{\Delta V'}{dt} \right) = C_p \left( -2 \frac{\Delta V'}{dt} \right) \]  

(1)

\[ i_{pp} = C_p \left( \frac{\Delta V'}{dt} + \frac{\Delta V'}{dt} \right) = C_p \left( 2 \frac{\Delta V'}{dt} \right) \]  

(2)

This analysis includes two assumptions. First, the parasitic capacitances have almost the same value \( C_p \approx C_s \). Second, all of the CM noise passes through the transformer because the most dominant CM noise path in the circuit is through the winding-to-winding capacitances of the transformer. Therefore, we can assume that the noise voltage \( V_0 \) is zero.

Similarly, the displacement currents for the other connection types can be calculated. The displacement currents for the quiet node and same-polarity connection shown in Fig. 5(b) are:

\[ i_{ps} = C_p \left( \frac{\Delta V}{dt} - \frac{\Delta V'}{dt} + \frac{\Delta V}{dt} + \frac{\Delta V'}{dt} \right) = C_p \left( 4 \frac{\Delta V}{dt} - 2 \frac{\Delta V'}{dt} \right) \]  

(3)

\[ i_{pp} = C_p \left( \frac{\Delta V'}{dt} + \frac{\Delta V'}{dt} \right) = C_p \left( 2 \frac{\Delta V'}{dt} \right) \]  

(4)

The displacement currents for the noisy node and anti-polarity connection shown in Fig. 5(c) are:

\[ i_{ps} = C_p \left( \frac{\Delta V}{dt} - \frac{\Delta V'}{dt} + \frac{\Delta V}{dt} + \frac{\Delta V'}{dt} \right) = C_p \left( 4 \frac{\Delta V}{dt} + 2 \frac{\Delta V'}{dt} \right) \]  

(5)

\[ i_{pp} = C_p \left( \frac{\Delta V'}{dt} + \frac{\Delta V'}{dt} + \frac{\Delta V'}{dt} + \frac{\Delta V'}{dt} \right) = C_p \left( 4 \frac{\Delta V'}{dt} + 2 \frac{\Delta V'}{dt} \right) \]  

(6)

The displacement currents for the noisy node and same-polarity connection shown in Fig. 5(d) are:

\[ i_{ps} = C_p \left( \frac{\Delta V}{dt} - \frac{\Delta V'}{dt} + \frac{\Delta V}{dt} + \frac{\Delta V'}{dt} \right) = C_p \left( 4 \frac{\Delta V}{dt} - 2 \frac{\Delta V'}{dt} \right) \]  

(7)

\[ i_{pp} = C_p \left( \frac{\Delta V'}{dt} - \frac{\Delta V'}{dt} \right) = C_p \left( 2 \frac{\Delta V'}{dt} \right) \]  

(8)

The displacement currents along the bobbin length for each connection type are described in Fig. 5. The total CM noise current should be equal to the integration of the displacement currents along the bobbin length. In the case of the quiet node and anti-polarity connection, which are shown in Fig. 5(a), the distribution of the displacement currents between the quiet node and the noisy node is symmetrical along the \( i_{ps} \)-axis. Therefore, we can expect that only the quiet node and anti-polarity connection method will cancel the total CM noise current because the displacement currents produced by the compensation winding have the same magnitude and different polarity values against the displacement currents of the primary winding on the end nodes.

IV. SIMULATION RESULTS

The simulation results of the displacement currents on the quiet and noisy nodes for each connection type are shown in Fig. 6. When we observe the magnitude and polarity of the simulated displacement currents, we find that the analyzed displacement currents in the previous section are in good agreement with the results of the circuit simulation.

The total CM noise current for the LLC resonant converter can be calculated by adding all the displacement currents shown in Fig. 6; the results are shown in Fig. 7. In addition, the total CM noise currents for the converter with the compensation winding are compared to those of the original converters, which do not have the compensation winding. As shown in Fig. 7(a), the total CM noise can be reduced effectively only by the method of the quiet node and anti-polarity connection for the compensation winding. Otherwise, the total CM noise is increased. This corresponds with our expectations from the previous analysis.

Fig. 8 shows the CM noise current spectrum for the transient simulation results of Fig. 7(a). We can see that the CM noise current is decreased by almost 50 dB in the MHz frequency region. The low frequency region shows a difference of about 7 dB because the steady-state value of the displacement current of the with-compensation winding structure is increased by the additional capacitance between the compensation winding and the secondary winding. The simulation result verifies that the proposed passive noise cancellation method can effectively reduce CM noise.

V. MEASUREMENT RESULTS

A prototype circuit was designed for consumer electronic applications and measured to observe the effects of the proposed method in real applications.
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Figs. 9 and 10 show the measurement setup and the circuit diagram with measuring points. An isolating transformer was used to achieve a floating ground because the AC power input should be protected from possible CM and DM noise. Power can then be supplied to all the devices by connecting them to the isolating transformer. The prototype DC-DC LLC resonant converter with two transformers converts 400 V input to 24–6.5 A and 12–3.5 A dual-output, and the load current is controlled by the electronic load. The 3.5 V DC power supply is for the converter’s on-off switch. We can measure and extract the returned noise by using the line impedance stabilization network (LISN), as shown in Fig. 10. The CM and DM noises that are extracted by the LISN can be separated through the EMI analyzer. We can then observe the CM noise with the spectrum analyzer.

Fig. 11 shows the designed prototype LLC resonant converter. The input and output lines are indicated in the figure, and the lines are connected to other devices as shown in Figs. 9 and 10. The quiet node and anti-polarity method are used in the transformer to connect the compensation winding to the primary winding as shown in the circuit diagram in Fig. 10.

The measured CM noise voltage spectrums are shown in Fig. 12. Compared to the original converter, employing the compensation winding reduces CM noise by 10–20 dB in the frequency range of 1–7 MHz. Since the measurement result is for the voltage spectrum, it can be related to the simulation’s current spectrum in Fig. 8 by the impedance of the CM noise path. The measurement result shows smaller attenuation than in the simulation, because there are other contributions to the total conducted emission that cannot be eliminated by the proposed method. For example, emission through the heat sink and coupling between the line patterns in the converter circuit board can be directly conducted to the supply input without passing through the transformer. The effects of the additional noise become more dominant,
especially in the high frequency region, and the proposed method is no longer effective. Nevertheless, both the simulated and measured spectrum results show a correlation indicating that CM noise can be effectively reduced from around $10^4$ to $10^8$ dB.

Fig. 7. Simulation results of the total common mode (CM) noise currents after adding the compensation winding. (a) Quiet node and anti-polarity connection. (b) Quiet node and same-polarity connection. (c) Noisy node and anti-polarity connection. (d) Noisy node and same-polarity connection.

Fig. 8. Comparison of the simulated common mode (CM) noise current spectrums.

Fig. 9. Measurement setup to observe the common mode (CM) noise. LISN= line impedance stabilization network, LLC= inductor-inductor-capacitor.
Fig. 10. Circuit diagram and measuring points.

Fig. 11. The designed prototype dual-output half-bridge inductor-inductor-capacitor (LLC) resonant converter.

Fig. 12. Measurement results of common mode (CM) noise voltage spectrums.

the MHz frequency region when a significant amount of CM noise occurs in the converter. The measurement result indicates that the proposed passive cancellation approach is effective for reducing the CM noise of the prototype LLC converters in the frequency range of 1–7 MHz.

VI. CONCLUSION

In this paper, we have examined the passive cancellation method for an LLC resonant converter. To match the parasitic capacitances, the compensation winding should have the same number of turns and a layer arrangement the same as that of the primary winding. Analysis of the displacement current confirmed that the quiet node and anti-polarity connection method using compensation winding is the only effective connection method for reducing CM noise. Use of a properly connected compensation winding reduced the CM noise by up to 20 dB in our frequency range of interest.

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REFERENCES

[1] D. Cochrane, D. Y. Chen, and D. Boroyevic, "Passive cancellation of common-mode noise in power electronic circuits," IEEE Transactions on Power Electronics, vol. 18, no. 3, pp. 756–763, 2003.

[2] K. S. Babu, M. Borage, S. Tiwari, S. Kotaiah, & R. Saxena, "Analysis and experimentation with passive cancellation of common-mode noise in buck converter," in Proceedings of the 9th International Conference on Electromagnetic Interference and Compatibility (INCEMIC), Bangalore, India, 2006, pp. 172–180.

[3] Y. P. Chan, B. M. H. Pong, N. K. Poon, and J. C. P. Liu, "Common-mode noise cancellation in switching-mode power supplies using an equipotential transformer modeling technique," IEEE Transactions on Electromagnetic Compatibility, vol. 54, no. 3, pp. 594–602, 2012.

[4] S. De Simone, C. Adragna, and C. Spini, "Design guideline for magnetic integration in LLC resonant converters," in Proceedings of International Symposium on Power Electronics, Electrical Drives, Automation and Motion (SPEEDAM 2008), Ischia, Italy, 2008, pp. 950–957.
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