Low Cost Design and Fabrication of Microstrip Stepped-Impedance Low-Pass Filter by using FR-4 substrate

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Abstract

A series of process from low cost design to fabrication of a microstrip line stepped-impedance low-pass filter is examined. Regarding the decrease of the cutoff frequency that occurs in the design theory, it is found that the problem is solved by the transmission line length adjustment by referring to the commercially available microwave experiment kit using a low loss and high cost Teflon substrate. Then the same design method is applied to a low cost FR-4 substrate. It is possible to obtain the similar LPF characteristics as a commercial microwave experiment kit by selecting a characteristic impedance ratio of the stepped-impedance line appropriately combined with the $\lambda/4$ transverse resonance technique in cutoff region. It is confirmed that the frequency characteristics of the manufactured sample agree well with the design value by the circuit simulator and the EM simulation up to 6 GHz. the manufacturing cost is reduced to about one tenth.

Keywords: microwave circuit, microwave education, CAD.

1. Introduction

The combination of theory, simulation, and experiment is effective for deepening all engineering disciplines, and microwave engineering is no exception. Therefore, confirmation and verification by experiments are finally required. There are many useful documents for mastering the theory of microwave engineering with the spread of the Internet, world-standard books have become readily available on an individual level[1,2]. In addition, expensive electromagnetic simulators that can be used at low cost for academics are becoming popular, and open source electromagnetic simulators have also been introduced. This trend is expected to accelerate further in the future. On the other hand for experiments, a waveguide experiment kit[3] is well known as a microwave standard experiment kit, and recently experimental kits based on the microstrip line (MSL) are also available[4-6]. However, the cost of introducing microwave experimental kit are still high, and in order to spread as an individual level, it is essential that the cost is lower than the performance.

For example, in the design of a stepped-impedance low-pass filter (SI-LPF) which is one of the most essential MSL circuit, it is known that there is freedom in how to determine the characteristic impedance of low impedance line and the high impedance line. The larger their ratio, the better the agreement with the design theory. However, in practice the constraints determined by the dielectric constant and the thickness of the substrate cannot be ignored, and the freedom of choice is not very high except for high cost substrate and advanced manufacturing techniques or higher order filter synthesis techniques[7-9]. Furthermore, it is known that the actual cut-off frequency is lower than the design value, the specific adjustment method is not described in any literature. And no attempt has been made to improve properties using low cost substrates. So in this study, we examined the case where a low loss and high cost Teflon substrate is substituted with a low cost FR-4 substrate with reference to a commercially available microwave standard experiment kit. Good LPF characteristics are obtained by appropriately selecting the impedance ratio of the stepped-impedance (SI) line with use of the cutoff frequency adjustment. This paper is organized as follows. Section 2 describes the design method including line impedance determination method, single SI element characteristics, and cutoff frequency adjustment method. Section 3 explains the fabrication and measurement method. Section 4 discusses the measurement results and finally Section 5 summarizes the results.
2. Design Method

2.1 Design Theory

Microstrip L and C elements can be expressed by gap / chip capacitors, loop / spiral inductors inserted in series\(^{(10)}\), open-ended or short-circuited resonators, and H-plane discontinuities that inserts short transmission lines with different line widths in series. In particular, the method using the discontinuous step of the transmission line is called a stepped impedance (SI) element and is similar to the inductive or capacitive windows known as waveguide \(L\) and \(C\) elements\(^{(11)}\). The design of the desired SI element is a little cumbersome, but it is an effective technique for reducing the overall circuit size compared to the method using a resonator. Following is the principle of the SI element design. The transmission line having the length \(l\) shown in Fig. 1(a) can be represented by the T-type equivalent circuit shown in Fig. 1(b) using conversion of the \(Z\) parameters and \(ABCD\) parameters. Here, \(Z_1, Z_2,\) and \(Z_3\) are given by Eq. (1) and (2)\(^{(12)}\).

\[
Z_1 = Z_2 = jZ_0 \tan \frac{\beta l}{2},
\]

\[
Z_3 = -jZ_0 \frac{1}{\sin \beta l},
\]

If the transmission line physical length \(l\) is short compared to the wavelength and the electrical length \(\beta l < \pi/2\), \(Z_1\) and \(Z_2\) in Eq. (1) are both inductive, then Eq. (3) is obtained by putting \(Z_1 = Z_2 = jX/2\).

\[
X = Z_1 \beta l,
\]

On the other hand, when \(Z_0\) is sufficiently small, that is, when \(Z_0 = Z_{\text{hi}}\), Eq. (4) can be approximated by Eq. (6), resulting in the parallel element of \(C\) as shown in Fig. 1(e).

\[
B = Y_1 \beta l = \frac{\beta l}{Z_1},
\]

The electrical length is given by Eq. (7) and (8).

\[
\beta l = \frac{R_0 L_k}{Z_{\text{hi}}},
\]

\[
\beta l = \frac{Z_{\text{hi}} C_k}{R_0},
\]

Where, \(L_k\) and \(C_k\) represent the low-pass prototype value \(g_0\), \(R_0\) is the filter impedance, and \(\omega_c\) is the cutoff angular frequency. If frequency scaling rule is applied, then the \(L\) and \(C\) element values \(L'\) and \(C'\) are given by Eq. (9) and (10).

\[
L' = \frac{R_0 L_k}{\omega_c},
\]

\[
C' = \frac{C_k}{R_0 \omega_c}.
\]
Assuming that the design specifications are the same as in the example, the cutoff frequency is $f_c = 2.5 \text{ GHz}$, the insertion loss is over 20 dB at 4 GHz, and the Butterworth filter of order $N = 6$ are selected. Then, the LPF consisting of LC circuit is found as shown in Fig. 2. In this study, the circuit simulator LTspice is used to derive the transmission characteristics.

2.2 Line impedance selection

Fig. 3 shows the variation of the microstrip line width $W$ with respect to the characteristic impedance $Z_0$. Four types of dielectric constant $\varepsilon_r$ and substrate thickness $d$ are selected from available dielectric substrates\(^6\)(12-13). From the point of view of design and manufacturing, the line width of high impedance $Z_H$ should be as large as possible because of the easy manufacturing, and the line width of low impedance $Z_L$ should be as small as possible in order to suppress the $\lambda/4$ resonance transverse to the transmission direction. Although these requirements are contrary to the nature of the MSL characteristic impedance, it is confirmed that the smaller dielectric constant $\varepsilon_r$ and the substrate thickness $d$ tend to be easier to design from Fig. 2. It is obvious that a thick FR-4 substrate is disadvantageous compared to a thin Teflon substrate. Because our motivation is to improve the low pass characteristics at low cost, a thick and high loss FR-4 substrate is used.

To verify the design theory, some $Z_L$ and $Z_H$ are examined by using the EM simulator HFSS v11.1 with the reference\(^{(12)}\). The substrate parameters are $\varepsilon_r = 4.2$, $\tan \delta = 0.02$, $d = 1.58 \text{ mm}$, $t = 30 \mu \text{m}$. The conductor is assumed as a perfect conductor. Fig. 4 shows the LPF characteristics for changes in $Z_H$ where $Z_L$ is fixed at 20 Ω. The lumped constant design theory by using the circuit simulator Fig. 3. Line width $W$ [mm] with respect to the characteristic impedance $Z_0$ [Ω].

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Fig. 4. LPF characteristics for changes in $Z_H$. Where low impedance line is fixed at $Z_L = 20 \text{Ω}$.

Fig. 5. LPF characteristics for changes in $Z_L$. Where high impedance line is fixed at $Z_H = 120 \text{Ω}$.

Fig. 6. Circuit patterns. $Z_0 = 50 \text{Ω}$ line with a length of 20 mm is connected to the both input and output port.
LTspice is plotted by a broken line. It is observed that increasing the $Z_H$ improves the attenuation level, but if it is too large, the line width becomes thinner. It leads to manufacturing difficulties. On the other hand, Fig. 5 shows the LPF characteristics for changes in $Z_L$ where $Z_H$ is fixed at 120 $\Omega$. The smaller the $Z_L$, the sharper the attenuation skirt characteristics, but if it is too small, it deviates from the lumped constant design theory. In particular, a large change appears in the skirt characteristics between $Z_L=20$ $\Omega$ and $Z_L=10$ $\Omega$, and it changes from low-pass characteristics to band-stop characteristics. This is considered to be caused by that $C$ sections start to work as $\lambda/4$ open stubs. From these examinations, it is found that $Z_H$ works to improve the attenuation level, and $Z_L$ works to determine the slope of the attenuation curve. The circuit patterns in Fig. 4 and 5 are as shown in Fig. 6. If the $\lambda/4$ transverse resonance generated in the line $Z_L$ is used as the cutoff region of the LPF, the high frequency cutoff characteristics can be improved. Therefore, in this study, we adopt $Z_L = 15$ $\Omega$, which resonates transversally around 7 GHz, as the low impedance line. Table 1 shows the design parameters used for comparison. Design parameters in reference (12) and this paper is similar, but the difference of the skirt characteristic is remarkable as shown in later.

2.3 Characteristics of single SI element

The reason why the selected values of $Z_L$ and $Z_H$ deviate from the LPF characteristics of the design theory is examined using an EM simulator, focusing on the characteristics of the $L$ and $C$ single-step elements. As
shown in Fig. 2, the lumped element values required for the design are 0.5-2.5 pF in the C section and about 2-6 nH in the L section. Fig. 7 and 8 show the frequency characteristics of a single ZL section. Two different section lengths, \( l = 10 \) mm and \( l = 5 \) mm, are examined. For comparison, the \( |S_{21}| \) and \( |S_{11}| \) characteristics of the theoretical lumped element C parallel circuit are shown.
The theoretical reflection and transmission characteristics of a shunt C element can be calculated by equations (11) and (12). Where \( Z_0 = \frac{1}{Y_0} \) is the characteristic impedance of the input/output lines.

\[
|S_{11}| = \frac{\omega C}{\sqrt{4Y_0^2 + (\omega C)^2}}, \quad (11)
\]

\[
|S_{21}| = \frac{2Y_0}{\sqrt{4Y_0^2 + (\omega C)^2}}, \quad (12)
\]

Similarly, the theoretical reflection and transmission characteristics of a series L element are given by equations (13) and (14).

\[
|S_{11}| = \frac{\omega L}{\sqrt{4Z_0^2 + (\omega L)^2}}, \quad (13)
\]

\[
|S_{21}| = \frac{2Z_0}{\sqrt{4Z_0^2 + (\omega L)^2}}, \quad (14)
\]

When \( l = 10 \) mm in Fig. 7, C value larger than 2.5 pF can be expressed on the low frequency, but the high frequency characteristics deteriorate due to the influence of the \( \lambda/2 \) passband resonance point near 7 GHz. However, the high-frequency characteristics are improved at \( l = 5 \) mm in Fig. 8, and it is a good approximation except for the \( \lambda/4 \) resonance point that operates as a stub with \( Z_L = 10 \) Ω and \( Z_L = 15 \) Ω. However, the \( \lambda/4 \) resonance near 8 GHz with \( Z_L = 15 \) can be used to reinforce the LPF stopband characteristics.

On the other hand, in the L section, the required inductances about 2-6 nH are obtained on the low frequency in Fig. 9, but on the high frequency, the frequency characteristics deteriorate due to the influence of the \( \lambda/2 \) passband resonance near 9 GHz. This is improved in the case of \( l = 5 \) mm in Fig. 10, but the required inductance value is not obtained. In both cases, if the section length \( l \) is increased, larger L and C element values can be obtained. However, since the passband resonance point appears, the spurious characteristics in the high frequency deteriorate. In summary, the desired L can be obtained by increasing \( l \), but the spurious response after 5 GHz deteriorates. If \( l \) is shortened, the desired L cannot be obtained, but the spurious response is improved. Since these are in a trade-off relationship, it is necessary to find and design a compromise.

### 2.4 Cutoff Frequency Adjustment

According to the design theory, the cutoff frequency \( f_c \) appears slightly lower than the design value as shown in Fig. 4 and Fig. 5. The fact that the cutoff frequency \( f_c \) is smaller than the designed value means that C or L represented by SI elements appears larger than the designed value. As a physical reason for this phenomenon, the fringing effect of C section and the skin effect of L section are considered. Therefore, it is considered that the method of reducing C is proper for the frequency adjustment, but there is no problem with the method of shortening L if the
purpose is to raise $f_c$. Though several experimental formulas have been introduced for the adjustment method of MSL\(^{(14)}\), only the lengths of the $L$ section are shortened in the commercially available microwave experiment kit\(^{(9)}\). So in this study, the frequency adjustment is conducted similarly by shortening only $L$ sections. Fig. 11 shows the circuit pattern before and after the frequency adjustment. The upper side shows the circuit pattern to which the design theory\(^{(12)}\) is applied, and the lower side shows the pattern in which the $L$ section is shortened to adjust the cutoff frequency. Since only the $L$ section is reduced to about 68% in the commercially available microwave experiment kit\(^{(6)}\), the FR-4 self-made model is reduced to 70% only in the $L$ section. The measured cutoff frequency before adjustment is 2.1 GHz which is 0.84 times of the design frequency 2.5 GHz. If this is applied to the cutoff frequency formula of Eq. (15), the cutoff frequency can be increased to 1.2 times, which is the reciprocal of 0.84, if $L$ section is shortened by 0.7 times. So, it can be seen that this adjustment rate is theoretically valid.

$$f_c = \frac{1}{2\pi\sqrt{LC}}, \quad (15)$$

Fig. 12 shows the frequency characteristics before and after $L$ adjustment by the EM simulator. The circuit patterns are shown in Fig. 11. Thick solid lines indicate $|S_{21}|$ and thin dashed lines indicate $|S_{11}|$, and the five types of data show EM simulation values before frequency adjustment and after adjustment, lumped element circuit model design values. It can be seen that the cut-off frequency is improved by adjusting the $L$ section. It is also found that the cutoff frequency can be improved in the same way using the help of the EM simulator even if only $C$ is reduced to 60% or both $L$ and $C$ are reduced to 90% and 70%, respectively. Adjusting $C$ is not easy due to the fringing effect. Adjustment requires an EM simulator.

3. Fabrication and measurement method

The image of a measurement sample is shown in Fig. 13 lower. And an image of the commercial experimental kit MW-1000 is also shown upper for comparison. Physical dimensions are as shown in Table 2 and Table 3. In this table, $N$ is the filter order, $g_k$ is the filter prototype value, $W$ is the line width, $l$ is the line length before adjustment, and $l'$ means after adjustment. Pattern fabrication is performed by the laboratory self-etching system\(^{(16)}\) using a glass fiber base epoxy resin copper clad laminate NZ-G30KR made by Sunhayato\(^{(13)}\). The measuring instrument used is a vector network analyzer (VNA) Agilent E8362C. Coaxial SMA connectors RS 526-5785 are attached to both ends of the substrate for connection to the coaxial calibration surface. The tip of the 3.5 mm coaxial connector is set as the calibration plane, then the measurement sample is inserted into the calibration surface, and $S_{21}$ and $S_{11}$ are measured in the range of 0.01-10 GHz.

4. Results and Discussion

The measurement results are shown in Fig. 14. Thick solid lines indicate $|S_{21}|$ and thin dashed lines indicate $|S_{11}|$, and the five types of data show measured value, EM simulation, lumped element circuit model design values, EM simulation of FR-4 in the case of $Z_L=20 \Omega$ and $Z_H=120 \Omega$ used for comparison, and measured value of Teflon

| $N$ | $g_k$ | $Z_L, Z_H[\Omega]$ | $\beta$ [deg] | $W$ [mm] | $l$ [mm] | $l'$ [mm] |
|-----|-------|-------------------|--------------|--------|--------|--------|
| 1   | 0.5176| 10                | 5.9          | 16.7   | 1.29   | 1.31   |
| 2   | 1.4142| 100               | 40.5         | 0.63   | 9.70   | 6.58   |
| 3   | 1.9318| 10                | 22.1         | 16.7   | 4.81   | 4.90   |
| 4   | 1.9318| 100               | 55.3         | 0.63   | 13.2   | 9.02   |
| 5   | 1.4142| 10                | 16.2         | 16.7   | 3.52   | 3.59   |
| 6   | 0.5176| 100               | 14.8         | 0.63   | 3.55   | 2.42   |

Table 2. Physical dimensions of MW-1000.

| $N$ | $g_k$ | $Z_L, Z_H[\Omega]$ | $\beta$ [deg] | $W$ [mm] | $l$ [mm] | $l'$ [mm] |
|-----|-------|-------------------|--------------|--------|--------|--------|
| 1   | 0.5176| 15                | 8.9          | 15.7   | 1.51   | 1.51   |
| 2   | 1.4142| 120               | 33.8         | 0.41   | 6.55   | 4.59   |
| 3   | 1.9318| 15                | 33.2         | 15.7   | 5.64   | 5.64   |
| 4   | 1.9318| 120               | 46.1         | 0.41   | 8.95   | 6.27   |
| 5   | 1.4142| 15                | 24.3         | 15.7   | 4.13   | 4.13   |
| 6   | 0.5176| 120               | 12.4         | 0.41   | 2.40   | 1.68   |

Table 3. Physical dimensions of FR-4 sample.
substrate MW-1000 used for comparison. When two types of EM simulation are compared, sufficient attenuation characteristics are not obtained in the case of $Z_L = 20 \Omega$. However, in this study where $Z_L = 15 \Omega$, it can be seen that attenuation exceeding the measured reference value of Teflon substrate is obtained by the $\lambda/4$ transverse resonance technique near 7 GHz. In addition, the measured value in this study agree well with the EM simulation up to around 6 GHz, and the design specifications are fully satisfied up to 7 GHz. The difference between the measured value and the simulation above 6 GHz is probably due to the skin effect which is not calculated accurately in the simulation because of the coarse mesh in FEM algorithm. Although, the high frequency spurious response is worse than the commercial reference model, the effectiveness of the presented design method using the $\lambda/4$ transverse resonance technique at cutoff region and the validity of the frequency adjustment method are confirmed.

5. Conclusions

A series of process from low cost design to fabrication of a SI-LPF which is one of the most essential MSL circuit is investigated by using a low cost FR-4 substrate instead of a low loss and high cost Teflon substrate. By appropriately selecting the impedance ratio of the SI line with use of the $\lambda/4$ transverse resonance technique at cutoff region, good LPF characteristics are obtained as in the commercial microwave experiment kit using a high cost Teflon substrate. So the manufacturing cost is reduced to about one tenth. Then, frequency adjustment is performed with reference to a commercial microwave experimental kit in addition to the standard design theory, and comparisons are made between the design values by the circuit simulator, the EM simulation and the measurement. It is confirmed that the frequency characteristics of the manufactured sample agree well up to 6 GHz. In the future, we plan to study the manufacturing accuracy of the high impedance line, the influence of the skin effect and the combination with an auxiliary circuit to improve the high frequency spurious characteristics.

References

(1) R. E. Collin : “Foundations for Microwave Engineering - 2nd edition”, Wiley-IEEE Press, 2001
(2) R. F. Harrington : “Time-Harmonic Electromagnetic Fields 2nd Edition”, Wiley-IEEE Press, 2001
(3) SPC : “Waveguide microwave experiment”, Available online: www.spc.co.jp/products/microwave (accessed on 17 April 2019)
(4) DreamCatcher Asia : “Complete resources for lecturers” Available online: www.dreamcatcher.asia/cw (accessed on 17 April 2019)
(5) FESTO : “LabVolt Series”, Available online: www.labvolt.com/solutions (accessed on 29 November 2019)
(6) Man & tel Co., Ltd. : “MW-1000”, Available online: www.pid-control.com (accessed on 29 November 2019)
(7) S. Arakelyan, H. Lee, D. Han, A. Babajanyan, G. Berthiau, B. Friedman, K. Lee, “Microwave Heating Visualization for Carbon Fibers Composite Material: Development of Tunable Microstrip Structures”, IEEE Trans. MTT. Vol. 66, No. 2, February 2018.
(8) C. Chen, C. Sung, Y. Su, “A Multi-Stub Lowpass Filter”, IEEE Microwave and Wireless Component Letters, Vol. 25, No. 8, August 2015
(9) A. Casanueva, A. Leon, O. Gonzalez, A. Mediavilla, “A compact microstrip step-impedance low-pass filter (SILPF) using complementary split ring resonators (CSRRs) ”, 2009 IEEE MTT-S International Microwave Workshop on Wireless Sensing, Local Positioning, and RFID
(10) D. M. Pozar : “Microwave Engineering - 3rd edition”, pp.227-228, Wiley, 2005
(11) N. Marcuvitz : “Waveguide handbook”, pp.217-229, IEE, 1993
(12) D. M. Pozar : “Microwave Engineering - 3rd edition”, pp.412-416, Wiley, 2005
(13) Sunhayato : “NZ-G30KR”, Available online:
www.sunhayato.co.jp (accessed on 29 November 2019)

(14) S. W. Wedge, R. Compton, D. Rutledge : “PUFF, Computer Aided Design for Microwave Integrated Circuits”, pp.27-29, 1997

(15) T. H. Lee : “Planar Microwave Engineering”, pp.175-176, Cambridge, 2004

(16) Y. Kusama : “Circuit board etching procedure manual”, Available online: www.kusamalab.org/standard/measure/etching.pdf (accessed on 18 April 2019)