ABSTRACT

In this work, we suggest Kernel Filtering Linear Overparameterization (KFLO), where a linear cascade of filtering layers is used during training to improve network performance in test time. We implement this cascade in a kernel filtering fashion, which prevents the trained architecture from becoming unnecessarily deeper. This also allows using our approach with almost any network architecture and let combining the filtering layers into a single layer in test time. Thus, our approach does not add computational complexity during inference. We demonstrate the advantage of KFLO on various network models and datasets in supervised learning.

Index Terms—linear overparameterization, structural reparameterization, kernel filtering / composition

1. INTRODUCTION

Deep neural networks have shown remarkable capabilities in computer vision and natural language processing. Research on improving the deployed networks’ performance has been done in many fields and different problem frameworks. In this work, we focus on the common case where the deployed model architecture is chosen in advance of training, and resources are abundant during training relative to inference time. These resources can be used to improve performance. Specifically, we focus on using a larger network during training that generates a smaller one for inference.

One may treat this problem setting as a network compression or a knowledge transfer problem. A known strategy for this task is Knowledge Distillation (KD). There, knowledge is transferred from a pre-trained teacher network to a desired student network using signals from the teacher. Interestingly, the student can match or even outperform the teacher’s performance while being notably less complex. KD was first introduced by [1] for model compression and generalized by Hinton et. al. [2]. Various KD extensions exist such as KD in generations [3], ensemble of students teaching each other [4], the teacher assistant [5], and many more.

A different approach suggests using structural reparameterization, term denoted by [6] for transforming one architecture’s parameters to produce the parameters used by another architecture. Specifically, in the context of this paper, a collapsed overparameterization. Common networks are typically overparameterized, yet numerous empirical results show that even deeper and/or wider networks can achieve better performance [7]. Recent works have suggested that this does not always result from stronger computational capabilities of the bigger network compared to the smaller one. The smaller one, which can have enough expressive power to memorize the dataset, is usually harder to train, that is, it is harder for the smaller network to achieve a good generalization as the bigger network; however, with the right training it might [5, 8]. Structural re-parameterization methods can improve performance by adding overparameterization to a desired architecture, while dropping the excess weights after training, so the deployed network at inference time is of the original desired architecture. We focus on linear overparameterization (LO) as most structural re-parameterization relies on linearity.

Linear networks and their convergence with gradient de-

Fig. 1. A brief preview of our method. Layers containing filtering weights are marked with a darker shade. (a) A common vanilla filtering layer. (b) Filtering layer with KFLO: The kernel that filters the input signal is the result of a pointwise convolution applied on a different filtering kernel. The weights outside the input signal’s path can be discarded after training, deploying only the Filtered Kernel. (c) LeNet-5 implemented with KFLO, for 28x28 grayscale images of 10 possible classes. Here too, the weights outside the signal’s main path can be discarded after training, resulting in the original LeNet-5 architecture to be used at inference time.
cent have been studied for a long time. It has been shown that using a deep linear neural network can have advantages over the use of a single linear layer, despite having the same computational power, e.g., by accelerating convergence [9]. This work also notes the vanishing gradient problem that can occur in deep linear networks with near-zero initialization.

A logical next step is to introduce LO to regular non-linear networks by adding redundant (transformable / collapsible) linear layers. ExpandNets [10] used LO by expanding every 2D convolution to multiple consecutive convolutions, keeping the same receptive field size. While being promising on shallow networks, it is less effective and even harmful for deep networks. Also, even for shallow networks, too deep LO can harm performance due to vanishing gradients [10, 9].

**Contribution.** In this paper, we propose a novel framework, Kernel Filtering Linear Overparameterization (KFLO), for improving the performance of a target network. Our approach attempts to better utilize linear overparameterization and handle the vanishing gradient problem, while also being compatible with every type of filtering layer and staying easy to implement. Conceptually redundant linear layers are added to the network in a kernel filtering fashion, which does not make the network deeper from the network’s input perspective. The overparameterized weights can be discarded after being used in training, thus making inference time lightweight. Our method enjoys a much smaller number of calculations needed during training compared to using a straightforward deeper LO. A brief preview of KFLO can be found in Figure 1. Code is available at https://github.com/AmitHenig/KFLO.

**2. METHOD**

**Notations.** An input/signal/feature-map tensor has the shape \([\text{batch} \times \text{channels} \times \text{spatial}]\) (the spatial dimensions of an image for example will be \(\text{Height} \times \text{Width}\)). The kernel of a filtering layer (such as convolution or fully connected) has the shape \([\text{output_channels} \times \text{input_channels} \times \text{spatial}]\). The kernel is regarded to have \(\text{output_channels}\) filters, each of shape \([1 \times \text{input_channels} \times \text{spatial}]\). From this point forward, we will examine a 2D convolutional layer as our filtering layer, though our solution can easily be extended to any spatial dimension (as we will see later), and fully connected layers can be treated as inputs and filters having spatial dimensions sizes of 1. Furthermore, for simplicity, we will omit the bias, as it can be easily added.

**LO.** Consider a linear cascade comprised of two 2D convolutional layers with corresponding kernels/weights \(\{W_l\}_{l=1}^{2}\), where the second layer is a pointwise convolution with a trivial sliding window. This cascade is a LO of some 2D convolutional layer with weights \(W\) and the same sliding window properties as \(W_1\). Meaning, that for the same input, kernel \(W\) gives the same output as the cascade \(\{W_l\}_{l=1}^{2}\):

\[
W' \ast X_1 = W_2 \ast (W_1 \ast X_1) = W_2 \ast X_2 = X_3
\]

\[
X_1 \in \mathbb{R}^{\text{batch} \times \text{ch}_1 \times (H \times W)} \quad ; \quad W_1 \in \mathbb{R}^{\text{ch}_2 \times \text{ch}_1 \times (M \times N)} \\
X_2 \in \mathbb{R}^{\text{batch} \times \text{ch}_2 \times (H' \times W')} \quad ; \quad W_2 \in \mathbb{R}^{\text{ch}_3 \times \text{ch}_2 \times (1 \times 1)} \\
X_3 \in \mathbb{R}^{\text{batch} \times \text{ch}_3 \times (H' \times W')} \quad ; \quad W' \in \mathbb{R}^{\text{ch}_3 \times \text{ch}_1 \times (M \times N)}
\]

Note that neither \(H', W'\) nor the receptive field size are affected by the pointwise convolution, and so are solely dependent on the properties of \(W_1\). This LO equality holds when:

\[
W'(\delta_1, \delta_1, m, n) = \sum_{\delta_2} \mathbb{W}_2(\delta_3, \delta_2, 0, 0) \cdot W_1(\delta_2, \delta_1, m, n) \quad (1)
\]

This can be shown by rearranging the convolution:

\[
X_3(b, \delta_3, h', w') = \sum_{\delta_2} \mathbb{W}_2(\delta_3, \delta_2, 0, 0) \cdot X_2(b, \delta_2, h', w')
\]

where \(X_2(b, \delta_2, h', w')\) is

\[
= \sum_{\delta_1} \sum_{m} \sum_{n} \mathbb{W}_1(\delta_2, \delta_1, m, n) \cdot X_1(b, \delta_1, \mu_{m,n}^{m,n}) \cdot \nu_{m,n}^{m,n}(b, \delta_2, h', w')
\]

and \(\mu, \nu\) are the kernel-to-input spatial mapping functions, defined by the sliding window properties (e.g., the mapping functions for a trivial sliding window are \(\mu = h' + m\) and \(\nu = w' + n\)). Eq. 1 shows that each filter in \(W'\) is a combination of \(\mathbb{W}_1\) filters, weighted by a corresponding filter in \(\mathbb{W}_2\).

**Kernel Filtering.** We introduce our kernel filtering by transforming Eq. 1 to a convolutional representation. Given the nature of the pointwise convolution, kernel filtering for this case may be represented with a 1D convolution; making its implementation independent of the number of spatial dimensions. This is done by convolution with the reshaped kernels. For some spatial location \(t = (M \cdot N) \cdot \delta_1 + N \cdot m + n:\n
\[
\mathbb{W}_1^R \in \mathbb{R}^{1 \times \text{ch}_2 \times (\text{ch}_1 \cdot M \cdot N)} \quad ; \quad \mathbb{W}_1^R(0, \delta_2, t) = \mathbb{W}_1(\delta_2, 1, m, n) \\
\mathbb{W}_2^R \in \mathbb{R}^{\text{ch}_3 \times \text{ch}_2 \times (1)} \quad ; \quad \mathbb{W}_2^R(0, \delta_3, 0, 0) = \mathbb{W}_2(\delta_3, \delta_2, 0, 0) \\
\mathbb{W}_3^R \in \mathbb{R}^{1 \times \text{ch}_3 \times (\text{ch}_1 \cdot M \cdot N)} \quad ; \quad \mathbb{W}_3^R(0, \delta_3, t) = \mathbb{W}_3(\delta_3, \delta_1, m, n) \\
\mathbb{W}^R(0, \delta_3, t) = \sum_{\delta_2} \mathbb{W}_2^R(\delta_3, \delta_2, 0) \cdot \mathbb{W}_1^R(0, \delta_2, t)
\]

We get a 1D pointwise convolution \(\mathbb{W}^R\) for input \(\mathbb{W}^R\) with batch size of 1, resulting in the output \(\mathbb{W}'^R\). Getting \(\mathbb{W}'^R\) from \(\mathbb{W}^R\) is of course done by reshaping.

\[
\mathbb{W}' \ast X_1 = (\mathbb{W}'^R)^R \ast X_1 = (\mathbb{W}_1^R \ast \mathbb{W}_2^R \ast X_1) = X_3
\]

**KFLO.** It can be shown that the above LO and kernel filtering equivalences hold for any number of pointwise kernels used in the linear cascade \(\{W_l \in \mathbb{R}^{\text{ch}_i+1 \times \text{ch}_i \times (1 \times 1)}\}_{i=2}^{B}\), reshaping them in the same manner to 1D pointwise convolutions:

\[
X_{B+1} = W_B \ast (W_{B-1} \ast \ldots \ast (W_2 \ast (W_1 \ast X_1) \ldots)) \quad (2) \\
= (\mathbb{W}_B^R \ast \mathbb{W}_{B-1}^R \ast \ldots \ast (\mathbb{W}_2^R \ast \mathbb{W}_1^R)) \ast X_1 \quad (3) \\
= (\mathbb{W}'^R)^R \ast X_1 = \mathbb{W}' \ast X_1 \quad (4)
\]
For a deployed kernel $W' \in \mathbb{R}^{ch_{out} \times ch_{in} \times (M \times N)}$, our method is implemented during training by creating weights $\{W_i\}_{i=1}^{B}$ and calculating the effective convolution used in the network using Eqs. 3, 4. After training is complete, the LO weights $\{W_i\}_{i=1}^{B}$ are discarded, only keeping $W'$ to be used at inference time (see Figure 1). Applying KFLO on a depthwise convolution layer is done exactly as for conventional convolution, keeping in mind that the number of input channels is $\#input_{channels}$. We define a scalar width multiplier $\rho$, and set all configurable LO widths $\{ch_i\}_{i=2}^{B}$ to $Round(\rho \cdot ch_{out})$. We also denote $B$ as the linear depth multiplier. KFLO for a desired deployed architecture is implemented by defining $B>1$ and $\rho>0$ (mainly choosing values greater than 1 for LO), and applying our method as described above for every filtering layer in the network (example with $B=2$, $\rho=4$ in Figure 1.c).

We present two explanations for KFLO success. First, we know that overparameterization helps train networks in general. Second, from fully linear deep networks we know that this redundant overparameterization can improve convergence. Comparing to a LO with vanilla feature filtering, KFLO does not make the trained architecture deeper, which is a big advantage as a common problem of deep LO/networks is the vanishing gradient which can really harm performance. Also, our kernel filtering gives a great reduction in the number of operations that LO adds. While in feature filtering each LO layer is applied to the full spatial size of the layer’s input tensor, with our kernel filtering the LO layers filter a tensor with spatial size of a convolution layer, which is usually significantly smaller than that of the layer’s input signal.

### 3. RELATION TO OTHER LO WORKS

DO-Conv [11] has a similar approach as ours. They utilize a linear depthwise separable convolutional layer, showing that it can be collapsed to a single convolution kernel. Similarly to us, DO-Conv suggests a kind of kernel filtering (kernel composition), to replace the vanilla feature filtering during training. There are two key differences between our method and DO-Conv. First, the linear convolutional cascade of our method is different than the linear depthwise separable convolution in DO-Conv. Second, while we performs kernel filtering by passing forward $W_i^B$ through the 1D linear cascade, DO-Conv passes the pointwise kernel backwards by filtering it with a transposed depthwise convolution. The work in Orthogonal Over-Parameterized Training [12] uses a tailored LO filtering weights, with further restrictions to enforce an orthogonal transformation on static weights. ACNet [13] suggests Asymmetric Convolution Block (ACB), which can be applied on a square convolution kernel. During training the kernel is replaced with three parallel paths, each with a convolution kernel followed by batch normalization, and their outputs are summed. In two of the paths the kernels are of 1D, one horizontal and the other vertical. This method does not make the network deeper during training in terms of filtering kernels and they show improvement in both small and big networks. Note that the commonly used pair of convolution followed by batch normalization can also act as a LO on its own. Diverse Branch Block (DBB) [14] suggests LO by replacing the convolution kernel with an Inception-like DBB instance during training. ACB (ACNet) can be viewed as a special case of DBB. LO in training was also utilized in [6, 15, 16]. The work in [17] uses LO during training alongside block-level KD on a student network initially obtained by compressing the pre-trained teacher. LO is applied in a vanilla feature filtering fashion by adding a pointwise convolution solely at the end of each defined block and is collapsed/merged after training. Compared to them, our kernel filtering is applied on every filtering layer with further wider LO (width multiplier $\rho>1$).

### 4. EXPERIMENTS

In this section we demonstrate our approach on two popular datasets: CIFAR-10 and CIFAR-100 [18]. We evaluate KFLO and compare it to both the vanilla approach and other relevant training methods. We present experimental results for different architectures and datasets, showing the advantages of our method. We also compare the performance of KFLO with different linear depth multipliers, $B$, and different width multipliers, $\rho$. Finally, we show that KFLO can have a similar effect of transfer learning. Note that with infinite memory and time, many methods can be combined and applied simultaneously, multiple times. However, in the scope of this paper we will only refer to training with no more than one LO method applied at a time.

**Networks.** WRN-D-K will denote a Wide Residual Network [7], with the wide-dropout residual block, of depth $D$ (with a 3 residual groups structure) and widening factor $K$. In addition, we test with Cifar-quick [19] and VGG16 [20] where the two fully connected layers are replaced with global average pooling followed by a fully connected layer that outputs 512 channels, as [13]. For both architectures, batch normalization were added after every convolution layer, again as [13].

**Implementation Details.** When using our training method, KFLO is applied to all the convolutional and fully connected kernels. KFLO with a linear depth multiplier $B$ and width multiplier $\rho$ will be denoted by KFLO $Bx$. We apply a weak weight decay of $10^{-9}$ on the pointwise convolutions in the linear cascades $\{W_i\}_{i=2}^{B}$, and initialize them with identity (dirac). We apply the vanilla training weight decay value on the generated weights $W'$, and no weight decay on the filtered kernels $W_i$, thus not adding a hyper parameter to tune. DO-Conv is implemented with $D_{mul}$ equals to the filter’s spatial size $M \times N$, as in [11]. Exponential moving average of the network weights when trained in a vanilla fashion will be referred to as EMA. The Experiments ran on NVIDIA GeForce RTX 2080 Ti. We followed the experimental settings of [13],
excluding WRN Experiments where we followed [4].

Transfer learning (TL) experiments were conducted on CIFAR-100, with only 40% of the training data available (uniformly sampled from each class). Networks pre-trained in the vanilla fashion on the full CIFAR-10 training set were used for weights initialization. In the classic strategy, a pre-trained network is used as the starting point and it is fine-tuned with a learning rate of an order of magnitude smaller.

With KFLO, more than one pre-trained model can be utilized in weight initialization. Setting the width multiplier $\rho$ to the number of pre-trained networks results in KFLO filtering kernels $\{\mathcal{W}_i\}$ wide enough to hold all relevant pre-trained kernel filters. Before Training starts, we stack the corresponding pre-trained kernels and use them as initialization weights for the relevant filtering kernels. Layers on which KFLO is not applied are initialized with the weights of the first pre-trained network. This with the Identity initialization of KFLO’s LO pointwise convolutions, means that the starting network gives identical outputs to the first pre-trained network.

### 4.1. Results

**Comparison.** We first present results comparing our method to vanilla training, EMA, ACNet [13] and DO-Conv [11]. Results for CIFAR-10 and CIFAR-100 can be found in Tables 1 and 2, respectively. We can see that KFLO achieves the highest accuracy with all featured architectures.

**Ablation study.** We compare the performance of our method with different linear depth multipliers $B$, and different width multipliers $\rho$. We show results for CIFAR-10 in Table 3 and for CIFAR-100 in Table 4. It seems that in most cases, the bigger the width multiplier the better the performance boost is. However, a larger depth multiplier harms performance.

**Transfer learning.** Table 5 shows results for the TL problem. It displays results for both networks trained from scratch, and networks trained with TL - denoted with the TL suffix. We can see the expected performance boost TL gives vanilla training. Yet, it would appear that KFLO does not benefit much from the pre-trained weights, and sometimes it can even harm performance. It seems that the straight forward method to incorporate the pre-trained networks with KFLO is not optimal. Nonetheless, KFLO (without TL) outperforms vanilla TL, and it would seem that it achieves a similar effect although it does not have access to the extra data (CIFAR-10).

### 5. CONCLUSION

This work introduced KFLO, a novel approach to train a neural network in a structural re-parameterization fashion by modeling kernels as the result of linear convolutional kernel filtering. Interestingly, this over-parameterization, which can create a very large redundancy, improves the network training. This stands in line with the recent both practical and theoretical findings that overparameterization improves the generalization of neural networks [21, 22, 23, 24].

We have focused on demonstrating our approach in supervised learning and transfer learning settings, though we believe that our proposed framework has large potential in being applied to other problems such as domain adaptation, semi-supervised learning and incremental learning. Moreover, one may consider in our scheme many other topologies for the kernel filtering block. We hope that this new tool with its different variations will be used to improve many other tasks. Its simplicity allows using and extending it to new setups easily.

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