Analysis of the Effects of Three Various Current Control Methods on BLDC Motors

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Abstract
Brushless direct current motors are very popular motors in a wide application area thanks to their superior features. Brushless motors are the most preferred motors in applications such as domestic, robotics, office machines, medical, military and space applications, unmanned aerial vehicles applications because they meet the special requirements of these applications. Brushless direct current (BLDC) motors have high volume torque ratio, high efficiency, fast dynamic response; they offer linear torque/speed characteristics. Speed control of BLDC motors achieves by implementing pulse width modulation (PWM) technique. Various PWM control methods are put in use for controlling of BLDC motor speed. These PWM methods have some certain advantages and disadvantages. In this study, three current control PWM methods which are mostly used in practices are examined. These are full conduction mode, independent and complementary PWM methods. For this purpose, three BLDC motor pre-drivers have been designed and manufactured and the effects of these three drivers on the BLDC motor have been examined. It has been found that complementary PWM mode has given to BLDC motor fast dynamic response. Full conduction mode has drawn higher dc link current from DC power supply. Independent PWM method performance is very close to complementary PWM method.

Keywords: BLDC Motor, Current Control, Pulse Width Modulation, Speed Control of BLDC Motors.

1. Introduction
Brushless direct current motors have some special advantages over the brushed direct current and induction motors (Pindoriya, R. M. et al., 2014; Batham R. and Singh R. 2017). BLDC motors have a good linear relationship between torque and speed; they don’t need maintenance owing to absence of brushes and commutator, high power

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density and efficiency, fast dynamic response, easy speed control. Thanks to these superior features, BLDC motors have been a good choice in a wide range of applications (Pindoriya, R. M. et al., 2014; Ozgenel M. C., 2017). Although BLDC motors have these superior features, they have also some disadvantages such as they need a driver for running even at fixed speed and initial cost is relative expensive compared to brushed and induction motor for the same power (Sathyan A. et al., 2009; Pindoriya, R. M. et al., 2014; Lengade S.R. and Mahjan D, 2018). In fact, these disadvantages are not indispensable importance, because induction and brushed motors also need drivers in a certain application that requires speed control. Now days, speed control is applied almost every applications due to fact that speed control ensures energy saving, reduces number of mechanical components in the system and prevents electrical and mechanical strain of the system. BLDC motor speed control is achieved by varying phase voltage employing pulse width modulation technique (Patel Vinod KR Singh and Pandey A. K., 2013; Arjun V. N., et al. 2016). Using PWM method ensures energy saving and efficiency compared to analog method in power electronics applications (Pindoriya, R. M. et al., 2014). PWM is the most used current control method in power control applications in power electronics (Mahesh S., et al., 2017).

In pulse width modulation method, conduction and non-conduction times (duty cycle) of power switches in inverter are changed in a fixed period. Thus, the speed and torque of the motor are changed by varying the terminal voltage of BLDC motor phase windings.

2. Running of BLDC Motor

A BLDC motor is a form of brushed direct current motor that is replaced its commutator and brushes with electronic commutation mechanism. While commutation is done via mechanical commutator and brushes in brushed dc motors, it is accomplished electronically in BLDC motors (Ozgenel M. C., 2017). In order to accomplish commutation in BLDC motors rotor position information is required. Rotor position information is achieved by using three Hall-effect magnetic sensors placed 120-degree apart on the stator of BLDC motor. Three Hall-effect sensors produce rotor position information as logic 1 and logic 0 roughly. There are six position states in one electrical period. Commutation circuit produces gate signals of power transistors in inverter according to rotor position information in order to energize the two phase windings just aligned with the rotor magnets. Thus, the magnetic field of the rotor magnet and the magnetic fields creating by phase current in the phase windings push each other and the rotor starts to rotate and it builds up speed and torque. The rotor position information changes after the rotor 60-electrical degree moves, and the commutation circuit generates new gate signals of transistors to energize the phase winding exactly aligned with the pole of rotor magnet. In this way, the rotor continues to rotate according to a renewed rotor position at every 60-electrical degree by energizing suitable two phase windings. Table 1 shows the sequence of energizing phase windings with respect to rotor position information. A typical three-phase inverter for BLDC motor driving is shown in Figure 1. There are six power transistors in inverter which can be insulated gate
bipolar transistor (IGBT) or metal oxide semiconductor field effect transistor (MOSFET). MOSFET is a low-current and voltage but high-frequency switching device. MOSFET is a voltage controlled switch. Because there is body-drain diode in MOSFETs, it is useful in dealing with freewheeling current applications. Since the resistance between drain and source is low, conduction losses are also lower. MOSFETs can operate at high frequencies and low voltages and they are perfectly suitable for faster switching operations with low-voltage drops. But these are restricted to be used at higher operating voltages in the range of around 500V. MOSFETs can operate in parallel each others.

Table 1. Sequence of energizing phase windings according to rotor position in BLDC motor.

| Step | Sc | Sb | Sa | Phase polarity |
|------|----|----|----|----------------|
| 1    | 0  | 0  | 1  | A- off B+ on C+ on |
| 2    | 1  | 0  | 1  | A+ off B+ on C+ on |
| 3    | 1  | 0  | 1  | A- off B+ on C+ on |
| 4    | 1  | 1  | 0  | A- off B+ on C+ off |
| 5    | 0  | 1  | 1  | A+ off B+ off C+ on |
| 6    | 0  | 1  | 1  | A+ off B+ off C+ off |

IGBTs are suitable for medium to high-power applications. It is a minority charge carrier device and has high input impedance. IGBT usage is predominated for higher voltage applications as it is unipolar and requires additional freewheeling diode for the reverse flow of current. Because of this additional diode across the IGBT it ensures very high performance compared to the MOSFET without body diodes. While the operating voltage of the IGBTs is up to 1400 volts, but the operating voltage of the MOSFETs is up to 600 volts. Higher operating voltages of IGBTs ensure that the current is low and therefore the switching losses are lower (www.efxkits.us, 2019). MOSFETs are needed less gate drive elements and they are easily driven than IGBTs. IGBTs also need dedicated gate drive chips, but MOSFETs can easily be driven than IGBTs.

Figure 1. BLDC motor driving scheme.

3. Speed Control of BLDC Motor Employing PWM Method

Speed control of BLDC motor is done by varying the voltage of phase windings terminals as it is in brushed direct current (DC) motors. The voltage across the phase winding can be varied implementing
analogue or digital switching method to power-transistors in inverter. Owing to have some restricts and the higher switching loss of analogue method, digital switching method is preferred in all electric power control applications (Pindoriya, R. M. et al., 2014). This digital switching method is known as pulse width modulation technique (PWM). In PWM current control technique, without making any changing in DC voltage source, DC voltage source is applied to the load in a very short time at several times during a period. By varying of the application time of DC voltage source to load is changed the load average voltage. The application of the DC source voltage to the load with short intervals is carried out by running the power transistor on and off states. Thus, PWM consists of on and off states of the power transistor in a period. If on time of power transistor is longer than it’s off time, the load average voltage is increased. On the contrary, if on time of power transistor is less than its off time of power transistor, the load average voltage is decreased. Therefore, the load average voltage can be easily changed by varying the on and off states of the power transistor. The rate of the \( t_{on} \) time of the switch to the period \( t_{on} + t_{off} \) is called duty cycle ratio. The relationship between average voltage, input voltage and duty cycle is expressed as following;

\[
V_{avg} = \frac{t_{on}}{t_{on} + t_{off}} \cdot V_{DC} \tag{1}
\]

Considering the duty cycle, equation (1) is re writable;

\[
V_{avg} = D \cdot V_{DC} \tag{2}
\]

Where \( V_{avg} \) is the output voltage, \( t_{on} \) is the conduction time of power transistor, \( t_{off} \) is the non-conduction time of power transistor, \( D \) is the duty cycle ratio and \( V_{DC} \) is source voltage.

In BLDC motor drivers, if duty cycle is %100, the average phase voltage is expressed as follows (Ozgenel M. C., 2017);

\[
V_{phase} = 0.41 \cdot V_{DC} \tag{3}
\]

Taking into account equations (2) and (3), if PWM is applied to BLDC motor windings, the value of the voltage in the phase windings can be written as follows;

\[
V_{phase} = 0.41 \cdot D \cdot V_{DC} \tag{4}
\]

Therefore, by varying the duty cycle (D) value in equations (1, 2, 4) the motor phase voltage is changed. Owing to fact that the speed of BLDC motors is directly proportional to phase voltage, the speed of BLDC motor can easily be controlled by varying duty cycle value in open loop or closed loop applications (Patel Vinod KR Singh and Pandey A. K., 2013; Tade S.L., et al., 2016; Arjun V. N., et al. 2016). In other words, the speed and torque of the BLDC motor are changed implementing the PWM method.

Owing to fact that BLDC motors are the most preferred motors in every area numerous commercial manufacturers produce PWM control chips targeted BLDC motor control (Ohm Dal Y. and Oleksuk R. J., 2002) such as MC33033 from Motorola, On semiconductor Corporations, FCM 8201 from Fairchild Corporation, L6235 from ST Microelectronics, DRV8332 from Texas Instruments, MC34929 from Freescale Semiconductor, Inc. and etc. While FCM 8201 and MC33033 are pre-driver ICs for inverter power transistors but L6235, DRV8332 and MC3429EP are
directly for driving BLDC motor. But some of chips targeted to BLDC motor control have some restricts and bad efficiency and also undesirable sound for some special applications. However there are different PWM schemes which give satisfactory performance to BLDC motor driving system (Ohm Dal Y. and Oleksuk R. J., 2002). In this experimental study, 3 different PWM methods which are used switching BLDC motor driving system will be examined and the results will be discussed in detail in the section 7.

4. PWM Switching Schemes

BLDC motors are mostly controlled by six-step switching mode. In this mode, suitable power transistors in inverter shown in Figure 1 are switched according to three-bit rotor position information. In BLDC motors, in order to produce maximum torque in any rotor position, the phase current must pass through the two-phase windings which are aligning with the rotor poles to be pushed by the winding magnetic field continuously. In BLDC motor inverters, any two-phase winding is energized at 120-electrical degree. Because commutation occurs at every 60-electrical degree one phase winding is connected to positive terminal of power supply and other phase winding is connected to negative terminal of power supply and these phase windings are connected series each other. Thus, the windings which are connected in series to each other vary in at each 60-electrical degree, but phase current passes through each phase winding during 120-electrical degree. It is necessary to change the phase current passing through the phase windings in order to control speed and torque. This can be easily implemented employing PWM method. There are various PWM schemes to control phase current in BLDC motor drivers. Each scheme has its own superior and disadvantageous properties. In this experimental study, three PWM methods are used in BLDC motors have been designed, manufactured and tested. These are:

4.1. Full conduction mode (Variable DC Link Voltage, FCM)

Although the using of this method is few, it is still used in some applications where high dynamic performance is not required. Because it is necessary to change the phase voltage to control BLDC motor speed, variable bus voltage control is employed in this topology as seen in Fig. 2 (Ohm Dal Y. and Park J. H., 1999). In this switching scheme, since power transistors in inverter run full conduction mode (FCM) during 120-electrical degree, this method is called six-step drive. For variable voltage, DC bus voltage is changed by thyristor bridge as shown in Fig. 2. In this topology, the energizing of the phase windings is made according to the three-bit rotor position information in the sequence shown in Table 1. In Table 1, at step 2, A-phase winding is connected to positive terminal of DC source B-phase winding is connected to negative terminal of DC source and C-phase winding is float, in this step A and B-phase windings are connected in series by A+ and B-transistors. By the third step, while A-phase winding is still connected to positive terminal of DC source and C-phase winding is float, in this step A and B-phase windings are connected in series by A+ and B-transistors. By the third step, while A-phase winding is still connected to positive terminal of DC source, but B-phase winding is float and C-phase winding is now connected to negative terminal of DC source by A+ and C-transistors. In this step, A-phase winding is connected in series with C-phase winding. Then, in the fourth step, A-phase winding becomes float state C-phase winding which remains its negative polarity is connected in series with B-phase
winding connected to positive terminal of DC source by C- and B+ transistors. Thus, from step 2 to step 4 A-phase winding has run with B-phase during 60-electrical degree later it has run with C-phase during 60-electrical degree. Thus, while each phase winding works together each other 60-electrical degree, but each phase runs 120-electrical degree. In every step, only two phases of three phase’s windings are energized. One phase is always positive and another phase is always negative during 120-electrical degree in this inverter topology. Energizing phase windings are made this manner according to Table 1. Since DC bus voltage is a function of triggering angle of thyristor bridge in order to control BLDC motor speed the firing angle of thyristors is changed in this driving topology (Ohn Dal Y. and Park J. H., 1999). In this mode, the voltage applied motor phase windings is 120 degrees and has a rectangular wave shape as it is shown in Fig 6(a). This configuration has some main drawbacks such as it has rich harmonics resulting losses in motor and needs a bulky capacitor in the DC bus line but this switching scheme has low switching losses (Gamazo-Real J.C., et al., 2010). Of course, the only way is not to obtain the DC voltage from the thyristor bridge, but there are also other forms of voltage sources such as DC-DC converters and AC-DC variable voltage sources.

4.2. Independent PWM mode (IPWM)

Inverter output voltage can be controlled by implementing PWM (IPWM) switching method to upper (A+, B+, C+) or lower sides switches (A-, B-, C-) in inverter. However, considering the ease of driving transistors, it is preferred to drive the transistors in the lower sides of the inverter. Upper side transistors in the inverter are on full conduction mode (120-degree). Thus, while upper sides transistors are on conduction mode at 120-degree in a period,
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but lower side of transistors of inverter are run in PWM mode at 120-degree. So, variable voltage from output of inverter is obtained and BLDC motor speed and torque are controlled. Fig. 3 illustrates the structure of independent PWM mode.

![Figure 3. Structure of independent PWM switching mode.](image)

In Fig. 3, while A-phase upper side transistor is at full conduction mode, lower side transistor run PWM mode. The other phase transistors run the same way. In this PWM scheme, when a PWM transistor is off, the phase current decreases slowly depending on the time constant of the phase winding (L/R). Because, this configuration is not suitable fast torque reversal on BLDC motors, it is exclusively imposed torque and speed directions are the same directions in other word 2-quadrant operation of BLDC motor is possible in this scheme (Ohm Dal Y. and Park J. H., 1999). Although independent PWM (IPWM) mode method has low switching losses in the inverter, but it has rich harmonics which results increase of losses in the BLDC motor. Since only transistors connected to the negative side of the DC source operate in PWM mode, it is easier to detect the rotor position using back electromotive force in sensorless applications (Gamazo-Real J.C., et al., 2010). Since control of power electronics circuitry is easy and simple design, this configuration is relatively low cost.

4.3. Complementary PWM mode (CPWM)

In this PWM structure, PWM is applied to both the top and bottom transistors of the same phase with a small dead time to avoid short circuit of DC source. So, decreasing of phase current becomes quicker during PWM off time. In Fig. 4, while A+ transistor is on, A- transistor is off state. When A+ transistor is switched off state A- transistor is also off state this time. After A+ switch is turned off, A- switch is turned on state after a short time that is called dead time, so both switches are not turned on at the same time and short circuit of DC source is prevented. Turning A- switch is on that ensures the phase current falls down quickly. This operation is the same for the B+, B- and C+ and C- switches. This mode of operation ensures a finer phase current control and removes the ripples on the current and torque as it is seen in Fig. 4. In addition, the 4-Quadrant operation is possible in this switching method. The complementary PWM (CPWM) scheme is preferred when fast speed reversal is required as in servo applications. The main drawbacks of this PWM method have higher switching losses and its design is more complex than the two other switching modes.
5. Experimental Setup

Three BLDC motor drivers using different PWM switching methods that are already mentioned in previous section have been designed and implemented to analyze the results. The first driver is variable DC bus voltage driver that can commutate inverter transistors according to rotor position. Inverter output voltage is changed by DC bus voltage in order to control speed of BLDC motor. The second one is independent PWM controlled inverter which is based SG3526 PWM generator chip using as inverter pre driver and PWM frequency is selected 20 kHz. In this driver the bottom transistors of inverter is run PWM mode but upper transistors of inverter run full conduction mode during 120-electrical degree. The third one is complementary PWM driver based on FCM8201 BLDC motor pre-driver. In this PWM mode, one phase upper and lower transistors are run PWM mode.
Three-phase, six poles BLDC motor with outer rotor is used as test motor which is mainly used in office machines in this study. BLDC motor is coupled a permanent magnet DC generator which is used BLDC motor load. BLDC motor has been run under various loads and injected three PWM methods and performance of BLDC motor has been examined. BLDC motor specifications are shown in Table 2.

Table 2. Specifications of BLDC motor using in experiment.

| Brand       | Rated voltage (V) | Rated Torque (mNm) | Rated speed (rpm) | Rated current (A) | Back EMF constant (V/krpm) | Number of poles | Phase resistance (Ω) | Phase inductance (mH) | Rotor type |
|-------------|-------------------|--------------------|-------------------|-------------------|---------------------------|----------------|---------------------|----------------------|------------|
| Shinano Kenshi | 24                | 160                | 1800              | 2.1               | 2.54                      | 6              | 0.71                | 0.937                | outer      |

Experiment is done by loading the same load to BLDC motor which is running three various speeds by employing three PWM switching methods.

6. Experimental Results

Experimental results consist of three sections. These are; testing gate signals, measuring DC link and motor phase currents and examination of results.

6.1. Test of gate signals

Initially, the gate signals of each inverter controller have been measured and tested. It has been found that the gate signals are the same as in theory as it shown in Fig. 6.
Fig. 6 illustrates that gate signals of three drivers are different from each others. It is understood from Fig. 6(a), each transistor in inverter remains conduction mode during 120-electrical degree. In this switching mode, PWM mode is not inserted to gates of switching elements. BLDC motor speed is varied by varying the inverter input DC bus voltage. Fig. 6(b) shows that the PWM signals are applied only to A-, B- and C-transistors which are connected to the negative terminal of the DC source shown by black parts. A+, B+ and C+ transistors are on full conduction mode during 120-electrical degree. Thus, while the transistors are connected to positive terminal of DC source are on 120-degree conduction mode, but A-, B- and C- transistors are run PWM mode during 120-electrical degree. BLDC motor speed is varied by changing duty cycle of gate signals of A-, B- and C-transistors. Fig. 6 (c) depicts that the transistors of the same phase (A+ and A-, B+ and B-, C+ and C-) run in the PWM mode as it is shown by black parts. BLDC motor speed is changed by varying duty cycle of both upper and lower transistors of the same phase in this PWM mode. From Fig. 6, it is understood that all gate signals are perfectly generated by pre-drivers.

6.2. Measuring DC link and phase currents

After the experimental setup has been build, three switching methods are tested and effects of these switching methods on the BLDC motor are examined. DC link and phase currents are measured and showed by oscilloscope as it shown in Figs. 9 and 10.

6.3. Experimental results

The BLDC motor has been run under constant load employing three PWM methods according to three speed ranges in order to reach more accurate results. Thus, the effects of these PWM methods on the inverter and BLDC motor have been compared. Table 3 shows that measured DC link and motor phase currents through the experiments.

If it is necessary to express as a percentage of DC link current for comparison, the following expression 5 can be used;
\[ \Delta I_{dc} = \frac{I_{dcFCM} - I_{dcCPM}}{I_{dcFCM}} \times 100 \] (5)

From Table 3 BLDC motor rotates at 1333rpm under the same load for each current control modes. The inverter drives 3.11A in full conduction mode, 2.35A in independent PWM mode and 2.30A in complementary PWM mode, from the DC source despite the load remains the same for each current control mode. Using expression (5) for comparison FCM with CPWM:

\[ \Delta I_{dc} = \frac{3.11 - 2.34}{3.11} \times 100 = 24\% \]

According this result, the driving system which is using CPWM mode draws 26% less current from DC source than full conduction mode. And the same way for comparison independent PWM with CPWM:

\[ \Delta I_{dc} = \frac{3.11 - 2.35}{3.11} \times 100 = 24\% \] are obtained.

Similarly, the driving system which is using CPWM and IPWM modes draw 24% less current from DC source than FCM current control mode. BLDC motor phase currents are almost equal to each other because the motor load is constant through the all experiments.

| Table 3. BLDC motor phase and DC link currents various speed ranges under the same load. |
|---------------------------------|-----|-----|-----|-----|-----|-----|-----|-----|
| Speed (rpm) | 600 | 1000 | 1333 |     |     |     |     |     |
| Switching method | FCM | IPWM | CPWM | FCM | IPWM | CPWM | FCM | IPWM | CPWM |
| Phase current (A) | 1.36 | 1.36 | 1.36 | 2.11 | 2.15 | 2.13 | 2.73 | 2.74 | 2.73 |
| DC link current (A) | 1.64 | 0.59 | 0.59 | 2.47 | 1.44 | 1.43 | 3.11 | 2.35 | 2.34 |

It is seen from Fig. 7 the DC link currents when the BLDC motor rotates at 600, 1000 and 1333 rpm, when the load is 2 \( \Omega \) which is connected to dc generator. It is clearly seen Fig. 7, DC link currents are very closely to each other for IPWM and CPWM methods, but DC link currents in FCM method are higher than other two methods. Whereas DC link voltage is lower in FCM than two other modes (Fig. (8)).
As it is seen from Fig. (7), while DC link currents are higher in FCM than IPWM and CPWM methods, but DC link voltages are lower in FCM method than the IPWM and CPWM schemes. In this experimental study, the effect of three different PWM methods on the dynamic behavior of BLDC motor is also investigated. For this purpose, the BLDC motor has been operated at the same speed under the same load and the dynamic response of the motor has been examined.

Figure 9. Speed up response of BLDC motor for three PWM current control methods for same speed under same load.
It is clearly seen from Fig. 9, 1333 rpm reference speed has given to BLDC motor in all three PWM modes and the BLDC motor reaches final speed (1333 rpm) within 400 ms when it runs in CPWM and IPWM modes. Thus, when the BLDC motor operates in CPWM and IPWM modes, the speed response is faster than FCM. CPWM and IPWM modes allow the BLDC motor to speed response faster than FCM. FCM gives the BLDC motor the slow speed response, BLDC motor reaches to final speed (1333 rpm) 100 ms later in this scheme.

**Figure 10.** When BLDC motor working rated phase current (2.1 Amp.) DC link currents with respect to three PWM schemes.

Fig. 10 shows DC link currents, when BLDC motor has been run under a certain load drawing its rated phase current 2.1 Amperes from inverter. Inverter has drawn higher DC link current from DC source than the two other PWM modes when BLDC motor working in FCM. When inverter has worked on IPWM and CPWM modes, their DC link currents are very close each other as it is shown in Fig. 7, 10 (b, c) and Table 3.

**Figure 11.** BLDC motor phase currents with respect to three PWM modes when the motor is working its rated phase current.

BLDC motor phase currents have been adjusted its rated current value and they have been recorded as they are seen in Fig. 11. Due to fact that BLDC motor speed is adjusted by varying DC link voltage in FCM, DC link voltage has been decreased to 10.4 Volts from 24.5 Volts to make phase current is 2.1 Amps. On the other hand, DC link voltage is 24.5 Volts when BLDC motor works other two PWM modes, since
motor speed controlling is achieved by changing duty cycle in IPWM and CPWM modes. When the BLDC motor works with FCM scheme, one transistor is in conduction mode for 8.4 milliseconds. While BLDC motor works both with IPWM and CPWM modes, one transistor is in conduction time for 20 microseconds so duty cycle is 40%.

Table 4. Inverter and BLDC motor parameters various speed ranges under the same load.

| Switching mode | Parameters                  | Frequency (Hz) and motor speed (rpm) |
|----------------|-----------------------------|--------------------------------------|
|                |                             | 30 (600)    | 50 (1000)   | 66.66 (1333) |
| FCM            | Phase current (A)           | 1.75        | 2.63        | 3.26         |
|                | DC link current (A)         | 2.08        | 2.97        | 3.66         |
|                | DC link voltage (V)         | 8.2         | 13.7        | 18.8         |
|                | $T_{on}$ (ms)               | 11.4        | 6.8         | 5.2          |
|                | DC power (W)                | 17          | 40.6        | 68.8         |
|                | Phase voltage (V)           | 8.2*0.41=3.36 | 13.7*0.41=5.61 | 18.8*0.41=7.7 |
| IPWM           | Phase current (A)           | 1.74        | 2.60        | 3.24         |
|                | DC link current (A)         | 0.71        | 1.66        | 2.81         |
|                | DC link voltage (V)         | 24.4        | 24.4        | 24.4         |
|                | $T_{on}$ (µs)               | 16          | 27          | 37           |
|                | DC power (W)                | 17          | 40.5        | 68.5         |
|                | Phase voltage (V)           | 24.4*0.32*0.41=3.20 | 24.4*0.54*0.41=5.4 | 24.4*0.74*0.41=7.4 |
| CPWM           | Phase current (A)           | 1.75        | 2.60        | 3.24         |
|                | DC link current (A)         | 0.70        | 1.67        | 2.79         |
|                | DC link voltage (V)         | 24.4        | 24.4        | 24.5         |
|                | $T_{on}$ (µs)               | 16          | 27          | 37           |
|                | DC power (W)                | 17          | 40.6        | 68.35        |
|                | Phase voltage (V)           | 24.4*0.32*0.41=3.20 | 24.4*0.54*0.41=5.4 | 24.4*0.74*0.41=7.4 |

As it is seen in Table 4, when BLDC motor rotates at 600, 1000 and 1333 rpm, inverter draws from DC power supply 17, 40.6 and 68 Watts power respectively. Duty cycles are equal in CPWM and IPWM modes (16, 27, 37 µs) but in FCM, due to it is full conduction mode, duty cycles are 11.4, 6.8 and 5.2 ms. Phase voltages are equal each others for three PWM schemes so BLDC motor performance is the same for CPWM and IPWM modes.
7. Consultations

Three types of inverter controllers are designed on provided that the inverter part remains the same; these controllers are 1- full conduction mode controller, 2- independent PWM controller and the last one 3- complementary PWM mode controllers. These three current control methods have different effects on the inverter, DC source and BLDC motor. These effects can be summarized as follows:

**7.1. Full conduction mode (FCM):**

Owing to draw the higher current from the DC source than two other PWM modes, this method is not suitable battery powered motor driving applications. Although designing and application of this method is easy, this current control method has unwanted noise for high power BLDC motors. Furthermore this conduction mode gives the BLDC motor the slowest speed response. Since this configuration gives slow response to BLDC motor, it is not suitable the applications where require fast dynamic response such as robotic and servo applications. This current control method can be preferred to the BLDC motors which have very low phase winding inductance. When PWM method applies to very low phase winding inductance, phase current becomes the same shape with the PWM voltage and phase current is not continuous. Full conduction scheme can be used where the DC source voltage is lower than the BLDC motor rated voltage and in small power applications. Start stop operation of BLDC motor should not be done using DC link switch, otherwise BLDC motor performance will be very slow. Start/stop operation should be done in position decoder part as it shown in Fig. 2.
7.2. Independent PWM mode (IPWM):

The current drawing from DC source is relatively low according to FCM in this current control mode and it draws DC link current very close to CPWM method. Implementing of this configure is simple and cheaper. Although this PWM scheme structure is simple and more economical, it gives the experimental BLDC motor the same performance as the CPWM method as it is seen Figs. 9 (b, c), 12 (b, c) and Table 4. This PWM scheme results slower response in BLDC motors which has highly inductance phase winding.

7.3. Complementary PWM mode (CPWM):

Implementation of this current control mode is more complex than two others modes. Owing to draw the lower current from DC source according to FCM method, this current control method must prefer mainly battery powered applications such as portable devices, battery powered vehicles, solar power applications. In addition to these, this current control method provides the BLDC motor fast dynamic speed response which is required in servo applications. In this respect, CPWM method should especially prefer on applications that require fast dynamic response such as robotics, medical areas, packaging, office machines and industrial applications.

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