Low-Temperature III–V Direct Wafer Bonding Surface Preparation Using a UV-Sulfur Process

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A technique for direct wafer bonding of III–V materials utilizing a dry sulfur passivation method is presented. Large-area bonding occurs for GaAs/GaAs and InP/InP at room temperature. Bulk fracture strength is achieved after annealing GaAs/GaAs at 400°C and InP/InP at 300°C for times less than 12 h without large compressive forces. X-ray photoelectron spectroscopy measurements of the treated, bonded, and subsequently delaminated surfaces of GaAs/GaAs confirm that sulfide is present at the interface and that the oxide components show a reduced concentration when compared with samples treated with only an oxide etch solution.

Key words: Wafer bonding, materials integration, III–V, sulfur passivation

INTRODUCTION

Direct wafer bonding promotes the integration of heterogeneous semiconductor materials without the constraints of lattice constant or crystal structure mismatch for epitaxially grown layers. Wafer bonding with an intermediate bonding layer such as SiO2 or silicon nitride can produce semiconductor-on-insulator structures with almost any combination of materials for applications requiring electrical isolation or for composite growth substrates. Wafer-bonded optoelectronic devices such as lattice-mismatched solar cells require the bonded interface to be transparent and conductive, such that the interface must be free of insulating layers or opaque metallic layers. III–V surfaces such as GaAs suffer from unfavorable native oxides that are difficult to eliminate, and the properties of the bonded interfaces are dominated by unfavorable mechanical and electronic properties. Often, annealing at elevated temperatures (>600°C) for times in excess of 30 min with large compressive forces must be employed to break up the interfacial oxide layer to form better contact between the III–V layers. These harsh processing conditions can be undesirable for processed samples with a limited thermal process budget. Lian et al. demonstrated that temperatures of at least 550°C for 1 h are required for AlGaAs/GaAs heterojunction bipolar transistors (HBTs) formed by wafer fusion methods, yet that the HBTs suffer gain degradation at annealing temperatures above 450°C. A surface treatment prior to bonding that reduces the inclusion of unfavorable oxides in the interface could reduce the need for high-temperature processing. However, this surface treatment must also maintain a low surface roughness that is compatible with subsequent bonding. Reduction of surface oxides and maintenance of low roughness surface are also important for development of III–V gate dielectrics.

The treatment of III–V surfaces with sulfur or sulfide-containing solutions is known to improve the surface electronic properties for Schottky contacts and heterojunction bipolar transistors, primarily due to replacement of surface oxide with a thin sulfide layer. Various sulfide solution passivation methods have been reported for direct bonding, including thiourea in NH4OH, (NH4)2S, and (NH4)2Si n-butanol, with the latter treatment shown to improve the interfacial conductivity of wafer-bonded p-GaAs/n-InP over an HCl treatment. In this study, modifications to a III–V surface passivation technique first described by Ashby et al. are performed to achieve high-strength bonding for GaAs/GaAs and InP/InP after low-temperature
(<400°C) annealing. The method utilizes elemental sulfur vapor activated by ultraviolet illumination under high vacuum to form a thin sulfide layer on the III–V surface, which through the addition of a step to remove excess physisorbed sulfur, yields a smooth hydrophobic surface suitable for direct wafer bonding.

**EXPERIMENTAL PROCEDURES**

Passivation and direct bonding experiments were performed on epiready Si-doped (001) \( n \)-GaAs with a carrier concentration of \( 1.2 \times 10^{18} \text{ cm}^{-3} \) and \( S \)-doped \( n \)-InP with a carrier concentration of \( 5 \times 10^{18} \text{ cm}^{-3} \). The wafers were degreased in acetone and isopropyl alcohol for 5 min, rinsed in deionized water, and subjected to 200 mTorr oxygen plasma treatment for 1 min at a power of 200 W. The oxidized surface was etched in 1:10 NH\(_3\)OH:H\(_2\)O for 2 min for GaAs and 1:10 HF:H\(_2\)O for 2 min for InP, then rinsed in water and dried under N\(_2\). After the surface oxide removal, the wafers were loaded into a vacuum chamber which contains a medium-pressure Hg-vapor ultraviolet light source, a temperature-controlled surface, and a crucible containing elemental sulfur powder. At a chamber pressure of \( 3 \times 10^{-6} - 10 \times 10^{-6} \) Torr, the UV lamp illuminated the bonding surfaces, and the temperature of the sulfur powder and samples were increased to 45°C to 60°C, as measured by a thermocouple. Within this temperature range, the vapor pressure of sulfur ranges between \( 10^{-5} \) Torr and \( 10^{-4} \) Torr, causing some of the powder to sublime to vapor in the vacuum chamber. The sulfur vapor becomes reactive under UV illumination through cleavage of the S\(_8\) ring into free radicals which can react with the III–V surface to form a thin passivation layer. The bonding surfaces were exposed to this sulfur vapor and UV light for 5 min. In some cases, to determine whether subsequent processing was necessary to maintain a surface free of excess sulfur deposits, the sulfur source was removed from the chamber and the samples were exposed to UV illumination again while being heated to 75°C for 5 min. After the treatment, the surfaces exhibit hydrophobic behavior. The samples were aligned face to face with bonding initiated by applying pressure at the center and allowing the bonding front to propagate to the wafer edges. The bonded samples were then annealed at 200°C to 400°C (with a ramp rate of 1.5°C/min) and only 5 kPa to 10 kPa compression for 1 h to 24 h. Reference bonded samples were prepared by bonding immediately after the oxide removal etch.

The surface morphology of processed samples was monitored by atomic force microscopy (AFM). Bonding area was monitored by transmission infrared (IR) imaging, and bond strength was determined using the razor crack opening method with a 240 μm razor blade thickness. Surface characterization was performed using x-ray photoelectron spectroscopy (XPS) with monochromatic Al K\(_\alpha\) radiation (1486.6 eV) and 20 eV band pass energy. XPS measurements were performed with a take-off angle of 20° between the surface and the detector to enhance the signal from the bonding surface. Arsenic 2p\(_{3/2}\) and gallium 2p\(_{3/2}\) spectra were analyzed for the presence of core level chemical shifts for oxides and sulfides, and the S 2p/Ga 3s region was used to confirm the presence of sulfur bonded at the surface. Peak deconvolution was performed with the free XPSPEAK software program and involved subtraction of a Shirley background and curve-fitting with a sum of 70% Gaussian and 30% Lorentzian lines.

**RESULTS AND DISCUSSION**

Initial bonding trials followed the passivation technique described by Ashby et al., such that the surfaces were contacted after the UV illumination and sulfur passivation step. The result was always a poor bond, which delaminated upon a 300°C anneal of 12 h. The debonded sample surface was yellow, and exhibited an uneven surface roughness, as shown by AFM in Fig. 1a. This presumably is the

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Fig. 1. AFM scan (40 μm × 40 μm) of (a) debonded GaAs wafer showing nonuniform surface roughness from excess elemental sulfur at the interface, (b) UV-sulfur processed GaAs sample showing a clean surface with RMS roughness of 2.3 Å.
result of elemental sulfur being deposited along with the UV-activated sulfur species, and while elemental sulfur was not much of a concern for applications of passivation, it does have a highly adverse effect in wafer bonding. To minimize this excess sulfur at the surface in favor of only a reacted sulfide or chemisorbed S layer, all subsequent samples were also heated in vacuum to 75°C under UV light without the sulfur source after the initial sulfur deposition. It is postulated that physisorbed sulfur will sublimate under these conditions while chemisorbed sulfide will remain. Figure 1b shows an AFM image of GaAs treated with this new procedure, referred to as UV-S, showing that the process maintains the original surface roughness of less than 3 Å root-mean-square (RMS) roughness that is suitable for direct bonding. Further investigation is underway into the kinetics of the removal of physisorbed sulfur from the surface for further process optimization.

The bonded area of UV-sulfur- and NH₄OH-treated reference samples were monitored by transmission IR after bonding and after annealing. Figure 2 shows an IR image of UV-S-bonded GaAs/GaAs 50-mm wafers with annealing for 1 h at 400°C. There were five interfacial voids initially present upon bonding. Subsequent annealing for any time length did not cause them to expand or new voids to form. Crack opening tests were performed on GaAs/GaAs bonded samples with 510 μm wafer thicknesses and annealing for 12 h at 300°C. The UV-S-treated sample had a crack length of 18 ± 0.5 mm, while the reference NH₄OH sample had a crack length of 20 ± 0.5 mm. Using a Young's modulus value for GaAs of 121.3 GPa for the [110] bending axis yields a bond strength of 830 ± 90 mJ/m² for the UV-S sample and 540 ± 50 mJ/m² for the NH₄OH etched sample. The value for the NH₄OH etched sample is comparable to the value of 425 ± 80 mJ/m² for GaAs etched in HCl and bonded at 300°C for 8 h. The measured bond strength of the UV-S sample is close to the bulk fracture energy for GaAs of 900 ± 100 mJ/m² as measured by cleaning and bonding in ultrahigh vacuum (UHV), and to the value of 750 mJ/m² reported for HCl-etched GaAs annealed at 400°C for 1 h and 600°C for 30 min. Annealing UV-S-bonded GaAs pairs at 400°C for 12 h resulted in an improvement in bond strength to that of bulk fracture energy such that the crack front causes bulk fracture through one of the wafers rather than propagating along the bonded interface. Bonded pairs of InP/InP were also prepared by the UV-S treatment, and razor crack measurements indicated that bulk bonding strength is achieved with annealing at 300°C for 2 h.

The XPS scan around the binding energy of 160 eV, where the core levels of Ga 3s and S 2p partially overlap, is shown in Fig. 3 for unprocessed, NH₄OH-etched, and UV-sulfur samples. The Ga 3s peak is present at 160 eV for all surface treatments, while the UV-sulfur-treated surface also shows the S 2p doublet with 2p₃/₂ located at 162 eV. This binding energy value is consistent with a sulfide
rather than bulk sulfur, as the latter is expected at 164 eV. This indicates that the sulfur vapor is indeed chemisorbed to form a sulfide at the surface.

Some reports about surface passivation or of wafer bonding preparation which use XPS characterization fail to state the take-off angle that would reveal how well tuned the measurement is to the top surface layer. For studying the chemical state of two surfaces being contacted to initiate a direct wafer bond, it is important to select a core level spectra which has a small sampling depth and to have measurable core level chemical shifts for different bonding states. The As 2p$_{3/2}$ peaks near 1323 eV and Ga 2p$_{3/2}$ peaks near 1117 eV excited with Al K$_{\alpha}$ radiation and taken at 20° grazing angle are very surface sensitive, with an information depth of around 8 Å and 12 Å, respectively. Figure 4 shows the deconvolution of the As 2p$_{3/2}$ and Ga 2p$_{3/2}$ spectra for unprocessed, NH$_4$OH-etched, and UV-sulfur samples. The spectra for unprocessed GaAs (Fig. 4a) is dominated by oxidized components with As 2p$_{3/2}$ showing 3 eV and 4.3 eV shifted peaks attributed to As$_2$O$_3$ and As$_2$O$_5$ and Ga 2p$_{3/2}$ showing a 1.1 eV shifted peak attributed to Ga$_2$O$_3$. These chemical shift values are marginally lower than those reported by Arabasz et al. of 3.3 eV and 4.5 eV for As-O 2p$_{3/2}$ and 1.2 eV for Ga-O 2p$_{3/2}$, but the overall peak trends are consistent. The peak area ratios of As-O to As-Ga and Ga-O to Ga-As are each about 4:1 on the unprocessed sample. For the NH$_4$OH-etched sample (Fig. 4b), the As$_2$O$_5$ peak is eliminated, and also a new peak shifted by 0.7 eV must be added to maintain good fitting. This minor peak is attributed to elemental arsenic. The signal ratio of oxidized to bulk components after the etch is reduced to 2:1 for As 2p$_{3/2}$ and 1:1 for Ga 2p$_{3/2}$, indicating that either the etching solution did not remove all the oxide or that some reoxidation occurred after removal from the solution. The XPS measurement of the UV-sulfur-treated sample (Fig. 4c) shows that the As-O to As-Ga ratio is lowered to 1.3:1, and there is the appearance of a new peak shifted by 1.7 eV with an area relative to As-Ga of 0.67. This 1.7 eV shifted peak is assigned to As-S formed during the UV-sulfur exposure, which is close to the 1.8 eV value reported by Ashby et al. for passivation with UV-activated sulfur vapor, and lower than the 2.13 eV reported by Arabasz et al. for aqueous
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Zavadil et al. reports that the passivation effect of oxygen and GaAs in the formation of an oxide layer may inhibit the reaction between physisorbed oxygen and GaAs in the formation of an oxide. Exposure to sulfur vapor under UV illumination is not expected to etch the surface as would a (NH₄)₂S solution, so this indicates that the sulfide layer may inhibit the reaction between physisorbed oxygen and GaAs in the formation of an oxide. Zavadil et al. reports that the passivation effect of UV-activated sulfur vapor is further improved if etching and loading of GaAs are kept in a N₂ environment to prevent oxidation before the sulfide layer can form on the surface. However, the current results of using UV-sulfur for wafer bonding show that the presence of the sulfide layer can improve the bond strength even without the complete absence of oxide.

CONCLUSIONS

Bonded samples of GaAs/GaAs treated with the UV-sulfur process have been produced and exhibit large-area bonding, achieving bulk bond strength with a 400°C anneal. The UV-S process has also been employed to bond InP/InP pairs, which attain bulk fracture strength with a 300°C anneal. The process is able to maintain a clean surface without roughening, through heating of the surface to 75°C with UV illumination under vacuum to remove excess sulfur. The combination of the high bond strength and the XPS data indicates that the surface heating/UV illumination helps remove excess sulfur at the surface and promotes effective bonding. Bonded wafers do not show the appearance of interfacial voids with annealing and do not require large compressive forces to form the bond. The UV-S process changes the chemical state of the bonding surface from being dominated by As-O and Ga-O species to having significantly less Ga-O, a mixture of As-S and As-O, and possibly some Ga-S bonds. The presence of III–V oxides at a wafer-bonded interface is often associated with low bond strength, and a large barrier to electrical conduction, while the UV-S method shows that sulfur passivation techniques can yield improvements in bond strength. Processing under a controlled atmosphere such as nitrogen could further improve the technique. Homogeneous GaAs/GaAs bonding serves as a route towards heterogeneous combinations such as GaAs/InP through better understanding of the problematic GaAs bonding surface, without added complications such as band alignment, compositional alloying, and thermal expansion mismatch. Our efforts are now focused on investigating the electrical properties of sulfur-bonded interfaces, and the role that such low-temperature bonding methods can have in integrating lattice-mismatched structures.

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