Adaptive hysteresis band current control of grid connected PV inverter

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1. INTRODUCTION
Due to the development of power electronic devise in power system generates the harmonics in voltage as well as current power high frequency which effect the performance of the loads. These are crucial role in power quality issues. There was a technical improvements are there in HVDC and FCTS device to reduce the generation of harmonics [1]. The VSC based converter are used in medium and low level power transmission for high level power SCR based HVDC system is used [2-5]. HVDC DC light is made of fully controlled switch such as IGBT or MOSFET along with PWM technique. It reduces the harmonic generation, minimizes the filter requirements [6, 7]. The HVDC system is a highly efficient alternative for transmitting large amounts of electricity over long distances and for special purpose applications. As a key enabler in the future energy system based on renewables, HVDC is truly shaping the grid of the future [8]. This hierarchical control structure is discussed which is important tool to maintain the dynamics, stationery performance of the micro grid with respective economical aspects [9]. Different control schemes are discussed in DC micro-Grid to mitigate the issues in it with the goal of providing control design guild lines [10, 11]. The high bandwidth hierarchical control structure for AC micro grid in distribution level.Conventionally to control Grid voltage and frequency were done by hierarchical linear control loops. they suffer from slow response with parametric effects. So the model predictive control technique is used to realize with heigh band width to control the multiple VSC. Furthermore, droop control and virtual impedance methods were employed to share active and reactive power. Based on a large-signal dynamic model of a micro grid, a linear parameter varying based state estimator that is localized in each distributed generation (DG) unit serves as an alternative
communication role and obtains the dynamics of the other DG units independently. A linear matrix inequality formulation for pole placement condition was derived for the state estimator design. A decentralized secondary voltage controller is, thus, able to achieve accurate reactive power sharing and average voltage restoration without any additional communication links [12]. A novel system operation strategy with energy management system was proposed in DC micro grid with different energy storage system where high penetration of renewable energy source. In both islanded and grid connected operation strategy of the micro grid, the ultra-capacitor unit has vital role to support the dc link. The micro-grid was controlled to deliver/absorb certain amount of power to grid. No power is dispatched to the grid in island mode [13].

In hysteresis controller, the error band is normally fixed to a certain value. Consequently, the switching frequency varies within a band because peak to peak of ripple current must be controlled at all points of the fundamental frequency wave. Adaptive hysteresis band current controller changes the hysteresis bandwidth according to the reference current to make switching frequency of inverter nearly constant and further reduces the THD of supply current. Adaptive hysteresis band current controller was used as current control method [14]. In section 2 modeling of the PV system with MPPT technique, hysteresis and adaptive hysteresis current control of the grid tied inverters with detailed modelling is presented. In Section 3 simulation results of hysteresis and adaptive hysteresis band controllers are inspected and compared in terms of % THD in grid current.

2. DESIGN AND IMPLEMENTATION OF THE SYSTEM
2.1. Design of the PV array
PV array is the combination of the PV modules. The primary element is PV cell, which converts the solar power to electrical power. Different modelling of the PV cell is given in literature survey such as single diode model, two diode models, and multiple diode models. In this paper two diode model of the PV cell is considered is shown in Figure 1. \( I_{pv} \) is the photo current. D1 and D2 are two anti-parallel diodes, \( R_s \) is leakage resistance and \( R_p \) is contact resistance. V is the output voltage of single cell and the output current of cell ‘I’ is given in (1) [15-19].

\[
I = I_{pv} - I_{D1} - I_{D2} - \left( \frac{V + I R_s}{R_p} \right) 
\]

\[
I_{D1} = I_{o1} \left[ \exp \left( \frac{q(V + I R_s)}{A_1 k T} \right) - 1 \right] 
\]

\[
I_{D2} = I_{o2} \left[ \exp \left( \frac{q(V + I R_s)}{A_2 k T} \right) - 1 \right] 
\]

\[
I_{pv} = [I_{scr} + K_i(T_k - T_{refk})] \times \lambda/1000 
\]

Here \( \lambda \) is solar irradiation, \( I_{scr} \) is short current of PV cell, \( T_k \) and \( T_{refk} \) are the actual and standard temperatures respectively. K is temperature coefficient of short-circuit current (A/ K), \( A_1 \) and \( A_2 \) are the ideality factors of the diodes. \( q \) is the charge of the electron, \( I_{o1} \) and \( I_{o2} \) are the reverse saturation currents of diodes D1, D2 respectively. As shown in (1) is modified for PV module as given in (5).

\[
I = N_p I_{pv} - N_p I_{D1} - N_p I_{D2} - \left( \frac{V + I R_s}{R_p} \right) 
\]

In equation substitute the (1) and (3).

\[
I = N_p \times I_{pv} - N_p \times I_{o1} \left[ \exp \left( \frac{q(V + I R_s)}{N_s A_1 k T} \right) - 1 \right] - N_p \times I_{o2} \left[ \exp \left( \frac{q(V + I R_s)}{N_s A_2 k T} \right) - 1 \right] - \left( \frac{V + I R_s}{R_p} \right) 
\]

Here \( N_s \) and \( N_p \) are the No. of cells are cascaded and shunted in module. These modules are connected in cascade and shunt to meet the voltage and power rating. PV array current is given in (7).

\[
I = N_{pp} \times I_{pv} - N_{pp} \times I_{o1} \left[ \exp \left( \frac{q(V + I R_s)}{N_{pp} A_1 k T} \right) - 1 \right] - N_{pp} \times I_{o2} \left[ \exp \left( \frac{q(V + I R_s)}{N_{pp} A_2 k T} \right) - 1 \right] - \left( \frac{V + I R_s}{R_p} \right) 
\]

Where \( N_{pp} \) and \( N_{pp} \) are the number of series connected modules and parallel connected series strings in PV array respectively. The standard KC200GT data sheet parameters are used for PV array simulation are given.
in Table 1. To extract the maximum power from PV array due to changes in atmospheric conditions. The different control techniques are presented in literature such as Incremental conductance method, perturb and observe method, and current sweep and constant voltage. Perturb and Observe method finds the change in output power of the PV panel. If the change power is positive, panel voltage will increase, otherwise panel voltage decreases. It is achieved with the help of the boost converter [20-22]. Generally, a DC-DC boost converter is used to extract the maximum power from the PV array. It is shown Figure 2. The inductor stores the energy when switch is ON, the stored energy is released to the load when it is OFF.

The design the parameters of the boost converter is given in (8).

\[
DutyCycleD = 1 - \frac{V_s}{V_o}; \quad Inductance \tan \delta L = \frac{V_o D}{\Delta I f}; \quad Capacitance \tan \delta C = \frac{I_0 \Delta V f}{\Delta V f}
\]

(8)

Where, \(V_s\)=Source voltage, \(V_o\)=Output voltage, \(D\)=Duty cycle, \(\Delta I\)=Ripples in current, \(\Delta V\)=Ripples in output voltage.

2.2. Perturb and observe method

In this work the DC link is derived from the PV array, due to uncertainties in the climatic conditions. The power from PV panel is variable. To extract the maximum power from the PV panel, different MPPT methods are proposed in literature. Such as fixed duty cycle method, beta method, Incremental conductance method, perturb and observe method, constant voltage current method, and fractional voltage method. Each method has its own advantages and disadvantages. In this paper perturb and observe (P&O) method is used due to the easy implementation [23].

2.3. Operation of 3-phase inverter

Currently 3-phase inverters are predominantly used in both AC/DC Microgrid operations. A 3-phase inverter consist of six switches as shown in the Figure 3. When upper device is ‘ON’, the load terminals are connected to the positive voltage and when lower device is ‘ON’, the load is connected to the negative terminal voltage. Applying proper pulses to the switches three phase voltage is generated, that is applied to the grid through a filter. To filter out the harmonics in the current, different filters ‘L’, ‘LCL’, ‘LC’ are available in literature [24]. ‘L’ filter was used to filter the harmonics in the current [25-26].

2.4. Hysteresis band controller

In this current control technique, actual grid current is compared to reference current as shown in Figure 4. If error current is more than the fixed band then the switches are turned ‘on’. If it is less than the lower limit, then lower switches in the respective phases are turned ‘on’. Thereby the grid current is restricted within the set limits. The harmonics presented in the grid current depends highly on the selected band for
controller, type of the filter and its value. One of the main disadvantages of this controller is variable switching frequency due to the fixed bandwidth. It is difficult to design the filter at variable frequency. This is overcome with a novel hysteresis current controller. Its design and implementation is discussed in the next section of this paper. The circuit diagram of the system with this controller is shown in Figure 3 [27].

![Circuit Diagram](image)

**Figure 3. 3-Ph inverter connected to the grid**

**Figure 4. Hysteresis controller**

### 2.5. Adaptive hysteresis band current controller

The switching pattern of the single phage of the 3-ph. inverter is described below. Conventional hysteresis current controller.

\[ I_{\text{INV}} \geq (I_R - HB) \text{ Then T1 is ON & T4 is OFF} \] (9)

\[ I_{\text{INV}} \geq (I_R + HB) \text{ Then T1 is OFF & T4 is ON} \]

The switching frequency of the hysteresis fixed band controller depends on the how fast inverter current changes from the upper limit to the lower limit and vice-versa. Slope of the inverter current depends on the filter value and DC input voltage of the grid connected inverter. Accordingly, the switching frequency varies [28].

Bandwidth of the hysteresis controller allows the inverter current error. By varying the bandwidth, variation in switching frequency can be achieved. As the switching frequency increases, the switching loss of the inverter and subsequently, electromagnetic interference (EMI) increases. So while selecting the bandwidth for a hysteresis controller, the above mentioned parameters are compromised. Hysteresis controller is simple to implement among PWM converters. It has inherent peak current limiting control and it does not require any system information. However, it suffers from variable frequency. Improved version of this technique is implemented for shunt active filters and drives [29-31]. In case of the adaptive hysteresis band controller, the band width of the hysteresis controller will be updated depending on the DC voltage, slope of the reference current, and filter values and grid voltage. as detailed in (10)-(19).

\[ \frac{dI_{\text{INV}}}{dt} = \frac{1}{L} (V_{\text{DC}} - V_s) \] (10)

\[ \frac{dI_{\text{INV}}}{dt} = -\frac{1}{L} (V_{\text{DC}} + V_s) \] (11)

From the geometry of the Figure 5,

\[ \frac{dI_{\text{INV}}}{dt} t_1 - \frac{dI_{\text{INV}}}{dt} t_1 = 2HB \] (12)

\[ \frac{dI_{\text{INV}}}{dt} t_2 - \frac{dI_{\text{INV}}}{dt} t_2 = -2HB \] (13)

\[ t_1 + t_2 = T_c = \frac{1}{f_c} \] (14)

Add (12) and (13) and substitute in (14).
\[
\frac{dI_{\text{Inv}}}{dt} + \frac{dI_{\text{Inv}}}{dt} - \frac{1}{f_c} \frac{dI_{R}^*}{dt} = 0
\]  

(15)

Subtract (12) and (13), we get:

\[
\frac{dI_{\text{Inv}}}{dt} - \frac{dI_{\text{Inv}}}{dt} - (t_1 - t_2) \frac{dI_{R}^*}{dt} = 4HB
\]  

(16)

Substitute (14) in (16). It results into;

\[
(t_1 + t_2) \frac{dI_{\text{Inv}}}{dt} - (t_1 - t_2) \frac{dI_{R}^*}{dt} = 4HB
\]  

(17)

Substitute the (14) in (17) and simplify;

\[
t_2 - t_1 = \frac{dI_{R}^*/dt}{f_c l(I_{\text{Inv}}/dt)}
\]  

(18)

From above equations, the hysteresis band is given (19).

\[
HB = \left\{ \frac{0.125V_{dc}}{f_c L} \left[ 1 - \frac{4L^2}{v_{dc}^2} \left( \frac{\nuS}{L} + m \right) \right] \right\}
\]  

(19)

where:

- \( f_c \): Switching frequency
- \( m \): Slope of the inverter current.

Adaptive hysteresis band current controller changes the bandwidth depending on the slope of the inverter current and DC voltage. It reduces the influence of current distortion on the reference wave. The expression for the HB is given in the equation. The simple block diagram of the adaptive hysteresis band current controller is given in Figure 6.

\[\text{Figure 5. Geometry of the reference and actual current} \]

\[\text{Figure 6. Simulation of the adaptive HB} \]

3. SIMULATION RESULTS

Simulation waveform of PV array voltage is shown in Figure 7, where the output voltage is 380 V upto 0.5 Sec. Due to change in temperature at 0.5 sec, voltage raises to around 480 V. The current and power supplied by the PV module is shown in Figure 8 and Figure 9 respectively. It is observed in Figure 9, that the current as well power supplied by the PV also changes at 0.5 sec. It is due to the constant reference current to the PV inverter.

Output voltage and current of the inverter are illustrated in Figure 10. It can be noticed that 3-ph. inverter output is changed at 0.5 sec. This is due to change in DC link voltage. Reference current is taken as 4 A. THD of the grid current with adaptive hysteresis controller is shown in Figure 11. THD of the grid current with Hysteresis controller is depicted in Figure 12. It is observed that THD is improved to 2.34% with adaptive hysteresis controller in comparison with 3.19 with hysteresis controller.
THD of the inverter current for the different reference currents values are tabulated in the Table 2. without changing the system parameters, the adaptive hysteresis band controller has given the less THD of the current, compare to the hysteresis controller. The voltage across switch is shown Figure 13. with hysteresis current controller. The time period of the switch is not constant and more ever the switch frequency is low. The adaptive hysteresis band controller the switching time low, means the switching frequency is high, where which requires the low value of the filter, is also constant shown Figure 14.

Table 2. Comparison of % THD for various inverter currents

| Inverter current (in A) | Adaptive hysteresis band current control | Hysteresis band current control |
|------------------------|----------------------------------------|-------------------------------|
| 2                      | 5.67                                   | 7.83                          |
| 3                      | 3.79                                   | 4.59                          |
| 4                      | 3.12                                   | 3.65                          |
| 5                      | 2.94                                   | 3.19                          |
| 6                      | 2.80                                   | 3.58                          |
| 7                      | 2.54                                   | 3.35                          |
| 8                      | 3.08                                   | 3.3                           |
| 9                      | 3.20                                   | 3.32                          |
| 10                     | 2.92                                   | 2.42                          |
4. CONCLUSION

In this paper both hysteresis and adaptive hysteresis band current controllers are implemented to the PV inverter tied to grid. In case of the adaptive hysteresis band current controller, the switching frequency is maintained almost constant. The adaptive hysteresis band controller changes the bandwidth based on the modulating frequency, supply voltage, input DC voltage and slope of the reference current, switching frequency is all most all constant. % THD of the grid current is also low with the adaptive hysteresis band controller. This technique can be suggested to control the power in case of micro grid application where current control is required.

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