Efficient and Self-Recursive Delay Vandermonde Algorithm for Multi-Beam Antenna Arrays

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Abstract

This paper presents a self-contained factorization for the delay Vandermonde matrix (DVM), which is the super class of the discrete Fourier transform, using sparse and companion matrices. An efficient DVM algorithm is proposed to reduce the complexity of radio-frequency (RF) N-beam analog beamforming systems. There exist applications for wideband multi-beam beamformers in wireless communication networks such as 5G/6G systems, system capacity can be improved by exploiting the improvement of the signal to noise ratio (SNR) using coherent summation of propagating waves based on their directions of propagation. The presence of a multitude of RF beams allows multiple independent wireless links to be established at high SNR, or used in conjunction with multiple-input multiple-output (MIMO) wireless systems, with the overall goal of improving system SNR and therefore capacity. To realize such multi-beam beamformers at acceptable analog circuit complexities, we use sparse factorization of the DVM in order to derive a low arithmetic complexity DVM algorithm. The paper also establishes an error bound and stability analysis of the proposed DVM algorithm. The proposed efficient DVM algorithm is aimed at implementation using analog realizations. For purposes of evaluation, the algorithm can be realized using both digital hardware as well as software defined radio platforms.

Keywords
Delay Vandermonde matrix, Efficient algorithms, Self-recursive algorithms, Complexity and performance of algorithms, Approximation algorithms, Wireless communications, Beamforming, Software defined radio

1 Introduction

The demand for wireless data communication networks having increased capacity and data transfer rates is growing at a tremendous rate. The wireless networks are on the verge of rolling out their newest generation of mobile networks; the so-called fifth generation (5G) network, which is expected to be the underlying data transfer network of emerging technologies such as the wireless internet of things (IoT), networks on chip, body-area networks and cyberphysical systems (CPS). Exponentially growing demands for capacity require exponentially growing bandwidth for a given SNR level. Emerging 5G and 6G wireless networks are built on mm-wave (mmW) bands (typically, 20-600 GHz), which are of sufficiently high frequencies to allow the necessary growth in system bandwidth. A 5G wireless data connection may operate around 60 GHz (indoors) and operate at about 200-1000 MHz of bandwidth, which shows 10- to 25-fold growth in capacity [29]. Such

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levels of growth are typical of a new generation of wireless networks. In this paper, we address the problem of obtaining a multitude of directional mmW RF beams using digital signal processing. The paper aims to reduce to arithmetic complexity of the beamforming operation that is based on multiplication of a Vandermonde matrix with an input vector obtained from the array of antennas. The proposed multi-beam signal processor is uses a low-complexity algorithm that enables acceptable digital circuit complexity in order to achieve multi-beam RF beams for a base-station.

The paper is organized as follows. Section 2 introduces the concept of wideband multi-beam beamforming for wireless communications. Section 3 proposes a self-contained factorization for the DVM and an efficient DVM algorithm, while Section 4 contains the derivation of arithmetic complexity and elaborate numerical results of the proposed DVM algorithm. Section 5 furnishes the derivation of a theoretical error bound, establishes numerical results, and addresses the numerical stability of the proposed DVM algorithm. In Section 6, applications of the DVM matrix factorization in the engineering discipline will be discussed. Finally, Section 7 concludes the paper.

2 Multi-Beam Wideband Array Signal Processing

2.1 Review of Antenna Beamforming

The coherent combination of multiple antennas is known as beamforming [15,30]. Fig.1 shows an overview of an array processing receiver that operates on an $N$-element array of antennas by applying a spatial linear transform $A_N$ across the array outputs $x_k(t), k = 1, 2, \ldots, N$ in continuous-time to produce a number of continuous-time outputs $y_k(t)$ corresponding to the RF beams.

The linear transform typically takes the form of a spatial discrete Fourier transform, implemented via spatial FFT, and leads to $N$ number of RF beams corresponding to directions pertaining to spatial frequencies of the incident waves that match DFT bin frequencies $2\pi k/N$. The use of an FFT produces beams that have a frequency dependent axis because it can be shown that the beam orientation is a function of wavelength.

By progressively delaying each antenna by a multiple of a constant time delay, the RF energy can be directed in a particular direction in a frequency independent manner. For example, let $X_k(e^{j\omega})$ be the Fourier transform of the input $x_k(t)$ for the array outputs $k = 1, 2, \ldots, N$. The application of a linear delay of duration $\tau$ causes a corresponding phase rotation by $\omega \tau$. In the frequency domain, the output of the delay becomes $X_k(e^{j\omega})e^{-j\omega \tau}$. The application of delays $\tau kl$ to the $k$th antenna, where $l \in \mathbb{Z}$ is an integer causes signal components to be rotated by the frequency dependent phase $\omega kl$. Delay and sum operations - described by a Vandermonde matrix (DVM) by input vector product- leads to $N$ RF beams that have beam orientations $\psi_k, k = 1, 2, \ldots, N$ that have no dependence on the wavelength.

Example beamformers for arrays can be found in [2,3,17,18,26,32,39,40,43,45]. These systems are limited in number of beams, although they do possess relatively high numbers of antennas, due to the inherent computational/circuit complexity of such multi-beam systems. Unlike DFT beamformers, delay-based Vandermonde matrix multi-beam beamformers have frequency independent beam angles- a property that is desired in wideband basestations.
Figure 1: A linear array with analog antenna outputs $x_k(t)$ for achieving $N$-beam wideband multi-beam beamformer using left) an analog DVM circuit that realizes a spatial linear transform $y = A_N x$ employing true time delay analog blocks for RF beams $y_k(t)$; and right) the digital signal flow graph for a parallel digital hardware or software defined radio approach in which an $M$-point discrete Fourier transform (DFT) is computed along the temporal dimension for binning the sampled wideband signals in temporal frequency domain. Each signal component temporal frequency bin is applied to the DVM algorithm by computing the efficient DVM algorithm for $M$ separate values of $\alpha = e^{-j\omega \tau}$ where $\omega = \frac{2\pi m}{N}$. In both cases, the DVM algorithm leads to $N$-beams by coherently combining radio waves in $N$ discrete directions $\psi_k = 1, 2, \ldots, N$. Here, temporal sample blocks consist of $M$ samples for the fast Fourier transforms (FFTs).
2.2 Wideband N-Beam Algorithms

For a linear-array with inter-element spacing $\Delta x$ and speed of light $c$, the marginal time-difference of arrival between antennas for direction $\psi$ is $\tau = \frac{\Delta x}{c} \sin \psi$. The $k$th true-time-delay RF beam can be realized by coherently summing the antenna signals $l = 0, 1, \cdots, N - 1$ such that the beam output is $y_k(t) = x_0(t) + x_1(t - k\tau) + \cdots + x(t - k(N - 1)\tau)$.

Assuming inter-element spacing $\Delta x = \lambda_{\text{min}}/2$, the $N$ simultaneous RF beams in the discrete domain can be written as the product of an $N \times N$ DVM containing the frequency-dependent phase-rotations and an input signal vector with elements $x_k(t)$ consisting of the spatial signals from the uniform linear array of antennas.

The wideband multi-beam beamforming algorithm therefore consists of the computation of a DVM-vector product $y = \mathbf{A}_N \mathbf{x}$ at time $t \in \mathbb{R}$ where $\mathbf{x}$ and $\mathbf{y}$ are input and output vectors containing signals $x_k(t)$ and $y_k(t)$ respectively. In our previous work [26], we have proposed a low-complexity DVM algorithm using the product of complex 1-band upper and lower matrices. The DVM algorithm in [26] extends the results in [21, 22, 42] utilizing complex nodes without considering quasiseparability and displacement equations as in [12, 13, 33]. Moreover, we have addressed error bounds and stability of the DVM algorithm in [26] by filling the gaps in [21, 22, 42]. There are several mathematical techniques available to derive radix-2 and split-radix FFT algorithms, as described in [5, 9, 16, 28, 33, 38]. On the other hand, among the known approximation algorithms which run in quadratic arithmetic time to compute Vandermonde matrices by a vector, the author in [25] presented linear arithmetic time approximation algorithm to compute Vandermonde matrices by vector. But our main intention in this paper is to propose an exact algorithm with self-contained factors not an approximation algorithm.

Even though the derivation of size $N$ DFT into two size $\frac{N}{2}$ DFTs can be done easily, the extension of this idea to the DVM is cumbersome as the useful DFT properties are not necessarily present in the DVM case. However, one could still derive an efficient algorithm for the delay Vandermonde matrix as an polynomial evaluation problem.

For wideband analog inputs where $\omega$ spans a continuous range of values, the phase rotations are to be realized using analog delay lines. In analog realizations the DVM fast algorithm is realized in an analog circuit consisting of wideband delays, amplifiers and adders. The low arithmetic complexity of the proposed algorithms will result in correspondingly low circuit complexity for wideband analog multi-beam beamforming circuits. For purposes of fast verification using software models, the proposed DVM algorithms assumes a particular input frequency (i.e., a constant $\omega$) for the incident waves. Such a simplification allows us to compute the relevant matrix-vector product using a computer based numerical simulation model.

During software-based numerical verification, we assume the input signals are over-sampled in time. This is because all computer models much be discrete in time. For sampled digital signal processing systems, the algorithms can be applied at a particular value of $\omega$ in the temporal frequency domain by first computing temporal FFTs for the antenna channels, and then applying the DVM algorithm for each bin of a temporal $M$-point DFTs. In such temporally-sampled software/digital implementations, each input $x_k(t)$ becomes $x_k(e^{-j2\pi m/M}, t), m = 0, 1, \cdots, M - 1$ and corresponding outputs $y_k(e^{-j2\pi m/M}, t)$ where $t = M\Delta T n$ for temporal sample period $T$ and $M$-point temporal FFT block number $n$. There needs to be $M$ parallel digital circuits, or $M$ calls to the DVM algorithm in software realizations of the fast algorithm to process all of the temporal frequency bins. There will be call to the DVM algorithm for each temporal FFT output bin at $e^{-j2\pi m/M}$ in order to support wideband operation, and therefore $M$ number of calls to the algorithm in order to process wideband signals transformed by $M$-point temporal FFTs. In our analysis, the code implementations assume
Now, we consider \((1,2)\), \((2,1)\), and \((2,2)\) blocks of \(P\). For \((1,2)\) block of \((2)\) we get: product of diagonal matrices.

Proof: We show (1) by divide-and-conquer technique. We first permute rows of \(\tilde{A}\) and \(C\) where \(\tilde{A} = [a^k]_{k,l=0}^{N-1}\) and \(D_N = \text{diag}(a^k)_{k=0}^{N-1}\). In the following, we will provide a self-contained sparse factorization for \(\tilde{A}\) followed by the DVM (i.e. \(A_N\)) over complex nodes \(a, a^2, \ldots, a^N\).

Lemma 3.1. Let the scaled delay Vandermonde matrix \(\tilde{A}_{N,a} = [a^k]_{k,l=0}^{N-1}\) be defined by nodes \(\{1, a, a^2, \ldots, a^{N-1}\} \in \mathbb{C}\) and \(N = 2^t (t \geq 1)\). Then \(\tilde{A}_{N,a}\) can be factored into

\[
\tilde{A}_{N,a} = P_N \begin{bmatrix} \tilde{A}_{1/2,a^2} & \tilde{I}_{1/2} & C_{1/2} \\ \tilde{D}_{1/2} & \alpha \tilde{C}_{1/2} \tilde{D}_{1/2} \end{bmatrix},
\]

where \(P_N\) is the even-odd permutation matrix, \(\tilde{A}_{1/2,a^2} = [a^{2k}]_{k,l=0}^{N-1}\) \(\tilde{I}_{1/2}\) is the identity matrix, \(\tilde{D}_{1/2} = \text{diag}(a^{k})_{k=0}^{N-1}\), and \(C_{1/2}\) is the companion matrix defined by the monic polynomial \(p(z) = (z-1)(z-a^2)(z-a^4)\ldots(z-a^{N-2})\).

Proof: We show (1) by divide-and-conquer technique. We first permute rows of \(\tilde{A}\) by multiplying with \(P_N\) and then write the result as the block matrices:

\[
P_N \tilde{A}_{N,a} = \begin{bmatrix} a^{2k} & a^{2(k+1)} \\ a^{2k+1} & a^{2(k+1)} \end{bmatrix}\begin{bmatrix} \tilde{A}_{1/2,a^2} & \tilde{I}_{1/2} & C_{1/2} \\ \tilde{D}_{1/2} & \alpha \tilde{C}_{1/2} \tilde{D}_{1/2} \end{bmatrix}.
\]

Now, we consider (1,2), (2,1), and (2,2) blocks of \(P_N\tilde{A}_{N,a}\) and represent each of these by \(\tilde{A}_{1/2,a^2}\) and the product of diagonal matrices.

For (1,2) block of \(2\) we get:

\[
a^{2k} a^{k} = \text{diag}(a^{kN})_{k=0}^{N-1} \cdot a^{2k}.
\]

3 Self-Contained Factorizations and fast DVM Beamforming Algorithm

The DVM \(A_N = [e^{-rk\tau}]_{k,l=0}^{N,N-1}\) is a Vandermonde structured matrix with complex entries. Here, we defined \(a = e^{-j\omega \tau}\) for notational convenience. Recall \(\tau = \Delta x/c\) is a time delay. On the other hand, the DFT matrix is also a well known Vandermonde structured matrix having \(N\)th roots of unity as nodes. In contrast to the DFT, however, the DVM does not necessarily possess nice properties, such as unitary, periodicity, symmetry, and circular shift.

The DVM is defined using distinct complex nodes \(a, a^2, \ldots, a^N\) and hence it is non-singular. The matrix \(A_N\) can be scaled as \(A_N = \tilde{A}_N D_N\), where \(\tilde{A}_N = [a^k]_{k,l=0}^{N-1}\) and \(D_N = \text{diag}(a^k)_{k=0}^{N-1}\). In the following, we will provide a self-contained sparse factorization for \(\tilde{A}_N\) followed by the DVM (i.e. \(A_N\)) over complex nodes \(a, a^2, \ldots, a^N\).

Let the scaled delay Vandermonde matrix \(\tilde{A}_{N,a} = [a^k]_{k,l=0}^{N-1}\) be defined by nodes \(\{1, a, a^2, \ldots, a^{N-1}\} \in \mathbb{C}\) and \(N = 2^t (t \geq 1)\). Then \(\tilde{A}_{N,a}\) can be factored into

\[
\tilde{A}_{N,a} = P_N \begin{bmatrix} \tilde{A}_{1/2,a^2} & \tilde{I}_{1/2} & C_{1/2} \\ \tilde{D}_{1/2} & \alpha \tilde{C}_{1/2} \tilde{D}_{1/2} \end{bmatrix},
\]

where \(P_N\) is the even-odd permutation matrix, \(\tilde{A}_{1/2,a^2} = [a^{2k}]_{k,l=0}^{N-1}\) \(\tilde{I}_{1/2}\) is the identity matrix, \(\tilde{D}_{1/2} = \text{diag}(a^{k})_{k=0}^{N-1}\), and \(C_{1/2}\) is the companion matrix defined by the monic polynomial \(p(z) = (z-1)(z-a^2)(z-a^4)\ldots(z-a^{N-2})\).

Proof: We show (1) by divide-and-conquer technique. We first permute rows of \(\tilde{A}\) by multiplying with \(P_N\) and then write the result as the block matrices:

\[
P_N \tilde{A}_{N,a} = \begin{bmatrix} a^{2k} & a^{2(k+1)} \\ a^{2k+1} & a^{2(k+1)} \end{bmatrix}\begin{bmatrix} \tilde{A}_{1/2,a^2} & \tilde{I}_{1/2} & C_{1/2} \\ \tilde{D}_{1/2} & \alpha \tilde{C}_{1/2} \tilde{D}_{1/2} \end{bmatrix}.
\]
For $(2,1)$ block of (2) we get:
\[
\left[ a^{(2k+1)l} \right]_{k,l=0}^{N-1} = a^{2kl} \left[ a^{(2kl)} \right]_{k,l=0}^{N-1} \cdot \text{diag}(a^1)_{l=0}^{N-1}.
\] (4)

For $(2,2)$ block of (2) we get:
\[
\left[ a^{(2k+1)(N+l)} \right]_{k,l=0}^{N-1} = a^{N} \text{diag}(a^{kN})_{k=0}^{N-1} \left[ a^{2kl} \right]_{k,l=0}^{N-1} \cdot \text{diag}(a^1)_{l=0}^{N-1}.
\] (5)

Thus by (3), (4), and (5) we can state (2) as:
\[
P_N \hat{A}_{N,a} = \begin{bmatrix}
    \hat{A}_{N,a}^{N,a^2} & \hat{D}_{N,a}^{N,a^2} \\
    \hat{D}_{N,a}^{N,a^2} & \hat{D}_{N,a}^{N,a^2}
\end{bmatrix},
\]

where \( \hat{D}_{N,a} = \text{diag}(a^{kN})_{k=0}^{N-1} \) and \( \hat{D}_{N,a} = \text{diag}(a^1)_{l=0}^{N-1} \). Set \( p(z) = (z-1)(z-a^2)(z-a^4) \cdots (z-a^{N-2}) = z^{N} + \sum_{i=0}^{N-1} w_i z^i \)
where \( w_i, i \in \mathbb{C} \). The following equality holds
\[
\hat{A}_{N,a}^{N,a^2} \mathcal{C}_{N,a}^{N,a^2} = \hat{D}_{N,a}^{N,a^2} \hat{A}_{N,a}^{N,a^2},
\] (6)

where
\[
\mathcal{C}_{N,a}^{N,a^2} = \begin{bmatrix}
    0 & 0 & \cdots & 0 & -w_0 \\
    1 & 0 & \cdots & 0 & -w_1 \\
    0 & 1 & \cdots & 0 & -w_2 \\
    \vdots & \vdots & \ddots & \vdots & \vdots \\
    0 & 0 & \cdots & 1 & -w_{N-1}
\end{bmatrix}
\] (7)

is the companion matrix of the polynomial \( p(z) \) with coefficients \( w_i (i = 0, 1, \cdots, N-1) \) and \( \hat{D}_{N,a} = \text{diag}(a^{kN})_{k=0}^{N-1} \).

By using (6), non-singularity of \( \hat{A}_{N,a}^{N,a^2} \), and induction on \( N \), one can easily show
\[
\hat{A}_{N,a}^{N,a^2} \mathcal{C}_{N,a}^{N,a^2} = \hat{D}_{N,a}^{N,a^2} \hat{A}_{N,a}^{N,a^2}
\] (8)

for any even number \( N \). Note that, \( \hat{D}_{N,a}^{N,a} = \hat{D}_{N,a}. \)
Thus
\[
P_N \hat{A}_{N,a} = \begin{bmatrix}
    \hat{A}_{N,a}^{N,a^2} & \hat{A}_{N,a}^{N,a^2} \mathcal{C}_{N,a}^{N,a^2} \\
    \hat{D}_{N,a}^{N,a^2} & \hat{D}_{N,a}^{N,a^2} \mathcal{C}_{N,a}^{N,a^2}
\end{bmatrix}
\]

and we get the result.

\[ \square \]

**Corollary 3.2.** Let the delay Vandermonde matrix \( A_{N,a} = [a^{kl}]_{k=1,l=1}^{N,N-1} \) be defined by nodes \( \{a, a^2, \cdots, a^N\} \) and
\( N = 2^t (t \geq 1). \) Then the DVM can be factored into

\[
\mathbf{A}_N, \alpha = \mathbf{P}_N^T \begin{bmatrix} \mathbf{A}_N^{\frac{\alpha}{2}, \alpha^2} & \mathbf{A}_N^{\frac{-\alpha}{2}, \alpha^2} \\ \mathbf{I}_N^{\frac{1}{2}} & \mathbf{C}_N^{\frac{1}{2}} \end{bmatrix} \begin{bmatrix} \mathbf{D}_N^{\frac{-1}{2}} \\ \mathbf{D}_N^{\frac{-1}{2}} \end{bmatrix}
\]

where \( \mathbf{A}_N^{\frac{\alpha}{2}, \alpha^2} = [a^{2k}]_{k=0}^{N-1} \) and \( \mathbf{D}_N^{\frac{-1}{2}} = \text{diag} \left[ \frac{1}{a^{2k}} \right]_{k=0}^{N-1} \).

**Proof:** This can easily be seen through the scaling of (1) by \( \mathbf{D}_N^{\frac{1}{2}} \) and \( \mathbf{D}_N^{\frac{-1}{2}} \). □

Note that in order to compute the companion matrix \( \mathbf{C}_N^{\frac{1}{2}} \) we have to compute the coefficients of the polynomial \( p(z) = (z - 1)(z - a^2)(z - a^4) \cdots (z - a^{N-2}) = z^N + \sum_{i=0}^{N-1} w_i \cdot z^i \). One can do this by setting \( p_N^{(0)}(z) = 1 \) and \( p_N^{(k+1)} = (z - a^{2k})p_N^{(k)} \) for \( k = 0, 1, \ldots, N - 1 \). Then take \( p_N^{(0)}(z) \) which is \( p(z) \). The following lemma gives this procedure.

**Lemma 3.3.** Let \( N \) be an even number, \( W = \{1, z^2, z^4, \cdots, z^{N-2}\} \), and \( q(z) = \sum_{i=1}^{k} w_i \cdot z^{2i} \), where \( k \leq \frac{N}{2} - 1 \). Then the coefficients of \( z^2 \cdot q(z) = \sum_{i=1}^{k+1} w_i \cdot z^{2i} \) can be computed by

\[
\begin{bmatrix} w_0 \\
\vdots \\
w_{k+1} \\
0 \\
\vdots \\
0 \end{bmatrix} = \begin{bmatrix} \mathbf{Z} & \mathbf{O}_{\frac{N}{2}-1} \\
\mathbf{e}_{\frac{N}{2}-1} & 0 \end{bmatrix} \begin{bmatrix} v_0 \\
\vdots \\
v_{k+1} \\
0 \\
\vdots \\
0 \end{bmatrix}
\]

where \( \mathbf{Z} = \begin{bmatrix} 0 & 0 & \cdots & 0 & 0 \\
1 & 0 & 0 & \vdots \\
0 & 1 & 0 & \ddots & 0 \\
\vdots & \ddots & \ddots & \ddots & 0 \\
0 & \cdots & 0 & 1 & 0 \end{bmatrix} \) is the lower shift matrix of size \( \left( \frac{N}{2} - 1 \right) \times \left( \frac{N}{2} - 1 \right) \), \( \mathbf{e}_{\frac{N}{2}-1} = \left[ \text{zeros} \left( 1, \frac{N}{2} - 2 \right) \right] \), \( \mathbf{O}_{\frac{N}{2}-1} = \text{zeros} \left( \frac{N}{2} - 1, 1 \right) \).

**Proof:** It is obvious that polynomials in \( W \) satisfy the recurrence relation \( z^k = z^2 \cdot z^{k-1} \) for \( k = 1, 2, \cdots, \frac{N}{2} - 1 \) with \( z^0 = 1 \). By matrix multiplication we can easily get:

\[
\begin{pmatrix} z^2 & z^4 & \cdots & z^{N-2} \\
1 & z^2 & z^4 & \cdots & z^{N-2} \\
0 & \cdots & 0 \end{pmatrix} = \begin{bmatrix} \mathbf{Z} & \mathbf{O}_{\frac{N}{2}-1} \\
\mathbf{e}_{\frac{N}{2}-1} & 0 \end{bmatrix}
\]

(11)
Multiplying (11) by the column of the coefficients we get the result. □

Lemma 3.3 can be used to compute the coefficients of the polynomial \( p(z) = (z - 1)(z - a^2)(z - a^4) \cdots (z - a^{N-2}) = z^{N/2} + \sum_{i=0}^{N/2-1} w_i \cdot z^i \) efficiently. Hence the companion matrix \( C_{N/2} \) can be computed efficiently using the Lemma 3.3.

To compute the self-contained DVM factorization, first we calculate the powers of the companion matrix. We will use the following result for the calculation of \( C_{N/2}^{m/2} \).

**Corollary 3.4.** Let \( N = 2^t (t \geq 2) \), \( m = 2^k (k \geq 2) \), and \( C_{N/2} = \begin{bmatrix} 0 & 0 & \cdots & 0 & -w_0 \\ 1 & 0 & \cdots & 0 & -w_1 \\ 0 & 1 & \cdots & 0 & -w_2 \\ \vdots & \vdots & \ddots & \vdots & \vdots \\ 0 & 0 & \cdots & 1 & -w_{N/2-1} \end{bmatrix} \). Then \( C_{N/2}^{m/2} \) can be computed via

\[
C_{N/2}^{m/2} = C_{N/2}^{m/2} \cdot C_{N/2}^{m/2},
\]

for \( 2 \leq m \leq N/2 \), where \( w_i \) for \( i = 0, 1, \cdots, N/2-1 \) are computed as in Lemma 3.3.

**Proof:** One can easily use induction for \( k \geq 2 \) to show (12). □

**Remark 3.5.** Although the factorization for the DVM can be stated as in Corollary 3.2, we should recall here that the classical Vandermonde matrix \( V \) is extremely ill-conditioned and in fact the condition number of the matrix \( V \) grows exponentially with the size \([17, 24, 37]\). In this paper, we will study how bad the complex structured DVM can be in terms of the choices for nodes in Section 5.

We will first state the following algorithm based on Lemma 3.3 to compute the coefficients of the polynomial

\[
p(z) = (z - 1)(z - a^2)(z - a^4) \cdots (z - a^{N-2}) = z^{N/2} + \sum_{i=0}^{N/2-1} w_i \cdot z^i.
\]

Later, the coefficients of \( p(z) \) will be used to construct the companion matrix \( C_{N/2} \) defined in (7).

**Algorithm 3.6.** (com(\( N, \alpha \)))

**Input:** Even \( N \), and \( \alpha \in \mathbb{C} \)

1. Set \( \begin{bmatrix} w^{(0)}_0 & w^{(0)}_1 & \cdots & w^{(0)}_{N/2-1} \end{bmatrix} = \begin{bmatrix} 1 & 0 & \cdots & 0 \end{bmatrix} \)

2. For \( k = 1 : N_1 - 1 \),

\[
\begin{bmatrix} w^{(k)}_0 \\ w^{(k)}_1 \\ \vdots \\ w^{(k)}_{N/2-1} \end{bmatrix} = \left( \begin{bmatrix} Z & \mathbf{0}_{N_1 - 1} \\ \mathbf{e}_{N_1 - 1} & 0 \end{bmatrix} - \alpha^{2(k-1)} \cdot I \right) \begin{bmatrix} w^{(k-1)}_0 \\ w^{(k-1)}_1 \\ \vdots \\ w^{(k-1)}_{N/2-1} \end{bmatrix}
\]
3. Take \( \begin{bmatrix} w_0 & w_1 & \cdots & w_{N-1} \end{bmatrix} = \begin{bmatrix} w_0^{(N-1)} & w_1^{(N-1)} & \cdots & w_{N-1}^{(N-1)} \end{bmatrix} \)

Output: Coefficients of \( p(z) \) (except the leading coefficient as \( p(z) \) is monic) i.e. \( (w_0, w_1, \ldots, w_{N-1}) \) satisfying (73).

We will use the output of algorithm 3.6 (i.e. \text{com}(N, \alpha)) to construct the companion matrix \( C_{N_1} \) (7). Following the self-contained DVM factorization (9), one has to compute the powers of the companion matrix \( C_{N_1} \) (7). Corollary 3.4 suggests the following algorithm to compute \( C_{N_1}^m \) for \( 2 \leq m \leq N_1 \), where \( m = 2^t (t_1 \geq 1) \).

**Algorithm 3.7.** (\text{comp}(N, \alpha))

*Input:* \( N = 2^t (t \geq 1) \), \( N_1 = \frac{N}{2} \), and \( \alpha \in \mathbb{C} \)

1. Set \( \mathbf{w} = \begin{bmatrix} w_0 & w_1 & \cdots & w_{N_1-1} \end{bmatrix} \) and \( C_{N_1} = \begin{bmatrix} \mathbf{Z} & \mathbf{w} \\ \mathbf{e}_{N_1-1} \end{bmatrix} \)

2. for \( m = 2 : N_1 \)
   \( C_{N_1}^m = C_{N_1}^\frac{m}{2} C_{N_1}^\frac{m}{2} \)
end

Output: \( C_{N_1}^{N_1} \).

We will use the output of the Algorithm 3.7 (i.e. \text{comp}(N, \alpha)) to construct \( \tilde{C}_N \) s.t. \( \tilde{C}_N = \begin{bmatrix} I_{N_1} & C_{N_1}^{N_1} \\ \tilde{D}_{N_1} & a^{N_1} C_{N_1}^{N_1} \tilde{D}_{N_1} \end{bmatrix} \) for all \( N \geq 4 \). The self-contained factorization for the scaled DVM i.e. Lemma 3.1 together with algorithms 3.6 and 3.7 lead us to establish a recursive radix-2 scaled DVM algorithm to compute \( \tilde{A}_{N, \alpha} = [\alpha^{k+1}]_{k=0}^{N-1} \) as stated next.

**Algorithm 3.8.** (\text{sdvm}(N, \alpha, z))

*Input:* \( N = 2^t (t \geq 1) \), \( N_1 = \frac{N}{2} \), \( \alpha \in \mathbb{C} \), and \( z \in \mathbb{R}^n \) or \( \mathbb{C}^n \).

1. Set \( \tilde{C}_N \).

2. If \( N = 2 \), then
   \( y = \begin{bmatrix} 1 & 1 \\ 1 & \alpha \end{bmatrix} z \).

3. If \( N \geq 4 \), then
   \( u := \tilde{C}_N z, \)
   \( v_1 := \text{sdvm}(N_1, a^2, [u_i]_{i=0}^{N_1-1}), \)
   \( v_2 := \text{sdvm}(N_1, a^2, [u_i]_{i=0}^{N_1}), \)
   \( y := P_N^T (v_1^T, v_2^T)^T. \)

Output: \( y = \tilde{A}_{N, \alpha} z \).

**Remark 3.9.** Recall that the delay Vandermonde matrix (i.e. \( A_{N, \alpha} \)) and scaled delay Vandermonde matrix (i.e. \( \tilde{A}_{N, \alpha} \)) is related via \( A_{N, \alpha} = \tilde{A}_{N, \alpha} \cdot D_N \), where \( D_N = \text{diag}(a^k)_{k=0}^{N-1} \). Thus, once the algorithm 3.8 is executed, we can scale the output of the algorithm by \( D_N \) to obtain a DVM algorithm. Although the computational cost of the DVM algorithm reduces in this fashion, the resulting DVM algorithm won’t be self-recursive.
In the following we will state a recursive radix-2 DVM algorithm with the help of Corollary 3.2, algorithms 3.6, and 3.7. For notation convenience, we define \( \bar{D}_N = \begin{cases} \bar{D}_N, & \text{for } N \geq 4 \end{cases} \)

Algorithm 3.10. \((dvm(N, \alpha, z))\)

**Input:** \(N = 2^t (t \geq 1), N_1 = \frac{N}{2}, \alpha \in \mathbb{C}, \) and \(z \in \mathbb{R}^n \text{ or } \mathbb{C}^n.\)

1. Set \(D_N, \bar{C}_N, \) and \(\bar{D}_N.\)
2. If \(N = 2, \) then
   \[ y = \begin{bmatrix} 1 & \alpha \\ 1 & \alpha^2 \end{bmatrix} z. \]
3. If \(N \geq 4, \) then
   \( u := D_N z, \)
   \( v := \bar{C}_N u, \)
   \( r := \bar{D}_N v, \)
   \( s1 := dvm(N_1, \alpha^2, [r_i]_{i=0}^{N_1-1}), \)
   \( s2 := dvm(N_1, \alpha^2, [r_i]_{i=N_1}^N), \)
   \( y := P_T^N(s1^T, s2^T)^T. \)

**Output:** \(y = A_{N,a} z.\)

## 4 Complexity of DVM Algorithms

The number of additions and multiplications required to carry out a computation is called the arithmetic complexity. In this section the arithmetic complexities of the proposed self-recursive scaled DVM and DVM algorithms are established.

### 4.1 Arithmetic Complexity of DVM Algorithms

Here we analyze the arithmetic complexity of the self-recursive scaled DVM and DVM algorithms presented in Section 3. Let \(\#a\) and \(\#m\) denote the number of complex additions and complex multiplications, respectively, required to compute \(y = \tilde{A}_{N,a} z\) or \(y = A_{N,a} z\) for scaled DVM and DVM. Note that we do not count multiplication by \(\pm 1, \pm \sqrt{-1},\) and permutation.

**Lemma 4.1.** Let \(N = 2^t (t \geq 2)\) be given. The arithmetic complexity on computing the scaled DVM algorithm 3.8 is given by

\[
\#a(sDVM, N) = \frac{1}{2} (Nt + 4^t - N), \\
\#m(sDVM, N) = \frac{3}{2} N t + \frac{1}{2} 4^t - 2N. \tag{14}
\]

**Proof:** Referring to the \(sdvm(N, \alpha, z)\) algorithm, we get

\[
\#a(sDVM, N) = 2 \cdot \#a\left(sDVM, \frac{N}{2}\right) + \#a\left(\tilde{C}_N\right). \tag{15}
\]
The matrix $\tilde{C}$ is constructed using $\tilde{D}$ and $C^N$. Moreover, to compute the powers of the Companion matrix $C^N$ we have used the divide-and-conquer technique via algorithm $\text{comp}(N, \alpha)$. Since $m = 2^t$ in algorithm $\text{comp}(N, \alpha)$, by solving a homogeneous first order linear difference equation with respect to $t$ such as $m = 2^t(t \geq 1)$ solving $\#a/\#m(C^m, 2^t) - 2 \cdot \#a/\#m(C^m, 2^{t-1}) = 0$ with initial condition $\#a(C^m, 2) = m - 1$ or $\#m(C^m, 2) = m$ respectively, we could obtain $\#a(C^m) = \frac{m^2}{2} - \frac{m}{2}$ and $\#m(C^m) = \frac{m^2}{2}$. This fact together with the construction of $\tilde{C}$ using $\tilde{D}$ and $C^N$, and $m = \frac{N^2}{4}$ gives us:

\begin{align*}
\#a(C_N) &= \frac{N^2}{4} + \frac{N}{2}, \\
\#m(C_N) &= \frac{N^2}{4} + \frac{3N}{2} 
\end{align*}

(16)

Using the above result we can write (15) as

\begin{align*}
\#a(sDVM, N) &= 2 \cdot \#a(sDVM, \frac{N}{2}) + \frac{N^2}{4} + \frac{N}{2}
\end{align*}

Since $N = 2^t$, the above simplifies to the first order difference equation with respect to $t \geq 2$

\begin{align*}
\#a(sDVM, 2^t) - 2 \cdot \#a(sDVM, 2^{t-1}) &= 4^{t-1} + 2^{t-1}.
\end{align*}

Solving the above difference equation using the initial condition $\#a(sDVM, 2) = 2$, we can obtain

\begin{align*}
\#a(sDVM, 2^t) &= \frac{1}{2} Nt + \frac{1}{2} 4^t - \frac{1}{2} N.
\end{align*}

Referring the scaled DVM algorithm 3.8 and (16), we could obtain another first order difference equation with respect to $t \geq 2$

\begin{align*}
\#m(sDVM, 2^t) - 2 \cdot \#m(sDVM, 2^{t-1}) &= 4^{t-1} + 3 \cdot 2^{t-1}.
\end{align*}

Solving the above difference equation using the initial condition $\#m(sDVM, 2) = 1$, we can obtain

\begin{align*}
\#m(sDVM, 2^t) &= \frac{3}{2} Nt + \frac{1}{2} 4^t - 2N.
\end{align*}

\[\square\]

**Lemma 4.2.** Let $N = 2^t(\geq 2)$, The arithmetic complexity on computing the DVM algorithm 3.10 is given by

\begin{align*}
\#a(DVM, N) &= \frac{1}{2} (Nt + 4^t - N), \\
\#m(DVM, N) &= \frac{7}{2} Nt + \frac{1}{2} 4^t - \frac{7}{2} N. 
\end{align*}

(17)

**Proof:** Referring to the $\text{dvm}(N, \alpha, z)$ algorithm, we get

\begin{align*}
\#a(DVM, N) &= 2 \cdot \#a(DVM, \frac{N}{2}) + \#a(D_N) + \\
\#a(C_N) + \#a(\tilde{D}_N)
\end{align*}

(18)
By following the structures of $D_N$ and $\bar{D}_N$ we get
\[
\#a(D_N) = 0, \quad m(D_N) = N \\
\#a(\bar{D}_N) = 0, \quad m(\bar{D}_N) = N
\]  
(19)

Thus by using the above and (16), we could state (18) as the first order difference equation with respect to $t \geq 1$
\[
\#a(DVM, 2^t) - 2 \cdot #a(DVM, 2^{t-1}) = 4^{t-1} + 2^{t-1}.
\]

Solving the above difference equation using the initial condition $#a(DVM, 2) = 2$, we can obtain
\[
#a(DVM, 2^t) = \frac{1}{2} Nt + \frac{1}{2} 4^t - \frac{1}{2} N.
\]

Now by using the $dvm(N, \alpha, z)$ algorithm, (16), and (19), we could obtain another first order difference equation with respect to $t \geq 2$
\[
#m(DVM, 2^t) - 2 \cdot #m(DVM, 2^{t-1}) = 4^{t-1} + 7 \cdot 2^{t-1}.
\]

Solving the above difference equation using the initial condition $#m(DVM, 2) = 2$, we can obtain
\[
#m(DVM, 2^t) = \frac{7}{2} Nt + \frac{1}{2} 4^t - \frac{7}{2} N.
\]

\[\Box\]

4.2 Numerical Results for the Complexity of DVM Algorithms

Numerical results for the arithmetic complexity of the proposed algorithms derived via Lemma 4.1 and 4.2 will be shown in this section. Figure 2 shows the arithmetic complexity of the proposed algorithms vs the direct matrix-vector computations with the matrix size varying from $4 \times 4$ to $4096 \times 4096$. We consider the direct computation of the matrix $\tilde{A}_N$ by the vector $z$ cost $N(N - 1)$ additions and multiplications (refer to Direct sDVM in Figure 2) and, the matrix $A_N$ by the vector $z$ cost $N(N - 1)$ additions and $N^2$ multiplications (refer to Direct DVM in Figure 2).

Following the Figure 2, the scaled DVM and DVM algorithms have the same addition counts and the similar multiplication counts. When the size of the matrices increases the proposed algorithms require fewer addition and multiplication counts as opposed to the direct matrix-vector computation. Moreover, for large $N$, the proposed algorithms have saved $\approx 50\%$ of addition and multiplication counts as opposed to the direct brute-force matrix-vector calculation. As we couldn’t distinguish the explicit addition and multiplication counts between the proposed algorithms through the Figure 2, we have included the explicit counts using the Tables 2 and 3 in Appendix A. These counts are based on the results obtained in Lemma 4.1 and 4.2.
Figure 2: Addition and multiplication counts in computing the scaled DVM and DVM algorithms vs the direct matrix-vector computation.
5 Analytic and Numerical Error Bounds of DVM Algorithms

5.1 Theoretical Bounds

Error bounds of computing the scaled DVM and DVM algorithms is the main concern in this section. To do so, we use the perturbation of the product of matrices (stated in [14]). Following the sdvm$(N,\alpha, \mathbf{z})$ and dvm$(N,\alpha, \mathbf{z})$ algorithms, we have to compute weights $\alpha^k$ for $k=0,1,\ldots,N-1$. These weights affect the accuracy of the DVM algorithms. Thus, we will assume that the computed weights $\hat{\alpha}^k$ are used and satisfy for all $k=0,1,\ldots,N-1$

$$\hat{\alpha}^k = \alpha^k + \epsilon_k, \quad |\epsilon_k| \leq \mu, \tag{20}$$

where $\mu := cu$, $u$ is the unit roundoff, and $c$ is a constant that depends on the method [38].

Let’s recall the perturbation of the product of matrices stated in [14, Lemma 3.7] i.e. if $A_k + \Delta A_k \in \mathbb{R}^{N \times N}$ satisfies $|\Delta A_k| \leq \delta_k |A_k|$ for all $k$, then

$$\left| \prod_{k=0}^{m} (A_k + \Delta A_k) - \prod_{k=0}^{m} A_k \right| \leq \left( \prod_{k=0}^{m} (1 + \delta_k) - 1 \right) \prod_{k=0}^{m} |A_k|,$$

where $|\delta_k| < u$. Moreover, recall

$$\prod_{k=1}^{N} (1 + \delta_k)^{\pm 1} = 1 + \theta_N \quad \text{where} \quad |\theta_N| \leq \frac{N\nu}{1-Nu} =: \gamma_N \quad \text{and} \quad \gamma_k + u \leq \gamma_{k+1}, \quad \gamma_k + \gamma_j \leq \gamma_{k+j} \quad \text{from} \quad [14 \text{ Lemma 3.1 and Lemma 3.3]},$$

and for $x, y \in \mathbb{C}$, $f I (x \pm y) = (x + y)(1 + \delta)$ where $|\delta| \leq u$, $f I (x y) = (x y)(1 + \delta)$ where $|\delta| \leq \sqrt{2} y_2$ from [14, Lemma 3.5].

In the following, we will prove the error bound on computing the scaled DVM and DVM algorithms.

**Theorem 5.1.** Let $\hat{\mathbf{y}} = f I(\hat{A}_N \mathbf{z})$, where $N = 2^t (t \geq 2)$, be computed using the sdvm$(N,\alpha, \mathbf{z})$ algorithm, and assume that (20) holds. Then

$$|y - \hat{\mathbf{y}}| \leq \frac{1}{1-t\eta} |P(0)||P(1)||P(t-2)||\hat{A}(t-1)| \left| \hat{C}(t-2)\cdots\hat{C}(1)|\hat{C}(0)||\mathbf{z} \right|,$$

where $\eta = (\mu + \gamma 2^t(1+\mu))$.

**Proof:** Using the sdvm$(N,\alpha, \mathbf{z})$ algorithm and the computed matrices $\hat{C}(s)$ at the step numbers (number of executions/iterations of the algorithm) $s = 0,1,2,\cdots,t-2$ in terms of computed weights $\hat{\alpha}^k$ for $k = 0,1,\ldots,N-1$, we get

$$\hat{\mathbf{y}} = f I \left( \hat{P}(0)\hat{P}(1)\cdots\hat{P}(t-2)\hat{A}(t-1) \right. $$

$$\left. \hat{C}(t-2)\cdots\hat{C}(1)|\hat{C}(0)||\mathbf{z} \right|,$$

where $\hat{P}(s) := 2^s$ block diagonal matrices of $P_{2^t-s}$ and $\hat{C}(s) := 2^s$ computed block diagonal matrices of $C_{2^t-s}$. Using the fact that each $\hat{C}(s)$ is computed using the powers of companion matrix $C_{2^t}$ with $2^{t-1-s}$ non-zero
entry per row, $\hat{D}$ with each one having one non-zero entry per row, and weight $a^k$, we get

$$\left| \Delta \hat{C}(s) \right| \leq \gamma_2^{s-1+3} \left| \hat{C}(s) \right| \text{ for } s = 0, 1, \ldots, t - 2.$$  \hspace{1cm} (22)

with the use of complex arithmetic. By considering the computed weights $\hat{a}^k$ and evaluation at the weight $a^2$ in each step i.e. using \note{20},

$$\hat{C}(s) = \check{C}(s) + \Delta \hat{C}(s), \quad |\Delta \hat{C}(s)| \leq \mu|\check{C}(s)|.$$  \hspace{1cm} (23)

Since $\hat{A}(t-1)$ has $2^{t-1}$ block diagonal matrices of $\begin{bmatrix} 1 & 1 \\ 1 & a \end{bmatrix}$, we get

$$|\Delta \hat{A}(t-1)| \leq \gamma_3 |\hat{A}(t-1)|.$$

By evaluating $\hat{A}(t-1)$ at the weight $a^2$ in each step, we obtain

$$\hat{A}(s) = \check{A}(s) + \Delta \hat{A}(s), \quad |\Delta \hat{A}(s)| \leq \mu|\check{A}(s)|.$$

Thus overall,

$$\hat{y} = P(0)P(1) \cdots P(t-2)(\hat{A}(t-1) + E(t-1))$$

$$(\hat{C}(t-2) + E(t-2)) \cdots (\hat{C}(1) + E(1))\hat{C}(0) + E(0)z,$$

where $|E(s)| \leq (\mu + \gamma_2(1+\mu))|\check{C}(s)|$ for $s = 0, 1, \ldots, (t-2)$ and $|E(t-1)| \leq (\mu + \gamma_3(1+\mu))|\hat{A}(t-1)|$. Let $\eta = (\mu + \gamma_2(1+\mu))$. Hence

$$|y - \hat{y}| \leq \frac{(1 + \eta^t - 1)}{(1 + \eta^t)} |P(0)||P(1)| \cdots |P(t-2)|$$

$$|\hat{A}(t-1)||\hat{C}(t-2)||\cdots||\hat{C}(1)||\hat{C}(0)||z|$$

$$\leq \frac{t \eta}{1 - t \eta} |P(0)||P(1)| \cdots |P(t-2)||\hat{A}(t-1)||\hat{C}(t-2)||\cdots||\hat{C}(1)||\hat{C}(0)||z|$$

Hence the result. \hfill \Box

**Theorem 5.2.** Let $\hat{y} = f(l(A_Nz))$, where $N = 2^t$ $(t \geq 2)$, be computed using the $dvm(N,\alpha,z)$ algorithm, and assume that \note{20} holds. Then

$$|y - \hat{y}| \leq \frac{(3t - 2)\eta}{1 - (3t - 2)\eta} |P(0)||P(1)| \cdots |P(t-2)|$$

$$|A(t-1)||D(t-2)||\cdots||D(1)||D(0)|$$

$$|C(t-2)||\cdots||C(1)||C(0)|$$

$$|D(t-2)||\cdots||D(1)||D(0)||z|$$  \hspace{1cm} (24)

where $\eta = (\mu + \gamma_2(1+\mu))$.

**Proof:** Using the $dvm(N,\alpha,z)$ algorithm and the computed matrices $\hat{D}(s)$, $\hat{C}(s)$, and $\hat{D}(s)$ at the step numbers (execution/iteration step of the algorithm) $s = 0, 1, \ldots, t - 2$ in terms of computed weights $\hat{a}^k$ for $k = 0, 1, \ldots, N - 1$,
we get

\[ \hat{y} = f I \left( \mathbf{P}(0) \mathbf{P}(1) \cdots \mathbf{P}(t-2) \hat{\mathbf{A}}(t-1) \right) \]

\[ \hat{D}(t-2) \cdots \hat{D}(2) \hat{D}(1) \hat{D}(0) \]

\[ \hat{C}(t-2) \cdots \hat{C}_N(2) \hat{C}(1) \hat{C}(0) \]

\[ \hat{D}(t-2) \cdots \hat{D}_N(2) \hat{D}(1) \hat{D}(0) \mathbf{z} \]

\[ = \mathbf{P}(0) \mathbf{P}(1) \cdots \mathbf{P}(t-2) \left( \hat{\mathbf{A}}(t-1) + \Delta \hat{\mathbf{A}}(t-1) \right) \]

\[ \left[ \hat{D}(t-2) + \Delta \hat{D}(t-2) \right] \cdots \left[ \hat{D}(2) + \Delta \hat{D}(2) \right] \]

\[ \left[ \hat{D}(1) + \Delta \hat{D}(1) \right] \left[ \hat{D}(0) + \Delta \hat{D}(0) \right] \]

\[ \left[ \hat{C}(t-2) + \Delta \hat{C}(t-2) \right] \cdots \left[ \hat{C}(2) + \Delta \hat{C}(2) \right] \]

\[ \left[ \hat{C}(1) + \Delta \hat{C}(1) \right] \left[ \hat{C}(0) + \Delta \hat{C}(0) \right] \]

\[ \left[ \hat{D}(t-2) + \Delta \hat{D}(t-2) \right] \cdots \left[ \hat{D}(2) + \Delta \hat{D}(2) \right] \]

\[ \left[ \hat{D}(1) + \Delta \hat{D}(1) \right] \left[ \hat{D}(0) + \Delta \hat{D}(0) \right] \mathbf{z}. \]

where \( \mathbf{P}(s) := 2^s \) block diagonal matrices of \( \mathbf{P}^{T}_{2^s} \), \( \hat{D}(s) := 2^s \) computed block diagonal matrices of \( \hat{\mathbf{D}}_{2^s} \), \( \hat{C}(s) := 2^s \) computed block diagonal matrices of \( \hat{\mathbf{C}}_{2^s} \), and \( \hat{D}(s) := 2^s \) computed block diagonal matrices of \( \hat{\mathbf{D}}_{2^s} \). Using the fact that each \( \hat{\mathbf{D}}(s) \) and \( \hat{\mathbf{D}}(s) \) have one non-zero entry per row and following complex arithmetic, we get

\[ |\Delta \hat{D}(s)| \leq \gamma_2 |\hat{D}(s)| \quad \text{for } s = 0, 1, \ldots, t-2. \]

and

\[ |\Delta \hat{D}(s)| \leq \gamma_2 |\hat{D}(s)| \quad \text{for } s = 0, 1, \ldots, t-2. \]

By considering the computed weights \( \hat{a}^h \) and evaluation at the weight \( a^2 \) in each step i.e. using (20), we have

\[ \hat{D}(s) = \bar{D}(s) + \Delta \hat{D}(s), \quad |\Delta \hat{D}(s)| \leq \mu |\bar{D}(s)| \]

and

\[ \hat{D}(s) = \bar{D}(s) + \Delta \hat{D}(s), \quad |\Delta \bar{D}(s)| \leq \mu |\bar{D}(s)|. \]

Since \( \hat{\mathbf{A}}(t-1) \) has \( 2^{t-1} \) block diagonal matrices of

\[ \begin{bmatrix} 1 & a \\ 1 & a^2 \end{bmatrix}, \]

we get

\[ |\Delta \hat{\mathbf{A}}(t-1)| \leq \gamma_4 |\mathbf{A}(t-1)|. \]

By evaluating \( \hat{\mathbf{A}}(t-1) \) at the weight \( a^2 \) in each step, we obtain

\[ \hat{\mathbf{A}}(s) = \mathbf{A}(s) + \Delta \mathbf{A}(s), \quad |\Delta \mathbf{A}(s)| \leq \mu |\hat{\mathbf{A}}(s)|. \]
Together with (22) and (23) and overall,

\[
\hat{y} = P(0)P(1) \cdots P(t-2)(A(t-1) + E(t-1)) \\
(\tilde{D}(t-2) + E_1(t-2)) \cdots (\tilde{D}(0) + E_1(0)) \\
(\tilde{C}(t-2) + E_2(t-2)) \cdots (\tilde{C}(0) + E_2(0)) \\
(D(t-2) + E_3(t-2)) \cdots (D(0) + E_3(0))z,
\]

where \(|E_1(s)| \leq (\mu + \gamma_2(1 + \mu))|\tilde{D}(s)|\), \(|E_2(s)| \leq (\mu + \gamma_2(1 + \mu))|\tilde{C}(s)|\), and \(|E_3(s)| \leq (\mu + \gamma_2(1 + \mu))|D(s)|\) for \(s = 0, 1, 2, \cdots (t-2)\) and \(|E(t-1)| \leq (\mu + \gamma_4(1 + \mu))|A(t-1)|\). Let \(\eta = (\mu + \gamma_2(1 + \mu))\) and \(\eta_1 = (\mu + \gamma_4(1 + \mu))\). Hence

\[
|y - \hat{y}| \leq (1 + \eta)^{-1}(1 + \eta_1)^{2^{t-1} - 1} |P(0)||\cdots||P(t-2)| \\
|A(t-1)||\tilde{D}(t-2)||\cdots||\tilde{D}(1)||\tilde{D}(0)| \\
|\tilde{C}(t-2)||\cdots||\tilde{C}(1)||\tilde{C}(0)| \\
|D(t-2)||\cdots||D(1)||D(0)||z| \\
\leq \frac{(3t-2)\eta}{1 - (3t-2)\eta} |P(0)||P(1)||\cdots||P(t-2)| \\
|A(t-1)||\tilde{D}(t-2)||\cdots||\tilde{D}(1)||\tilde{D}(0)| \\
|\tilde{C}(t-2)||\cdots||\tilde{C}(1)||\tilde{C}(0)| \\
|D(t-2)||\cdots||D(1)||D(0)||z|
\]

Hence the result. \(\Box\)

Lemma 5.1 shows that the forward error bound of the proposed scaled DVM algorithm depends on the size of the matrices \(N\), norms of the matrices \(\tilde{C}(k)\) for \(k = 0, 1, \cdots, t-2\), and the computed weights. Also, Lemma 5.2 shows that the forward error bound of the proposed DVM algorithm depends on the size of the matrices \(N\), norms of the matrices \(\tilde{D}(k), \tilde{C}(k),\) and \(D(k)\) for \(k = 0, 1, \cdots, t-2\), and the computed weights. Thus, the error bound of the proposed scaled DVM and DVM algorithms rapidly increase with the size of the matrices, norms of the powers of matrices, and powers of \(a\)’s. Hence, the proposed algorithms can not be computed stably for large matrices. This will further be shown through the numerical results in section 5.2.

5.2 Numerical Results

In this section, we state numerical results in connection to the stability of the proposed algorithm 3.10 using MATLAB (R2014a version) with machine precision 2.2204e-16. Forward error results are presented by taking the exact solutions as the output of the scaled DVM or DVM algorithm computed with the double precision and the computed value as the output of the proposed \texttt{sdvm}(N,\alpha,z) or \texttt{dvm}(N,\alpha,z) algorithms with single precision. We will show numerical results for matrix sizes from 4 \times 4 to 128 \times 128 with \(|a| = 1\).

We compare the relative forward error \(e\) of the proposed scaled DVM and DVM algorithms defined by

\[
e = \frac{\|y - \hat{y}\|_2}{\|y\|_2},
\]

where \(y = A_Nz\) or \(y = A_Nz\) is the exact solution computed using the scaled DVM or DVM algorithm, respec-
tively, with double precision and $\hat{y}$ is the computed solution of the algorithms $\text{sdvm}(N, \alpha, z)$ or $\text{dvm}(N, \alpha, z)$, respectively, with single precision.

Table 1 shows numerical results for the forward error of the proposed $\text{sdvm}(N, \alpha, z)$ and $\text{dvm}(N, \alpha, z)$ algorithms with $|\alpha| = 1$, and random real and complex inputs $z_1$ and $z_2$, respectively, of the scaled DVM and DVM, say Err-sDVM and Err-DVM, respectively.

Table 1: Forward error in calculating the scaled DVM and DVM algorithms with $\alpha = e^{-\pi i \frac{32}{\alpha}}$, uniformly distributed random input in the interval $(0,1)$, say $z_1$ for each $N$, and uniformly distributed random input with real and imaginary parts in the interval $(0,1)$ for each $N$, say $z_2$.

| N   | Err-sDVM with $z_1$ | Err-DVM with $z_1$ | Err-sDVM with $z_2$ | Err-DVM with $z_2$ |
|-----|---------------------|---------------------|---------------------|---------------------|
| 4   | 2.367e-08           | 5.648e-08           | 3.577e-08           | 6.855e-08           |
| 8   | 6.118e-08           | 4.952e-08           | 5.959e-08           | 6.820e-08           |
| 16  | 4.676e-08           | 5.529e-08           | 1.010e-08           | 7.449e-08           |
| 32  | 1.022e-08           | 1.262e-07           | 1.067e-08           | 1.279e-07           |
| 64  | 7.008e-08           | 1.138e-07           | 1.568e-07           | 1.373e-07           |
| 128 | NaN                 | NaN                 | NaN                 | NaN                 |

As shown in Table 1, when $\alpha = e^{-\pi i \frac{32}{\alpha}}$ and $N \geq 128$, the MATLAB output will produce NaN for the forward error of the proposed algorithms. This is because the nodes will be repeated and hence the resulting singular matrices while the proposed algorithms are executed for $N > 64$. Even if $\alpha \neq e^{-\pi i \frac{32}{\alpha}}$, but $|\alpha| = 1$ and not a root of unity, we have to compute very large powers of matrices (recall that we compute powers of companion matrices having large powers of $\alpha$'s) and differences of close numbers. Thus, the entries resulting from such operations cannot be represented as conventional floating-point values and hence lead to undefined numerical values through MATLAB output. This is also evident from the theoretical error bounds obtained in section 5.

6 Future Engineering Tasks

6.1 Analog and/or Digital Circuits that Realize the DVM Algorithm

Engineering applications require the real-time implementation of the DVM algorithm using a variety of computational platforms. High-speed applications revolving around wireless communications and radar systems typically necessitate analog implementations, which operate on analog signals from an array of sensors, such as antennas. These analog implementations typically employ approximations to ideal time delays in the signal flow graphs, using techniques such as transmission line segments, passive resistor-capacitor lattice filters, or other types of analog delays. Analog realizations, in their most direct form, utilize microwave transmission lines to implement the delays. A microwave transmission line of length $l$ approximates to sufficient accuracy the time delay $T$ where $T = \alpha l / c$ for which $\alpha \leq 1$ is the velocity factor of the transmission line. Typically, these transmission lines can be a length of cable of copper track (coplanar waveguide) on a printed circuit board. When the physical size requirements necessitate smaller circuits, transmission lines can be approximated using analog all-pass filters that can be implemented using integrated circuits [1][41].

Unlike analog DVM circuits requiring delays, digital DVM implementations, may either be in software, using computer software realizations where the speeds of operation are relatively low (for example, graphics
processor units), or in custom digital hardware integrated circuits, for high-speed realizations based on very large scale integration. In both cases, the true time delays found as a basic building block of the DVM algorithm will be approximated using discrete time interpolation filters [27]. For example, various time delays can be rational fractions of the digital systems clock sample period, and can therefore be approximately realized using both finite impulse response digital interpolation filters as well as infinite impulse response digital interpolation filters. A detailed discussion of the possible approaches for real-time implementation of the DVM algorithms, albeit analog or digital, remains for a future exploration.

6.2 Low-complexity Algorithms based on Matrix Approximation

In several applied contexts, the physics of the problem admits an appreciable level of error tolerance. For instance, this is illustrated in the context of still image compression [6], video encoding [7], beamforming [34], motion tracking [10], and biomedical image processing [8]. Therefore, the exact operation of a given matrix computation can be relaxed into an approximate calculation that is carefully tailored to demand a lower arithmetic complexity when compared to the original exact computation. This can be accomplished by deriving an approximate matrix based on the exact matrix.

Approximate matrices can be designed by several methods, including rough inspection, number representation in dyadic rationals, and integer optimization, to cite a few. Integer optimization is often the method of choice due to its generality. The general framework is described as follows:

$$\hat{T}^* = \arg \min_{\hat{T} \in \mathcal{M}_P(N)} \text{error}(\hat{T}, T)$$

where $T$ is the matrix to be approximated and $\hat{T}$ is a candidate matrix defined over a low-complexity matrix set [35]. The error function is closely linked to the physics of the context where the approximation is intended to be applied. Common error functions are the Frobenius norm or the mean square error [7]. The search space $\mathcal{M}_P(N)$ is the set of $N \times N$ matrices with entries defined over the low-complexity integer set $P$. A particular common choice for the set $P$ includes the set of trivial multiplicands $P_1 = \{0, \pm 1, \pm 2\}$ [5] or $P_1^2$ for approximations over complex integers [34].

For the DVM matrices, there are two major approaches for deriving approximations: (i) directly approximating the non-factorized delay Vandermonde matrix by means of solving (25) and (ii) approximating only the non-trivial multipliers in the DVM factorized form (Corollary 3.2). The former approach has the advantage of being less restrictive, but a fast algorithm (factorization) of the obtained approximation is left to be derived. On the other hand, by approximating from the factorized form, one has the fast algorithm readily available by construction, however the derived approximation is tied to the particular structure of the considered factorization. As demonstrated in the context of trigonometric discrete transforms, approximations lead to a tunable trade-off between performance and arithmetic complexity, often resulting in dramatic reductions in computational cost. DVM matrices could benefit from similar strategies.

7 Conclusion

We have proposed an efficient and self-recursive DVM algorithm having sparse factors. Arithmetic complexities of the proposed algorithm are provided to show that the proposed algorithm is much more efficient than
the direct computation of DVM by a vector. The theoretical error bound on computing the proposed algorithm is established. Numerical results of the forward relative error are utilized to analyze the stability of the proposed algorithm. The proposed algorithm lowers the computational complexity of the computation of $N$ parallel RF beams using an array of antennas as detailed in the preceding analysis. Engineering approaches to real-time implementation of the proposed fast algorithms generally take two forms: 1) analog implementations, employing signals which are continuous in time, continuous in their range, and free of aliasing and quantization effects, and 2) digital implementations, employing signals which are discrete in time (i.e., sampled sequences), discrete in their range (e.g., quantized to be in a set of known values), and therefore susceptible to both aliasing and quantization noise. Typically, discrete domain signals are processed using digital electronics and software, while analog signals are processed using RF integrated circuits, microwave- and mm-wave passive circuits, or photonic integrated circuits. The algorithms proposed here are agnostic to the type of implementation and lend themselves to all types of engineering approaches based on photonics, analog circuits and digital systems, and hybrids thereof. The potential applications of such systems span emerging 5G/6G wireless networks, wireless IoT, CPS, radio astronomy instrumentation, radar and wireless sensing systems, among others.

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**A. Addition and Multiplication Counts**

The explicit addition and multiplication counts of the proposed scaled DVM and DVM algorithms (proved in Lemma 4.1 and 4.2) opposed to the direct matrix-vector (which we call as the Direct Add and Direct Multi) computation are shown in Tables 2 and 3.

| $N$  | Direct Add | $\#a(DVM, N)$ | Direct Multi | $\#m(DVM, N)$ |
|------|------------|---------------|--------------|---------------|
| 4    | 12         | 10            | 12           | 12            |
| 8    | 56         | 40            | 56           | 52            |
| 16   | 240        | 152           | 240          | 192           |
| 32   | 992        | 576           | 992          | 688           |
| 64   | 4032       | 2208          | 4032         | 2496          |
| 128  | 16256      | 8576          | 16256        | 9280          |
| 256  | 65280      | 33664         | 65280        | 35328         |
| 512  | 261632     | 133120        | 261632       | 136960        |
| 1024 | 1047552    | 528896        | 1047552      | 537600        |
| 2048 | 4192256    | 2107392       | 4192256      | 2126848       |
| 4096 | 16773120   | 8411136       | 16773120     | 8454144       |

Table 2: Arithmetic complexity of the scaled DVM algorithm vs Direct computation
Table 3: Arithmetic complexity of the DVM algorithm vs Direct computation

| N   | Direct Add | #a(DVM,N) | Direct Multi | #m(DVM,N) |
|-----|------------|-----------|--------------|-----------|
| 4   | 12         | 10        | 16           | 22        |
| 8   | 56         | 40        | 64           | 88        |
| 16  | 240        | 152       | 256          | 296       |
| 32  | 992        | 576       | 1024         | 960       |
| 64  | 4032       | 2208      | 4096         | 3168      |
| 128 | 16256      | 8576      | 16384        | 10880     |
| 256 | 65280      | 33664     | 65536        | 39040     |
| 512 | 261632     | 133120    | 262144       | 145408    |
| 1024| 1047552    | 528896    | 1048576      | 556544    |
| 2048| 4192256    | 2107392   | 4194304      | 2168832   |
| 4096| 16773120   | 8411136   | 16777216     | 8546304   |

B Frequently used Abbreviations and Notations

B.1 Abbreviations

- Cyberphysical systems (CPS)
- Delay Vandermonde matrix (DVM)
- Discrete Fourier transform (DFT)
- Fast Fourier transform (FFT)
- Forward error of the DVM algorithm (Err-DVM)
- Forward error of the scaled DVM algorithm (Err-sDVM)
- Integrated circuit (IC)
- Internet of things (IoT)
- mm wave (mmW)
- multiple-input multiple-output (MIMO)
- radio-frequency (RF)
- Scaled delay Vandermonde matrix (sDVM)
- Signal to noise ratio (SNR)
B.2 Notations

Circular frequency $\omega$
Companion matrix of $p(z)$ $C_N$
Diagonal matrix $\tilde{D}_N = \text{diag}(\alpha^j)_{j=0}^{N-1}$
DVM $A_N := A_{N,\alpha} = [a^k]_{k=1, l=0}^{N, N-1}$
DVM algorithm $\text{dvm}(N, \alpha, z)$
Node $\alpha \equiv e^{-j\omega \tau}$
Number of additions in computing $#a(DVM, N)$
DVM algorithm Number of additions in computing $#a(sDVM, N)$
scaled DVM algorithm Number of multiplications in computing $#m(DVM, N)$
DVM algorithm Number of multiplications in computing $#m(sDVM, N)$
scaled DVM algorithm Polynomial with zeros $a^{2k}$ $p(z)$
Scaled DVM $\tilde{A}_N := \tilde{A}_{N, \alpha} = [a^k]_{k=1, l=0}^{N-1}$
Scaled DVM algorithm $\text{sdvm}(N, \alpha, z)$
Speed of light $c$
Temporal frequency $f$
true-time-delay $\tau$

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