Reachability Analysis of Variants of Communication-Free Petri Nets

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SUMMARY Communication-free Petri nets provide a net semantics for Basic Parallel Processes, which form a subclass of Milner’s Calculus of Communicating Systems (CCS) a process calculus for the description and algebraic manipulation of concurrent communicating systems. It is known that the reachability problem for communication-free Petri nets is NP-complete. Lacking the synchronization mechanism, the expressive power of communication-free Petri nets is somewhat limited. It is therefore important to see whether the power of communication-free Petri nets can be enhanced without sacrificing their analytical capabilities. As a first step towards this line of research, in this paper our main concern is to investigate, from the decidability/complexity viewpoint, the reachability problem for a number of variants of communication-free Petri nets, including communication-free Petri nets augmented with ‘static priorities,’ ‘dynamic priorities,’ ‘states,’ ‘inhibitor arcs,’ and ‘timing constraints.’

key words: reachability, communication-free Petri nets

1. Introduction

Petri nets are one of the most popular tools for modeling concurrent systems. In spite of their popularity, conventional Petri nets are very difficult to analyze. Over the years, various subclasses of Petri nets have been defined and investigated in the literature, hoping that by imposing certain structural or behavioral constraints on the basic model of Petri nets, such restricted classes become easier to analyze while retaining sufficient expressive power. A communication-free Petri net is a Petri net in which each transition has exactly one input place, and the firing of a transition removes exactly one token from its input place. As a modeling tool, the computational power of communication-free Petri nets is somewhat limited. The limitation is a direct consequence of the inability for communication-free Petri nets to model ‘synchronization’ actions, which require places to synchronize through transition firings.

With respect to the reachability problem, it has been shown in [7] that the problem is NP-complete for communication-free Petri nets (equivalently commutative context-free grammars). Considering the difficulty of analyzing conventional Petri nets [13], communication-free Petri nets exhibit a ‘relatively low’ complexity, which is attractive from an algorithmic viewpoint. On the negative side, however, the absence of synchronization mechanisms makes such a Petri net class a bit too weak from a practical viewpoint. Consequently, it is interesting to see how the complexity of checking reachability is affected when such Petri nets are extended with the notions of, say, ‘priority,’ ‘time,’ among others. As a first step towards this line of investigation, this paper is concerned with the study the reachability problem from a decidability/complexity viewpoint for a number of extended communication-free Petri nets. In our setting, the basic model of communication-free Petri nets are augmented with ‘static priorities,’ ‘dynamic priorities,’ ‘states,’ ‘inhibitor arcs,’ and ‘timing constraints.’

In static-priority communication-free Petri nets, priorities are assigned to transitions statically, and at any instant, only transitions among the highest priority class are allowed to fire. For dynamic-priority communication-free Petri nets, the assignment of priorities to transitions varies from marking to marking, as opposed to a fixed assignment in the static-priority case. State-extended communication-free Petri nets can be viewed as a restricted class of vector addition systems with states whose addition vectors have at most one ‘-1’ position (i.e., each vector can subtract one from a single position at most). For communication-free Petri nets with inhibitor arcs, the test-for-zero capabilities are explicitly allowed. Several timed versions of Petri nets have been proposed in the literature to introduce the timing information (either implicitly or explicitly) into the Petri net model (see, e.g., [14], [16]). A common approach is to associate each of the time-related transitions with an upper and lower bounds, defining the interval in which the transition must fire. In a more recent article [6] (see also [4]), a new model of ‘timed’ Petri nets for which transitions are annotated by clock constraints was introduced. Such a model can be thought of as a generalization of the so-called timed automata model [11]. To model real-time systems, real-value clocks are incorporated into the model of Petri nets in such a way that clocks measure the advances of time, and, with the help of the associated clock constraints, timing requirements for transition firings can be enforced. (To distinguish from those timed versions of Petri nets previously defined in the literature, such a model will be coined “clocked Petri nets”, throughout the remainder of this paper.) As opposed to conventional timed Petri nets for which enabled transitions must
Table 1  Decidability/complexity results of the reachability problem for a variety of communication-free PNs. (NP-c denotes NP-complete; NEXPTIME = \( \bigcup_{k\geq0} \text{NTIME}(2^{k}) \).)

|                | + priority | + inhibitor arc | + clock | + priority |
|----------------|------------|-----------------|---------|------------|
|                | static     | dynamic         | general | w/o inhibitor cycle | general | alter. RQ | + clock |
| basic          | NP-c       | undecid.        | ?       | NEXPTIME     | decidable | NP-c     | undecid. |
| state-extended | undecid.   | undecid.        | undecid.| undecid.     | decidable | decidable| undecid. |

fire within their time bounds, clocked Petri nets (of [6]) allow time to elapse, causing enabled transitions to become disabled without being fired. In fact, this sort of ‘lazy firing’ semantics of clocked Petri nets is the key behind the decidability of the reachability problem. In this paper, we define the so-called alternating RQ communication-free Petri nets where \( R \) denotes the resets and \( Q \) denotes the queries — a restricted class of clocked Petri nets in which, aside from being communication-free structurally, resets and queries of a clock along any computation must appear alternatively. Our definition of alternating RQ communication-free Petri nets is mainly motivated by the work of [11] in which the so-called alternating RQ timed automata were defined. As it turns out, alternating RQ timed automata admit more efficient verification algorithms, in comparison with that for the general model of timed automata [11] (see also [12]).

As indicated in [11],[12], many practical examples in the literature meet the alternating RQ constraints. Inspired by [4],[11],[12], we tailor the alternating RQ conditions defined in [11],[12] to our clocked communication-free Petri net model. Our results are summarized in Table 1.

2. Definitions

Let \( Z \) (resp., \( N \) and \( R \)) denote the set of integers (resp., non-negative integers and reals). A Petri net (PN) is a triple \( (P,T,\varphi) \), where \( P \) is a finite set of places, \( T \) is a finite set of transitions, and \( \varphi \) is a flow function \( \varphi : (P \times T) \cup (T \times P) \to N \). A marking (or configuration) is a mapping \( \mu : P \to N \). A transition \( t \in T \) is enabled at a marking \( \mu \) iff for every \( p \in P \), \( \varphi(p,t) \leq \mu(p) \). A transition \( t \) may fire at a marking \( \mu \) if \( t \) is enabled at \( \mu \). We then write \( \mu \xrightarrow{t} \mu' \), where \( \mu'(p) = \mu(p) - \varphi(p,t) + \varphi(t,p) \) for all \( p \in P \). A sequence of transitions \( \sigma = t_1 \ldots t_n \) is a firing sequence from \( \mu_0 \) iff \( \mu_0 \xrightarrow{t_1} \mu_1 \xrightarrow{t_2} \cdots \xrightarrow{t_n} \mu_n \) for some sequence of markings \( \mu_1, \ldots, \mu_n \). (We also write \( '\mu_0 \xrightarrow{t_1} \cdots \xrightarrow{t_n} \mu_n' \).) We write \( '\mu_0 \xrightarrow{\sigma} \mu' \) to denote that \( \sigma \) is enabled and can be fired from \( \mu_0 \), i.e., \( \mu_0 \xrightarrow{t} \) iff there exists a marking \( \mu \) such that \( \mu_0 \xrightarrow{t} \mu \).

A marked PN is a pair \( (P,T,\varphi,\mu_0) \), where \( (P,T,\varphi) \) is a PN, and \( \mu_0 \) is a marking called the initial marking. Let \( \mathcal{P} = ((P,T,\varphi,\mu_0)) \) be a marked PN. The reachability set of \( \mathcal{P} \) is the set \( \text{R} (\mathcal{P}) = \{ \mu \mid \mu_0 \xrightarrow{\sigma} \mu \text{ for some } \sigma \in T \} \). The reachability problem is that of determining, given a marked PN \( \mathcal{P} \) and a marking \( \mu \), whether \( \mu \in \text{R} (\mathcal{P}) \).

For ease of expression, the following notations will be used extensively throughout the rest of this paper. (Let \( \sigma \) be a transition sequence, \( p \) be a place, and \( t \) be a transition.)

- \( \#_{\sigma} (t) \) represents the number of occurrences of \( t \) in \( \sigma \).
- \( \Delta(\sigma) \): the displacement of \( \sigma \) is defined as \( \Delta(\sigma) = \mu' - \mu \), provided that \( \mu \xrightarrow{\sigma} \mu' \).
- \( \text{Tr}(\sigma) = \{ t \in T \mid \#_{\sigma} (t) > 0 \} \), denoting the set of transitions used in \( \sigma \).
- \( \sigma^* = (t_1 t_2 \cdots t_n) \), \( \sigma^+ = (t_1 t_2 \cdots t_n) \), \( \sigma^- = (t_1 t_2 \cdots t_n) \), \( \sigma^0 = (t_1 t_2 \cdots t_n) \).

Given a computation \( \mu \xrightarrow{\sigma} \mu' \), a sequence \( \sigma' \) is said to be a rearrangement of \( \sigma \) if \( \#_{\sigma'} = \#_{\sigma} \) and \( \mu \xrightarrow{\sigma'} \mu' \). Given a directed graph \( G = (V,E) \), we write \( u \leftrightarrow v \) to denote a path from nodes \( u \) to \( v \).

A marked PN \( (P,T,\varphi,\mu_0) \) is said to be communication-free (abbreviated as cf) [5] if

1. \( \forall t \in T, \#_{\sigma} (t) = 1 \), (i.e., every transition has exactly one input place), and
2. \( \forall p \in P, t \in T, \varphi(p,t) \leq 1 \) (i.e., every arc going from a place to a transition has weight 1).

3. Variants of Communication-Free Petri Nets

In this section we recall the definitions of variants of cf-PNs. The cf-PNs is special class of Petri nets in which each transition has exactly one input place, and the firing of a transition removes exactly one token from its input place. A motivation comes from cf-PNs exhibit a relatively low complexity from an algorithmic viewpoint, as opposed to general Petri nets. It is worth to take a look and see how the complexity of checking complexity is affected when such Petri nets are extended with the notations of priority, time, among others.

- **State-extended cf-PNs**

  A state-extended cf-PN is a \( (S,s_0,(P,T,\varphi),\delta) \), where \( S \) is a finite set of states, \( s_0 \in S \) is the initial state, \( (P,T,\varphi) \) is a cf-PN, and \( \delta \subseteq S \times S \times T \) defines the transition relation. A configuration is a pair \( (p,x) \), where \( p \) is in \( S \) and \( x \) is a vector in \( N^k \). \( (s_0,v_0) \) is the initial configuration, where \( v_0 \) is the initial marking. The transition \( (p,q,t) \) can be applied to the configuration \( (p,v) \) and yields the configuration \( (q,v + \Delta(t)) \), provided that \( t \) is fireable in \( (p,v) \) in \( (P,T,\varphi) \). In this case, \( (q,v + \Delta(t)) \) is said to follow \( (p,v) \). Let \( \sigma_0 \) and \( \sigma_1 \) be two configurations. Then \( \sigma_1 \) is said to be reachable from \( \sigma_0 \) if \( \sigma_0 = \sigma_1 \) or there exist configurations \( \sigma_1, \ldots, \sigma_{r-1} \) such that \( \sigma_{r+1} \) follows \( \sigma_r \) for \( r = 0, 1, \ldots, t-1 \).
Some equivalence issues of state-extended BPP (equivalently, state-extended cf-PNs) have been studied in, e.g., [9], [10]. In particular, bisimulation equivalence has been shown to be undecidable for state-extended BPP.

**Priority cf-PNs**

A static-priority PN is a tuple \((P, T, \varphi, \rho)\), where \((P, T, \varphi)\) is a PN, and \(\rho \subseteq T \times T\) defines the so-called priority relation over \(T\). Intuitively, \((t_1, t_2) \in \rho\) means that \(t_2\) takes precedence over \(t_1\) in transition firing. Let \(\tilde{\rho}\) denote \(\{(t, t')(t', t) \notin \rho\}\) (i.e., \(\tilde{\rho}\) is the set of pairs of incomparable transitions). In this paper (so is in [2]), \(\rho\) is assumed to be irreflexive, asymmetric, and transitive, and \(\tilde{\rho}\) be an equivalence relation. Given a \(\rho\), transition \(t\) is \(\rho\)-enabled at \(\mu\) iff \(t\) is enabled at \(\mu\) and for every \(t' \in T\) enabled at \(\mu\), \((t, t') \notin \rho\) (i.e., \(t\) is of the highest priority among those enabled at \(\mu\)).

In a static-priority PN \((\varphi, T, \rho, \varphi)\), a transition \(t\) may fire at a marking \(\mu\) if \(t\) is \(\rho\)-enabled at \(\mu\); we then write \(\mu \Rightarrow \mu'\), where \(\mu'(p) = \mu(p) - \varphi(t, p) + \varphi(t, p)\) for all \(p \in P\), and \(t\) is \(\rho\)-enabled at \(\mu\) and \(t' \in T\) enabled at \(\mu\) (but not both); we write \(\mu \Rightarrow \mu'\), where \((t, t') \in \rho\) (resp., \((t', t) \in \rho\)). Without loss of generality, we assume that \(\tilde{\Omega}_1, \ldots, \tilde{\Omega}_d\) be enumerated in increasing priority throughout the rest of this paper. Given a \(t \in T\) and a \(\rho\), we let \(\text{class}(t) = i\) if \((t, t') \in \tilde{\Omega}_i\) (i.e., \(\text{class}(t)\) is the index of the equivalence class containing \(t\)).

A dynamic-priority PN is a tuple \((P, T, \varphi, \rho)\), where \((P, T, \varphi)\) is a PN, and \(\rho : N^k \rightarrow 2^{T \times T}\) defines the so-called dynamic priority relation over \(T\). The main difference between static and dynamic priorities is that the priority relation is fixed for the former, whereas it is a function of the marking for the latter. More precisely, for a marking \(\mu, \rho(\mu) \subseteq (T \times T)\) defines the priority relation at marking \(\mu\). For dynamic-priority PNs, the notions of enabledness as well as reachability are analogous to that in the static priority case. See [3] for more about PNs with dynamic priorities.

For cf-PNs, it is assumed that for arbitrary transitions \(t\) and \(t'\), if \(t = t'\) (i.e., they share the same input place), then \(t\) and \(t'\) have the same priority. (If this is not the case, the lower priority one can never be fired.)

**CF-PNs with inhibitor arcs**

A PN with inhibitor arcs is a tuple \((P, T, \varphi, I)\), where \(P\), \(T\), and \(\varphi\) are the same as that in conventional PNs defined earlier, and \(I \subseteq P \times T\) defines the set of inhibitor arcs. (We assume \((p, t) \in I \implies \varphi(p, t) = 0\).) A transition \(t\) may fire at a marking \(\mu\) if for every \((p, t) \in I, \varphi(p, t) \leq \mu(p)\) and for every \((p, t) \in I, \mu(p) = 0\).

As its name suggests, a cf-PN with inhibitor arcs is a PN with inhibitor arcs such that the structure of the PN is communication-free.

**Clocked cf-PNs**

Given a set \(X = \{x_1, x_2, \ldots, x_n\}\) of clock variables, the set \(\Phi(X)\) of clock constraints \(\delta\) is defined inductively by

\[
\delta := x \leq c \mid c \leq x \mid \neg \delta \mid \delta \land \delta,
\]

where \(x\) is a clock in \(X\) and \(c\) is a constant in \(N\). A clock reading is a mapping \(v : X \rightarrow \mathbb{R}\) which assigns each clock a real value. For \(\eta \in \mathbb{R}\), we write \(\nu + \eta\) to denote the clock reading which maps every clock \(x\) to the value \(\nu(x) + \eta\). A clock reading \(\nu\) for \(X\) satisfies a clock constraint \(\delta\) over \(X\), denoted by \(\delta(\nu) \equiv \text{true}\), iff \(\delta\) evaluates to \(\text{true}\) using the values given by \(\nu\).

A clocked PN is a tuple \(\mathcal{N} = ((P, T, \varphi, X, r, q))\), where \((P, T, \varphi)\) is a PN, \(X\) is a finite set of real-value clock variables, \(r : T \rightarrow 2^X\) is a labeling function assigning clocks to transitions, and \(q : T \rightarrow \Phi(X) \cup \{l\}\) is a labeling function assigning clock constraints to transitions. Intuitively, \(r(t)\) contains those clock variables which are reset when transition \(t\) is fired, and \(q(t)\) is a ‘guard’ governing the condition under which the clock reading must meet in order for \(t\) to fire. Notice that when \(r(t) = \emptyset\) and \(q(t) = \lambda\), transition \(t\) behaves just like a transition in an ordinary PN. A transition is said to be clocked it is associated with either resets or queries. In our subsequent discussion, when a transition \(t\) in a PN graph is annotated by \(R(x)\), it means \(x \in r(t)\). A configuration of a clocked PN consists of a marking \(\mu\), the global time \(\eta\) and the present clock reading \(v\). We use the 3-tuple \((\mu, \eta, v)\) to denote such a configuration. Note that the clock reading \(v\) is continuously being updated as \(\eta\), the global time, advances. Hence, \(\nu\) and \(\eta\) are not completely independent. Given a configuration \((\mu, \eta, v)\) of a clocked PN \(\mathcal{P}\), a transition \(t\) is enabled if \(\mu \rightarrow t\), and \(q(t)(v) \equiv \text{true}\). Let \(\mu\) be the marking and \(v\) the clock reading at time \(\eta\). Then \(t\) may fire at \(\eta\) if \(t\) is enabled in the marking \(\mu\) with the clock reading \(v\). We then write \((\mu, v)(\eta) \rightarrow (\mu', v')\), where \(\mu' = \mu + \Delta(t)\), \(v'(x) = 0\) (for all \(x \in r(t)\)) and \(v'(x) = v(x)\) (for all \(x \notin r(t)\)). Note that the global time remains unchanged as a result of firing \(t\). That is, the firing of a transition is assumed to let no time elapse at all. The global clock will start moving immediately after the firing of a transition is completed. A marked clocked PN is a pair \(\langle \mathcal{N}, \mu_0 \rangle\), where \(\mathcal{N}\) is a clocked PN and \(\mu_0\) is the initial marking. Initially, we assume the initial global time \(\eta_0\) and clock reading \(v_0\) to be \(\eta_0 = 0\) and \(v_0(x) = 0\) (for all \(x \in X\)), respectively.

It is important to point out that in the execution semantics defined above, enabledness is necessary but not sufficient for transition firing. Unlike the case in the strong firing semantics of timed (or time) PNs ([16]), it is not required to fire all the enabled transitions at any point in time during the course of a computation. The reachability problem for clocked PNs is to decide, given a marked clocked PN \(\langle \mathcal{N}, \mu_0 \rangle\) and a marking \(\mu\), deciding whether \(\mu_0 \omega \mu\), for some \(\omega = (\sigma, \tau)\), where \(\sigma\) and \(\tau\) are transition and time sequences, respectively.
4. Complexity and Decidability Analysis of the Reachability Problem

4.1 Priority Communication-Free Petri Nets

A 2-counter machine consists of a finite-state control equipped with two counters, in which a transition from state \( p \) to state \( q \) (operating on counter \( C \)) is of one of the following three: increment \( (p \rightarrow q, C) \), decrement \( (p \rightarrow q, C = 0) \), and test-for-zero \( (p \rightarrow q) \). 2-counter machines are computationally equivalent to Turing machines.

**Theorem 4.1:** The reachability problem for dynamic-priority cf-PNs is undecidable.

**Proof:** It suffices to show that given a 2-counter machine \( M \), a dynamic-priority cf-PN \( \mathcal{P} \) and a marking \( \mu \) can be constructed in such a way that \( \mu \) is reachable in \( \mathcal{P} \) iff \( M \) halts.

Consider \( M \)'s transitions \( p \rightarrow q \) and \( p \rightarrow q' \), we use the cf-PN fragment shown in Fig. 1 to simulate these two transitions. Here place \( c \) simulates Counter \( C \) and places \( a, d, f \) simulate states \( p, q', q, \) respectively. (The simulation of \( p \rightarrow q \) is rather straightforward, and hence, is left to the reader.) Before the simulation begins, it is the case that \( \mu(a) = 1 \) (indicating \( p \) being the current state), \( \mu(c) \) equals the counter \( C \)'s value, and the remaining places are empty. In our setting, only two priority classes, namely, \( [\text{High}, \text{Low}] \), are needed to make the simulation work. More precisely,

1. \( \text{High} = \{t_1\} \) and \( \text{Low} = \{t_2, t_3, t_4, t_5\} \), when \( \mu(a) = 1 \land \mu(c) = 0 \).
2. \( \text{High} = \{t_2\} \) and \( \text{Low} = \{t_1, t_3, t_4, t_5\} \), when \( \mu(a) = 1 \land \mu(c) > 0 \land \mu(e) = 0 \).
3. \( \text{High} = \{t_3\} \) and \( \text{Low} = \{t_1, t_2, t_4, t_5\} \), when \( \mu(b) = 1 \land \mu(c) > 0 \land \mu(e) = 0 \).
4. \( \text{High} = \{t_4\} \) and \( \text{Low} = \{t_1, t_2, t_3, t_5\} \), when \( \mu(b) = 1 \land \mu(e) = 1 \).

(5) \( \text{High} = \{t_5\} \) and \( \text{Low} = \{t_1, t_2, t_3, t_4\} \), when \( \mu(b) = 0 \land \mu(e) = 1 \)

(6) the remaining transitions belong to class ‘Low’.

\( p \rightarrow q' \) is simulated through the firing of transition \( t_1 \), whose feasibility is guaranteed by Case (1) above. \( p \rightarrow q \), on the other hand, requires the execution of a sequence of transitions \( t_2 t_3 t_4 t_5 \), which is feasible as the result of the priorities assigned in Cases (2)–(5) above.

By reducing from the halting problem of 2-counter machines, the following is not difficult to show.

**Theorem 4.2:** The reachability problem is undecidable for state-extended cf-PNs with static priorities.

For cf-PNs with static priorities, we are able to show the reachability problem to be NP-complete. We require the following lemma, whose proof is similar in style to a result in [2] concerning the so-called EQUAL-conflict P/T nets.

**Lemma 4.3:** Given a cf-PN \( (P, T, \varphi) \) and a priority relation \( \rho \), if \( \mu \rightarrow \mu' \) and for every \( t \) enabled at \( \mu' \), \( t \in \Omega_1 \) (i.e., \( t \) is in the lowest priority class induced by \( \bar{\rho} \)), then \( \mu \rightarrow \mu' \), for some permutation \( \sigma' \) of \( \sigma \).

**Proof:** Consider a path from \( \mu \) to \( \mu' \) along which \( \mu_1 \) is the leftmost marking at which the priority requirement is violated, and \( t_1 \) be the transition fired at \( \mu_1 \). We claim that one of the highest priority transitions enabled at \( \mu_1 \) must fire in between \( \mu_1 \) and \( \mu' \), for only transitions belonging to the class \( \Omega_1 \) are enabled at \( \mu' \). We let \( \mu_2 \rightarrow \mu_3 \) be the marking and its associated transition such that \( \mu_2 \) is the nearest location (following \( \mu_1 \)) with \( t_2 \) among the highest priority transitions enabled at \( \mu_1 \). (More precisely, every transition occurring between \( \mu_1 \) and \( \mu_2 \) has its priority lower than \( t_2 \).) Since \( t_1 \) and \( t_2 \) (which have different priorities) do not share a common input place, firing \( t_2 \) at \( \mu_1 \) followed by \( t_1 \) remains a valid path. By repeatedly applying such a rearrangement to the remaining path, a path meeting the priority requirement can be constructed.

The following lemma (shown in [17]) suggests that the reachability problem for cf-PNs can be characterized as an Integer Linear Programming problem.

**Lemma 4.4:** (from Theorem 4 in [17]) Given a cf-PN \( \mathcal{P} = (P, T, \varphi) \), a system of linear inequalities \( ILP(\mathcal{P}, \mu, \mu') \) can be constructed in NP in such a way that \( \mu' \) is reachable from \( \mu \) iff \( ILP(\mathcal{P}, \mu, \mu') \) has a solution over the integers.

With the above two lemmas, we are ready to derive the complexity of the reachability problem for static-priority cf-PNs. The proof closely parallels that of a recent result concerning priority conflict-free PNs [18].

**Theorem 4.5:** The reachability problem for static-priority cf-PNs is NP-complete.

4.2 Communication-Free Petri Nets with Inhibitor Arcs

A cf-PN with inhibitor arcs \( \mathcal{P} = (P, T, \varphi, I) \) is said to be
cycle-free if it is the case that for every cycle length (i.e., a closed loop) in the PN graph of \( P \), there exists a transition \( t \) along that the following condition holds:

\[
\begin{align*}
\text{(Condition A):} \\
\forall p \in t \ast, \forall t' \in T, (p, t') \notin I
\end{align*}
\]

(i.e., the firing of \( t \) does not deposit tokens to a place from which an inhibitor arc emanates.)

To analyze the reachability problem of cf-PNs with cycle-free inhibitor arcs, we require the following lemma, which demonstrates the possibility of rearranging a path into some sort of a ‘canonical form’.

**Lemma 4.6:** Given a cf-PN with cycle-free inhibitor arcs \( P = (P, T, \varphi, I) \), suppose \( \mu \xrightarrow{\sigma_{t_1} \cdots t_i} \mu' \) is a computation in \( P \) such that \( t \) is an o-transition, \( t \notin Tr(\sigma_1) \), and \( \sigma \) satisfies Condition A above, then \( \sigma \) can be rearranged into \( \sigma_1 t_1 \cdots t_i \sigma_2 t_2 \cdots t_i \sigma_3 t_i \cdots t_i \sigma_m t_m \cdots t_m \sigma_{m+1} \), for some \( i \), such that \( t \notin Tr(\sigma_3) \). (That is, all the occurrences of \( t \) in \( \sigma \) can be fired all at once.)

**Proof:** Since none of \( t \)’s output places is the input of an o-transition (Condition A), firing \( t \) earlier (compared to the original firing position) does not affect the fireability of the remaining transitions. The rest is then clear.

By repeatedly applying the above lemma, we have the following:

**Lemma 4.7:** Let \( P = (P, T, \varphi, I) \) be a cf-PN with cycle-free inhibitor arcs, and \( \mu \xrightarrow{\sigma} \mu' \) be a computation in \( P \). The \( \sigma \) can be rearranged into \( \sigma_1 t_1 \cdots t_1 \sigma_2 t_2 \cdots t_2 \sigma_m t_m \cdots t_m \sigma_{m+1} \), such that

1. \( \forall 1 \leq i \leq m, t_i \) is an o-transition,
2. \( \forall 1 \leq i \leq m + 1, \) none of the transitions in \( \sigma_i \) is an o-transition, and
3. \( m \leq 2^k \), where \( k \) denotes the number of o-transitions in \( T \).

(Notice that \( t_i, 1 \leq i \leq m, \) do not have to be distinct.)

**Proof:** According to Lemma 4.6, any computation \( \mu \xrightarrow{\sigma} \mu' \) can be rearranged into \( \mu \xrightarrow{\sigma_1 t_1 \cdots t_1} \mu_1 \xrightarrow{t_2} \mu_2 \xrightarrow{t_2} \mu' \), for some o-transition \( t \notin Tr(\sigma_1) \cup Tr(\sigma_3) \). As a result, the number of o-transitions in \( \sigma_1 \) and \( \sigma_3 \), respectively, is at most \( k - 1 \). By recursively applying Lemma 4.6 to \( \sigma_1 \) and \( \sigma_3 \) (with respect to \( P, T - \{t\}, \varphi_{T - \{t\}}(\mu), \varphi_{T - \{t\}}(\mu') \)), our lemma follows.

**Theorem 4.8:** The reachability problem for cf-PNs with cycle-free inhibitor arcs is solvable in \( \text{NEXPTIME} \).

**Proof:** From Lemma 4.7, any computation \( \mu \xrightarrow{\sigma} \mu' \) can be rearranged into \( \mu \xrightarrow{\sigma_1} \mu_1 \xrightarrow{t_1 \cdots t_1} \mu_1' \xrightarrow{t_2} \mu_2 \xrightarrow{t_2} \mu' \), for some markings \( \mu_1, \mu_1', \mu_2, \mu' \), and positive integers \( n_1, \ldots, n_m \). As each \( \sigma_i, 1 \leq i \leq m, \) does not contain any o-transitions, its computation can be captured by an instance of integer linear programming, as Lemma 4.4 suggests. By ‘guessing’ those o-transitions \( t_1, t_2, \ldots, t_m \), checking whether \( \mu' \) is reachable from \( \mu \) in \( P \) can be formulated as

\[
\begin{align*}
&\begin{cases}
    x_0 = \mu \\
    \forall 1 \leq i \leq m + 1
    \end{cases} \\
&\text{ILP}(P, T', \varphi, x_{i-1}, x_i) \\
&\forall i \in T(\sigma), x_i(t_i) = 0 \\
&x_i' = x_i + n_i(t_i) \geq 0 \\
&x_{m+1} = \mu
\end{align*}
\]

where \( T' \) is the set \( \{t \mid t \notin T, t \text{ is not an o-transition}\} \), vector variables \( x_0 \) and \( x_{m+1} \) represent markings \( \mu \) and \( \mu' \), respectively, \( x_i, x_i', 1 \leq i \leq m, \) are vector variables representing markings \( \mu_i, \mu_i' \), respectively, and \( n_i, 1 \leq i \leq m, \) are scalar variables. As \( m \) is bounded by an exponential function in the size of \( P \), the above instance of integer linear programming is solvable in \( \text{NEXPTIME} \).

The reachability problem for cf-PNs with cycle-free inhibitor arcs is clearly \( \text{NP-hard} \), as the problem for original cf-PNs is \( \text{NP-complete} \). It would be interesting to see whether the lower bound can be improved.

By reducing from the halting problem of 2-counter machines, we can show the following:

**Theorem 4.9:** The reachability problem is undecidable for state-extended cf-PNs with cycle-free inhibitor arcs.

4.3 Alternating RQ Communication-Free Petri Nets

For general clocked Petri nets, we are able to show the following result using a standard technique based on the concept of clock regions [1]. The idea is to reduce the reachability problem for clocked Petri nets to that for vector addition systems with states (by associating each clock region with a state), which are known to be computationally equivalent to Petri nets.

**Theorem 4.10:** The reachability problem is decidable for general clocked Petri nets.

In view of the above, together with the fact that checking reachability in cf-PNs is easier to solve than in general PN (the former is in \( \text{NP} \) whereas the latter is \( \text{EXPSPACE-hard} \)), a natural question to ask is how difficult the reachability problem is for clocked cf-PNs. As one might expect, the property of ‘communication-freedom’ is crucial in making cf-PNs easier to analyze (from a computational complexity viewpoint) than their general counterparts. The introduction of timing constraints to cf-PNs, however, renders (in an implicit way) the net not communication-free behaviorally. In fact, for static-priority cf-PNs with clocks, the following result shows the reachability problem to be undecidable. (Recall that for static-priority cf-PNs without clocks, the problem was shown to be solvable in \( \text{NP} \) in the previous section.)
**Theorem 4.11:** The reachability problem is undecidable for static-priority clocked cf-PNs.

**Proof:** Following Theorem 4.2, it suffices to show that any static-priority state-extended cf-PN can be simulated by a static-priority clocked cf-PN. The idea of the simulation is depicted in Fig. 2. Figure 2(1) shows a fragment of a state-extended cf-PN in which the firing of PN transition $t$ is controlled by $p \rightarrow q$. In our construction we associate two distinct clocks, namely $x$ and $y$ in Fig. 2(2), with each transition $t$. Clocks $x$ and $y$ are used to 'synchronize' the actions between 'moving a token from places $p$ to $q$' (which corresponds to the 'state' portion of the state-extended cf-PN) and firing transition $t$ itself. We assume the clock values of $x$ and $y$ to be greater than 1 initially; hence, they have to be reset before $t$ becomes fireable. (This ensures that only the transition associated with the 'current state', namely, $p$, has the right to fire.) It is easy to see that the sequence utv is fireable. In addition, upon firing $v$, the values of $x$ and $y$ are 2 and 3, respectively, satisfying our earlier assumption that the values of $x$ and $y$ are greater than 1.

In view of the above and Theorem 4.5, cf-PNs with clocks seem to be harder to analyze than their unclocked counterparts. As a first step towards a complexity analysis of clocks, it is easier to analyze, compared to their general counterparts, seems to lie in the so-called simple path property, suggesting that if a state $q$ is reachable from $p$ in a timed automaton, then $q$ can be reached from $p$ through a path without cycles, and if a cycle is traversable once, it can be traversed an arbitrary (finite of infinite) number of times. The intuition behind the justification of imposing the alternating RQ constraints is that, in many cases, if we want to inquired about a timing status at a certain point during a computation via a query, we often reset the associated clock before the query. Furthermore, if the same clock is to be inquired twice, different clocks can be used for each query. As indicated in [11], [12], many practical examples in the literature meet the alternating RQ constraints. Inspired by [4], [11], [12], in our subsequent discussion we tailor the alternating RQ conditions defined in [11], [12] to our clocked PN model.

Given a sequence $\sigma$ of transitions, the RQ sequence of $\sigma$, denoted by $\Gamma(\sigma)$, is the sequence of resets and queries encountered along $\sigma$. Given an RQ sequence $\Gamma(\sigma)$, the RQ sequence with respect to clock $x$, denoted by $\Gamma(x)\sigma | x$, is obtained from $\Gamma(\sigma)$ by deleting all the resets and queries that do not involve $x$. An RQ sequence $\Gamma(\sigma)$ is alternating, if, for each clock $x$, $\Gamma(x)$ is of the form $R(x)Q(x)R(x)Q(x)\cdots$, where $R(x)$ and $Q(x)$ denote reset and query operations, respectively, with respect to clock $x$.

An alternating RQ cf-PN is a clocked cf-PN satisfying the following constraints:

1. For each clock $x$, there is only one pair of reset and query associated with $x$ in the PN.
2. For each computation $\omega = (\sigma, \tau)$ starting from the initial configuration, $\Gamma(\sigma)$ is alternating, i.e., for each $x$, resets and queries in $\Gamma(\sigma)\sigma | x$ appear in an alternating fashion.
3. Suppose $(\mu, \eta, \nu)$ is a reachable configuration with $\mu(p_i) \geq 1$ and $p_1t_1p_2t_2\cdots p_nt_n$ is an arbitrary simple path in the PN graph. Then there exists a time sequence $\tau$ such that $(\mu, \eta, \nu) \xrightarrow{(t_1, \cdots, t_n, \tau)}$ is fireable.
4. Each strongly connected component in the PN graph...
contains at most one clocked circuit.

The reason why multiple clocked circuits are disallowed (as Condition (4) indicates) is that the time delay induced by traversing a clocked circuit might invalidate the clock constraints associated with another clocked circuit (more precisely, the number of times a clocked circuit can be traversed hinders on another clocked circuit) — violating the simple path property mentioned earlier. As a consequence, at the current stage of our investigation we only consider cf-PNs which exhibit ‘simple’ circuit structures, although we surmise that a more general condition also works. Although not discussed in this paper, it is worthy of investigating the complexity of deciding whether a given PN is an alternating RQ cf-PN or not.

Given Condition (1) in the above definition, for convenience we define two mappings \( R : X \rightarrow T \) and \( Q : X \rightarrow T \) which indicate the transitions at which resets and queries, respectively, reside. \( R(x) = t \) indicates that clock \( x \) is reset whenever \( t \) is fired. Likewise, \( Q(x) \) represents the transition at which a query concerning clock \( x \) resides. A transition (resp., place) is said to be timed if and only if there exists a path in the PN graph from the transition (resp., place) to a clocked transition. Let \( T_{\text{timed}} \) and \( P_{\text{timed}} \) be the sets of timed transitions and places, respectively, and \( T_{\text{untimed}} = T - T_{\text{timed}}, P_{\text{untimed}} = P - P_{\text{timed}}. \) The following result shows an important feature regarding the behavior of alternating RQ cf-PNs.

**Lemma 4.12:** Let \( \mathcal{P} = (N, \mu_0) \) be an alternating RQ cf-PN. If \( p_1 \rightarrow p_2 \rightarrow t \rightarrow \mu(p_1) + \mu(p_2) \leq 1, \) for every reachable configuration \((\mu, \eta, \nu)\). (Here \( p_1 \rightarrow p_2 \) can be null; in this case, \( p_1 = p_2 \).)

**Proof:** Suppose \( t \) is the first clocked transition along the path \( t \rightarrow p_2 \rightarrow t \). Let the transition sequences (excluding \( t \)) in \( p_1 \rightarrow p_2 \) and \( p_2 \rightarrow t \) be \( \sigma_1 \) and \( \sigma_2 \), respectively. Consider the following cases:

- **Case 1:** \( \mu(p_2) \geq 2 \). Due to the property of being an alternating RQ cf-PN, there exists a time sequence \( \eta \) such that \( \mu(\sigma_2) \mu' \), for some \( \mu' \). As none of the transitions in \( \sigma_2 \) is clocked, \( \mu(\sigma_2) \mu'' \), for some \( \mu'' \), violating the RQ alternating property as \( t \) is fired twice consecutively.

- **Case 2:** \( \mu(p_1) \geq 1 \) and \( \mu(p_2) \geq 1 \). In this case there exist computations \( \mu(\sigma_1) \mu_1, \mu(\sigma_2) \mu_2 \), for some \( \mu_1 \) and \( \mu_2 \). It then follows that \( \mu(\sigma_1) \mu_1, \mu(\sigma_2) \mu_2 \), for some \( \mu_3 \) violating the RQ alternating property as \( t \) is being fired twice consecutively.

- **Case 3:** \( \mu(p_1) \geq 2 \). Similar to Case 1.

If \( t \) is not the first clocked transition in \( p_2 \rightarrow t \), choose the first such transition, and then apply the above argument. \( \square \)

To simplify our subsequent discussion, we require the following result whose proof is not difficult.

**Lemma 4.13:** Given an arbitrary alternating RQ cf-PN and a marking \( \mu \), we can construct in polynomial time an ‘equivalent’ (as far as reaching \( \mu \) is concerned) alternating RQ cf-PN \( \mathcal{P} \) satisfying the following two properties:

1. \( \psi(t, p) \leq 1, \forall t \in T_{\text{timed}} \) (i.e., firing any timed transition puts only one token into each of its output places).
2. For every \( p \in P_{\text{untimed}}, \) there is at most one timed transition in \( \psi(p) \).

It is clear from the definitions of timed and untimed transitions and places that in any circuit \( c \), if there is an untimed transition or place, then all the transitions and places in \( c \) must be untimed as well. Such an observation applies to circuit collections. Hence, a circuit \( c \) is called a timed circuit of \( \mathcal{P} \) if \( \forall p \in c \) and \( t \in c, p \in P_{\text{timed}} \) and \( t \in T_{\text{timed}} \). A circuit collection \( S \) is called a timed circuit collection if for every \( c 

tation in an alternating RQ cf-PN must behave in a rather regular fashion. It is important to point out that such a regularity is exhibited in the PN not only structurally but also behaviorally. Take Fig. 3 as an example for illustrating what we mean by ‘regularity.’ Figure 3 represents the life cycle of a token, say, \( x \), in existence initially, along with those tokens generated by \( x \) directly or indirectly as \( x \) progresses. In a graph-theoretic sense, the portion of the PN involved in the course of token \( x \)’s computation consists of circuit collections (see \( S_1 \) and \( S_2 \) in Fig. 3) interleaved by line segments (see transitions \( t_1 - t_4 \) in Fig. 3). In a line segment, token \( x \) can only move downwards, while possibly generating new token (to either timed or untimed portions of the PN) as it progresses. Once \( x \) moves inside a circuit collection, it is entitled to circle around an arbitrary number of times (provided the timing requirement is also met) while spinning off new tokens. There are several key features (which will be proven later) regarding a circuit collection:

- At any instant, at most one token is present in a circuit collection.
- Tokens can only be injected into untimed places as a result of spin-off (see \( t_5 \) in Fig. 3).
- To exit from a circuit collection, a token must leave from a unique exit place.
- Tokens in the timed portion can never interfere with one another.

With the exception of generating new tokens (or being generated by another token), a token, during its entire life span, never interferes with the rest of the tokens in existence in the PN.

Enough for the intuition, we are in a position to embark on the derivation of the NP upper bound. To do so, a few lemmas are in order.

**Lemma 4.14:** Let \( \mathcal{P} = ((P, T, \varphi), X, r, q) \) be an alternating RQ cf-PN with initial marking \( \mu_0 \). For every reachable marking \( \mu \) of \( \mathcal{P} \), there exists a ‘canonical’ computation \( \omega = (\sigma, \tau) \) (where \( \sigma_a \in (T_{\text{timed}})^* \) and \( \sigma_b \in (T_{\text{untimed}})^* \), such that \( \mu_0 \xrightarrow{(\sigma_a, \tau_a)} \mu' \xrightarrow{(\sigma_b, \tau_b)} \mu \), and for the intermediate marking \( \mu' \), \( \mu'(p) = \mu(p), \forall p \in P_{\text{timed}} \) and \( p^* \subseteq T_{\text{timed}} \).

**Proof:** Given a reachable marking \( \mu \), there exists a computation \( \omega_0 \) such that \( \mu_0 \xrightarrow{\omega_0} \mu \), for some time sequence \( \tau \). Let \( t_1 \) and \( t_2 \) be the first pair of transitions fired consecutively in \( \omega_0 \), with \( t_1 \in T_{\text{untimed}}, t_2 \in T_{\text{timed}} \) and \( \eta_1 \leq \eta_2 \), where

\[
\mu_0 \xrightarrow{(\sigma_1, \tau_1)} \mu_1 \xrightarrow{(t_1, \eta_1)} \mu_2 \xrightarrow{(t_2, \eta_2)} \mu_3 \xrightarrow{(\sigma_2, \tau_2)} \mu.
\]

Let \( \{p_1\} = \{t_1, p_2\} \). Obviously, \( p_1 \) and \( p_2 \) are marked in \( \mu_1 \) and \( \mu_2 \), respectively. (Hence, \( \mu_1(p_1) \geq 1 \) and \( \mu_2(p_2) \geq 1 \).) By definition, \( p_2 \) is a timed place; hence, \( \mu_2(p_2) = 1 \) (Lemma 4.12). We also have \( t_1 \nrightarrow t_2 \), since \( t_1 \) is untimed while \( t_2 \) is timed. Therefore \( p_2 \notin \tau \) and \( \mu_1(p_2) \geq \mu_2(p_2) = 1 \). Also from Lemma 4.12, \( p_1 \neq p_2 \). Since \( t_1 \) is an untimed transition, none of the clock variables is reset upon firing \( t_1 \). Let \( v_1 \) and \( v_2 \) be the clock readings at time \( \eta_1 \) and \( \eta_2 \), respectively. Clearly \( v_2 = v_1 + (\eta_2 - \eta_1) \), regardless of the firing of \( t_1 \). By skipping \( t_1 \) and firing \( t_2 \) in \( \mu_1 \) at time \( \eta_2 \), we have \( \mu_0 \xrightarrow{(\sigma_1, \tau_1)} \mu_1 \xrightarrow{(t_2, \eta_2)} \mu_2 \) for some \( \mu_2 \), and \( 1 \leq \mu_1(p_1) \leq \mu_2(p_1) \). Since \( t_1 \) is an untimed transition, it is still enabled in \( \mu_2 \) at time \( \eta_2 \), in spite of being delayed. Hence we have

\[
\mu_0 \xrightarrow{(\sigma_1, \tau_1)} \mu_1 \xrightarrow{(t_1, \eta_1)} \mu_2 \xrightarrow{(t_1, \eta_2)} \mu_3 \xrightarrow{(\sigma_2, \tau_2)} \mu,
\]

for some \( \mu' \).

By repeatedly applying the above to every pair of neighboring transitions in which the untimed one precedes the timed, a canonical rearrangement \( \mu_0 \xrightarrow{(\sigma_a, \tau_a)} \mu' \xrightarrow{(\sigma_b, \tau_b)} \mu \) with all the timed (resp., untimed) transitions appear in \( \sigma_a \) (resp., \( \sigma_b \)) is obtained. It remains to show that \( \mu'(p) = \mu(p), \forall p \in P_{\text{timed}} \) and \( p^* \subseteq T_{\text{timed}} \). Let \( p_1 \in P_{\text{timed}} \) and \( p_1^* \subseteq T_{\text{timed}} \). Since \( p_1 \in P_{\text{timed}} \), all the input transitions of \( p_1 \) are timed transitions which will not be fired in \( \sigma_b \). In other words, no tokens are put into \( p_1 \) during the course of firing \( \sigma_b \); so \( \mu'(p_1) \leq \mu(p_1) \). On the other hand, by assumption all the output transitions of \( p_1 \) are also timed, so that they won’t be fired in \( \sigma_b \). Since no tokens leave \( p_1 \) during \( \sigma_b \), \( \mu'(p_1) \geq \mu(p_1) \). Therefore, \( \mu'(p_1) = \mu(p_1) \).

In our subsequent discussion, we simply use \( \mathcal{P}_T \) to denote the marked timed subnet \( ((P_{\text{timed}}, \varphi), X, r, q) \) (with initial marking \( \mu_0 \)) of a marked alternating RQ cf-PN \( \mathcal{P} \). Note that \( \mathcal{P}_T \) itself is an alternating RQ cf-PN and all the transitions in \( \mathcal{P}_T \) are timed transitions. So there are only “timed” circuits and circuit collections in \( \mathcal{P}_T \).

**Lemma 4.15:** Let \( \omega = (\sigma, \tau) \) be a computation of \( \mathcal{P}_T \).
where \( \mu_0 \xrightarrow{\omega} \mu \). For every transition \( t \) in \( P_T \), if \( t \) is not on any circuit, then \( t \) can be fired at most once in \( \omega \), i.e., \( \#_t(\omega) \leq 1 \).

**Proof:** Suppose \( \#_t(\omega) \geq 2 \) and let \( \mu_0 \xrightarrow{(\tau_1, \tau_2)} \mu \xrightarrow{(\tau_3)} \mu' \). Let \( \tau = \{ p \} \), then we have \( \mu_1(p) = 1, \mu'_1(p) = 0 \) and \( \mu_2(p) = 1 \). There must be a path \( \chi \) (in the PN graph) which starts from a marked place \( p_a \) and ends in \( p \), through which a token is deposited to \( p \) before time \( \eta_2 \). (Let the firing sequence corresponding to path \( \chi \) be \( \sigma_\omega \).) Since \( p_a \) is a timed place, \( \mu'_1(p_a) = 1 \). Furthermore, no transition in \( \sigma_\omega \) ever moves a token from \( p \).

Since transitions of \( \sigma_\omega \) are carried out in \( \sigma_2 \) (perhaps interleaved with other transitions), i.e., \( \#_{\sigma_\omega} \leq \#_{\sigma_2} \). Hence \( \mu'_1 \xrightarrow{(\tau_4, \tau_5)} \mu'_1 \xrightarrow{(\tau_6, \tau_7)} \mu'_2 \) is a legal firing sequence, for some \( \tau_4, \tau_5, \tau_6, \tau_7 \). If \( p \notin \tau' \), \( \mu_1(p_a) = 1 \). \( \mu'_1(p) = 2 \), contradicting Lemma 4.12. Hence, \( \tau \leftarrow p_a \leftarrow \tau \), contradicting the assumption. So \( \mu_1(p_a) = 0 \). Since \( \mu_1 \xrightarrow{(\tau_4, \tau_5)} \mu'_1 \) and \( \mu'_1(p_a) = 1 \), it means \( p \in \tau' \) and \( p_a \) is then reachable from \( \tau \). This, together with the fact that \( p_a \xrightarrow{\tau} \tau \), results in a circuit containing \( \tau \) — a contradiction. This completes the proof of the Lemma.

Let \( c = p_1 t_1 p_2 \ldots p_n t_n p_1 \) be a circuit in an alternating RQ cf-PN \( \mathcal{P} \). A place \( p_i \) in \( c \) is called an entry of \( c \) if \( \exists t \in \bullet p_i, t \notin c \). A place \( p_i \) in \( c \) is called an exit of \( c \) if \( \exists t \in \bullet p_i, t \notin c \). A transition \( t_i \) in \( c \) is called a leak of \( c \) if \( \exists p \in \bullet t_i, p \notin c \). In short, an entry of a circuit is a place through which tokens are injected into the circuit. An exit, in contrast, is a place from which tokens leave the circuit. A leak is a transition which spins off tokens whenever the circuit is traversed.

The following is easy to show.

**Lemma 4.16:** Let \( c \) be a clocked circuit and \( \mu \) be a reachable marking with \( \mu(c) \geq 1 \). Then for arbitrary \( a \in N \) there exists a computation \( \omega = (\sigma, \tau) \) such that \( \mu \xrightarrow{\omega} \tau \) and \( \#_r = a \cdot \#_e \), for some \( r \). (That is, \( c \) can be traversed a times in \( \mu \).)

**Corollary 4.17:** Let \( c \) be a circuit that is traversed during the course of the computation. If \( t \) is one of its leaks, then \( \forall p \in \bullet t, p \notin c \) implies \( p \) is an untripped place.

We can also show the following:

**Lemma 4.18:** Given a clocked circuit \( c \) and a number \( d \in N \), we can decide in NP whether traversing circuit \( c \) once (if it is traversable) needs time \( < d \).

A computation \( \omega = (\sigma, \tau) \) of \( P_T \), the traversed net of \( P_T \) associated with \( \omega \), denoted by \( \hat{\mathcal{P}}(\omega) \), is the subnet consisting of places and transitions involved in \( \omega \). Notice that some places in \( P_T \) may not be timed.

From Lemma 4.12, we have

**Lemma 4.19:** Let \( s = \{ c_1, c_2, \ldots, c_r \} \) be a circuit collection in the traversed net \( \hat{\mathcal{P}}(\omega) \). For any reachable marking \( \mu \) of \( \hat{\mathcal{P}}(\omega) \), we must have \( \sum_{i=1}^{r} \mu(c_i) \leq 1 \). In other words, there can be at most one token within a circuit collection in \( \hat{\mathcal{P}}(\omega) \) at any time during the computation \( \omega \).

**Lemma 4.20:** Let \( p \) be a place in \( \hat{\mathcal{P}}(\omega) \). If \( p \) does not belong to any circuit, then \( p \) has at most one input (and output) transition in \( \hat{\mathcal{P}}(\omega) \).

**Proof:** First consider the input case. According to Lemma 4.13, if \( p \) is an untimed place, \( p \) will have at most one timed input transition visible in \( \hat{\mathcal{P}}(\omega) \). Therefore the above statement is true. Next, we consider the case when \( p \) is a timed place.

Assume that \( p \) has two input transitions \( t_1 \) and \( t_2 \) in \( \hat{\mathcal{P}}(\omega) \). Let \( \{ p_1 \} = \bullet t_1 \) and \( \{ p_2 \} = \bullet t_2 \). Without loss of generality, we assume that \( t_1 \) is fired earlier than \( t_2 \) during the computation \( \omega = (\sigma, \tau) \), where

\[
\mu_0 \xrightarrow{(\sigma_1, \tau_1)} \mu_1 \xrightarrow{(t_1, \tau_2)} \mu'_1 \xrightarrow{(\sigma_2, \tau_2)} \mu_2 \xrightarrow{(t_2, \mu_2)} \mu'_2.
\]

Since \( \mu_1(p_1) = 1 \) and \( \mu'_1(p_1) = \mu_1(p_1) - 1 = 0 \). Since \( 1 \geq \mu_1(p) = \mu_1(p) + 1 \), we will have \( \mu'_1(p) = 1 \). That is, in \( \mu'_1 \), the place \( p \) has one token.

Since \( \mu_2(p_2) = 1 \), there exists a place \( p' \) marked in \( \mu'_2 \) such that \( p' \xrightarrow{\tau} p_2 \). Since \( p \) is not on any circuits and \( p_2 \xrightarrow{t_2} p \), \( p \xrightarrow{t_2} p_2 \) (the assumption of the lemma). Therefore \( p' \neq p \). Now we have at \( \mu'_2 \), \( \mu'_1(p) = \mu'_1(p) = 1 \) and \( p' \xrightarrow{\tau} p \xrightarrow{t_2} \), for some clocked transition \( t \) — violating Lemma 4.12.

The proof of the output case is similar, and hence, the details are left to the reader.

Let \( s = \{ c_1, c_2, \ldots, c_r \} \) be a circuit collection in \( \hat{\mathcal{P}}(\omega) \) and \( t \) be a transition which is not in \( s \). (That is, \( t \notin c_i \forall 1 \leq i \leq r \)) \( t \) is said to be an output of \( s \) if \( \exists p \in c_j \) for some \( j \) where \( 1 \leq j \leq r \), such that \( p \in \bullet t \), and \( t \) is an output of \( s \) if \( \exists p \in c_j \) for some \( j \) where \( 1 \leq j \leq r \) such that \( p \in \bullet t \).

**Corollary 4.21:** If \( s \) is a circuit collection in \( \hat{\mathcal{P}}(\omega) \), then \( s \) has at most one input and one output.

The following lemma is from [17].

**Lemma 4.22:** (From Lemma 1 in [17]) Let \( C = \{ c_1, c_2, \ldots, c_n \} \) be a set of connected circuits in a cf-PN \( \mathcal{P} \) and \( \mu \) be a marking with \( \mu(c_i) > 0 \), for some \( i \). For arbitrary integers \( a_1, a_2, \ldots, a_n \geq 0 \), there exists a sequence \( \sigma \) such that \( \mu \xrightarrow{\sigma} \Rightarrow \#_r = \sum_{j=1}^{n} a_j(\#_{e_i}) \). (In words, from \( \mu \) there exists a fireable sequence \( \sigma \) utilizing circuit \( c_j \) exactly \( a_j \) times, for every \( j \).)

We are in a position to put all the pieces together, in order to devise a system of linear inequalities to capture \( \mu_0 \xrightarrow{(\sigma_1, \tau_1)} \mu \xrightarrow{(\sigma_2, \tau_2)} \mu \) (where \( \sigma_a \in T_{timed} \) and \( \sigma_b \in T_{untimed} \) for alternating RQ cf-PNs. (Assume the set of timed transitions \( T_{timed} \) and the set of timed places \( P_{timed} \) of \( \mathcal{P} \) are precomputed in advance.)

1. Guess \( T' \subseteq T_{timed} \) such that \( T' = T(\sigma_a) \), the portion actually involved in the computation.
2. Guess the tree structure of \( T' \) discussed earlier (see
Fig. 3), and then verify its validity. Let \( S = \{s_1, s_2, \ldots, s_g\} \) be circuit collections in \( T' \), and \( s_i = \{c^i_1, \ldots, c^i_{r_i}\} \) be the set of circuits of \( s_i \) for some \( r_i \). We also guess \( d_1, d_2, \ldots, d_g \in N \). Then the following are verified.

a. for each \( s_i \), check the connectivity condition of \( c^i_1, \ldots, c^i_{r_i} \) (doable in polynomial time (Lemma 4.4)),

b. for each circuit collection \( s_i \), verify the condition of Lemma 4.21 (i.e., whether there is at most one input and output for each \( s_i \)),

c. for any timed place \( p \) not in \( s_i \) check if \( p \) has at most one input (and output) transition (Lemma 4.20),

d. verify that the clocked circuit in \( s_i \), \( 1 \leq i \leq g \), can be traversed in \(<\) (or \( \leq \)) \( d_i \) time (Lemma 4.18).

3. Disregarding timing constraints, a system of linear inequalities can be set up (guaranteed by Lemma 4.4) to capture the essence of a marking \( \mu' \) reachable from the initial marking. (Here \( \mu' \) is a vector variable.) In setting up such a system of inequalities, the number of times a circuit is traversed in a circuit collection is kept as a scalar variable (see Lemma 4.4). Now we focus on how timing requirements can be enforced by adding additional inequalities to the system. To give the reader a better feel for how this is done, consider the example given in Fig. 3. As discussed earlier, for each transition \( t \) in \( T' \) not in any circuit collection, \( t \) is fired exactly once during the course of the computation. We therefore assign a variable \( d_i \) to denote the global time at which \( t \) is fired. Since we have allocated variables to represent the numbers of times circuits in \( T' \) (including clocked circuits) are traversed, linear inequalities with respect to variables \( d_i \) can easily be set up, taking into account the number of times clocked circuits are traversed. Take the path from \( t_0 \) to \( t_4 \) for example (assuming that the minimum delays (which can be decided in NP as Lemma 4.18 suggests) of circuit collections \( S_1 \) and \( S_2 \) are 2 and 4 time units, respectively). The associated inequalities for timing constraints along this path are:

\[
\begin{align*}
&d_i \leq d_{i+1}, \forall 1 \leq i \leq 3 \\
&n_1 \times 2 \leq d_i - d_{i-1} \leq 50 \\
&n_2 \times 4 + (d_i - d_{i-1}) \leq d_{i+1} - d_i \\
&20 < d_{i+1} - d_i
\end{align*}
\]

where \( n_1 \) and \( n_2 \) (which are scalar variables) represent the numbers of times the clocked circuits in \( S_1 \) and \( S_2 \), respectively, are executed. The details for the general case are simply a matter of technicality, and hence, are left to the reader.

4. To see whether marking \( \mu \) (given as part of the problem’s instance) can be reached from \( \mu' \) (which is a vector variable) using only transitions belonging to the untimed portion of the PN, Lemma 4.4 again can be used for setting up a system of linear inequalities.

Based on the above idea, we have the following result.

**Theorem 4.23:** The reachability problem for alternating RQ cf-PNs is NP-complete.

**Example 4.1:** A flexible manufacturing system with time constraint is specified as follows. This machine disassembles one component to provide finished or unfinished products. Figure 4 is the alternating RQ cf-PN model of an assembly machine which manufactures four types of products denoted by \( p_2, p_5, p_{12} \) and \( p_{17} \) using one type of component denoted by \( p_0 \). Workstation 1 divides Material A into two Parts B and C. Then, Part B needs to be processed by Workstation 2 with a portion of rework by the same workstation; and Part C needs to be processed by either Workstation 3 or 4. Workstation 2 with a timing constraint produces two types of products \( p_{12} \) and \( p_{17} \). Workstation 3 with a time constraint produces one type of product \( p_2 \). Workstation 4 produces one type of product \( p_5 \).

Let \( P = (P, T, \varphi, X, r, q) \) be an alternating RQ cf-PN with initial marking \( \mu_0 = (1, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0) \) as shown in Fig. 4 where the shaded place and transitions are denoted by untimed places and transitions. \( P \) can be divided into timed subnet \( P_T = ((P, T_{\text{timed}}, \varphi_T), r, q) \) and untimed subnet \( P_U = (P, T_U, \varphi_u) \). Given a marking \( \mu = (0, 0, 0, 0, 0, 1, 0, 0, 0, 0, 0, 0, 2, 0, 0, 0, 1) \), we want to know whether marking \( \mu \) can be reached from \( \mu_0 \). To answer this question, the idea is to construct a system of linear inequalities \( L \) that \( \mu \) is reachable in \( P \) iff \( L \) has an integer solution.

According to Lemma 4.14, there exists a computation \( \omega = (\sigma, \tau) = (\sigma_a \sigma_b, \tau_a \tau_b) \), where \( \sigma_a \in (T_{\text{timed}})^* \) and \( \sigma_b \in (T_{\text{untimed}})^* \), such that \( \mu_0 \xrightarrow{a, \tau} \mu \). We can determine whether marking \( \mu \) of the alternating RQ cf-PN \( P \) is reachable by the following two steps. First, find an intermediate marking \( \mu' \) which is reachable from the initial marking \( \mu_0 \) by firing only the timed transitions. Next, we have to determine if \( \mu \) is reachable in the untimed subnet from the intermediate marking \( \mu' \).

1. Consider \( \mu_0 \xrightarrow{a, \tau} \mu' \), disregarding timing constraints, a
system of linear inequalities can be set up (guaranteed by Lemma 4.4) to capture the essence of a marking $\mu'$ reachable from the initial marking as follows:

$$\mu' = \mu_0 + A_1 \bar{x}_1$$

in which $A_1$ represents the adjacency matrix associated with subnet $P_1$ and is of dimension $18 \times 18$, $\bar{x}_1$ is an $18 \times 1$ column vector of nonnegative integers which is called the firing count vector. Now we focus on how timing requirements can be enforced by adding additional inequalities to the system. We assign a variable $d_i$ to denote the global time at which transition $i$ is fired and the linear inequalities with respect to variables $d_i$ can be set up. The associated inequalities for timing constraints along the path are

$$\begin{align*}
  d_{i_1} &\leq d_{i_2} \\
  5 &\leq (d_{i_2} - d_{i_1}) \\
  d_{i_{11}} &\leq d_{i_{15}} \\
  (d_{i_{15}} - d_{i_{11}}) &\leq 20
\end{align*}$$

2. Consider $\mu' \rightarrow \mu$. Lemma 4.4 again can be used for setting up a system of linear inequalities as follows:

$$\mu = \mu' + A_2 \bar{x}_2$$

in which $A_2$ represents the adjacency matrix associated with subnet $P_2$ and is of dimension $18 \times 18$, $\bar{x}_2$ is an $18 \times 1$ column vector of nonnegative integers which is called the firing count vector.

According to the above results, we can construct a system of linear inequalities $L$ such that $\mu$ is reachable in $P$ iff $L$ has an integer solution.

$$L = \begin{align*}
  \mu' &= \mu_0 + A_1 \bar{x}_1 \\
  \mu &= \mu' + A_2 \bar{x}_2 \\
  d_{i_1} &\leq d_{i_2} \\
  5 &\leq (d_{i_2} - d_{i_1}) \\
  d_{i_{11}} &\leq d_{i_{15}} \\
  (d_{i_{15}} - d_{i_{11}}) &\leq 20
\end{align*}$$

In this example, the $L$ has an integer solution $\bar{x}_1 = (1, 0, 0, 0, 0, 0, 0, 3, 2, 2, 1, 1, 1, 2, 1, 1)$, $\bar{x}_2 = (0, 0, 1, 3, 1, 2, 2, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0)$, $d_{i_1} = 1$, $d_{i_2} = 6$, $d_{i_{15}} = 10$ and $d_{i_{11}} = 5$. 

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