Generalization of pinching operation to binary matroids

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Abstract: In this paper, we generalize the pinching operation on two edges of graphs to binary matroids and investigate some of its basic properties. For \( n \geq 2 \), the matroid that is obtained from an \( n \)-connected matroid by this operation is a \( k \)-connected matroid with \( k \in \{2, 3, 4\} \) or is a disconnected matroid. We find conditions to guarantee this \( k \). Moreover, we show that Eulerian binary matroids are characterized by this operation and we also provide some interesting applications of this operation.

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1. Introduction

The matroid and graph notations and terminology used here will follow [4] and [7]. Let \( G \) be a graph and \( S \) be a subset of the edge set \( E(G) \). We denote by \( G^p_S \) the graph that is obtained from \( G \) by adding a new vertex \( w \) and replacing any edge \( uv \in S \) with two edges \( uw \) and \( wv \). The transition from \( G \) to \( G^p_S \) is called the \( k \)-pinching operation [2] where \( k = |S| \). If \( S \) is the empty set, then 0-pinching means adding a new single vertex \( w \). If \( |S| = 1 \), then applying the pinching operation is equivalent to the subdivision of \( uv \) where \( S = \{uv\} \). If \( S = E(G) \), then we have the parallel classes on the new vertex \( w \) on \( G^p_S \) such that the sequence of the cardinality of them is equal to the degree sequence of the vertices of \( G \).

Applying the pinching operation on graphs is a useful method for solving \( 2k \)-edge-connectivity problems for graphs [2]. For example, suppose that \( G \) is a \( 2k \)-edge-connected graph. Then the graph \( G' \) obtained from \( G \) by pinching together any \( k \) edges of \( G \) is also \( 2k \)-edge-connected.

Raghunathan, Shikare, and Wapare [5] extended the splitting operation and Azadi [1] extended the splitting off and the element splitting operations from graphs to binary matroids. These operations are defined as follows.

Definition 1.1. Let \( M \) be a binary matroid on a set \( E \) and \( A \) be a matrix that represents \( M \) over \( GF(2) \). Consider two elements \( x \) and \( y \) of \( E(M) \). Let \( A_{x,y} \) be the matrix that is obtained by adjoining an extra
row to A whose entries are zero everywhere except in the columns corresponding to x and y where it takes value 1. Let $M_{x,y}$ be the matroid that represents by the matrix $A_{x,y}$. Then the transition from M to $M_{x,y}$ is called the splitting operation.

**Definition 1.2.** Let M be a binary matroid on a set $E$ and A be a matrix that represents M over $GF(2)$. Consider two elements x and y of $E(M)$. Let $A'_{x,y}$ be the matrix that is obtained by adjoining an extra row to A whose entries are zero everywhere except in the columns corresponding to x and y where it takes value 1 and then adjoining an extra column to the resulting matrix with this column being zero everywhere except in the last row. Let $M'_{x,y}$ be the matroid that represents by the matrix $A'_{x,y}$. Then the transition from M to $M'_{x,y}$ is called the splitting operation.

**Definition 1.3.** Let M be a binary matroid on a set $E$ and A be a matrix that represents M over $GF(2)$. Consider two elements x and y of $E(M)$. Let $A_{xy}$ be the matrix that is obtained by adjoining an extra column to A which is the sum of the columns corresponding to x and y, and then deleting the two columns corresponding to x and y. Let $M_{xy}$ be the matroid that represents by the matrix $A_{xy}$. Then the transition from M to $M_{xy}$ is called the splitting off operation.

Splitting off operation on graphs is defined as follows.

**Definition 1.4.** Let G be a graph. Consider two adjacent non-loop edges $x = uw$ and $y = vw$. The splitting off a pair $(x, y)$ of edges from a vertex w means that we replace the edges x and y by a new edge $\alpha = uw$. If $u = v$ then the resulting loop is deleted from the graph.

## 2. 2-pinning operation in 2-connected graphs

Let G be a 2-connected graph with the edge set $E(G)$ and $S \subseteq E(G)$ where $S = \{x = u_1v_1, y = u_2v_2\}$. To apply the 2-pinning operation, we first delete x and y and add new vertex w. Then we add new edges $x' = u_1w$, $x'' = wv_1$, $y' = u_2w$ and $y'' = vw_2$. When x and y are adjacent, we take $v_1 = v_2 = v$. Therefore, $x' = u_1w$, $x'' = wv$, $y' = u_2w$ and $y'' = vw$ (Figure 1). We denote by $G^p_{xy}$ the graph that is obtained by applying the 2-pinning operation on edges x and y. Note that we can retrieve the graph G from $G^p_{xy}$ by splitting off the pair $(x', x'')$ and then splitting off the pair $(y', y'')$.

![Figure 1. 2-pinning operation on \(x, z\) and \(x, y\).](image)

Now let G be a 2-connected graph and $x, y \in E(G)$. Then G has a cycle $C_n$ of length n containing both x and y. Assume that the cycle $C_n$ has the minimum cardinality among all such cycles. After applying the 2-pinning operation on x and y, the cycle $C_n$ transforms into two cycles $C'_{n+2}$ and $C'_{n-2}$, where k is the length of shortest path Q among endpoints of x and y such that $x'' \in C'_{k+2}$ and $x', y' \in C'_{n-k}$. In other words, $C'_{k+2} = E(Q) \cup \{x'', y''\}$ \(^1\) and $C'_{n-k} = (C_n - (E(Q) \cup \{x, y\})) \cup \{x', y'\}$. Note that if $k = 0$, then x and y are adjacent and $C_n$ transforms to $C_2 = \{x'', y''\}$ and $C'_n = (C_n - \{x, y\}) \cup \{x', y'\}$.

\(^1\) In this section, for cycle $C_n$ of length n from a graph G, we consider just its edge set.
Theorem 2.1. Let $G$ be a graph and $x, y \in E(G)$. Suppose that $G$ has a cycle $C_n$ with a minimum length containing both $x$ and $y$. Let $x', x'', y', y'' , C_{k+2}^n$ and $C_{n-k}$ be the notations as mentioned above. Moreover, let $\delta = \{C_{k+2}^n, C_{n-k}^n\}$ and $C$ be a subset of edges of $G_{xy}^\delta$. Then $C$ forms a cycle of $G_{xy}^\delta$ if and only if $C \in \delta$ or $C$ satisfies one of the following conditions.

(i) $C$ is a cycle of $G$ which contains neither $x$ nor $y$;

(ii) $C = (C' - \{x\}) \cup \{x', x''\}$ where $C'$ is a cycle of $G$ containing $x$ but not $y$, or $C = (C'' - \{y\}) \cup \{y', y''\}$ where $C''$ is a cycle of $G$ containing $y$ but not $x$; and

(iii) $C = C_1 \Delta C_2$ where $C_1 \in \delta$, and $C_2$ is a cycle of type (i) or (ii) such that $C_1 \cap C_2 \neq \emptyset$.

Proof. It is clear that if $C \in \delta$ or if $C$ is a cycle of $G$ containing neither $x$ nor $y$, then $C$ is a cycle of $G_{xy}^\delta$. Now suppose that $C = (C' - \{x\}) \cup \{x', x''\} = (C'' - \{u_1v_1\}) \cup \{u_1w, v_1w\}$ where $C'$ is a cycle of $G$ containing $x$ but not $y$ and has length $k' + 1$, so

$$C' = v_1u_1z_1...z_{k'-1}z_{k'}v_1.$$  

After applying the pinching operation on $x$ we delete $x$ and add $x' = u_1w$ and $x'' = v_1w$. Thus \[ (C' - \{x\}) \cup \{x', x''\} = u_1z_1...z_{k'-1}e_{k'}z_{k'}v_1. \]

Therefore $C$ is a cycle of $G_{xy}^\delta$. Similarly, if $C = (C'' - \{y\}) \cup \{y', y''\} = (C'' - \{u_2v_2\}) \cup \{u_2w, v_2w\}$ where $C''$ is a cycle of $G$ containing $y$ but not $x$, then $C$ is a cycle of $G_{xy}^\delta$. Now suppose that $C_1$ is a cycle of type (i) or (ii) and $C_2 \in \delta$ such that $C_1 \cap C_2 \neq \emptyset$. To show that $C_1 \Delta C_2$ is a cycle of $G_{xy}^\delta$, we consider the following two possible cases.

![Figure 2. Internally disjoint paths.](image)

Case(1) Let $C_1$ be a cycle of type (i). Assume that $C_1$ has a non-empty intersection with both $C_{k+2}^n$ and $C_{n-k}^n$. Let $Q_1$ and $Q_2$ be the paths such that $C_1 \cap C_{n-k}^n = E(Q_1)$ and $C_1 \cap C_{k+2}^n = E(Q_2)$. Moreover, let $Q_3, Q_4$ be internally disjoint paths which have the same end vertices as of $Q_1$ and $Q_2$ such that $C_1 = E(Q_1) \cup E(Q_2) \cup E(Q_3) \cup E(Q_4)$ (See figure 2). Suppose that $C_{n-k}^n = E(Q_1) \cup E(Q_2) \cup E(Q_7)$ and $C_{k+2}^n = E(Q_2) \cup E(Q_3) \cup E(Q_8)$ where $Q_i$ and $Q_j$ for $i, j \in \{1, 2, 5, 6, 7, 8\}$ are internally disjoint paths. Then clearly $C_1 \Delta C_{n-k}^n$ and $C_1 \Delta C_{k+2}^n$ are cycles of $G_{xy}^\delta$. Similarly, if $C_1$ has a non-empty intersection with exactly one of $C_{n-k}^n$ or $C_{n-k}^n$, then $C_1 \Delta C_{n-k}^n$ or $C_1 \Delta C_{k+2}^n$ is a cycle of $G_{xy}^\delta$.

Case(2) Let $C_2 = E(Q_3) \cup E(Q_5) \cup E(Q_6)$ and $C_3 = E(Q_4) \cup E(Q_7) \cup E(Q_8)$. Clearly, $C_2$ and $C_3$ are cycles of type (ii). It is easy to see that $C_2 \Delta C_{n-k}^n, C_2 \Delta C_{k+2}^n, C_3 \Delta C_{n-k}^n$ and $C_3 \Delta C_{k+2}^n$ are cycles of $G_{xy}^\delta$.

Now suppose that $x$ and $y$ are adjacent. Then $v_1 = v_2 = v$ and $C_{k+2}^n = vy'vy''wv$. An argument similar to one as given above shows that $C_1 \Delta C_2$ and $C_1 \Delta C_n$ are cycles of $G_{xy}^\delta$ where $C_1$ is a cycle of type (i) or (ii) such that $C_1 \cap C_n \neq \emptyset$. 

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Conversely, let $C$ be a cycle of $G_{xy}$. By the definition of $G_{xy}$ one can see that $x, y \notin E(G_{xy})$ and $x', x'', y', y'' \notin E(G)$. Let $N = \{x', x'', y', y''\}$. We know that the induced subgraph of $N$ is either the graph (a) or the graph (b) of the following figure.

![Figure 3. Two possible induced subgraphs of $N$.](image)

It is straightforward to check that if $C$ is a cycle of $G_{xy}$, then $C$ contains a subset $N'$ of $N$ with $|N'| \in \{0, 2\}$. Therefore, every cycle of $G_{xy}$ is a cycle of type (i), (ii) or (iii).

### 3. 2-pinching operation for binary matroids

Now, by using Theorem 2.1 we extend the notion of the 2-pinching operation from 2-connected graphs to binary matroids.

Let $M$ be a binary matroid and $x, y \in E(M)$. We denote by $C_p$ the collection of all the circuits of $M$ which contain both $x$ and $y$. Choose a member of $C_p$ and denote it by $C_{xy}$. Partition $C_{xy} - \{x, y\}$ into sets $X_1$ and $X_2$ where $X_1$ or $X_2$ may be empty. We denote such a partition by $P = (X_1, X_2)$. Clearly, when $C_p$ is empty or $|C_{xy}| = 2$, there is no such partition and we say that $P$ is empty.

**Definition 3.1.** Let $A$ be a matrix that represents the matroid $M$ over $\mathbb{GF}(2)$. For $x, y \in E(M)$, let $C_p \neq \emptyset$ and $P = (X_1, X_2)$ constructed as above. Let $A_P^{xy}$ be the matrix obtained from $A$ by the following way.

1. **Adjoin four extra columns to $A$ with the labels $\alpha_1, \alpha_2, \alpha_3$ and $\alpha_4$ such that:**
   - The corresponding entries of $\alpha_1$ and $x$ are equal;
   - $\alpha_2$ is a zero column;
   - $\alpha_3, \alpha_4$ are columns whose entries are 1 or 0 such that sum of the corresponding entries of the corresponding columns of $X_1 \cup \{\alpha_1, \alpha_3\}$ and $X_2 \cup \{\alpha_2, \alpha_4\}$ are zero.

2. **Delete the two columns corresponding to $x$ and $y$**

3. **Adjoin an extra row with this row being zero everywhere except in the columns corresponding to the new elements $\alpha_k$ where it takes value 1, for $1 \leq k \leq 4$.

We call a vector matroid obtained in this way a pinching on $x$ and $y$ with respect to $P$ or 2-pinching with respect to $P$ and denoted it by $M_P^{xy}$.

**Definition 3.2.** Let all hypotheses of Definition 3.1 be established except that $C_p = \emptyset$ is an empty set. To build $A_{xy}^\emptyset$ from $A$, carry out all the steps of Definition 3.1 with the following changes.

- The corresponding entries of $\alpha_1$ and $x$ are equal;
- The corresponding entries of $\alpha_3$ and $y$ are equal;

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2 This means $x$ or $y$ or both are cocircuits of $M$
• α₂ and α₄ are zero columns;

**Remark 3.3.** Let x and y be two elements of a matroid M. Note that, when |Cₓᵧ| = 2, we can build Aₓᵧ with both Definition 3.1 and Definition 3.2. So throughout this paper, we may assume that Cₓ ≢ ∅ unless in special cases of some proofs.

Figure 4. \{x=6,y=7\} and \{P={\{1\},\{4\}}\).

**Example 3.4.** Consider the cycle matroid of the graph in Figure 4 with the following matrix A over GF(2) that represents it:

\[
A = \begin{bmatrix}
1 & 2 & 3 & 4 & 5 & 6 & 7 \\
1 & 0 & 0 & 1 & 0 & 0 & 0 \\
0 & 1 & 0 & 1 & 1 & 0 & 1 \\
0 & 0 & 1 & 0 & 1 & 1 & 1
\end{bmatrix}.
\]

Let x = 6 and y = 7. Then Cₓ = \{\{2,6,7\},\{1,4,6,7\}\}. Let Cₓᵧ = \{1,4,6,7\}. Partition Cₓᵧ – \{x,y\} to two sets X₁ = \{1\} and X₂ = \{4\}. So by definition 3.1, after applying the pinching operation on x and y with respect to P₁ = (\{1\},\{4\}), the matrix A transforms into the following matrix.

\[
A^{P₁}_{xy} = \begin{bmatrix}
1 & 2 & 3 & 4 & 5 & α₁ & α₂ & α₃ & α₄ \\
1 & 0 & 0 & 1 & 0 & 0 & 0 & 1 & 1 \\
0 & 1 & 0 & 1 & 1 & 0 & 0 & 0 & 1 \\
0 & 0 & 1 & 0 & 1 & 1 & 0 & 1 & 0 \\
0 & 0 & 0 & 0 & 0 & 1 & 1 & 1 & 1
\end{bmatrix}.
\]

One can easily check that the vector matroid of A^{P₁}_{xy} is isomorphic to M*(K₃,3). Hence the 2-pinching operation on a graphic matroid may not yield a graphic matroid. Moreover, M*(K₃,3) is a 3-connected matroid while the original matroid is not. Clearly, if Cₓᵧ = \{2,6,7\}, then P₂ = (\{2\},∅) and M^{P₂}_{xy} is a graphic matroid.

**Theorem 3.5.** Let M = (E,C) be a binary matroid on the set E together with the collection C of circuits. Let x, y ∈ E(M), and for a given circuit Cₓᵧ, let P = (X₁, X₂) be a partition of Cₓᵧ – \{x,y\}. Suppose that Γ = \{α₁,α₂,α₃,α₄\} such that αᵢ ∉ E(M) for 1 ≤ i ≤ 4. Then M^{P}_{xy} = ((E – \{x,y\}) ∪ Γ, C^{P}_{xy}) with C^{P}_{xy} = C₀ ∪ C₁ ∪ δ ∪ C₂ ∪ C₃ is a binary matroid where

(i) C₀ = \{C ∈ C : C contains neither x nor y \};

(ii) C₁ = \{(C – \{x\}) ∪ \{α₁,α₂\} : C ∈ C, and C contains x but not y \} ∪ \{(C – \{y\}) ∪ \{α₃,α₄\} : C ∈ C, and C contains y but not x \};

(iii) δ = \{X₁ ∪ \{α₁,α₃\}, X₂ ∪ \{α₂,α₄\}\};

(iv) C₂ = \{C₁ΔC₂ : C₁ ∈ C₀ ∪ C₁, and C₂ ∈ δ, and if C₁ ∈ C₀, then C₁ ∩ C₂ ≠ ∅; and there is no circuit C of C₀ and C₁ such that C ⊆ C₁ΔC₂ \}; and
(v) $C_3$ = The set of minimal members of $\{ (C - \{x,y\}) \cup \Gamma : x, y \in C \text{ and } C \neq C_{xy} \}$.

**Proof.** It is easy to see that every member of $C_{xy}^P$ is a circuit in $M_{xy}^P$. To show that $C_{xy}^P$ is the collection of circuits of a binary matroid on $(E - \{x,y\} \cup \Gamma )$, we must show that if $X, Y \in C_{xy}^P$, then $X \not\subseteq Y$ and $Y \not\subseteq X$, and $X \Delta Y$ contains at least one member of $C_{xy}^P$. So there are fifteen cases to check and checking them is straightforward.

Let $A$ be a matrix that represents $M$ over $GF(2)$. Let $C \subseteq E(M)$. Then $C$ is a circuit of $M$ if and only if $C$ is minimal and the mod-2 sum of the columns of $A$ corresponding to $C$ is a zero columns [4]. By using this fact, a consequence of the last theorem is the following partial result.

**Corollary 3.6.** Under the hypotheses of Theorem 3.5, Let $A$ be a matrix that represents $M$ over $GF(2)$, then the collection of minimal dependent sets of vector matroid of the matrix $A_{xy}^P$ is $C_{xy}^P$.

**Proof.** Depending on how to construct the matrix $A_{xy}^P$, the proof is straightforward.

**Corollary 3.7.** Under the hypotheses of Theorem 3.5, Let $r(M)$ and $r(M_{xy}^P)$ be the ranks of $M$ and $M_{xy}^P$, respectively. Then $r(M_{xy}^P) = r(M) + 1$.

**Proof.** Suppose that $B$ is a basis of $M$ such that $C_{xy}$ is a fundamental circuit with respect to $B$. Then exactly one of the following holds.

(i) $\{ C_{xy} - \{x\} \} \subseteq B$.

Let $B_1 = (B - \{y\}) \cup \{\alpha_3, \alpha_4\}$. Then $|B_1| = |B| + 1$. By Theorem 3.5(iii), $C_1' = X_1 \cup \{\alpha_1, \alpha_3\}$ and $C_2' = X_2 \cup \{\alpha_2, \alpha_4\}$ are circuits of $M_{xy}^P$. Clearly, $B_1$ does not contain either $C_1'$ or $C_2'$, nor any member of $C_0 \cup C_1 \cup C_2 \cup C_3$. So $B_1$ is an independent set. Since $B_1 \cup \alpha_1$ contains $C_1'$ and $B_1 \cup \alpha_2$ contains $C_2'$, the set $B_1$ is a maximal independent set and so $B_1$ is a basis of $M_{xy}^P$.

(ii) $\{ C_{xy} - \{y\} \} \subseteq B$.

Similar to (i) we can see that if $B_2 = (B - \{x\}) \cup \{\alpha_1, \alpha_2\}$, then $B_2$ is a maximal independent set and $|B_2| = |B| + 1$.

(iii) $C_{xy} - \{z\} \subseteq B$ where $z \notin \{x, y\}$.

Suppose that $z \in X_1$ and $B_3 = (B - \{x, y\}) \cup \{\alpha_1, \alpha_2, \alpha_3, \alpha_4\}$. Consider two sets $(X_1 - \{z\}) \cup \{\alpha_1, \alpha_3\}$ and $X_2 \cup \{\alpha_2, \alpha_4\}$. Clearly, $(X_1 - \{z\}) \cup \{\alpha_1, \alpha_3\}$ is an independent set and $X_2 \cup \{\alpha_2, \alpha_4\}$ is a circuit of $M_{xy}^P$. Hence $B_3$ cannot be a basis of $M_{xy}^P$, but $B_4 = B_3 - \{z'\}$ where $z' \in (X_2 \cup \{\alpha_2, \alpha_4\})$ is a basis of $M_{xy}^P$ and $|B_4| = |B| + 1$.

By (i),(ii) and (iii), we conclude that $r(M_{xy}^P) = r(M) + 1$.

4. Connectivity of $M_{xy}^P$  

In this section, by studying the cardinality of some circuits and cocircuits of $M_{xy}^P$, we explore the relationship between connectivity of a given connected binary matroid $M$ and $M_{xy}^P$.

**Theorem 4.1.** Let $M_{xy}^P$ be the pinching of a connected binary matroid $M$. If $C_1 \cup C_3 = \emptyset$, then $M_{xy}^P$ is a disconnected matroid. Otherwise, $M_{xy}^P$ is a connected matroid.
Proof. Since $M$ is a connected matroid, for every pair $(x, y)$ of two elements of $E(M)$, there exists a circuit of $C(M)$ containing both $x$ and $y$. Now assume $C_1 \cup C_3 = \emptyset$. By Theorem 3.5, we conclude that there is no circuit of $M$ which contains $x$ but not $y$, also there is no circuit of $M$ which contains $y$ but not $x$. Let $P = (X_1, X_2)$. Then $\delta = \{C_1', X_1 \cup \{\alpha_1, \alpha_3\}, C_2' = X_2 \cup \{\alpha_2, \alpha_4\}\}$ and $C_2 = \{C_4' \Delta C : C \in C_0, \text{ and } C \cap C_1' \neq \emptyset\} \cup \{C_4' \Delta C : C \in C_0, \text{ and } C \cap C_2' \neq \emptyset\}$. Hence every member of $C_2$ contains either $\{\alpha_1, \alpha_3\}$ or $\{\alpha_2, \alpha_4\}$. Since $C_4' = C_0 \cup \delta \cup C_2$, there is no member of $C_4'$ containing $\{\alpha_1, \alpha_2\}, \{\alpha_3, \alpha_4\}, \{\alpha_1, \alpha_4\}$ and $\{\alpha_2, \alpha_3\}$. Therefore $M_{xy}^4$ is a disconnected matroid. Similarly, it can be shown that if $C_1 \cup C_3$ is non-empty, then $M_{xy}^4$ is a connected matroid.

Definition 4.2. We say that two elements $x$ and $y$ from given matroid $M$ are equivalent and denoted by $x \sim y$, if at least one of $x$ and $y$ is a coloop of $M$ or $\{x, y\}$ is a cocircuit of $M$.

If a matroid $M$ has at least one pair of equivalent elements, then $M$ is a connected or is a disconnected matroid. Our important task of this section is to rely connectivity of obtained matroid by applying the 2-pinning operation on a given $n$-connected matroid. So we investigate the cocircuits of matroid obtained by this operation.

It is well known that all circuits and all cocircuits of an $n$-connected matroid and at least $2(n-1)$ elements have at least $n$ elements [4]. For a given binary matroid $M$ with matrix representation $A$ and two elements $x, y \in E(M)$, we denote by $R(C^*_x)$ and $R(C^*_y)$ the cocircuit space of $A$ and $A_{xy}$, respectively. In the following lemmas, we use the fact that the intersection of a circuit and a cocircuit of a binary matroid $M$ has even cardinality [4].

Lemma 4.3. Let $M_{xy}^p$ be a pinching of a binary matroid $M$. Then $\Gamma = E(M_{xy}^p) - E(M)$ is a cocircuit of $M_{xy}^p$ if and only if $x$ is not equivalent to $y$.

Proof. Suppose that $M_{xy}^p$ is such a pinching with $P = (X_1, X_2)$ and $\Gamma = \{\alpha_1, \alpha_2, \alpha_3, \alpha_4\}$. Let $A$ be the matrix that represent $M$ over $GF(2)$. Build $A_{xy}^p$ for $M_{xy}^p$ according to Definition 3.1. Since there is a row in the row space of $A_{xy}^p$ which this row is zero everywhere except in the corresponding entries of $\Gamma$, we get $\Gamma \in R(C^*_y)$. Assume, without loss of generality, that $x \sim y$. Then exactly one of the following holds.

(i) $\{x, y\}$ is a cocircuit of $M$. Then, there is a row in the row space of $A$ such that has the following form

$$
E(M) - \{x, y\} \begin{bmatrix} x & y \\ 0 & 0 & \ldots & 0 & 1 & 1 \end{bmatrix}.
$$

Let $\delta_{X_i}$ be the sum of the corresponding entries of $X_i$ in this row, for $i \in \{1, 2\}$. Clearly, $\delta_{X_1} = \delta_{X_2} = 0$. Thus, after applying the pinching operation on $M$, we have the following row in the row space of $A_{xy}^p$

$$
E(M) - \{x, y\} \begin{bmatrix} \alpha_1 & \alpha_2 & \alpha_3 & \alpha_4 \\ 0 & 0 & \ldots & 0 & 1 & 0 & 1 & 0 \end{bmatrix}.
$$

We conclude that $\{\alpha_1, \alpha_3\}$ and $\{\alpha_2, \alpha_4\}$ are in $R(C^*_x)$. By Theorem 3.5, $C_1 = X_1 \cup \{\alpha_1, \alpha_3\}$ and $C_2 = X_2 \cup \{\alpha_2, \alpha_4\}$ are circuits of $M_{xy}^p$ and since $|C_j \cap \{\alpha_i\}| = 1$ for some $j \in \{1, 2\}$ and some $i \in \{1, 2, 3, 4\}$. Hence $\{\alpha_1, \alpha_3\}$ and $\{\alpha_2, \alpha_4\}$ are minimal and so $\Gamma$ is not a cocircuit of $M_{xy}^p$.

(ii) $\{x\}$ is a cocircuit of $M$. Then $M$ is a disconnected matroid and so $\{\alpha_2, \alpha_4\}$ is a circuit of $M_{xy}^p$. We see that in the row space of $A_{xy}^p$ the corresponding entries of $\alpha_1$ and $\alpha_3$ are 1 and 0, respectively and $\delta_{X_1} = \delta_{X_2} = 0$. Therefore $\{\alpha_1\}$ is a cocircuit of $M_{xy}^p$ and so $\Gamma$ is not.
To complete the proof, we must show that $\Gamma$ is minimal when $x$ is not equivalent to $y$. On the contrary, let $Z$ be a proper subset of $\Gamma$ which is minimal. Clearly, if $|Z| = 1$ or 3, then there is a circuit in $M_{xy}^P$ such that its intersection with $Z$ or $Z \Delta \Gamma$ is a singleton set and if $|Z| = 2$, we get $x \sim y$; a contradiction. Hence $\Gamma$ is a cocircuit of $M_{xy}^P$.

For a given matroid $M$, The girth $g(M)$ is the minimum circuit size of $M$ unless $M$ has no circuits, in which case, $g(M) = \infty$. The cogirth is the girth of the dual of $M$. By Lemma 4.3, we conclude that if $M$ is an $n$-connected matroid with $n \geq 4$, then the cogirth of $M_{xy}^P$ is 4. Now suppose that the girth of such a matroid $M$ is $k$ and $|C_{xy}| = k$. Then clearly, $k \geq n$ and by Theorem 3.5, there is a matroid $M_{xy}^P$ which will decrease the girth of $M$ to $w$ where $2 \leq w \leq \left[\frac{k}{2}\right] + 1$. Note that if $g(M) = \infty$, for a given matroid $M$, then $g(M_{xy}^P) = 2$.

**Lemma 4.4.** Let $M$ be an $n$-connected binary matroid with $n \geq 4$ and $|E(M)| \geq 2(n - 1)$. Let $x, y \in E(M)$ and $P = (X_1, X_2)$ be a partition of $C_{xy} - \{x, y\}$ with $X_i \neq \emptyset$ for $i \in \{1, 2\}$. Then $(\Gamma, E(M_{xy}^P) - \Gamma)$ is a 4-separation of $M_{xy}^P$ where $\Gamma = E(M_{xy}^P) - E(M)$.

**Proof.** Suppose that $M$ is an $n$-connected binary matroid with $n \geq 4$ and $|E(M)| \geq 2(n - 1)$. Let $M_{xy}^P$ be a pinching of $M$ with respect to partition $P$ with non-empty parts. Then $|E(M_{xy}^P)| \geq 2n$. Consider the partition $(X, Y)$ of $E(M_{xy}^P)$ with $X = \Gamma$ and $Y = E(M_{xy}^P) - \Gamma$ where $\Gamma = E(M) - E(M_{xy}^P)$. Clearly, $\min\{|X|, |Y|\} = 4$. Suppose that $r$ and $r'$ be the rank functions of $M$ and $M_{xy}^P$, respectively. The set $\Gamma$ is an independent set and $Y \subseteq E(M)$. By using Theorem 3.7

\[
r'(X) + r'(Y) - r'(M_{xy}^P) \leq 4 + r(M) - r'(M_{xy}^P) \leq 3.
\]

Hence, for $n \geq 4$, the partition $(X, Y)$ is a 4-separation of $M_{xy}^P$. 

The last result says that for every $n$-connected matroid with $n \geq 4$, the marroid $M_{xy}^P$ is a $k$-connected matroid for some $k \in \{2, 3, 4\}$.

**Lemma 4.5.** Let $x$ and $y$ be two elements of an $n$-connected binary matroid $M$, for $n \geq 3$, and let $M_{xy}^P$ be a pinching of $M$. Let $Z$ be a cocircuit of $M_{xy}^P$ such that $Z \neq \Gamma$ where $\Gamma = E(M_{xy}^P) - E(M)$. Then $|Z| \geq n$.

**Proof.** Assume, without loss of generality, that $Z'$ is an $n$-element cocircuit of $M$ and $P = (X_1, X_2)$ is a partition of $C_{xy} - \{x, y\}$. Let $A$ be a matrix that represents $M$ and $A_{xy}^P$ constructed from $A$ by Definition 3.1. Suppose that $\delta X_i$, for $i \in \{1, 2\}$ is the sum of the corresponding entries of $X_i$ in the corresponding row of $Z'$ in row space of $A$. So $\delta X_i \in \{0, 1\}$. Then exactly one of the following holds.

(i) $x, y \notin Z'$ and $|Z' \cap X_1|$ and $|Z' \cap X_2|$ are even. Then $\delta X_1 = \delta X_2 = 0$ and therefore all corresponding entries of $\Gamma$ in the corresponding row in row space of $A_{xy}^P$ are 0. We conclude that $Z'$ is a cocircuit of $M_{xy}^P$.

(ii) $x, y \notin Z'$ and $|Z' \cap X_1|$ and $|Z' \cap X_2|$ are odd. Then $\delta X_1 = \delta X_2 = 1$ and therefore the corresponding entries of $\alpha_1$ and $\alpha_2$ in $A_{xy}^P$ are 0 and $\alpha_3$ and $\alpha_4$ are 1. So $Z' \cup \{\alpha_3, \alpha_4\}$ is a cocircuit of $M_{xy}^P$.

Note that there is no cocircuit $Z'$ of $M$ not containing $x$ and $y$ such that $|Z' \cap X_1|$ is odd and $|Z' \cap X_2|$ is even or vice versa. Since in this case $|Z' \cap C_{xy}|$ is odd; a contradiction.

(iii) $x$ is in $Z'$ but $y$ not, then $|Z' \cap X_1|$ is odd and $|Z' \cap X_2|$ is even or vice versa. Then $(Z' - \{x\}) \cup \{\alpha_1\}$ or $(Z' - \{x\}) \cup \{\alpha_1, \alpha_3, \alpha_4\}$ is a cocircuit of $M_{xy}^P$.

(iv) $y$ is in $Z'$ but $x$ not, then $|Z' \cap X_1|$ is odd and $|Z' \cap X_2|$ is even or vice versa. Then $(Z' - \{y\}) \cup \{\alpha_3\}$ or $(Z' - \{y\}) \cup \{\alpha_4\}$ is a cocircuit of $M_{xy}^P$. 

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(v) $x, y \in Z'$ and $|Z' \cap X_1|$ and $|Z' \cap X_2|$ are even. Then $\delta X_1 = \delta X_2 = 0$ and therefore the corresponding entries of $\alpha_2$ and $\alpha_4$ in $A_{xy}^P$ are 0 and $\alpha_1$ and $\alpha_3$ are 1. So $(Z' - \{x, y\}) \cup \{\alpha_1, \alpha_3\}$ is a cocircuit of $M_{xy}^P$.

(vi) $x, y \in Z'$ and $|Z' \cap X_1|$ and $|Z' \cap X_2|$ are odd. Then $\delta X_1 = \delta X_2 = 1$ and therefore the corresponding entries of $\alpha_2$ and $\alpha_3$ in $A_{xy}^P$ are 0 and $\alpha_1$ and $\alpha_4$ are 1. So $(Z' - \{x, y\}) \cup \{\alpha_1, \alpha_4\}$ is a cocircuit of $M_{xy}^P$.

Clearly, every cocircuit $Z$ in (i)-(vi) has at least $n$ elements and $x$ is not equivalent to $y$. By Lemma 4.3 $\Gamma$ is a cocircuit of $M_{xy}^P$, and if $Z'' \triangle \Gamma$ is a cocircuit of $M_{xy}^P$, then $|Z''| \geq n$.

Consider the following proposition [4], which is straightforward consequence of the fact that all circuits and all cocircuits of an $n$-connected matroid have at least $n$ elements where the ground set of such matroid has at least $2(n - 1)$ elements.

**Proposition 4.6.** Let $(X, Y)$ be an $n$-separation of an $n$-connected matroid and suppose that $|X| = n$. Then $X$ is either a coinddependent circuit or an independent cocircuit.

By using this Proposition and the last three lemmas, for a given $n$-connected matroid with $n \geq 4$, we have the following result to determine connectivity of pinching matroid with respect to $P$.

**Theorem 4.7.** Let $M$ be an $n$-connected binary matroid with $n \geq 4$ and $|E(M)| \geq 2(n - 1)$. Then $M_{xy}^P$ with respect to $P = (X_1, X_2)$ is a $k$-connected matroid for $k \in \{2, 3, 4\}$, if $\min\{|X_1|, |X_2|\} \geq k - 2$.

**Proof.** Let $M$ be an $n$-connected binary matroid and let $C_{xy}$ be an $n'$-element circuit of $M$ where $n' \geq n$. Assume that $P = (X_1, X_2)$ with $|X_1| = n' - k$ and $|X_2| = k - 2$. Let $X = (X_2 \cup \{\alpha_2, \alpha_4\})$ and $Y = E(M_{xy}^P) - X$ and let $r$ and $r'$ be the rank functions of $M$ and $M_{xy}^P$, respectively. Since $X$ is a circuit of $M_{xy}^P$, so $r'(X) = k - 1$. Clearly, $r'(Y) \leq r'(E(M_{xy}^P))$. Now by these facts, we have

$$r'(X) + r'(Y) - r'(M_{xy}^P) \leq k - 1.$$ 

Since $\min\{|X_1|, |Y|\} = k$, the partition $(X, Y)$ is a $k$-separation of $M_{xy}^P$. To complete the proof, we shall show that $M_{xy}^P$ has no $j$-separation for $j < k$. On the contrary, let $(X', Y')$ be a $j$-separation of $M_{xy}^P$ with $|X'| = j$. Then, by Proposition 4.6, the set $X'$ must be a coinddependent circuit or independent cocircuit in $M_{xy}^P$. But such circuit or cocircuit by Theorem 3.5 and Lemma 4.5 does not exist; a contradiction.

**Definition 4.8.** Let $M$ be an $n$-connected binary matroid and $x, y \in E(M)$ such that $x$ is not equivalent to $y$. We say that a collection of some subsets of $E(M_{xy}^P)$ is a Pinched set if all its members have at least $(n + 1)$ elements. Note that if we consider $C_{xy}^*(M_{xy}^P) - \Gamma$ where $\Gamma = E(M_{xy}^P) - E(M)$, then the cardinality of its members can be characterized by Lemma 4.5.

**Theorem 4.9.** Let $M$ be an $n$-connected binary matroid for $n \in \{2, 3\}$ and $x, y \in E(M)$ such that $x$ is not equivalent to $y$ and $C_{xy}^*$ has at least one member with $k$-elements where $k \geq 2n$ and every $n$-element circuit of $M$ contains exactly one of $x$ and $y$. Let $C_{xy}^*(M_{xy}^P) - \Gamma$ is a Pinched set. Then $M_{xy}^P$ is an $(n + 1)$-connected matroid.

**Proof.** Suppose that $M$ is an $n$-connected binary matroid and $x, y \in E(M)$. Choose $C_{xy}$ from $C_{xy}^*$ with $|C_{xy}| \geq 2n$. Partition $C_{xy} - \{x, y\}$ to two sets $X_1$ and $X_2$ with $|X_1| = n - 1$ and $|X_2| \geq k - n - 1$. Let $X = X_1 \cup \{\alpha_1, \alpha_2\}$ and $Y = E(M_{xy}^P) - X$. Then, $\min\{|X|, |Y|\} = n + 1$. Therefore, by the similar argument in Theorem 4.7, one can easily show that $(X, Y)$ is a minimal $(n + 1)$-separation for $M_{xy}^P$. 


5. Applications

In this section, we study the relations between the pinching operation and other binary operations such as splitting, the splitting off operation and element splitting. Using these relations, we will extend the notions of subdivision on to edges and special case of vertex identification on two vertices from graphs to binary matroids. Moreover, we shall show that Eulerian binary matroids are characterized in terms of the 2-pinching, splitting and splitting off operations.

The next theorem shows the relationship between $M$ and $M_{xy}^P$ by combining the notions of minors, the splitting off and element splitting operation.

**Theorem 5.1.** Let $M$ be a connected binary matroid. Let $x,y \in E(M)$ and $\Gamma = \{u,v,u',v'\}$ such that $\Gamma \not\subset E(M)$. Consider the matroid $M_{xy}^P$ with $\delta = \{X_1 \cup \{u,u'\}, X_2 \cup \{v,v'\}\}$. Then

(i) $M_{xy}^P \setminus \Gamma = M \setminus \{x,y\}$.

(ii) \((M_{xy}^P)_{uv} = (M_{xy}^P)_{u'v'} \approx M\).

(iii) If $X_1 = \emptyset$, then $M_{xy}^P/u,u' = M_{xy}^P/\{u\} \setminus \{u'\} = M_{xy}^P/\{u\} \setminus \{u'\} = M$ where $\{u,u'\}$ is a 2-circuit of $M_{xy}^P$. Moreover, $M_{xy}^P$ is a single parallel extension of $M_{x,y}$, or $M_{xy}^P \setminus u = M_{x,y}^P$ and $M_{xy}^P \setminus u' = M_{x,y}^P$.

**Proof.** The proofs are straightforward. \(\square\)

A binary matroid $M$ with ground set $E$ is **Eulerian** if there exist disjoint circuits $C_1,C_2,...,C_P$ such that $E = C_1 \cup C_2 \cup ... \cup C_P$ and $M$ is called **bipartite** matroid if every circuit has even cardinality. For a given connected graph $G$, the matroid $M(G)$ is Eulerian if and only if $G$ is an Eulerian [6]. A matroid $M$ is Eulerian if and only if $M_{xy}$ is Eulerian [3]. The next theorem, says that this property is preserved under 2-pinching operation. To prove this theorem, we consider the following lemma and we use the fact that $M$ is a binary matroid if and only if the symmetric difference of any set of circuits is a disjoint union of circuits [4].

**Lemma 5.2.** Let $M_{xy}^P$ be a pinching of an Eulerian binary matroid $M$. Then $E(M)$ can be partitioned into a collection of disjoint circuits of $M$ such that this partition contains $C_{xy}$.

**Proof.** For some $k$, let $\pi = (C_1,C_2,...,C_k)$ be a partition of $E(M)$ such that $C_{xy} \not\subset \pi$. Choose all circuits of this partition that have a non-empty intersection with $C_{xy}$. Let $V$ be the union of these circuits and let $V'$ be the union of other circuits of this partition which are not in $V$. Then, there is a circuit $C$ that is not in $\pi$ and contains $x$, $y$ or both such that $C \cap V' = \emptyset$ and $V \Delta C$ is a disjoint union of circuits and contains $C_{xy}$. Now $E(M) = V' \cup (V \Delta C)$. Clearly, this union is a disjoint union of circuits. \(\square\)

**Theorem 5.3.** Let $M$ be a binary matroid on a set $E$ and $x,y \in E$. Then $M$ is Eulerian if and only if $M_{xy}^P$ is Eulerian.

**Proof.** Suppose that $M$ is a binary Eulerian matroid and $x,y \in E(M)$. Then, for some $k$, there are disjoint circuits $C_1,C_2,...,C_k$ of $M$ such that $E(M) = \bigcup_{i=1}^{k} C_i$. Let $C_{xy}^P$ be a pinching of $M$. If $x \in C_i$ and $y \in C_j$ where $i,j \in \{1,2,...,k\}$ and $i \neq j$, then $C' = (C_i - \{x\}) \cup \{\alpha_1, \alpha_2\}$ and $C'' = (C_j - \{y\}) \cup \{\alpha_3, \alpha_4\}$ are disjoint circuits of $M_{xy}^P$. Clearly, $E(M_{xy}^P) = C_{xy} \cup ... \cup C_{i-1} \cup C' \cup C_{i+1} \cup ... \cup C_{j-1} \cup C'' \cup C_{j+1} \cup ... \cup C_k$ and these circuits are pairwise disjoint. If $x,y \in C_{i'}$, for $i' \in \{1,2,...,k\}$, then we have two following cases.

(i) If $C_{xy} = C_{i'}$ and $P = (X_1,X_2)$, then $E(M_{xy}^P) = C_1 \cup ... \cup C_{i'-1} \cup (X_1 \cup \{\alpha_1, \alpha_3\}) \cup (X_2 \cup \{\alpha_2, \alpha_4\}) \cup C_{i'+1} \cup ... \cup C_k$. Clearly, any two members of this union are disjoint.
(ii) If $C_{xy} \neq C_{y'}$, then we have two following cases:
   - If $((C_y - \{x,y\}) \cup \{\alpha_1, \alpha_2, \alpha_3, \alpha_4\}) \in C_3$, then $E(M^P_{xy}) = C_1 \cup \ldots \cup C_{r-1} \cup (C_y - \{x,y\}) \cup \{\alpha_1, \alpha_2, \alpha_3, \alpha_4\} \cup C_{r+1} \cup \ldots \cup C_k$. Clearly, any two members of this union are disjoint.
   - If $((C_y - \{x,y\}) \cup \{\alpha_1, \alpha_2, \alpha_3, \alpha_4\}) \notin C_3$, then by Lemma 5.2, there is another partition of $E(M)$ containing $C_{xy}$ and by part (i) of this proof we can see that $M^P_{xy}$ is an Eulerian matroid.

We conclude that $M^P_{xy}$ is an Eulerian matroid. To prove the converse, by Theorem 5.1, $M = ((M^P_{xy})_{\alpha_1,\alpha_2})_{\alpha_3,\alpha_4}$. Therefore when $M^P_{xy}$ is an Eulerian matroid, $M$ is Eulerian.

**Corollary 5.4.** Every uniform matroid $U_{n,n+1}$ can be constructed from $U_{1,2}$ by a sequence of the 2-pinching, splitting and splitting off operations.

**Proof.** Let $M = U_{1,2}$ and so there is no partition $P$. Then $M^P_{xy} \cong U_{1,2} \oplus U_{1,2}$. Now let $\{x',y'\}$ be $\{\alpha_1,\alpha_2\}$ or $\{\alpha_3,\alpha_4\}$. Then $(M^P_{xy})_{x',y'} \cong U_{2,4}$ and $((U_{3,4})_{x',y'}) \cong U_{2,3}$. By repeating this process (first applying the 2-pinching operation with respect to partition $P$ with one empty part, then applying splitting and splitting off operations, respectively), we eventually obtain $U_{n,n+1}$ and then $U_{n-1,n}$, respectively.

On combining Theorem 5.3 with Corollary 5, we immediately obtain the following result.

**Theorem 5.5.** A binary matroid is Eulerian if, by repeated applications of splitting and 2-pinching operations, it can be transformed to a direct sum of copies of $U_{1,2}$ and $U_{2,3}$.

Now we extend the notions of subdivision of edge and vertex identification from graphs to binary matroids by the pinching operation in the following way.

Let $G$ be a 2-connected graph and $x, y \in E(G)$. After applying the pinching operation on $x$ and $y$, let $\{x',y'\}$ and $\{x'',y''\}$ be the disjoint subsets of members of $\delta$. Clearly, if we apply the splitting operation using $\{x',x''\}$ or $\{y',y''\}$, then the obtained graph is isomorphic to the graph resulting by subdivision of $G$ on $x$ and $y$.

Let $G$ be a 2-connected graph and $u, v, w, z \in E(G)$, such that $t_1$ and $t_2$ are the common vertices of $\{u, v\}$ and $\{w, z\}$, respectively, and there are no other edges on $t_1$ and $t_2$. Suppose that $\alpha$ and $\alpha'$ are the new elements of the obtained graph after applying the splitting off operation on $\{u, v\}$ and then $\{w, z\}$, respectively. Now if we implement the pinching operation on $\{\alpha, \alpha'\}$, then the obtained graph is isomorphic to the graph obtained by vertex identification of $t_1$ and $t_2$.

**Definition 5.6.** Let $M$ be a connected binary matroid and $x, y \in E(M)$. Consider the matroid $M^P_{xy}$ with $\delta = \{X_1 \cup \{u, u'\}, X_2 \cup \{v, v'\}\}$. We call $N_1$ as the subdivision of $M$ on $x$ and $y$ if $N_1 \cong (M^P_{xy})_{u,v} \cong (M^P_{xy})_{u',v'}$.

**Definition 5.7.** Let $M$ be a connected binary matroid and $\Gamma \subseteq E(M)$ such that $\Gamma = \{u, v, u', v'\}$. Suppose that $\{u, v\}$ and $\{u', v'\}$ are cocircuits of $M$ and $E(M_{uv}) - E(M) = \{\alpha\}$ and $E(M_{u'v'}) - E(M) = \{\alpha'\}$. We call $N_2$ is the identification $\{u, v\}$ with $\{u', v'\}$ in $M$, if $N_2 \cong (M_{uv})_{\alpha} \cong (M_{u'v'})_{\alpha'}$.

**Corollary 5.8.** The matroids $N_1$ and $N_2$, defined as in definitions 5.6 and 5.7, are extensions of the notions of subdivision and vertex identification from graphs to connected binary matroids.

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