Article

Sample-Wise Aiding in GPS/INS Ultra-Tight Integration for High-Dynamic, High-Precision Tracking

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Academic Editor: Jörg F. Wagner
Received: 7 March 2016; Accepted: 6 April 2016; Published: 11 April 2016

Abstract: By aiding GPS receiver tracking loops with INS estimates of signal dynamics, GPS/INS ultra-tight coupling can improve the navigation performance in challenging environments. Traditionally the INS data are injected into the loops once every loop update interval, which limits the levels of dynamics accommodated. This paper presents a sample-wise aiding method, which interpolates the aiding Doppler into each digital sample of the local signal to further eliminate the dynamic errors. The relationship between the tracking error and the aiding rate is derived analytically. Moreover, the effects of sample-wise aiding using linear and spline interpolations are simulated and compared with traditional aiding under different INS data update rates. Finally, extensive tests based on a digital IF (intermediate frequency) signal simulator and a software receiver validate the theoretical equations and demonstrate that the dynamic stress error can be significantly reduced by sample-wise aiding.

Keywords: ultra-tight coupling; GPS/INS integration; Doppler aiding; high dynamics; tracking loop

1. Introduction

GPS (global positioning system) and INS (inertial navigation system) have complementary characteristics and their integration has been widely explored [1]. High dynamics and high precision impose conflicting requirements on noise bandwidths of conventional tracking loops in GPS receivers [2]. GPS/INS ultra-tight coupling or deep integration can relieve this conflict by aiding the tracking loops with inertial measurements and, thus, boost immunity to high dynamics, severe interferences and brief line-of-sight blockages [3–5]. The idea of using ultra-tight GPS/INS integration to improve performance in challenging environments has drawn wide attention of the navigation community [6–13]. Yang, Y. & El-Sheimy, N. discussed the implementation and performance analysis of a PLL (phase locked loop) aided by a MEMS (micro electromechanical system) IMU (inertial measurement unit) [10]. Details of the IMU mechanization and state propagation can be found in [8]. Alban, S., et al., used a feed-forward aided PLL model and its transfer function to facilitate the performance analysis of carrier tracking loops with external aiding [11]. Gebre-Egziabher, D., et al., quantified the effects of tracking loop bandwidth, receiver clock jitter, aiding Doppler error and platform vibration on the quality of the carrier phase estimate provided by the aided PLL [12]. Chiou, T.Y. took account of different orders of carrier tracking loops with external aiding and detailed the process of data collection and experimental setup [13]. With the development of vector tracking based receiver techniques [14], the GPS/INS deep coupling architecture (where the tracking loops are implemented as part of a larger navigation filter) has been studied increasingly by a number of authors. Gustafson, D., et al., proposed a vector-based GPS receiver which can be deeply integrated with inertial
and alternate sensors [15]. Petovello, M.G. & Lachapelle, G. investigated three different Kalman filter implementation options in both scalar-tracking mode and vector-tracking mode with particular consideration for carrier phase tracking [16]. The analyses and tests in [17,18] showed improved tracking and reacquisition performances of vector-based deep integration at the cost of increased computational loads. Babu, R. & Wang, J. noticed that, typically, the integration filter runs at a rate of 1 to 100 Hz, while the carrier tracking loop normally runs at about 1000 Hz [19]. An interpolator based on a CIC (cascaded integrator comb) filter is proposed to interpolate the low rate Doppler measurements from the navigation filter to 1000 Hz. Babu, R. & Wang, J. concerned the quality of IMU-derived Doppler estimates for integration, and derived the relationship between the baseband I (in-phase)/Q (quadrature) and the INS error for performance analysis [20]. Several studies addressed the algorithms of the pre-filter and the centralized navigation filter [21,22]. Hwang, D.B., et al., provided a unified frame applicable to several different algorithms of GPS/INS deep fusion [23]. Xie, F., et al., proposed an adaptive robust deeply coupled GNSS/INS system based on a federated architecture which can process the GPS L1 and BeiDou B1 signals together [24]. Qin, F., et al., assessed the performance using low-cost IMU and vector tracking-based integration for high dynamic applications [25]. Recent work in [26] presented a federated GPS/INS deep coupling system employing three Kalman filters and providing adaptation ability to improve the navigation performance under various jamming conditions.

All of these reported aiding methods, however, inject the externally-estimated carrier Doppler directly into the tracking loop as a constant during the coherent integration period, which is at least 1 millisecond (ms). This limits the aiding rate not higher than 1000 Hz which is insufficient to the suppression of high dynamic stress errors. For example, the carrier Doppler may change up to 10.5 Hz over a 1 ms interval under a 2000 m/s^2 acceleration. Therefore, the effects of the traditional aiding are restricted.

This paper proposes a sample-wise aiding method, which interpolates the aiding data onto each sampling point before controlling local reference signal generation to further eliminate the high-order dynamic errors with only minor modification of the receiver NCO (numerically controlled oscillator). Section 2 goes deep into the digital realization of the Doppler-aided PLL to explore the influence of the aiding data update rate on the scalar tracking performance. Theoretical derivation of the phase error transfer function reveals that the aiding effect has a lot to do with the product of the frequency of signal dynamics and the aiding update interval. Section 3 presents the sample-wise aiding method to break the performance limitation of the traditionally aided PLL, and simulates the effects of linear and spline interpolations in comparison with the traditional aiding. In Section 4 a testbed for the performance limitation of GPS/INS ultra-tight integration is implemented, which employs a digital IF (intermediate frequency) GPS signal simulator to test the tracking errors of a software GPS receiver ultra-tightly coupled with INS data in various high dynamic scenarios. The test results coincide with theoretical expectations and demonstrate that the sample-wise aiding can eliminate the dynamic errors much more effectively than the traditional aiding.

2. Performance of Traditional Doppler Aiding

Both the scalar tracking-based ultra-tight integration and the vector tracking-based deep integration use Doppler-aided tracking loops, which allows the INS or the integrated navigation filter to account for the platform dynamics such that the receiver can narrow its loop bandwidths. To simplify the analysis of the effects of the aiding data update rate, this section will focus on the dynamic process of the scalar tracking based PLL.

According to the fundamental theory of PLL [27], the linear phase model of a PLL can be extended to describe the dynamic process of a PLL aided by external Doppler data, as shown in Figure 1.

In Figure 1, \( \theta_c \) is the phase error between the input phase \( \theta_i \) and the PLL output phase \( \theta_o \); \( f_a \) is the aiding Doppler provided by the INS estimate; \( F(\omega) \) denotes the transfer operator of the loop filter in the time-domain analysis, whose counterpart is \( F(s) \) in the Laplace domain.
For an unaided PLL, the closed-loop phase transfer function is \( H(s) = \frac{F(s)}{1 + F(s)} \), and the phase error transfer function is \( H_e(s) = 1 - H(s) = \frac{s}{s + F(s)} \). The phase error \( \theta_e(s) \) is determined by the input signal phase \( \theta_i(s) \) and the phase noise \( n(s) \) as follows:

\[
\theta_e(s) = H_e(s) \theta_i(s) - H(s) n(s)
\]  

(1)

Generally the term “high dynamics” is embodied in two aspects: high frequency and large amplitude of the range/velocity/acceleration/jerk variations. For the case study of the behavior of the loop under high dynamics, a typical sinusoidal trajectory in the LOS (line of sight) direction is selected in this paper due to its following characteristics: (1) any complicated LOS dynamics can be decomposed into a number of sinusoidal components with different amplitudes and frequencies; and (2) it can provide severe high-order dynamics with continuous derivatives and limited range of motion, which is easy to control and convenient for testing. Therefore the LOS Doppler is simply modeled as:

\[
f_d(t) = A\sin(\omega t)
\]

(2)

The carrier phase of the incoming signal is:

\[
\theta_i(t) = -\frac{A}{\omega} \cos(\omega t) + \theta_0
\]

(3)

Since second-order and higher-order PLLs are not sensitive to frequency/phase steps, the steady state carrier phase tracking error is only caused by the first item in Equation (3):

\[
\theta_e(t) = -\frac{A}{\omega} |H_e(\omega)| \cos[\omega t + \Phi_e(\omega)]
\]

(4)

It is seen that the sinusoidal dynamic stress error cannot be completely eliminated, and the amplitude of the error transfer function \( |H_e(\omega)| \) represents the ratio of the amplitude of the phase error to that of the incoming phase.

For a Doppler aided PLL, it can be easily derived from Figure 1 such that:

\[
\theta_e(s) = H_e(s) \theta_{ia}(s) - H(s) n(s)
\]

(5)

\[
\theta_{ia}(s) = \theta_i(s) - \theta_d(s)
\]

(6)

\[
\theta_d(s) = \frac{1}{s} f_d(s)
\]

(7)

where \( \theta_{ia}(s) \) is defined as the equivalent input phase of the aided PLL, and \( \theta_d(s) \) is the feedback phase introduced by Doppler aiding.

Ideally, if the INS-estimated Doppler is exactly the derivative of the input phase, there will be \( \theta_{ia}(t) = 0 \) and \( \theta_e(s) = H(s) n(s) \), which means that the LOS dynamics can be completely eliminated by Doppler aiding. As a matter of fact, the signal frequency shift is also contributed by the receiver clock drift, the satellite clock drift, and even induced by the ionosphere/troposphere propagation, (which are beside the point of the paper). In this case, the design of the loop filter only needs to account for the phase noise instead of tracking the signal dynamics. The analog domain analysis, however,
cannot cover all aspects of a digital realization. (1) In reality, a PLL in a modern receiver is implemented in its digital version—the DPLL where, traditionally, the aiding Doppler value remains constant over an external data update interval $T_d$ (which may be equal to, smaller than, or larger than the pre-detection integration time $T_i$; nevertheless most reported systems keep $T_d > T$ as considered in [19]); whereas the Doppler variation within this interval cannot be ignored in high dynamic situations; and (2) the carrier NCO control word actually comes from the Doppler estimate of the previous interval rather than the current interval. Taking these two factors into account, we replace the analog NCO integrator in Figure 1 with a digital accumulator operated at the sampling rate $R_d$, as shown in Figure 2. Note that another input of the adder before the integrator in Figure 1, namely the loop filter output, has been dedicated to investigating the feedback phase of the aiding data.

Assuming that the aiding Doppler is provided at an update rate $R_d = 1/T_d$, and the Doppler data update interval is the integral multiple of the digital signal sample interval $T_s$: $m = T_d/T_s = R_d/T_r$.

At the $n$th sample time $nT_s = (km + l)T_s = kT_d + lT_s$, $l = 0, 1, \cdots, m - 1$, the feedback phase can be expressed as $\theta_i(nT_s) = \sum_{p=0}^{k-1} f_d(pT_d) \cdot T_d + f_d(kT_d) \cdot lT_s$. The equivalent input phase of the loop at each aiding Doppler update epoch $(n = km, l = 0)$ is:

$$\theta_i(kT_d) = \theta_i(kT_d) - \theta_i(kT_d) - \sum_{p=0}^{k-1} f_d(pT_d) \cdot T_d = \sum_{p=0}^{k-1} [\theta_i(pT_d + T_a) - \theta_i(pT_d) - f_d(pT_d) \cdot T_d] + \theta_i(0) \quad (8)$$

Assuming that the INS can provide exact Doppler estimates synchronized with the LOS signal Doppler, we have $f_d(pT_d) = A\sin(\omega pT_d)$ for the sinusoidal input carrier phase in Equation (3). Substituting them into Equation (8) and neglecting the constant item (because it contributes nothing to the steady state tracking error), we obtain:

$$\begin{align*}
\theta_i(kT_d) &= \sum_{p=0}^{k-1} \left[ -\frac{A}{\omega} \cos(\omega pT_d + \omega T_d) + \frac{A}{\omega} \cos(\omega pT_d) - A\sin(\omega pT_d) \cdot T_d \right] \\
&= A \sum_{p=0}^{k-1} \left[ C_{a2}(\omega T_d) \sin(\omega pT_d) + C_{a1}(\omega T_d) \cos(\omega pT_d) \right] \cdot T_d \\
&= A C_{a2}(\omega T_d) \sum_{p=0}^{k-1} \sin[\omega pT_d + \phi_a(\omega T_d)] \cdot T_d \\
C_{a2}(\omega T_d) &= \frac{1 - \cos(\omega T_d)}{\omega T_d} = C_0(\omega T_d) \sin[\phi_a(\omega T_d)] \quad , \quad C_{a1}(\omega T_d) = \frac{\sin(\omega T_d) - \omega T_d}{\omega T_d} = C_0(\omega T_d) \cos[\phi_a(\omega T_d)] \\
C_a(\omega T_d) &= \sqrt{C_{a1}^2(\omega T_d) + C_{a2}^2(\omega T_d)} \quad , \quad \phi_a(\omega T_d) = \text{atan2}(1 - \cos \omega T_d, \sin \omega T_d - \omega T_d) \\
&= (10)
\end{align*}$$

**Figure 2.** Feedback phase of traditionally aided DPLL.
When the product of the frequency of signal dynamics and the aiding update interval satisfies $\omega T_a < 2\pi$, the accumulation of discrete sinusoidal signals in Equation (9) can be approximated by the integration of the corresponding analog signal as the following:

$$\theta_{ua}(kT_a) \approx A C_a(\omega T_a) \int_{kT_a}^{(k+1)T_a} \sin[\omega t + \phi_a(\omega T_a)] \, dt = -\frac{2}{\omega} C_a(\omega T_a) \cos[\omega kT_a + \phi_a(\omega T_a)] + \frac{4}{\omega^2} C_a(\omega T_a) \cos[\phi_a(\omega T_a)]$$

(11)

Consequently, the phase error of the aided loop will follow the sinusoidal shape with the same angular frequency of the input signal phase as shown in Equation (4), yet with an amplitude of $C_a(\omega T_a)$ times that of the unaided PLL. The equivalent phase error transfer function of the aided loops is:

$$H_{e-a}(\omega) = H_e(\omega) C_a(\omega T_a) \exp[\phi_a(\omega T_a)]$$

(12)

When $\omega T_a > 2\pi$, we can find a frequency $\omega_b$ meeting $\omega_b T_a < 2\pi$ and $\omega T_a = \omega_b T_a + m 2\pi$, which yields $\theta_{ua}(kT_a) \approx -\frac{2}{\omega} C_a(\omega T_a) \cos[\omega_b kT_a + \phi_a(\omega T_a)] + \frac{4}{\omega^2} C_a(\omega T_a) \cos[\phi_a(\omega T_a)]$. For this case Equation (12) still suffices. It is accurate enough, provided that the product of $\omega_b$ and $T_a$ satisfies $\omega_b T_a < 2\pi$, as can be validated by the simulation in Section 3.2.

Thus, $C_a(\omega T_a)$ is the ratio of the phase error amplitudes of aided loop to unaided loop. Figure 3 shows how this ratio varies with $\omega T_a$ according to Equation (10). Note that the $C_a(\omega T_a)$ expression is applicable to any loop orders except for 1.

![Figure 3. Ratio of phase error amplitudes of aided loop to unaided loop.](image)

It can be seen that: (1) If $\omega T_a < 2.3311$, the Doppler aiding can improve tracking performance by $C_a(\omega T_a) < 1$; (2) When $\omega T_a \gg 2\pi$, $C_a(\omega T_a) \approx 1$, the aiding can hardly help to mitigate the dynamic stress error; (3) When $\omega T_a < 2\pi$, $C_a(\omega T_a) \approx \frac{1}{2} \omega T_a$, $\lim_{\omega T_a \to 0} C_a(\omega T_a) = 0$. Hence, for the traditional Doppler aided loop, the aiding rate is expected to be as high as possible to efficiently reduce the dynamic stress error. However, the practical INS-offered data rate may not satisfy the cases with high frequency component or large amplitude of the LOS dynamics.

3. Sample-Wise Doppler-Aided PLL

3.1. Ideal Sample-Wise Doppler Aiding

In order to reduce the high dynamic stress error, it is effective to increase the aiding rate. Actually, the external aiding data can be interpolated to each sample point of the digital signal, and the control words of the carrier NCO for generating the local reference carrier can be updated at a rate up to the sampling rate $R_s$ without major modification of the receiver architecture, as shown in Figure 4. This is the main idea of the sample-wise Doppler aiding method proposed in this section. Compared with traditionally-aided DPLL in Figure 2, the only additional cost of sample-wisely aided DPLL is the frequency interpolation unit before the NCO phase accumulator.
In Figure 4 the control word of the carrier NCO is $f_{sa}(0 : T_s : T_p) = \text{interp1} \left[ f_a(0 : T_a : T_p) \right]$, where interp1(·) is the one-dimensional data interpolation function and $T_p$ is the time length of the data. In contrast to the traditionally aided DPLL, the equivalent input carrier phase in a sample-wisely Doppler aided DPLL becomes:

$$\theta_{ia}(nT_s) = \theta_i(nT_s) - \theta_a(nT_s) = \theta_i(nT_s) - \sum_{q=0}^{n-1} f_{sa}(qT_s) \cdot T_s$$

(13)

If there are still no aiding Doppler error and no time alignment error, ignoring the interpolation error (which will be investigated in the next subsection), we can substitute $f_{ia}(qT_s) = A \sin (qT_s)$ and $\theta_i(t) = -\frac{A}{\omega} \cos (\omega t)$ into Equation (13) to obtain:

$$\theta_{ia}(nT_s) \approx -\frac{A}{\omega} C_a(\omega T_s) \cos [\omega nT_s + \phi_a(\omega T_s)] + \frac{A}{\omega} C_a(\omega T_s) \cos [\phi_a(\omega T_s)]$$

(14)

It is easy to meet that:

$$C_a(\omega T_s) \approx \frac{1}{2} \omega T_s$$

(15)

Thereby the equivalent phase error transfer functions of the sample-wisely aided loop is:

$$H_{e \rightarrow sa}(\omega) = H_e(\omega) C_a(\omega T_s) \exp [\phi_a(\omega T_s)]$$

(16)

The equations indicate that the aiding performance can be significantly improved by increasing the aiding rate from $R_a$ to $R_s$. Smaller dynamic stress error can be achieved with a higher sampling frequency.

As stated in [14], a second-order PLL is superior to a third-order PLL when aided. Taking typical third-order and second-order PLL designs in [2] as examples, Figure 5 plots the amplitudes of the phase error transfer functions of the unaided second-order PLL, the unaided third-order PLL, the traditionally-aided second-order PLL, and the sample-wisely-aided second-order PLL, respectively, with the PLL bandwidth $B_{PLL} = 15$ Hz, the Doppler aiding rate $R_a = 1/T_a = 1$ KHz, and the signal sampling rate $R_s = 1/T_s = 10$ MHz.

Figure 5 shows that:

1. When the frequency of the LOS dynamic vibration goes much higher than the natural frequency of the loop, the unaided carrier tracking loop has an amplitude magnification of near 1 with hardly any suppression on the dynamic stress error.
2. Except for the frequency range of $6.7 < \omega < 28.6$ (rad/s), the unaided third-order PLL shows better dynamic performance than the unaided second-order PLL.
3. For the dynamic vibrations with $\omega > 2331$ rad/s, the traditional Doppler aiding cannot improve tracking performance efficiently.
4. Since $T_s$ is normally several orders of magnitude smaller than $T_a$, the phase error can be significantly reduced by sample-wise Doppler aiding.
Figure 5. Amplitudes of phase error transfer functions.

3.2. Effects of Interpolation Techniques

In practice, we cannot obtain the exact value of the signal Doppler at each sample point by interpolation. This section explores the effects of different interpolation techniques on the dynamic stress error, still ignoring the errors in the INS estimates. Two typical interpolation approaches are investigated: (1) the linear interpolation; and (2) the cubic spline interpolation.

A linear interpolation is generally the simplest that yields a good result when up-sampling. Figure 6 illustrates the block diagram of a second-order PLL sample-wisely-aided by the Doppler data linearly-interpolated from the INS data rate $R_s$ to the sampling rate $R$. 

Figure 6 follows the phase model of Figure 1 in the time domain and omits the input noise for simplicity. The components shown in purple are operated at the sampling rate $R_s$ and often implemented in hardware for real-time processing; whereas the components shown in white and green are operated at the loop filter update rate $R$ (usually the reciprocal of the pre-detection integration time $T$) and the INS data update rate $R_s$, respectively. One exception is the carrier phase discriminator, which is usually composed of the carrier lookup table, the multipliers for carrier and code wipe-offs, and the integration and dump accumulator. The accumulator performs low-pass filtering and down-samples the data from $R_s$ to $R = 1/T$. The cost of replacing traditional aiding with sample-wise aiding is the
linear interpolation unit shown in the top block of Figure 6, which can also be regarded as an extra stage of NCO accumulator.

In high dynamic situations, the simple linear interpolation may not meet the requirements of curve smoothness and approximation. More complicated up-sampling should be used to construct a smoother velocity and approximate complex trajectory. Among the interpolation techniques including polynomials, splines, Gaussian/low-pass processes, wavelets, etc., the cubic spline is very popular for its tradeoff between accuracy and computational cost. The trajectory constructed by cubic splines makes the velocity, acceleration, and jerk (VAJ) continuous, and the VAJ of the initial and final positions can be flexibly configured [28,29].

Simulations are carried out for the two interpolation techniques from a wide range of INS data rates to three sampling rates. Figure 7 compared their effects on the ratios of phase error amplitudes of aided PLLs to unaided loops, with (a) \( \omega = 1 \text{ rad/s} \), (b) \( \omega = 50\pi \text{ rad/s} \), respectively. The simulation results of traditional aiding are also included and compared with the theoretical prediction in Equation (10).

![Figure 7. Ratio of phase error of aided PLL to unaided PLL, with (a) \( \omega = 1 \text{ rad/s} \); (b) \( \omega = 50\pi \text{ rad/s} \).](image)

It can be seen that:

1. Equation (10) is accurate to quantify the effectiveness of traditional Doppler aiding, regardless of the aiding data error and the time alignment error.
2. When \( \omega T_d > 2.33 \text{ rad/s} \), any aiding technique cannot suppress the dynamic stress error efficiently and, thus, is not worth the bother.
3. When \( T_d \) begins to decrease from its upper threshold \( T_d(\omega) \approx 2.33/\omega \), sample-wise aiding with linear interpolation shows better tracking performance than traditional aiding, yet its improvement cannot catch up with the spline interpolation, until that \( T_d \) reaches a certain lower threshold, denoted by \( T_l(\omega, R_s) \). When \( T_d < T_l(\omega, R_s) \), the ratio of the phase error amplitudes of sample-wisely aided PLL to unaided PLL will be only determined by the theoretical limit \( C_d(\omega T_d) \approx 0.5 \omega T_d \), independent of \( T_d \), and cannot be improved by employing other more advanced interpolation techniques. In addition, before reaching the lower threshold, there is a middle threshold \( T_m(\omega, R_s) \) of \( T_d \), such that for the \( T_m(\omega, R_s) < T_d < T_u(\omega) \) region, the improvement effect of sample-wisely aided PLL to unaided PLL is only determined by \( T_d \) and the interpolation method, independent of \( R_s \), and can be improved by employing other more advanced interpolation techniques.

It is interesting to divide each sample-wise aiding curve into the following four regions by using three thresholds of \( T_d \) as shown in Table 1.
where, $h_D = 500$ m, then the maximum velocity, acceleration, and jerk are $500$ m/s, $500$ m/s, and $500$ m/s, respectively. With a satellite elevation angle of $\alpha = 28.67^\circ$, the LOS velocity is:

$$V_{LOS} = (D \sin \alpha) \omega \sin (\omega t) \approx 239.88 \sin (t)$$  \hspace{1cm} (17)$$

The receiver first tracks the high dynamic signals using a third-order PLL, and then changes to a second-order PLL aided by a priori known carrier Doppler after decoding the navigation message. Meanwhile the reception time (in GPS time) of each individual sample point can be obtained by using the single-SV (satellite vehicle) timing method proposed in [30]. For high dynamic scenarios, the known user trajectory rather than a static user location is utilized in the timing process. In this way the time alignment necessary for the aiding can be achieved.
The loop parameters recommended in Tables 5.6 in [2] are adopted in the tests. In addition, the PLL is configured with a loop bandwidth of 15 Hz and a loop update interval of 1 ms, while the DLL taking a 1 Hz bandwidth and a 20 ms loop update interval. For traditional aiding, the external Doppler data are injected to the PLL every 1 ms, and to the DLL every 20 ms whereas, for the sample-wise aiding, the receiver first interpolates the aiding data using cubic splines from 1000 Hz to the sampling rate, and then injects the interpolated carrier Doppler and code Doppler values into the carrier NCO and the code NCO, respectively.

Finally the code phase errors and carrier phase errors can be obtained by subtracting the simulator recorded data from the receiver measurements.

4.2. Aiding Effects on Carrier Phase Errors with no Thermal Noise in the Signal

To exclude the effects of thermal noises and cross-correlation interferences, a single-SV scenario is necessary to achieve high C/N0 (carrier-to-noise ratio) and, thus, high precision dedicated for the dynamic stress error test [30]. Although no noise is added to the output signal of the digital IF signal simulator, the C/N0 estimated by the receiver is around 128 dB-Hz owing to both the inevitable quantization noise and the limited range of the wideband-narrowband power ratio method (PRM) [31]. This is near the best PRM-estimated C/N0 achieved by our test platform so far. The carrier phase tracking errors in a 5 g (≈50 m/s^2) dynamic condition are shown in Figure 8.

![Figure 8](image_url)

**Figure 8.** Carrier phase error using traditional aiding with 5 g dynamics and no thermal noise. (a) From unaided (0–30 s) to aided (30–200 s) status; (b) Aided period (30–200 s).

In the first 30 s, the receiver is in unaided state as the epoch time has not been solved. The top plot in Figure 8 shows that the carrier phase error is greatly reduced after external Doppler aiding being turned on. The bottom plot discards the first 30 s data and examines the aided period. It can be seen that the carrier phase error remains a sinusoidal wave with the same angular frequency of 1 rad/s, as predicted by Section 2.

The tests have been conducted using six sets of signals with the dynamics of 5 g, 20 g, 50 g, 100 g, 200 g, and 500 g, respectively. The carrier phase tracking errors using traditional Doppler aiding are shown in Figure 9a. Figure 9b calculates their amplitudes by combining Equations (4), (10) and (12), and the experimental results shown in the red triangle symbols are well in accordance with the theoretical expectation in the blue line.
The small discrepancies between the test results and the theoretical value of the ratio are due to the

**Figure 9.** Carrier phase errors using traditional aiding with different dynamics and no thermal noise. (a) Test results; and (b) Comparison with theoretical error amplitude.

Table 2 summarizes the test results of the error amplitudes for the unaided third-order PLL and the aided second-order PLL and their ratios. The theoretical calculation of this ratio follows:

\[
 r = \frac{|H_{3}(j\omega)|}{C_{a} (\omega T_{a}) |H_{2}(j\omega)|} \approx 230.04
\]

(18)

**Table 2.** Test results of carrier phase errors using traditional Doppler aiding.

| Dynamics (Acceleration) | Error Amplitude (m) for Unaided 3rd-Order PLL, B = 15 Hz | Error Amplitude (m) for Aided 2nd-Order PLL, B = 15 Hz | Ratio of Unaided to Aided Error Amplitudes |
|-------------------------|----------------------------------------------------------|--------------------------------------------------------|------------------------------------------|
| 5 g                     | 0.00345                                                  | 1.50 × 10^{-5}                                         | 230.00                                   |
| 20 g                    | 0.0138                                                   | 6.03 × 10^{-5}                                         | 228.86                                   |
| 50 g                    | 0.0345                                                   | 1.49 × 10^{-4}                                         | 231.54                                   |
| 100 g                   | N/A                                                      | 3.00 × 10^{-4}                                         | N/A                                      |
| 200 g                   | N/A                                                      | 6.00 × 10^{-4}                                         | N/A                                      |
| 500 g                   | N/A                                                      | 1.50 × 10^{-3}                                         | N/A                                      |

**Figure 10.** Carrier phase errors using sample-wise aiding with different dynamics and no thermal noise.

The test results demonstrate the correctness of the equations of carrier tracking errors in Section 2. The small discrepancies between the test results and the theoretical value of the ratio are due to the
impacts of the time alignment errors and the quantization errors of the test platform, as well as the round-off errors of the error amplitudes.

Corresponding to Figure 9a, Figure 10 shows the tracking results using the sample-wise aided PLL with 5 g, 50 g, and 500 g dynamics. It is seen that the dynamic errors have been further eliminated. It should be noted that the tracking error achieved by the test platform even in a static user scenario is at the $0.8 \times 10^{-6}$ m level in terms of the standard deviation of the carrier phase error. The errors in Figure 10 are also contributed by the alignment errors between the simulator and the receiver (related to the signal dynamics) and the quantization errors.

4.3. Aiding Effects on Code/Carrier Phase Errors with a C/N0 of 80 dB-Hz

The carrier tracking loop is usually the weak link in a receiver and the carrier phase error characterizes the receiver dynamic performance. For the traditionally-aided receiver tracking in high dynamic conditions, however, the code tracking loop may also become the dynamically weak part since the DLL has a narrower bandwidth and a longer integration time and sometimes a narrow correlator spacing for the sake of noise and interference suppression. Both the carrier phase error and the code phase error are concerned in this subsection. The signal $C/N_0$ is configured to be 80 dB-Hz in the test for the following two reasons: (1) the $1\sigma$ thermal noise code tracking jitter is about 0.0059 m under 80 dB-Hz and a narrow correlator spacing of 0.1 chip. If the $C/N_0$ is set too low, all the test results of the aided receiver will be buried in the thermal noise jitter; and (2) one job in the future is to experiment based on a RF (radio frequency) signal simulator and a RF signal collection device, where the 80 dB-Hz $C/N_0$ has already been achieved at RF level using a single-SV scenario [31].

Figures 11–13 show the test results of the traditional aiding method and the sample-wise method with user dynamics of 5 g, 50 g and 500 g, respectively. Figure 14 summarizes the standard deviations of carrier tracking errors and code tracking errors of the two aiding methods in the test results. For comparison, Figure 14 also shows the theoretical values of the thermal noise floors at 80 dB-Hz and 45 dB-Hz (in normal signal conditions) as well as the theoretical values of the total $1\sigma$ errors at 80 dB-Hz (including contributions from both the dynamic stress and the thermal noise) for the traditional aiding method.

![Figure 11](image1.png)  
(a)  
(b)  

Figure 11. Tracking errors with 5 g dynamics and 80 dB-Hz C/N0. (a) Carrier phase error; (b) Code phase error.

![Figure 12](image2.png)  
(a)  
(b)  

Figure 12. Tracking errors with 50 g dynamics and 80 dB-Hz C/N0. (a) Carrier phase error; (b) Code phase error.
5. Conclusions

The eliminability of the sample-wise Doppler-aided receiver using sample-wise aiding, which indicates that the dynamic stress errors have been almost completely significantly reduced by traditional Doppler aiding, they can still become dominant in carrier phase errors; when the dynamics are dominated by the thermal noise jitters; and (2) although the dynamic stress errors can be significantly reduced by traditional Doppler aiding, they can still become dominant in carrier phase errors when the dynamics are higher than 3400 m/s², and dominant in code phase errors when the dynamics are higher than 390 m/s² under a normal C/N₀ of 45 dB-Hz. As a contrast, the tracking errors remain almost the same values near the noise floor with the increase of user dynamics when using sample-wise aiding, which indicates that the dynamic stress errors have been almost completely eliminated by the sample-wise Doppler-aided receiver.

5. Conclusions

1. By examining the digital implementation of the Doppler-aided PLL in scalar tracking-based GPS/INS ultra-tight integration, a theoretical expression of the ratio of phase error amplitudes of an aided loop to an unaided loop is derived, which indicates how the aiding effects are impacted by the product of the frequency of signal dynamics and the INS aiding rate.

2. A new sample-wise aiding method in GPS/INS ultra-tight integration for high-dynamic, high-precision tracking has been presented. Its effectiveness and advantage over traditional aiding have not only been analytically expressed and numerically simulated, but also physically tested using a digital IF signal simulator and a software receiver.

![Figure 13. Tracking errors with 500 g dynamics and 80 dB-Hz C/N₀. (a) Carrier phase error; (b) Code phase error.](image)

![Figure 14. Comparison of tracking errors using traditional and sample-wise aiding.](image)
3. The simulation with respect to the effects of two interpolation techniques on the sample-wise aiding performance helps to find the boundary conditions of the aiding methods in terms of the INS aiding data update rate.

Although the research in this paper is based on the scalar tracking architecture in GPS/INS integration, the sample-wise aiding method can be introduced into the vector tracking-based deep coupling to satisfy high-dynamic, high-precision requirements. In the vector tracking case, the navigation filter output can be interpolated to the sampling rate before controlling the local signal generation of each satellite channel. Similarly, the method can also be extended to the fields of map aiding and integration of GNSS with other wireless positioning. Related future work also includes the analysis of the performance limitation due to the INS data error and the misalignment between GPS and INS.

Acknowledgments: This work was supported by the National Natural Science Foundation of China (Grant No. 61271197).

Author Contributions: Yanhong Kou conceived the sample-wise aiding idea. Yanhong Kou and Han Zhang elaborated the problem formulation, designed the simulations and the experiments. Han Zhang performed the tests. Yanhong Kou analyzed the data and wrote the paper.

Conflicts of Interest: The authors declare no conflict of interest.

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