INFLUENCE OF TCSC FACTS DEVICE ON STEADY STATE VOLTAGE STABILITY

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Abstract—The influence of series Flexible AC Transmission Systems (FACTS) device namely, Thyristor-Controlled Series Capacitors (TCSC) on the steady state voltage stability is the main objective of this paper. The Line stability Index LSI under excepted lines outage contingencies is used to identify the critical line which is considered as the best location for TCSC. A modal analysis is used to define the weakest bus of the studied system. The FACTS device is implemented and included into the Newton-Raphson power flow algorithm, and the control function is formulated to achieve the voltage stability enhancement goal. The analysis is preformed on standard IEEE 30 bus system. The proposed scheme are tested under different loading conditions and different nonlinear voltage dependent loads. The simulation results demonstrate the feasibility and effectiveness of the device and the proposed algorithm.

Keywords—voltage stability; TCSC; FACTS; voltage dependent load

1. INTRODUCTION

The utilities interest about the voltage instability and voltage collapse problems increase due to structural changes in the electrical sector, such as those caused by privatization and deregulation, modification of the network topology, as well as ever increasing in load demands brought by economic and environmental pressures that led the power systems to operate near its stability limits. Several blackouts are reported in many countries relate to voltage stability problem [1]. As an example, there are six blackouts during six weeks affecting millions of people in US, Sweden, UK, and Denmark [2].

Generally, voltage collapse is the process by which the sequence of events accompanying voltage instability leads to a low unacceptable voltage profile in a significant part of the power system. Voltage collapse may be a possible outcome of voltage instability, which is defined as the attempt of load dynamics to restore power consumption beyond the capability of the combined transmission and generation system [3].

A large number of researchers have been studied the voltage stability problem. Their attention has resulted with a numerous number of papers, books, and reports being published. Most of these are reported in the extensive bibliography [4].

The voltage instability may be classified into transient and steady state, the latest is the main concern in this paper. Steady state voltage stability or Small-disturbance voltage stability refers to the system’s ability to maintain steady voltages when subjected to small perturbations such as incremental changes in system load [5].

Many of measures used to prevent voltage instability [6] such as, (i) Placement of series and shunt capacitors, (ii) Generation rescheduling, (iii) Installation of synchronous condensers, (iv) Under-Voltage load shedding, (v) Blocking of Tap-Changer under reverse operation, (vi) Placement of FACTS controllers. The last method is considered in this study.

FACTS is a terminology that embrace a wide range of power electronics controllers. These devices use no delay and high current power electronic devices available today for safe and accurate responses. They are able to control the parameters such as voltage magnitudes and their angles, line impedances, active and reactive power flows [7].

There are many types of FACTS such as, Superconducting magnetic energy storage (SMES), Static Var Compensator (SVC), Static Synchronous Compensator (STATCOM), Static Synchronous Series Compensator (SSSC), Thyristor Controlled Series Capacitor (TCSC), Interline Power Flow Controller (IPFC), and Unified Power Flow controller (UPFC).

TCSC is considered in this paper to enhance steady state voltage stability by incorporate the device into the Newton-Raphson process under different types of voltage dependent loads.

The rest of this paper is structured as follows. In section II, the concept of the steady state voltage stability model is introduced. The structure and operation principles of TCSC is presented in section III. In section IV, the detailed static voltage stability model of TCSC is described. The mathematical model of the voltage dependent loads is explained in section V. In section VI the proposed methodology for the best placement of TCSC is considered. The results obtained for the test system is given and discussed in Section VII. Finally, Section VIII contains the conclusion.
II. STEADY STATE VOLTAGE STABILITY

The steady state (static) analysis methods mainly depend on the steady state model, such as power flow model or a linearized dynamic model described by the steady state operation. These methods [8-10] can be divided into:

1. Load flow feasibility methods, which depend on the existence of an acceptable voltage profile across the network. This approach is concerned with the maximum power transfer capability of the network or the existence of a solved load flow case. There are many criteria proposed under this approach. Some of these criteria are the following:
   - The reactive power capability (Q-V curve).
   - Maximum power transfer limit (P-V curve).
   - Voltage stability proximity index (VSI) or the load flow feasibility index (LFF index).

2. Steady state stability methods, which test the existence of a stable equilibrium operating point of the power system. Some of the criteria proposed under this approach are:
   - Eigenvalues of linearized dynamic equations.
   - Sensitivity matrices.
   - Singular value of Jacobian matrix (SVJ).

The maximum power transfer limit (P-V curve) method is used here as a measure for voltage stability. The procedures used to study the influence of TCSC on the static voltage stability begin with the power flow as the first step.

The power flow model is used to study steady state voltage stability since the power flow equation yield adequate results, as singularities in related power flow Jacobian can be associated with actual singular bifurcation of the corresponding dynamical system [11].

The Newton-Raphson power flow equation represented by:

\[
\begin{bmatrix}
\Delta P \\
\Delta Q
\end{bmatrix} = \begin{bmatrix} J_{11} & J_{12} \\ J_{21} & J_{22} \end{bmatrix} \begin{bmatrix} \Delta \theta \\ \Delta V \end{bmatrix} = J \begin{bmatrix} \Delta \theta \\ \Delta V \end{bmatrix}
\]

The power flow model for voltage stability analysis is represented by:

\[
F(x, \lambda) = \begin{bmatrix} \Delta P (x, \lambda) \\
\Delta Q (x, \lambda) \end{bmatrix} = 0
\]

where \( F(x, \lambda) \) is power flow equation and \( \lambda \) is Loading Factor (LF) or system load change that drives the system to collapse in the following way:

\[
P_{D,i} = \lambda_P \cdot P_{Di,i} \\
Q_{D,i} = \lambda_Q \cdot Q_{Di,i}
\]

where \( P_{Di} \) and \( Q_{Di} \) represent the initial active and reactive loads at bus \( i \) and constants \( \lambda_P \) and \( \lambda_Q \) respectively represent the active and reactive load increase direction of bus \( i \).

Wherever Times is specified, Times Roman or Times New Roman may be used. If neither is available on your word processor, please use the font closest in appearance to Times. Avoid using bit-mapped fonts if possible. True-Type 1 or Open Type fonts are preferred. Please embed symbol fonts, as well, for math, etc.

III. STRUCTURE AND OPERATION PRINCIPLES OF TCSC

Thyristor controlled series compensator (TCSC) is one of the most popular FACTS controllers, which allows rapid and continuous modulation of the transmission line impedance [12]. TCSC vary the electrical length of the compensated transmission line which enables it to be used to provide fast active power flow regulation [7]. It is also, provides powerful means of controlling and increasing power transfer level of a system by varying the apparent impedance of a specific transmission line [13].

The basic structure of TCSC is a thyristor controlled reactor (TCR) connected in parallel with a capacitor as shown in Fig. 1.

![Figure 1. Schematic diagram of TCSC between bus \( i \) and bus \( j \).](image)

The impedance characteristics curve of a TCSC device is shown in Fig. 2, that is drawn between effective reactance of TCSC and firing angle \( \alpha \) [14,15].

![Figure 2. Impedance characteristics curve of a TCSC.](image)

Impedance characteristics of TCSC shows, both capacitive and inductive region are possible through varying firing angle \( \alpha \) as follows:

\[90 < \alpha < \alpha_{lim}\] Inductive region
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αlim < α < αclim Capacitive region
αclim < α < 180° Resonance region

While the maximum and minimum value of firing angles should be selected in such a way as to avoid the TCSC operating in high impedance region (at resonance) which results in high voltage drop across the TCSC. This limitation can be used as a constraint during load flow analysis [7].

IV. MODELING OF TCSC CONTROLLER FOR STATIC VOLTAGE STABILITY

For static applications, FACTS devices can be modeled by power injection models (PIM) [16]. The injection model describes FACTS as devices that inject a certain amount of active and reactive power to a node, so that a FACTS device is represented as a current source Is in parallel with a current dependent voltage source, which is later directly used as the control variable in the power flow equations.

The current through the line after inserting TCSC is obtained by:

\[
I_{km} = (V_k - V_m) / (R_{km} + jX_{km})
\]

The series capacitor is initially represented as a current dependent voltage source, which is later transformed into a current source Is in parallel with the line [18] where,

\[
I_s = -jX_{km}I_{km} / (R_{km} + jX_{km})
\]

The corresponding power injection model of the TCSC incorporated within the transmission line is shown in Fig. 4. The injected powers \( S^f_k \) and \( S^f_m \) are defined by:

\[
S^f_k = P^f_k + jQ^f_k = V_k I_k
\]

\[
S^f_m = P^f_m + jQ^f_m = V_m I_m
\]

\[
S^f_k = V_k (-I_k^*) = V_k \left( -\frac{jX_{sc}}{R_{km} + jX_{km}} V_k - \frac{V_m}{R_{km} + jX_{km}} \right)
\]

\[
S^f_m = V_m (-I_m^*) = V_m \left( -\frac{jX_{sc}}{R_{km} + jX_{km}} V_m - \frac{V_k}{R_{km} + jX_{km}} \right)
\]

The real and reactive power injections due to the series capacitor of TCSC at buses \( k \) and \( m \) are given by (8) to (11) [19]:

\[
P^f_k = V_k^2G_{kk}^f - V_k V_m [G_{km}^f \cos \delta_{km} + B_{km}^f \sin \delta_{km}] (8)
\]

\[
Q^f_k = -V_k^2B_{kk}^f - V_k V_m G_{km}^f \sin \delta_{km} - B_{km}^f \cos \delta_{km} (9)
\]

\[
P^f_m = V_m^2G_{mm}^f - V_k V_m [G_{km}^f \cos \delta_{km} + B_{km}^f \sin \delta_{km}] (10)
\]

\[
Q^f_m = -V_m^2B_{mm}^f - V_k V_m G_{km}^f \sin \delta_{km} - B_{km}^f \cos \delta_{km} (11)
\]

Where,

\[
G_{kk}^f = \frac{X_{sc}R_{km}^2 (X_{sc} - 2X_{km})}{(R_{km}^2 + X_{km}^2)(R_{km}^2 + (X_{km} - X_{sc})^2)}
\]

\[
B_{kk}^f = \frac{-X_{sc}R_{km}^2 (X_{sc} - 2X_{km})}{(R_{km}^2 + X_{km}^2)(R_{km}^2 + (X_{km} - X_{sc})^2)}
\]

\[
G_{mm}^f = -G_{km}^f = G_{kk}^f
\]

\[
B_{mm}^f = -B_{km}^f = B_{kk}^f
\]

To implement voltage control function model of TCSC in Newton-Raphson algorithm, there are two model of TCSC. In the first one, \( X_{sc} \) is considered as the state variable. Where the series reactance is adjusted automatically, within limits, to satisfy a specified amount of active power flows through it. In the second model TCSC firing angle is chosen to be the state variable in the Newton–Raphson power flow solution. Where TCSC reactance–firing-angle characteristic, given in the form of a nonlinear relation. The first model is used in this study.

To improve the static voltage stability, The bus voltage control mode is used. So the bus voltage control constraint of bus \( k \) is given by

\[
V_C = V_k - V_k^{sp} = 0
\]
Where \( V_n \) is the bus voltage control reference.

After inserting TCSC between bus \( k \) and bus \( m \), the power flow relationship is changed to as:

\[
\begin{bmatrix}
\frac{\partial P_h}{\partial h} & \frac{\partial P_h}{\partial k} & \frac{\partial P_h}{\partial m} & \frac{\partial P_h}{\partial V_h} & \frac{\partial P_h}{\partial V_k} & \frac{\partial P_h}{\partial V_m} & \frac{\partial P_h}{\partial \delta_h} & \frac{\partial P_h}{\partial \theta_h} \\
\frac{\partial Q_h}{\partial h} & \frac{\partial Q_h}{\partial k} & \frac{\partial Q_h}{\partial m} & \frac{\partial Q_h}{\partial V_h} & \frac{\partial Q_h}{\partial V_k} & \frac{\partial Q_h}{\partial V_m} & \frac{\partial Q_h}{\partial \delta_h} & \frac{\partial Q_h}{\partial \theta_h} \\
\frac{\partial P_k}{\partial h} & \frac{\partial P_k}{\partial k} & \frac{\partial P_k}{\partial m} & \frac{\partial P_k}{\partial V_h} & \frac{\partial P_k}{\partial V_k} & \frac{\partial P_k}{\partial V_m} & \frac{\partial P_k}{\partial \delta_k} & \frac{\partial P_k}{\partial \theta_k} \\
\frac{\partial Q_k}{\partial h} & \frac{\partial Q_k}{\partial k} & \frac{\partial Q_k}{\partial m} & \frac{\partial Q_k}{\partial V_h} & \frac{\partial Q_k}{\partial V_k} & \frac{\partial Q_k}{\partial V_m} & \frac{\partial Q_k}{\partial \delta_k} & \frac{\partial Q_k}{\partial \theta_k} \\
\frac{\partial P_m}{\partial h} & \frac{\partial P_m}{\partial k} & \frac{\partial P_m}{\partial m} & \frac{\partial P_m}{\partial V_h} & \frac{\partial P_m}{\partial V_k} & \frac{\partial P_m}{\partial V_m} & \frac{\partial P_m}{\partial \delta_m} & \frac{\partial P_m}{\partial \theta_m} \\
\frac{\partial Q_m}{\partial h} & \frac{\partial Q_m}{\partial k} & \frac{\partial Q_m}{\partial m} & \frac{\partial Q_m}{\partial V_h} & \frac{\partial Q_m}{\partial V_k} & \frac{\partial Q_m}{\partial V_m} & \frac{\partial Q_m}{\partial \delta_m} & \frac{\partial Q_m}{\partial \theta_m} \\
\frac{\partial P_C}{\partial h} & \frac{\partial P_C}{\partial k} & \frac{\partial P_C}{\partial m} & \frac{\partial P_C}{\partial V_h} & \frac{\partial P_C}{\partial V_k} & \frac{\partial P_C}{\partial V_m} & \frac{\partial P_C}{\partial \delta_C} & \frac{\partial P_C}{\partial \theta_C} \\
\frac{\partial Q_C}{\partial h} & \frac{\partial Q_C}{\partial k} & \frac{\partial Q_C}{\partial m} & \frac{\partial Q_C}{\partial V_h} & \frac{\partial Q_C}{\partial V_k} & \frac{\partial Q_C}{\partial V_m} & \frac{\partial Q_C}{\partial \delta_C} & \frac{\partial Q_C}{\partial \theta_C} \\
\end{bmatrix}
\]

(13)

Where,

\( h = 2, 3, ..., n \)

Equation (13) is a modification of (1), which represent Newton-Raphson power flow equation with TCSC.

V. MATHEMATICAL MODELS OF VOLTAGE DEPENDENT LOADS

A static model expresses the active and reactive powers at any instant in time as functions of the bus voltage magnitude and frequency at the same instant.

Static load model is used both for essentially static load components (e.g., resistive and lighting loads), and as an approximation for dynamic load components [20].

The exponential function of voltage can be expressed in terms of nominal operating point designed by the subscripts "0".

\[
P_i = P_0 \left( \frac{V}{V_0} \right)^\alpha
\]

\[
Q_i = Q_0 \left( \frac{V}{V_0} \right)^\beta
\]

where, \( P_i, Q_i \) are load active and reactive power, \( P_0, Q_0 \) are active and reactive power consumption at rated voltage \( V_0 \), \( \alpha \) is the active power exponent, \( \beta \) is the reactive power exponent, \( V \) is the bus voltage, and \( V_0 \) is the rated voltage.

There are three types of static load modeling depending on the values of \( \alpha \) and \( \beta \) as follows:

- **Constant current model**: When \( \alpha \) and \( \beta \) equal 1, the static model power varies directly with voltage variation.

- **Constant impedance model**: When \( \alpha \) and \( \beta \) equal 2, the static load model power varies directly with the square of voltage magnitude.

- **Constant power model**: When \( \alpha \) and \( \beta \) equal zero the static model power is constant in spite of voltage magnitude variations. It’s also called a constant MVA model.

VI. BEST PLACEMENT METHODOLOGY FOR TCSC FACTS DEVICE

To determine the best location of TCSC device, the proposed methodology begins with identifying the critical line using LSI during line outage contingency. Then, modal analysis is utilized to define the weakest bus, to form the voltage control function. The procedures are done as follows:

A. Identifying Critical Line Using LSI and Line Outage Contingency Analysis

When a line outage occurs the Jacobian matrix needs to be modified to reflect the outage effect [21]. To make such modification a nominal circuit of an outage line \( i-j \) is presented in Fig. 5. The two power injections and which represent the effect of the outage [22].

The outage effect is simulated by making the two power injection and equal to the power flows on the outage line with opposite signs. Therefore,

\[
S_{pi} = P_{ci} + jQ_{ci} = jV_iV_j^* - V_i^2 V_j^* \left[ e^{j\delta_i} + jV_iV_j \right] e^{j(\delta_i + \theta_i - \theta_j)}
\]

(15)

\[
S_{pj} = P_{cj} + jQ_{cj} = jV_iV_j^* - V_i^2 V_j^* \left[ e^{j\delta_i} + jV_iV_j \right] e^{j(\delta_i + \theta_i - \theta_j)}
\]

(16)

\[
P_{ci} = V_iV_j \left| \frac{\sin(\delta_i + \theta_i - \theta_j)}{\sin(\delta_i + \theta_i - \theta_j)} \right| V_i^2 \left| \cos(\delta_i + \theta_j - \theta_j) \right|
\]

(17)

\[
Q_{ci} = V_iV_j \left| \frac{\sin(\delta_i + \theta_i - \theta_j)}{\sin(\delta_i + \theta_i - \theta_j)} \right| V_i^2 \left| \sin(\delta_i + \theta_i - \theta_j) \right|
\]

(18)

![Figure 5. Line outage power injection model for line i-j](image-url)
where \( Y_s = 1/Z_s = \mathbf{Y} \mathbf{V} \mathbf{e}^{-j\delta_s} \)

Using (17) to (20) the Jacobian matrix form in (1) is modified to reflect the effects of the active and reactive power injections at buses \( i \) and \( j \). Totally 16 elements need to be modified, and they are combined together to form the matrix \( \Delta J \)

\[
\Delta J = \begin{bmatrix}
\Delta \frac{\partial P_i}{\partial \theta_i} & \Delta \frac{\partial P_j}{\partial \theta_i} & \Delta \frac{\partial P_i}{\partial V_i} & \Delta \frac{\partial P_j}{\partial V_i} \\
\Delta \frac{\partial P_i}{\partial \theta_j} & \Delta \frac{\partial P_j}{\partial \theta_j} & \Delta \frac{\partial P_i}{\partial V_j} & \Delta \frac{\partial P_j}{\partial V_j} \\
\Delta \frac{\partial Q_i}{\partial \theta_i} & \Delta \frac{\partial Q_j}{\partial \theta_i} & \Delta \frac{\partial Q_i}{\partial V_i} & \Delta \frac{\partial Q_j}{\partial V_i} \\
\Delta \frac{\partial Q_i}{\partial \theta_j} & \Delta \frac{\partial Q_j}{\partial \theta_j} & \Delta \frac{\partial Q_i}{\partial V_j} & \Delta \frac{\partial Q_j}{\partial V_j}
\end{bmatrix}
\]

The elements of \( \Delta J \) (which are listed in [22]) should be added to their corresponding positions in the original \( J \). This process is represented in matrix form as follows:

\[ J' = J + M \Delta M \]

Where \( M \) has the following form:

\[
M = \begin{bmatrix}
N & | & 0 \\
--- & | & --- \\
0 & | & N
\end{bmatrix}
\]

Where \( 0 \) is \( nx2 \) zero matrix, \( N \) is a sparse matrix in the form \( N = [e_i, e_j] \), and \( e_i, e_j \) are spares column vectors with only one unity element at position \( i \) and \( j \) respectively.

The line stability index LSI is used in this paper in contingency ranking [23]. LSI can be defined by:

\[
LSI_{ij} = \frac{R_{ij}P_{ij} + X_{ij}Q_{ij}}{0.25V_i^2}
\]

Where \( R_{ij}, X_{ij} \) are the resistance and reactance between sending and receiving buses. \( P_{ij}, Q_{ij} \) are the reactive and active power at receiving bus. \( V_i \) is voltage at sending bus.

The computational procedures are as follows:

i. Base load flow computation is done, and LSI values are computed.

ii. The values of LSI are ranked and the highest values are recorded in list 1.

iii. All lines outages contingencies are simulated by removing each line at a time.

iv. Run load flow program under selected lines outages and reevaluate LSI values for all lines in each case.

v. The highest LSI value from every line outages are selected and registered in List 2.

vi. By comparing the two lists the common lines are extracted, and the line outage with highest rank is identified as the best TCSC location.

B. Defining the Weakest Bus of the Network

After defining the proper line to locate TCSC, modal analysis is used to select and assure the weakest bus required to form the voltage control function. Modal or eigenvalues analysis method can predict voltage collapse in complex power system networks. It involves mainly the computing of the smallest eigenvalues and associated eigenvectors of the reduced Jacobian matrix obtained from the load flow solution. The participation factor can be used effectively to find out the weakest nodes or buses in the system [24].

In order to concentrate on the reactive demand and to minimize computational effort by reducing the Jacobian matrix in Newton-Raphson power flow equation represented by (1), \( \Delta P \) is putted to be zero so that

\[
\Delta \theta = -J_{11}^{-1}J_{12} \Delta V \tag{25}
\]

And

\[
\Delta Q = J_{21} \Delta \theta + J_{22} \Delta V \tag{26}
\]

From (25) and (26)

\[
\Delta Q = J_R \Delta V = [J_{22} - J_{21}J_{11}^{-1}J_{12}] \Delta V \tag{27}
\]

Where \( J_R \) is the reduced Jacobian matrix of the system.

The eigenvalues and eigenvectors of the reduced order Jacobian matrix \( J_R \) are used for the voltage stability characteristics analysis. To detect voltage instability, modes of the eigenvalues matrix \( J_R \) is identified. The magnitude of the eigenvalues provides a relative measure of proximity to instability.

Eigenvalue analysis of \( J_R \) will be as follows:

\[
J_R = \Phi \Lambda \Gamma
\]

Where

\[
\Phi = \text{right eigenvector matrix of } J_R \\
\Gamma = \text{left eigenvector matrix of } J_R \\
\Lambda = \text{diagonal eigenvalue matrix of } J_R
\]

And \( \Phi \Gamma = 1 \)

Equation (28) may be written as:

\[
J_R^{-1} = \Phi \Lambda^{-1} \Gamma
\]

From (29) and (27) \( \Delta V = \Phi \Lambda^{-1} \Gamma \Delta Q \) or

\[
\Delta V = \sum \Phi \frac{\lambda_i}{\lambda_i} \Delta Q \tag{30}
\]
Where is the eigenvalue, is the column right
eigenvector and is the row left eigenvector of matrix

Each \( l^\text{th} \) eigenvalue \( \lambda_i \) and corresponding right and
left eigenvectors define the \( l^\text{th} \) mode of the system.
The \( l^\text{th} \) modal reactive power variation is defined as:

\[
\Delta Q_m = K_i \Phi_l
\]

Where \( K_i \) is a scale factor to normalize vector \( \Delta Q_l \)
so that

\[
K_i^2 \sum_l \Phi_l = 1
\]

With \( \Phi_{ij} \) the \( j^\text{th} \) element of \( \Phi_l \)
The corresponding \( l^\text{th} \) modal voltage variation is:

\[
\Delta V_{mi} = \frac{1}{\lambda_i} \Delta Q_{mi}
\]

Equation (33) indicate that if all the eigenvalues
are positive, \( J_R \) is positive definite and the V-Q
sensitivities are also positive, and the system is
voltage stable [25]. The system is considered voltage
unstable if at least one of the eigenvalues is negative.
A zero eigenvalue of \( J_R \) means that the system is close
to voltage instability. Furthermore, small eigenvalues
of \( J_R \) determine the proximity of the system to being
voltage unstable. So, Once the minimum eigenvalues
and the corresponding left and right eigenvectors have
been calculated, the participation factor can be used to
identify the weakest node or bus in the system.

The procedure may be summarized as follows:

- Obtain the load flow for the base case of the
  system and get the Jacobian matrix \( J \) and the
  reduced Jacobian \( J_R \).
- Compute the eigenvalues to identify how the
  system close to instability and find the
  minimum eigenvalue \( \lambda_{\text{min}} \) of \( J_R \).
- Calculate the right and left eigenvectors of \( J_R \)
  and compute the participation factors \( P_l \) for
  \( \lambda_{\text{min}} \). The highest \( P_l \) indicate the most
  participated \( k^\text{th} \) bus to \( l^\text{th} \) mode (which is the
  closest mode to instability) in the system.
- Generate the Q-V curve to the \( k^\text{th} \) bus. By
  using Q-V curves, it is possible to know what
  is the maximum reactive power that can be
  achieved or added to the weakest bus before
  reaching minimum voltage limit or voltage
  instability.

VII. SIMULATION RESULTS

Voltage stability enhancement using the proposed
TCSC FACTS device is done through the simulation
of IEEE 30- bus test system (shown in Fig. 6). Studied
system data is obtained from [26]. All the results are
produced by programs developed in MATLAB software package.

The system consists of 6 machine, 30 bus, and 41
lines. Bus 1 is considered as slack bus, while 5 nodes
as PV buses and other buses as PQ buses. For all
cases, the convergence tolerance is \( 1e^{-12} \) p.u. and
system base is 100 MVA.

As explained in the previous sections LSI under
line outage analysis, and modal analysis are used to
identify the best location of the TCSC, and the
weakest bus required to form the voltage control
function, then the TCSC device is incorporated to
the system. The effect of the system without and with
TCSC is studied under different loading conditions
and different load types to investigate the ability of the
FACTS device to enhance steady voltage stability of
the studied system.

\[ (32) \]

![Figure 6. The IEEE 30-bus power system.](image)

Figure 6. The IEEE 30-bus power system.

A. Best Location for TCSC Placement

To define the appropriate placement of TCSC,
firstly the base load flow study is carried out, the LSI
is computed and ranked, and the most ten severe lines
according to LSI values are recorded in Table I. Then
the lines outages are simulated and the LSI are
computed for each line outage case and the highest
LSI value for each case is extracted, and the most
serious outage contingency are identified and listed in
Table II. The outage of L38 and L39 give non
convergence results "NC" and LSI greater than one
which mean that these lines cases the system unstable.
From the two tables, it is appeared that L38, L39, and
L20 are the common lines between the critical lines
lists in the base case and in the line outages
contingency cases. And the line L38 (the line
connecting buses 27-30) is the most critical line which
have the highest LSI value. Furthermore investigating
the LSI values of Table II indicate that the line L38
itself has the highest LSI value under most of the lines
contingencies. So, the line L38 is chosen to place
TCSC device.

| Line No | From - To | LSI | Rank |
|---------|-----------|-----|------|
| 38      | L 27-30   | 0.1765 | 1    |
| 13      | L 9-11    | 0.1722 | 2    |
| 39      | L 29-30   | 0.1378 | 3    |
| 32      | L 23-24   | 0.1159 | 4    |
| 8       | L 5-7     | 0.0907 | 5    |
| 1       | L 1-2     | 0.0904 | 6    |
| 20      | L 14-15   | 0.0867 | 7    |
| 31      | L 22-24   | 0.0864 | 8    |
| 16      | L 12-13   | 0.0822 | 9    |
| 27      | L 10-21   | 0.0752 | 10   |

Table I. The Highest Ranked Lines According to LSI
B. Identification of The Weakest Bus

To define the weakest bus, the modal analysis method is applied to the suggested test systems (as in section VI.B). The voltage profile of the buses is presented in Fig. 7.

Then, the minimum eigenvalues of the reduced Jacobian matrix are calculated and registered in Table III.

| Line Outage | L1 | L2 | L3 | L4 | L5 | L6 | L7 | L8 | L9 | L10 | L11 | L12 | L13 | L14 | L15 | L16 | L17 | L18 | L19 | L20 | L21 | L22 | L23 | L24 | L25 | L26 | L27 | L28 | L29 | L30 | L31 | L32 | L33 | L34 | L35 | L36 | L37 | L38 | L39 |
|------------|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|----|
| Bus No     | 1  | 2  | 3  | 4  | 5  | 6  | 7  | 8  | 9  | 10 | 11 | 12 | 13 | 14 | 15 | 16 | 17 | 18 | 19 | 20 | 21 | 22 | 23 | 24 | 25 | 26 | 27 | 28 | 29 | 30 | 31 | 32 | 33 | 34 | 35 | 36 | 37 | 38 | 39 |
| Eigenvalue | 107.48766 | 100.92729 | 59.71765 | 47.33923 | 37.90579 | 34.85240 | 33.50484 | 22.81071 | 21.51305 | 13.69709 | 19.83127 | 4.06940 | 3.58637 | 5.48372 | 6.04582 | 16.44144 | 15.58759 | 12.90193 | 13.69709 | 8.82146 | 7.48722 | 5.69517 |

After that, the weakest load buses, which are subjected to voltage collapse, are identified by computing the participating factors. The results are shown in Fig. 8.
The total number of eigenvalues of the reduced Jacobian matrix $J_R$ is 24, as there are 24 PQ buses. These eigenvalues are shown in Table III. All the eigenvalues are positive which means that the system voltage is stable. It can be noticed that the minimum eigenvalue that equal to 0.513 is the most critical mode. The participating factor for this mode has been calculated and the results are shown in Fig. 8. The results show that, the buses 30, 29 and 26 have the largest participation factors. The highest participation factor value at bus 30 illustrates the remarkable role of this bus in the voltage collapse.

The Q-V curves are depicted in Fig. 9 for the weakest buses of the critical mode as expected by the modal analysis method. The curve verifies the results obtained previously by modal analysis method. It can be seen that buses 30, and 26 are the critical buses compared the bus 29 but with keeping in mind the participation factors bus 30 will be the most critical one, where any more increase in the reactive power demand in that bus will cause a voltage collapse. Therefore Bus 30 is selected to form the voltage control function.

C. Simulation Results With Effect of TCSC Using Linear Loads

To investigate the effect of the TCSC device using linear loads (P-constant load), PV curves of the critical buses 30, 29, and 26 without and with TCSC (TCSC at line 27-30) are shown in Fig. 10 to Fig. 12. Fig. 10 indicates that the device succeed to fix the voltage of the most critical bus 30 to the objective value (1 p.u.), despite the increasing of the loading factor to 1.4. Also, Fig.11 and Fig. 12 show an improvement in the voltage profiles in buses 29 and 26. So, all the results are shown that the voltage profiles are enhanced and consequently the voltage stability margin of the studied system are improved due to using TCSC.
D. Simulation Results with Effect of TCSC using Voltage Dependent Loads

To explore the effect of the TCSC device on the proposed system under different nonlinear voltage dependent loads, PV curves of the buses 26, 29, and 30 without and with TCSC are plotted in Fig. 13 to Fig. 21. Figures are zoomed when required to explain the case. Also, the loading factor are changed according to case stability.

Figures 13 to 15 simulate the change in voltage magnitude of the three buses in the studied system without TCSC under constant current (CI), constant impedance (CZ), and constant power (CP) loads. These figures indicate that the voltage magnitude are decreased to undesirable levels that lead to voltage collapse.

In figures 16 to 18 a comparison between the system with TCSC and without TCSC using constant current loads are depicted. Also, In figures 19 to 21 the same process is done, using constant impedance load types (constant power case are studied as linear load). In figures 12, 18 and 21 the TCSC has a small effect on bus 26 this is because of bus 26 is not connected directly to bus 30 that is connected to TCSC this means that the redistribution of reactive powers by the device has not a large effect of this bus. In general TCSC shows a good performance and enhance the voltage stability margin of the system.
VIII. CONCLUSION

In this paper the influence of TCSC on steady state voltage stability was investigated. Detailed steady state model of FACTS device was presented focusing on the inclusion of the devices into the power flow analysis process. A novel technique for selecting best placement of the device and to form the voltage control function were proposed. The studied system was tested under different loading conditions and different linear and nonlinear load types. The device proved their ability to enhance voltage stability margin.

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