Quantum dynamics simulations beyond the coherence time on NISQ hardware by variational Trotter compression

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We demonstrate a post-quench dynamics simulation of a Heisenberg model on present-day IBM quantum hardware that extends beyond the coherence time of the device. This is achieved using a hybrid quantum-classical algorithm that propagates a state using Trotter evolution and then performs a classical optimization that effectively compresses the time-evolved state into a variational form. When iterated, this procedure enables simulations to arbitrary times with an error controlled by the compression fidelity and a fixed Trotter step size. We show how to measure the required cost function, the overlap between the time-evolved and variational states, on present-day hardware, making use of several error mitigation methods. In addition to carrying out simulations on real hardware, we investigate the performance and scaling behavior of the algorithm with noiseless and noisy classical simulations. We find the main bottleneck in going to larger system sizes to be the difficulty of carrying out the optimization of the noisy cost function.

I. INTRODUCTION

Simulating quantum dynamics of interacting many-body systems is one of the main potential applications of quantum computing, going back to Feynman’s visionary work on simulating physics with quantum computers [1]. For quantum many-body systems evolving under a generic nonintegrable Hamiltonian $H$, such simulations are exponentially hard on classical devices due to the growth of entanglement during the simulation time [2, 3]. Quantum processing units (QPUs), on the other hand, can efficiently simulate quantum dynamics with resources scaling polynomially in the number of particles $N$ and in the simulation time $t$. Since Lloyd’s original proposal [4] to use a first-order Lie-Trotter product formula to decompose the unitary time-evolution operator into elementary gates, several other methods have been discovered that exhibit a more favorable asymptotic scaling of the required number of gates at or near the optimal scaling of $O(N^2 t)$ [5–12]. Concrete resource estimates, however, show that this often comes at the cost of requiring a large number of ancilla qubits or having a large constant overhead of gates [9]. Therefore, product formulas still remain a preferred choice on noisy intermediate-scale quantum (NISQ) hardware [13] due to their simplicity and competitive performance for physically relevant (e.g., local) Hamiltonians that fulfill additional properties [14].

Despite this progress, direct quantum simulation algorithms face a critical drawback on NISQ QPUs: the limited coherence time of the device imposes an upper bound on the depth of the quantum circuits that can be implemented with high fidelity. This in turn upper-bounds the simulation time $t$ that can be reached before the output is overwhelmed by errors—on current hardware, this time scale is roughly of order one in the natural units imposed by the Hamiltonian being simulated [15]. Quantum simulation beyond the coherence time using “fast-forwarding” algorithms [16–19] is possible, but only for nongeneric Hamiltonians, including those that can be mapped to free fermions [20].

Variational quantum algorithms (VQAs) provide a promising route to overcome the coherence-time obstacle for generic Hamiltonians [21, 22]. Their starting point is to represent the wavefunction by a variational ansatz $|\psi(\vartheta)\rangle = \prod_{j=1}^{N} e^{-i\vartheta_j A_j} |\psi_0\rangle$ with $N$ real parameters $\vartheta = (\vartheta_1, \ldots, \vartheta_N)$, initial state $|\psi_0\rangle$, and Hermitian generators $A_j$, which are often chosen to be single Pauli strings or sums of commuting Pauli strings. To simulate quantum dynamics, the parameters $\vartheta_j$ are updated in a way that allows the variational state to follow the exact dynamics; different algorithms to perform the update have been proposed in the literature.

In one class of VQAs, one derives an equation of motion for the variational parameters, $\sum_j M_{ij} \dot{\vartheta}_j(t) = V_i$ [23–25], by extremizing the distance between the variational state and the exact time-evolved state at every (infinitesimally small) time step in the evolution. Here, the matrix $M_{ij}$ and vector $V_j$ must be obtained at every time step by performing measurements on the QPU, and the main bottleneck of the algorithm is the large number of measurements. Since the number of components $M_{ij}$ scales quadratically with the number of variational parameters $N$, the number of measurements $M$ scales as $M \propto N^2$.

An alternative approach that reduces the number of measurements is to determine the time-dependence of the variational parameters $\vartheta(t)$ by optimizing the state overlap fidelity $F = | \langle \psi(\vartheta) | \psi(t) \rangle |^2$ between the variational state and the exact time-evolved state $|\psi(t)\rangle$ [26, 27].

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practice, the exact state can be approximated with high fidelity, for example, using Trotter evolution over a time interval $\tau$ sufficiently short to guarantee a desired high accuracy at finite gate depth. Such a “variational Trotter compression” (VTC) approach combines the desirable aspects of Trotter evolution with the shallow gate depth requirements of variational methods.

Here, we expand on previous works that explored VTC algorithms on classical computers [26, 27] by performing the first implementation and benchmarking of a VTC algorithm on real quantum hardware. We explicitly demonstrate a simulation of quantum quench dynamics in a Heisenberg spin chain beyond the coherence time on the IBM Santiago QPU. To achieve this goal, we employ several error mitigation strategies: we combine zero-noise extrapolation (ZNE) with Pauli twirling [23, 28] and symmetry-based post-selection. At the current levels of noise on the QPU, we find it advantageous to avoid the gradient based optimization used in the variant of the VTC algorithm described in Ref. [27], and instead choose the non-gradient based genetic optimization algorithm CMA-ES [29]. We compare different ways to choose the non-gradient based genetic optimization algorithm HVA [34, 35] and that takes a layered form, as well as different methods to determine the overlap cost function.

The remainder of the paper is organized as follows: in Sec. II we present the general VTC algorithm and point out differences with previous works [26, 27]. We also discuss different quantum circuit implementations of the optimization cost function. Then, in Sec. III, we apply VTC to simulate post quench dynamics in antiferromagnetic Heisenberg chains. In Sec. III A, we introduce the model and describe the variational ansatz we use, and in Sec. III B we benchmark the capability of the ansatz to capture the exact time-evolved state. We then execute VTC on different classical simulators and on real quantum hardware. In Sec. III C, we present results obtained on a statevector simulator. In Sec. III D, we use an ideal circuit simulator to consider sample noise due to a finite number of quantum measurements, and in Sec. III E, we show results from classical noisy circuit simulations, where the noise model parameters correspond to the IBM Santiago backend. Finally, in Sec. III F, we demonstrate our key result: a quantum dynamics simulation beyond the qubit coherence time on the real IBM Santiago device. We conclude and discuss future research directions in Sec. IV.

II. ALGORITHM

In this section we describe the different parts of the variational Trotter compression (VTC) algorithm.

A. Choice of ansatz

First, one needs to choose a particular way to build the variational ansatz $|\psi(\theta)\rangle$. The main part of the algorithm is independent of the form of the ansatz, but requires that it is able to faithfully represent the time-evolved state up to a desired fidelity. Since the entanglement content of the state increases during time evolution, the complexity of the ansatz and the number of required parameters grows with time $t$ (and also with system size $M$). We characterize this growth in detail for a specific model in Sec. III. A key insight from previous works is that the growth with $t$ of the number of variational parameters can be much slower (linear versus exponential) than in purely classical algorithms based on matrix product states (MPSs) [26], giving the VTC algorithm a potential quantum advantage.

The variational ansatz can either be chosen to have a fixed form or to be adaptively modified during the computation [25, 32, 33]. Here, we choose a fixed ansatz that is inspired by the Hamiltonian variational ansatz (HVA) [34, 35] and that takes a layered form,

$$|\psi(\theta)\rangle = U(\theta)|\psi_0\rangle = \prod_{l=1}^{\ell} \prod_{i=1}^{N} e^{-i\theta_l A_i} |\psi_0\rangle .$$  

Here, $\ell$ denotes the number of layers and $N$ is the number of variational parameters per layer. We choose the Hermitian operators $A_i$ to be sums of commuting Pauli strings that correspond to the terms in the Hamiltonian being simulated. Finally, $|\psi_0\rangle$ is an initial state that can be chosen arbitrarily; in particular, it does not need to correspond to the state of the system at the initial time $t_i$, which we denote by $|\psi_i\rangle$.

B. Variational Trotter compression

The central part of the algorithm is the variational compression step. The key idea is to accurately propagate the variational state over a short, finite time interval $\tau$ using Trotter evolution, and then to re-express the resulting state, $U_{\text{trot}}(\tau)|\psi(\theta_i)\rangle$, in variational form via optimization of the overlap cost function

$$C = |\langle \psi(\theta_{i+\tau})|U_{\text{trot}}(\tau)|\psi(\theta_i)\rangle|^2. \hspace{1cm} (2.2)$$

The cost function is measured on the QPU and feeds into a classical optimization routine that determines the updated variational parameters as $\theta_{i+\tau} = \arg\min_{\theta_{i+\tau}} C$. The complete compression step is then iterated until the final time $t_f$.

The maximal value of $\tau$ in a single compression step is determined by the number of Trotter steps $n_{\text{max}}$ that can be executed on the noisy QPU, and by the desired Trotter error threshold $\varepsilon$, which is a function of $\tau/n_{\text{max}}$ [4, 36]. There exist different methods to determine the overlap cost function on a QPU, which will be discussed in more
detail below in Sec. II.C. The simplest method concatenates the Trotter and the variational ansatz circuits and calculates the probability that the system returns to the initial state, i.e.,

\[ C = |\langle \psi_0 | U^\dagger (\vartheta_{t+\tau}) U_{\text{trot}}(\tau) U(\vartheta_t) | \psi_0 \rangle|^2 . \]  

(2.3)

Here, \( U_{\text{trot}}(\tau) \) is a (first-order) Trotter circuit using \( n \) steps. We find this straightforward method to be robust under noise and preferable on NISQ hardware with nearest-neighbor connectivity compared to alternative methods that use ancilla qubits or multiple SWAP operations [30, 31]. Since the variational and Trotter circuits are applied consecutively in Eq. (2.3), the depth of the ansatz, which can be expressed as the number of layers \( \ell \), must be taken into account when choosing the number of Trotter steps \( n \). Loosely speaking, one must have that \( 2\ell + n < n_{\text{max}} \), where \( n_{\text{max}} \) is the maximal number of Trotter steps that can be executed within the finite coherence time of the device.

Different methods can be used for the classical optimization of the overlap cost function \( C \). Ref. [27] proposes a gradient-based optimizer, where the gradient is measured directly using a quantum circuit, while Ref. [26] performs the optimization purely classically using tensor network based methods. We have compared the performance of gradient- and non-gradient-based methods and find that non-gradient-based methods such as CMA-ES [29] are preferable for noisy cost functions when considering realistic noise levels present on current hardware.

C. Quantum circuit implementation of cost function measurement

As mentioned previously, when implementing the VTC algorithm on a real quantum device one needs to decide how to evaluate the overlap cost function \( C \) in Eq. (2.2), since the full wavefunction is not accessible. We consider two methods to evaluate \( C \) that are physically realizable on current NISQ devices, and we compare their resource scaling. Since the fidelities of two-qubit entangling gates are much lower than those of single-qubit gates on current hardware, we focus on the scaling of the number of two-qubit gates.

1. SWAP test circuit

One common method for computing the overlap of two quantum states is the SWAP test [30, 37], whose circuit implementation is shown in Fig. 1. Here, the two crosses denote swap operations between pairs of individual qubits in the two quantum registers containing the states \( |\Phi\rangle \) and \( |\Psi\rangle \). The ancilla expectation value \( \langle Z \rangle = |\langle \Psi | \Phi \rangle|^2 \) is equal to the overlap of the states and thus serves as a cost function for VTC. Specifically, we initialize the states \( |\Phi\rangle = U_{\text{trot}}(\tau) U(\vartheta_t) | \psi_0 \rangle \)

and \( |\Psi\rangle = U(\vartheta_{t+\tau}) | \psi_0 \rangle \) and find the optimal parameters \( \vartheta_{t+\tau} \) by maximizing \( \langle Z \rangle \) for the ancilla.

Let us now estimate the number of two-qubit gates required to implement this circuit on two quantum registers of length \( M \), each corresponding to a system of \( M \) qubits. Swapping arbitrary \( M \)-qubit states \( |\Phi\rangle \) and \( |\Psi\rangle \) requires \( M \) controlled-SWAP gate operations, each of which can be broken down into two controlled-NOT (CNOT) gates and one Toffoli gate. The Toffoli gate can be further decomposed into a circuit containing 6 CNOTs. Therefore, the swap test incurs a cost of \( 8M \) CNOTs in total. One major drawback of using the SWAP test on QPUs with local qubit connectivity, however, is that the ancilla must be coupled to every other qubit during the cascading controlled-SWAPs. As the current generation of superconducting QPUs lacks such nonlocal connectivities, one would need to compile a nonlocal SWAP into many local CNOTs, which significantly increases the resource cost of the SWAP test.

2. Double time contour circuit

The simplest way to evaluate the overlap cost function, which does not require any qubit overhead or ancillas, is to implement a “double time contour” circuit, corresponding to the direct implementation of the overlap matrix element in Fig. 2. The key idea is that the inverse of the updated variational circuit \( U^\dagger (\vartheta_{t+\tau}) \) effectively undists the evolution induced by \( U_{\text{trot}}(\tau) U(\vartheta_t) \). The probability \( p_{|\psi_0\rangle} \) for the system to end up in its initial state \( |\psi_0\rangle \) is maximal for the optimal parameters \( \vartheta_{t+\tau} \).

We find this cost function to be robust to noise under realistic conditions. The circuit is shown in Fig. 2 and consists of a consecutive application of three unitary circuits onto a fixed initial state \( |\psi_0\rangle \). For convenience, we choose the initial state \( |\psi_0\rangle \) to be a \( Z \)-basis state. This method to evaluate \( C \) does not require any additional overhead and the number of required qubits is equal to \( M \), the number of qubits in the system. Note, however, that the depth of the circuit \( U^\dagger (\vartheta_{t+\tau}) U_{\text{trot}}(\tau) U(\vartheta_t) \) is about 1.5 times larger than that required for the SWAP test.
an initial product state $|\psi_0\rangle = |0101\cdots\rangle$:

$$|\psi(\vartheta^{(\ell)})\rangle = \prod_{l=1}^{\ell} U_{\text{even}}(\theta_l) U_{\text{odd}}(\phi_l) |\psi_0\rangle .$$  \hspace{1cm} (3.3)

Here, $\vartheta^{(\ell)} \equiv (\theta_1, \phi_1, \ldots, \theta_{\ell}, \phi_{\ell})$ are $M\ell$ variational parameters and

$$U_{\text{odd}}(\theta_l) = \prod_{j \text{ odd}} e^{-i\theta_{l,j}(X_jX_{j+1}+Y_jY_{j+1}+Z_jZ_{j+1})} \hspace{1cm} (3.4a)$$

$$U_{\text{even}}(\phi_l) = \prod_{j \text{ even}} e^{-i\phi_{l,j}(X_jX_{j+1}+Y_jY_{j+1}+Z_jZ_{j+1})} \hspace{1cm} (3.4b)$$

are the unitary operators acting on the odd and the even bonds of the chain, respectively. Here, $\theta_{l,j}$ is the $j$th entry of the parameter vector $\theta_l$, and $\phi_{l,j}$ is the $j$th entry of $\phi_l$. The quantum circuit has depth $2\ell$, corresponding to the number of unitaries applied to every qubit. Note that the length of the vector $(\theta_l, \phi_l)$ is equal to the number of sites $M$ and $\vartheta^{(\ell)}$ has $M\ell$ components. Additionally, for odd $M$, the ansatz contains a boundary term $U_{\text{boundary}}$ that does not commute with either $U_{\text{odd}}$ or $U_{\text{even}}$. A compact quantum circuit representation of the unitary operators in Eqs. (3.4) has been given in Ref. 38 and is shown in Fig. 4.

For the nonintegrable model $H_1$, we add an additional unitary to each brickwall layer $l$:

$$U_Z(\gamma_l) = \prod_{j=1}^{M} e^{-i\gamma_{l,j}Z_jZ_{j+2}} .$$  \hspace{1cm} (3.5)

This doubles the number of parameters in the ansatz, which is given by $2M\ell$ in the nonintegrable case.

### B. Required number of layers $\ell$

To benchmark the ability of the variational ansatz in Eq. (3.3) to represent the exact wavefunction within a desired accuracy $\epsilon$, we determine the minimal number

\begin{figure}[h]
\centering
\includegraphics[width=\textwidth]{fig2}
\caption{Double time contour quantum circuit for ancilla-free evaluation of the overlap cost function $C$ in Eq. (2.3). Circuit is shown for $M = 5$.}
\end{figure}

\begin{figure}[h]
\centering
\includegraphics[width=\textwidth]{fig3}
\caption{Quantum circuit implementing the brickwall ansatz $|\psi(\vartheta^{(\ell)})\rangle$ in Eq. (3.3) for $\ell = 2$ and $M = 6$. For simplicity, we show the circuit for OBC. For PBC, there exists an additional even layer gate $U_{\text{even}}(\phi_{l,M})$ between the first and the last qubit.}
\end{figure}
of layers $\ell_{\text{min}}(t, \epsilon, M)$ needed such that the variational ansatz can represent the exact time-evolved state $|\psi(t)\rangle$ at time $t$ up to an infidelity $1 - F < \epsilon$. To do this, for each $t$ we numerically minimize the infidelity

$$1 - F(t, \hat{\theta}(\ell)) = 1 - |\langle \psi(\hat{\theta}(\ell)) | \psi(t) \rangle|^2$$

(3.6)

over the parameters $\hat{\theta}(\ell)$ to obtain the optimal parameters $\hat{\theta}(\ell)$. Repeating this for different values of $\ell$, we find $\ell_{\text{min}}(t, \epsilon, M)$ as the smallest $\ell$ for which the minimal infidelity $1 - F(t, \hat{\theta}(\ell))$ falls below the desired error threshold $\epsilon$. Here, we obtain the exact state $|\psi(t)\rangle = e^{-iHt} |\psi_i\rangle$ via exact diagonalization with $H = H_0$ (or $H = H_1$) and initial state $|\psi_i\rangle = |0101\cdots\rangle$. The optimization is performed using the gradient-based optimizer L-BFGS-B \[39\].

We first study the time-dependence of the minimal infidelity for a fixed number of layers $\ell$. As shown in Fig. 5, $1 - F(t, \hat{\theta}(\ell))$ first increases rapidly after the quench (unless $\ell$ is sufficiently large) and then saturates at a value that decreases with increasing layer number. This can be understood by comparison to the behavior of the entanglement entropy when tracing out half the system’s degrees of freedom [see inset of Fig. 5(b)]; it grows linearly over time until it reaches saturation due to the finite system size. Since each layer of the brickwall quantum circuit couples only nearest-neighbor qubits, the spread of entanglement across the system in the variational state is limited by the total number of layers $\ell$. As a result, for a fixed layer number $\ell$, the infidelity $1 - F$ grows as a function of time as the variational ansatz is unable to capture the entanglement that builds up in the system during time evolution.

In the bottom panel of Fig. 5, we show the growth with time of the layer number $\ell^*(t)$ required to keep the infidelity at time $t$ below a fixed threshold that we set to $\epsilon = 1 \times 10^{-4}$ (see also dashed line in the top panel). Like the entanglement entropy, the required layer number grows linearly in time before it reaches saturation. This linear growth in the number of layers and variational parameters provides an opportunity for quantum advantage, since one generically expects state-of-the-art classical techniques based on matrix product techniques to exhibit an exponential scaling with time of the number of parameters \[2, 26\]. Finally, due to finite system size, the required layer number saturates at long times. We denote the saturation value of the layer number by $\ell^* \equiv \lim_{t \to \infty} \ell^*(t, \epsilon, M)$. In practice, we choose a time $t_f$ much larger than the saturation timescale and define $\ell^* \equiv \ell^*(t_f)$. Note that an ansatz with $\ell^*$ layers is able to represent the time-evolved state out to arbitrary times (see also top panel of Fig. 5).

We now systematically study the dependence of the saturation layer number $\ell^*$ on system size $M$ and desired error threshold $\epsilon$. We therefore fix a final time $t_f = 50J^{-1}$, which is larger than the saturation time scale of the entanglement entropy for all system sizes we consider. In Fig. 6 we show the minimal infidelity, $1 - F(t_f, \hat{\theta}(\ell))$, as a function of layer number $\ell$. We present results for the integrable Heisenberg model $H_0$ in panel (a) and for the nonintegrable model $H_1$ in panel (b). As a function of $\ell$, the infidelity curves are first relatively flat until a characteristic $M$-dependent value, where they start to plunge to much smaller values. For definiteness, we set the desired error threshold to $\epsilon = 5 \times 10^{-3}$, which is indicated by the black dashed line in Fig. 6. The insets show the resulting $\ell^*(\epsilon = 5 \times 10^{-3}, M)$ as a function of system size $M$. We find that $\ell^*$ grows exponentially with $M$, which implies an exponential growth of the number of variational parameters $N \propto M \ell$ that are needed to describe the long time dynamics. For $M = 10$ we find $\ell^*(M = 10) \approx 40$ in the integrable model and $\ell^*(M = 10) \approx 20$ in the non-integrable model.
defines \( \epsilon \), dashed line shows a fixed error threshold \( H \). Modeling with system size \( M \) differs from the integrable one. In both cases, the number of variational parameters \( N \approx 350 \), since the nonintegrable ansatz has twice as many parameters per layer as the integrable one. This exponential scaling behavior at large times constitutes a bottleneck of the approach when considering larger system sizes. Note, however, that at short times we find a favorable linear scaling of \( \ell \) and \( N \) with time \( t \), which provides an opportunity for quantum advantage. Such behavior was also reported previously using a sequential quantum circuit ansatz [26].

**C. Statevector simulator results**

We now discuss the performance of the VTC algorithm in simulating post-quench dynamics of the antiferromagnetic Heisenberg chain. In the following we focus on the integrable model \( H_0 \), since the results of Fig. 6 indicate that the algorithm’s performance will not differ substantially between the two cases. In this section, we discuss results obtained using an exact statevector simulator, where we have direct access to the overlap cost function \( C \) in Eq. (2.2). Recall that the VTC algorithm consists of two steps: a propagation step, where we use a first-order Trotter product formula to evolve the state from time \( t \) to \( t + \tau \), and a compression step, where the time-evolved state is compressed into variational form by numerical optimization of the overlap cost function \( C \).

In Fig. 7, we present results for the state overlap fidelity of the VTC-evolved state with the exact state: \( F(t) = |\langle \psi(t) | \psi(0) \rangle|^2 \), where \( |\psi(t)\rangle = e^{-iHt}|\psi(0)\rangle \) and \( \hat{\vartheta}_i \) are the optimal variational parameters maximizing the cost function \( C \) at time \( t \). Note that in this and the following sections, we drop the \( (\ell) \) superscript when referring to the variational parameters. Instead, we always use \( \ell^* \equiv \lim_{n \to \infty} \ell^*(t, \epsilon, M) \) for a specified \( \epsilon \) and \( M \). The VTC fidelity \( F(t) \) is shown as a green dashed line in Fig. 7. Here, we focus on simulations at system size \( M = 11 \) over a long time \( t_f = 140J^{-1} \) using a variational ansatz with \( \ell = 76 \) layers that is able to represent the exact time-evolved state over the full time interval. We find a sizable fidelity \( F(t_f) = 0.83 \) at the end of the simulation after performing 9 propagation and compression steps at times \( t = m\tau \) with \( \tau = 15.2 \) and \( m = 1, \ldots, 9 \). Here, we have chosen \( n = \ell \) Trotter steps to propagate the state from time \( t \) to \( t + \tau \).

This value of \( F(t_f) \) should be contrasted with the fidelity obtained by direct Trotter simulation using the same resources as VTC: the Trotter simulation fidelity \( F_{\text{Trot}}(t) = |\langle \psi_{\text{Trot}}(t) \rangle|^2 \) falls to zero already at time \( t = 120J^{-1} \) (grey curve). We note that the direct Trotter simulation uses a fixed number of \( 3\ell \) Trotter steps. This is because the double time contour circuit used to measure the VTC cost function \( C \) is of length \( 3\ell \) if one chooses \( n = \ell \) during the VTC propagation step [see Eq. (2.3)].

During the VTC algorithm there are two independent sources of error that occur during the propagation and the compression step, respectively. First, a Trotter error occurs during state propagation that is controlled by the Trotter step size \( \tau/n \). Second, a compression error arises that is given by the final (i.e., minimal) value of \( 1-C \) at the end of the numerical optimization. This error is controlled by the optimization parameter \( \epsilon \) that sets the threshold for convergence. Of course, a smaller value of \( \epsilon \) makes the optimization more difficult and time consuming. To contrast these two sources of error, we include in Fig. 7 results for a perfect compression (orange line), corresponding to the Trotter simulation fidelity \( F_{\text{Trot}}(t) \) with fixed step size, where the only source of error is the finite, fixed Trotter step size \( \tau/n \). In the ideal case of perfect compression, this error could be brought down arbitrarily by reducing \( \tau \). However, since we have to perform more compression steps for smaller \( \tau \), this will increase the compression error in practice. This leads to our choice of parameters for the VTC simulation in Fig. 7, where the two errors are comparable.

Finally, the main bottleneck of the algorithm in going to larger system sizes is the time needed to perform the compression optimization. Due to the large number of parameters, e.g. \( N = M\ell = 836 \) for \( M = 11 \) and \( \ell = 76 \), the optimization of the variational ansatz to produce a
single compression point in Fig. 7 takes a few hours of CPU time, preventing long-time simulations for larger systems. On the other hand, if one limits the simulation to shorter times, once can reduce the number of layers \( \ell \) in the variational ansatz while keeping the product \( N = M \ell \) constant, and thereby simulate larger systems. This was demonstrated for a mixed-field Ising model using a different classical optimization method based on tensor network techniques in Ref. [26].

D. Ideal quantum circuit simulator results

Next, we investigate the performance of the VTC algorithm on a noiseless quantum circuit simulator, where we evaluate the overlap cost function using either the SWAP-test (Fig. 1) or the double time contour circuit (Fig. 2) using a finite number of quantum measurements, or "samples." The fact that the overlap cost function now exhibits sample noise increases the difficulty of the numerical optimization during the compression step. In particular, we find that the non-gradient-based optimizer CMA-ES is more reliable and converges much faster to a minimum of the noisy overlap cost function compared to a gradient-based optimizer such as L-BFGS-B. While the sample noise can in principle be brought arbitrarily low by increasing the number of samples, we here focus on a realistic number of samples, between \( 2^{14} \) and \( 2^{16} \), that could be executed on current NISQ hardware.

The presence of sample noise limits the largest system size we are able to simulate using VTC to \( M = 6 \). We do not observe any significant difference in the performance of the algorithm depending on whether we use the SWAP-test or the double time contour circuit to measure the cost function. As shown in Fig. 8(a), the fidelity for the VTC simulation is consistently larger than that for the noiseless direct Trotter simulation at large times.

As expected, the VTC fidelity increases with the number of circuit samples. Results are for \( M = 6 \), \( \ell = n = 7 \), \( \tau = 2.8 \), and compression error threshold \( \epsilon = 5 \times 10^{-3} \). We use CMA-ES for the compression optimization. The direct Trotter simulations (grey) use \( 3\ell = 21 \) Trotter steps and are run on a statevector simulator. (b) Analysis of the fidelity data for \( 2^{16} \) samples in the vein of Fig. 7. The data points represented by black and red crosses are calculated as described in the caption of Fig. 7.
the accumulation of Trotter error. Note that we use a statevector simulator for the direct Trotter calculations.

To further analyze the algorithm’s performance for the maximal number (2^{16}) of samples, we include in Fig. 8(b) the fidelity (with respect to the exact time-evolved state) of the best possible compression during the next propagation period $\tau$. Explicitly, this is given by the overlap $|\langle \psi(t)|U_{\text{Trot}}|\psi(\hat{\vartheta}_{t-\tau})\rangle|^2$; the data are represented as black crosses in the figure. The red crosses indicate the overlap of the best possible compression state, $U_{\text{Trot}}|\psi(\hat{\vartheta}_{t-\tau})\rangle$, with the state to which the algorithm converged; explicitly, this is given by $|\langle \psi(\vartheta_i)|U_{\text{Trot}}|\psi(\hat{\vartheta}_{t-\tau})\rangle|^2$. Note that this is equal to the value of the cost function $C$ at the end of the optimization. An interesting feature relative to the noiseless simulation considered in Fig. 7 is that the VTC algorithm can converge to a state whose fidelity with the exact time-evolved state exceeds that of the “best compression” state $U_{\text{Trot}}|\psi(\hat{\vartheta}_{t-\tau})\rangle$—this is visible in the last data point in Fig. 8(b). This feature is a result of the sample noise impacting the optimization during the compression step. Indeed, for the noiseless statevector simulation, the actual fidelity of the converged VTC state is upper bounded by the “best-compression” value, as expected.

A single compression step for the parameters in Fig. 8 takes up to ten hours of CPU time. The difficulty of the noisy optimization during the compression step constitutes the main bottleneck of the circuit simulations in going to larger system sizes. This bottleneck could be mitigated by parallelizing the circuit evaluations across different CPUs (or indeed QPUs), which would allow the accumulation of more samples to reduce the sample noise. Another possibility worth exploring is to replace the overlap cost function by, e.g., a reduced density matrix fidelity [40] defined only over a subset of the full system where local quantities of interest are to be computed.

E. Noisy circuit simulator results

We now discuss VTC simulation results on a noisy quantum circuit simulator that takes gate imperfections and finite qubit coherence times into account. To connect with the simulations on the real IBM QPU Santiago, which will be discussed below, we choose a noise model with parameters drawn from that chip, as implemented in Qiskit Aer [41]. Due to the increased noise in the simulations arising from gate errors, readout errors, and finite qubit coherence times $T_1$ and $T_2$, the numerical optimization during the compression step is even more challenging than for the ideal quantum circuit simulator. We use the non-gradient-based method CMA-ES for the classical optimization, which we find to be more reliable than gradient-based approaches.

To reduce the noise in the cost function, it is absolutely essential to exploit a combination of standard and specifically tailored error mitigation techniques. We employ standard readout error mitigation as built into Qiskit Ignis, and zero-noise extrapolation (ZNE) with a linear fit, which we implement using the software package Mitiq [42]. We also apply a specific postselection protocol to the results: since the Heisenberg model preserves the magnetization operator $S_z = \sum_{i=1}^{\beta} Z_i$, only computational basis states with the same expectation value of $S_z$ as the initial Néel state are physically allowed. We thus discard any counts of computational basis states that do not fulfill the total $S_z$ conservation law after performing readout error mitigation.

In Fig. 9(a) we present VTC results for the post-quench dynamics in an $M = 3$ spin chain with open boundary conditions using the noisy quantum circuit simulator. The noise parameters are drawn from the IBM QPU Santiago. We use $\ell = 2$ layers in the variational ansatz, $n = 2$ Trotter steps during the VTC propagation step, and average over 8192 circuit samples. Since the calcula-
F. Results on IBM QPU Santiago

We now demonstrate quantum dynamics simulations beyond the qubit coherence time on real IBM quantum hardware. This is achieved by running VTC simulations on the IBM QPU Santiago for a system of size $M = 3$ with open boundary conditions. We use a variational ansatz with $\ell = 2$ layers, employ $n = 2$ Trotter steps during the state propagation step of VTC, and average over $2^{13}$ circuit samples. The complete algorithm is executed on real quantum hardware, i.e., all circuits during the compression optimizations are being executed on the QPU. In addition to the error mitigation techniques described in Sec. III E, we found it essential to apply a Pauli twirling technique, which dresses the two-qubit CNOT gates with random Pauli gates [43]. This is important in order to convert the noise on the real device into a stochastic form, which is necessary to justify ZNE. Without the additional Pauli twirling protocol, we observed that the ZNE error mitigation method was not reliable.

In Fig. 9(b), we present VTC simulation results on the IBM Santiago backend. These results were collected over the course of three days, since calculation of each compression point takes about 2-3 hours, which includes waiting time in the IBM execution queue. Most importantly, we find that at times $t > 9 J^{-1}$ the VTC fidelity largely exceeds the fidelity obtained from noiseless direct Trotter simulations that use the same circuit resources. Note that the direct Trotter simulations are performed using statevector simulations and thus do not contain any of the noise that is present in the VTC simulations. While the direct Trotter fidelity vanishes for times larger than $t > 16 J^{-1}$, the VTC fidelity remains at a value of 0.8 at that time. This explicitly demonstrates that even though the impact of noise on the real QPU is more severe compared to the noisy simulator, we are able to achieve quantum dynamics simulations beyond the coherence of the device using the VTC algorithm.

In Fig. 9(b) we also analyze the data from the QPU using the methodology of Figs. 7 and 8. We find that the fidelity of the state to which the compression optimization converges can again exceed that of the best possible Trotter compression, i.e., $| \langle \psi(t) | \hat{U}_{\text{Trot}} | \psi(\hat{\theta}_{t-\tau}) \rangle |^2$ (black crosses). This effect, which was also apparent in the results of Fig. 8, is much more pronounced on the real QPU due to the presence of both sample and gate noise. The red crosses again represent the fidelity between the best possible compression and the state found by the algorithm. Over the 10 compression steps, the algorithm was able to recreate the Trotter evolution of the state from the previous compression step with a mean fidelity of 0.969.

IV. CONCLUSION

We have demonstrated a simulation of the post-quench dynamics in a three-site antiferromagnetic Heisenberg chain beyond the qubit coherence time on real quantum hardware. This was achieved by compressing the Trotter time-evolved state at intermediate time steps into a variational form. The state overlap served as a cost function for the compression optimization step, which was executed on NISQ hardware without any additional overhead using a double time contour circuit. We have further benchmarked this variational Trotter compression algorithm on larger Heisenberg chains using statevector as well as noiseless and noisy circuit simulators. While most of our results were obtained for an integrable model, we also tested the ability of a variational ansatz to capture the dynamics generated by the Heisenberg Hamiltonian in the presence of an integrability-breaking term. These tests revealed that the performance of the VTC algorithm does not depend strongly on the presence or absence of integrability—indeed, an ansatz with the same number of parameters captures the wavefunction to the same accuracy in both cases.

The main goal moving forward is to increase the system sizes accessible to VTC simulations, for which several key issues need to be addressed. First, the noise level of the overlap cost function needs to be reduced in order to accelerate the classical optimization that is performed during the compression step. In addition to reducing device errors by hardware improvements, one can devise several error mitigation strategies [44] such as probabilistic error cancellation [28], virtual distillation [45, 46], clifford data regression [47] or a combination thereof [48–52]. Sample noise can be reduced by increasing the number of circuit shots beyond the current limit of $2^{13}$ on the IBM backend we used. The fidelity of the Trotter time evolution during the propagation step can be increased by using pulse level control of the gates, which can often lead to shorter gate times. Finally, other future directions would be to explore alternative variational ansaetze such as hardware efficient ones, and to employ other classical optimizers for noisy cost functions.
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