Abstract

This paper presents an approach to power added efficiency (PAE) increase for Quasi-Doherty power amplifier (Q-DPA) design. For this aim, active feedback is utilized instead of a passive quarter wavelength transmission line ($\frac{\lambda}{4}$ TL) usage, which is conventionally used in the DPA schematic. PAE increase can be done by applying an accurate load modulation to the main amplifier ($P_{\text{main}}$), especially for technologies in which output impedance of the main power amplifier ($Z_{\text{out,main}}$) considerably varies in both low and high power regions. Because such precise modulation is still based on a modified TL, this approach suffers from the inherent narrowband behavior of that TL. As a consequence, expecting a wideband DPA may not be satisfied in all cases. To deal with this issue, active feedback is used to play a role in reaching $P_{\text{main}}$, which is not saturated before, to its maximum efficiency at the highest level of received input power ($P_{\text{in}}$) in the high power region. Following $Z_{\text{out,main}}$ trajectories in power and frequency sweeps simultaneously just by a passive TL are not needed anymore. Still, for the sake of preventing total PAE degradation due to the consummated power by the feedback path’s power amplifier ($P_{\text{feedback}}$) should be limited, analytical confinement is provided in this work. A comparison is made between GaAs pHEMT 0.25um MMIC technology-based conventional DPA and the
proposed revised approach based-DPA to verify the mentioned approach. The proposed PA shows maximum output power of 33.4 dBm, maximum PAE of 41.6, fractional bandwidth of 11%. The Q-DPA works with a maximum power gain of 24.16.

**Keywords**: Doherty, High Efficiency, Load Modulation, PAE, Wideband.

**Introduction**

In the era of traveling to untouchable space spots, the nadir of the oceans,wirelessly connected systems are strongly needed. The world of wireless networks is interminably insatiable for developing the current high speed and high-efficiency subsystems for the sake of realizing a modern, power-efficient globe. This future world's perspective entices researchers to introduce modified structures and theories for the communication subsystems. By studying the field of designing PAs, in retrospect, it can be understood several different PA design methods are introduced to increase PAs’ PAE, bandwidth, to name but a handful. Such methods comprise envelope tracking [1-5], switching mode class PAs [6-10], harmonically tuned class PAs [10-15], Cherix outphasing [16-20], and DPA [21-25]. DPA is one such PA that is still under more scrutiny to subdue the related restrictions of exerted technology and try to reach the best possible performance of that PA [26-30]. This PA comprises two power amplification paths. The first one is called main; its related PA works as class AB-PA and contributes to amplification in the whole input power range. The second path is named auxiliary and is biased as a class C PA. The latter path is only active when the input level power increases. Hereafter we call this power region the high-power region. To prevent efficiency degradation due to saturation of the main power amplifier in the high-power region, load modulation is done at the output of PA_{main} using a \( \frac{\lambda}{4} \) TL. Many developing strategies have been applied to the conventional DPA [25]. The common
point of almost all of them is that these designs are based on a $\frac{\lambda}{4}$ TL-based load modulation in which $Z_{out,main}$ trajectory change in power, and frequency sweep are not precisely modeled. According to [23], revising load modulation can be done based on linear modeling of the variations in $Z_{out,main}$’s trajectory in power sweep at modulation equation. This load modulation modification led to the PAE increase in comparison by the condition in which a conventional modulation was applied to DPA. Since that revised modulation-based DPA does not provide a broadband performance, in order to design a wideband DPA, more consideration should be attended to. In this work, to present a high PAE and wideband DPA in GaAs technology in which $Z_{out,main}$ shows two disparate trajectories in power and frequency sweeps, the strategy of load modulation is modified.

Fulfilling an ideal wideband load modulation that precisely follows $Z_{out,main}$ variation in power sweep leads to significant complexity in design. Therefore, in this work, active wideband feedback is applied to PA$_{main}$ to reach this PA to its high efficiency in high power region. PA$_{main}$ design is done by keeping it away from saturation before presenting its highest efficiency. According to the non-saturated status of the PA$_{main}$, point-to-point narrowband following of $Z_{out,main}$ variation in high power region is not needed anymore. Expunging the narrowband modulation can pave the way to design a wideband DPA.

Furthermore, bias and size selection of PA$_{feedback}$ should be made to reach the required $P_{out}$ feedback while its consumed power is not high. Such a design of feedback path contributes to keeping the PAE as high as possible. Analytical studies of PA$_{feedback}$ design for PAE’s sake are done in the next section, design theory.

**Design Theory**
Active feedback utilization

According to the conventional DPA concept, in the high-power region, when PAaux contributes to power amplification, $\frac{\lambda}{4}$ TL-based load modulation averts DPA’s efficiency roll-off [25]. However, this approach has its own disadvantages: bandwidth degradation due to narrowband behavior of $\frac{\lambda}{4}$ TL, not expecting $Z_{out,main}$ variation with power in which such changes are needed to be applied in a revised load modulation [23], to name but a handful. Load modulation begins when PAaux commences power amplification. As it can be construed from Eq.1, $\frac{i_{aux}}{i_{main}}$ term plays a role in $Z_{out,main}$ decrease by which DPA’s efficiency roll-off can be prevented.

$$Z_{Mout,main} = \frac{Z_0^2}{R_L} - Z_0 \frac{i_{aux}}{i_{main}},$$

(1)

Still, it highly depends on $\frac{\lambda}{4}$ TL usage to fulfill the task of modulation. By considering $\frac{\lambda}{4}$ TL inherent narrowband feature and load modulation lockage in multi-stage DPA[Grebin], some modifications for the load modulation approach in some cases are needed. Class AB PAs are designed for providing the maximum efficiency in the highest input power level, and after that, applying higher Pin leads to their considerable efficiency roll-off. In this work, we designed PA$_{main}$ so that it does not reach its maximum efficiency at the end of the low-power region. Active feedback starts injecting extra power to PA$_{main}$’s second stage as power increases-whole PA works in the high-power region. By doing so, PA$_{main}$ provides its maximum drain efficiency when the input power is high.

Efficiency concerns in the presence of active feedback
From another scope, to have a higher efficiency of PA\textsubscript{main} and active feedback than conventional AB-class PA in TL-based DPA, more considerations should analytically discuss that hereunder we delved into them.

First, the total efficiency of PA\textsubscript{main} and the active feedback should be more than PA\textsubscript{main} in conventional DPA. To realize this efficiency priority, the consumed power of PA\textsubscript{feedback} should follow a restriction by which presenting higher efficiency of the feedback-based PA is guaranteed.

The following equations mathematically show how this condition can be satisfied.

\[
\frac{(0.9P\text{\textsubscript{in,AB}} + P\text{\textsubscript{out,feedback}})G\text{\textsubscript{AB}} + P\text{\textsubscript{out,C}}}{(0.9P\text{\textsubscript{in,AB}} + P\text{\textsubscript{out,feedback}})G\text{\textsubscript{AB}} + P\text{\textsubscript{DC,feedback}} + P\text{\textsubscript{out,C}}} \geq \frac{(P\text{\textsubscript{in,AB}})G\text{\textsubscript{AB}} + P\text{\textsubscript{out,C}}}{(P\text{\textsubscript{in,AB}})G\text{\textsubscript{AB}} + P\text{\textsubscript{out,C}}} \quad (2)
\]

\[
P\text{\textsubscript{DC,feedback}} \leq \frac{P\text{\textsubscript{out,feedback}} G\text{\textsubscript{AB}}}{\eta\text{\textsubscript{C}}} \frac{P\text{\textsubscript{in,AB}} G\text{\textsubscript{AB}} + \eta\text{\textsubscript{AB}} P\text{\textsubscript{out,C}}}{\eta\text{\textsubscript{AB}} G\text{\textsubscript{AB}} + P\text{\textsubscript{out,C}}} \quad (3)
\]

The restriction of Eq.3 indicates the condition for bias and size selection of PA\textsubscript{feedback}, a 6×150 transistor with -1V and 5V bias voltages for its gate and drain.

**Feedback junction to the main path**

One of the typical conjunction of the feedback path to the main loop is the resistive one. Due to power dissipation in this method, capacitive or inductive ones are preferred. In the case of the capacitive junction, due to the degradation effect of large capacitors in the circuit’s performance and realization in MMIC, inductive junction is selected.

Among several configurations of the inductive junction, quasi T-junction is chosen. The insertion of this junction should be so that no significant bandwidth degradation has occurred in OMN of the main path and whole DPA. Since OMNs designing both paths are correlated together, bandwidth degradation prevention due to inductive junction insertion should also be done. To deal
with this issue, both networks are designed with a bandwidth margin to ensure that even with inductive insertion, the proposed DPA will provide the desired bandwidth. Figure 1. exhibits the bandwidth of inductive junction, OMNs of \( \text{PA}_{\text{main}} \), and \( \text{PA}_{\text{aux}} \). This figure proves that the DPA is able to provide the aimed bandwidth.

![Figure 1. Scattering parameters of the proposed Q-DPA matching networks](image)

**Class of \( \text{PA}_{\text{feedback}} \) selection**

To satisfy Eq.2, designing \( \text{PA}_{\text{active}} \) in class C seems to be a proper strategy to take. Still, by considering class C PA’s inherent narrowband performance, some more considerations for choosing the class in which \( \text{PA}_{\text{feedback}} \) operates should be scrutinized. For the sake of presenting a wideband performance, \( \text{PA}_{\text{feedback}} \) can be biased as an AB-class PA, and for controlling its consumed power, the drain voltage of \( \text{PA}_{\text{feedback}} \) should be as low as possible.

In the aspect of stability, two main issues should be addressed here: preventing oscillation after applying active feedback in the circuit. The other is the transistor’s stability. For the sake of coping with undesired oscillation due to feedback presence, prevention of the ring oscillator presence, feedback path mandatorily should enjoy the odd number of PA stages so that total there are even PAs in the feedback loop, \( \text{PA}_{\text{main}} \) is also included.
As a consequence, the recurrent signal of the active feedback to PA\textsubscript{main} should undergo 180 phase change, which can obtain by common source figuration. By considering the desired \( P_{\text{out,feedback}} \), a one-stage common source structure is selected for PA\textsubscript{feedback}. To deal with the transistor’s stability as a by default unstable device, large-sized transistor usage in this technology needs to utilizing a low-resistance resistor in the transistors’ stabilizer network. Such a network has the most negligible degradation on transistor’s gain; therefore, the desired output power can be delivered to the input of PA\textsubscript{main,2nd stage}.

For the sake of the non-saturated status of PA\textsubscript{feedback} in the high power level, the capacitance of the capacitor in that PA’s stabilizer network is set in a medium value to presents proper power gain while it is not saturated. It should be noticed that properly low-Q LC networks design matching networks of PA\textsubscript{feedback}, and this point is valid for all other matching networks for PA\textsubscript{main} and PA\textsubscript{auxiliary}. These appropriate low-Q networks present the desired bandwidth (see Figure 1.).

**Input power divider**

According to the strategy of this work to prevent PA\textsubscript{main} saturation during high-power region, input power should be controlled so that the main application path delivers a small portion of power. According to what proposed in [31], the characteristic impedance of main branch is matched to main path’s optimum impedance at low power level to satisfy this condition. The auxiliary branch’s impedance characteristic is matched to the optimum impedance of the auxiliary amplification path. By doing so in the high power region, most of the input power is assigned to the auxiliary power amplification path.

**Phase compensation**
In the conventional DPA, a $\frac{\lambda}{4}$ TL at the input of the auxiliary path of amplification is placed to compensate for the $90^\circ$ difference in the main path phase. This difference is due to the $\frac{\lambda}{4}$ TL contributing to the conventional load modulation. In this work, by modifying the strategy of modulation, the second $\frac{\lambda}{4}$ TL utilization is not needed anymore. Still, due to the difference in these two paths of amplification, both paths’ consequent phase differences should be compensated. According to [21], the difference in the phase of main and auxiliary paths is compensated in designing the matching networks of main path.

**Bandwidth**

One of the primary concern in this design is providing wideband performance. To satisfy the desired 11% fractional bandwidth with a revised approach for load modulation, all sections of DPA, such as input power divider and matching networks, should provide the wideband performance. Lumped model of the Wilkinson power divider presents the aimed 11% bandwidth. Besides, the appropriately low-Q LC networks can serve as wideband matching networks. Figure 1. shows the bandwidth of each section and the whole DPA. It can be easily understood that the desired bandwidth is reached.

Power combination concerns

The low impedance of $Z_{\text{out,main}}$, which is due to its saturated status, in the high-power region can destructively affect the power combination of DPA’s output port. According to DPA’s concept [25], to address this issue, the $\frac{\lambda}{4}$ TL-based load modulation plays a role. In our case, in which saturation of PA$_{\text{main}}$ is prevented, the output impedance of this PA doesn’t engender any concern. It should be noticed the only thing worthy of attention is the output matching networks of both
paths should be designed so that the output port of our proposed Q-DPA be matched to standard 50 Ω. Figure 2. shows the output impedance of main and auxiliary paths of amplification. The resultant impedance is matched to the mentioned standard one. Figure x1. It also indicates the bandwidth providing by these networks and confirmed their wideband performance.

![Figure 2. Output impedance of PA_{main} and PA_{aux}](image)

**High order harmonic analysis**

Except for harmonic tuned PAs, all other PAs should provide approximately all of their power in the first harmonic for efficiency's sake. In other words, power leakage in other higher harmonics should be averted.

Conventionally, 2\textsuperscript{nd} and 3\textsuperscript{rd} harmonics rejection networks are used in the PA’s design. These networks can lead to bandwidth restrictions. A wideband PA can be designed from another scope so that the power level in the 2\textsuperscript{nd} and 3\textsuperscript{rd} harmonics can be neglected. In such a case, for the sake of prevention of the bandwidth degradation, 2\textsuperscript{nd} and 3\textsuperscript{rd} harmonic rejection insertion is skipped. Figures 3. indicate the output power’s higher harmonics that are as low as can be ignored. Therefore, the design of harmonic rejection networks is neglected.
Comparison

This work presents a Q-DPA in which active feedback is used. This feedback helps PA_{main} reaches its maximum efficiency in the high power region. Meanwhile, the input power divider prevents saturation status of PA_{main} in low power region. Doing so prevents PA_{main} being suffered from non-ideal load modulation in which bandwidth degradation and Z_{out,main} trajectory in power sweep are not modeled. Therefore, to indicate the proposed Q-DPA’s effectiveness, a comparison should be made between feedback-based Q-DPA and conventional one. Figure 4., Figure 5. and Figure 6. show the schematics of conventional and proposed DPAs, respectively.
Figure 4. Schematic of the conventional load modulation based DPA (the elements in the grey boxes are the last and the first elements of OMN and IMN of first and second stages PAs, which are merged in circuit’s layout, and the exact amount of all elements is provided in Appendix).

Figure 5. Schematic of the proposed Q-DPA (the elements in grey boxes are the last and the first elements of OMN and IMN of first and second stages PAs, which are merged in circuit’s layout, and the exact amount of all elements is provided in Appendix).

Figure 6. Schematic of the feedback path in the proposed Q-DPA (and the exact amount of all elements is provided in Appendix).
Figure 7. and Figure 8. depicts PAE, Pout, power gain, and bandwidth of both circuits. It can be construed that utilizing the active feedback leads to $\text{PA}_{\text{main}}$’s reaching its maximum efficiency in the high power region.

![Figure 7. PAE, Power Gain, Output Powers of the conventional DPA](image1)

![Figure 8. PAE, Power Gain, Output Powers of the proposed Q-DPA](image2)

Consequently, utilization of this feedback results in PAE and Pout increase. Furthermore, since $\text{PA}_{\text{main}}$ doesn’t saturate during high power region, there is no need to make an ideal load
modulation by which the exact $Z_{\text{out,main}}$ trajectory in power sweep should be followed in order to have the maximum possible PAE.

For a better understand, the performances of both designs in power sweep are shown in Figure 9. and Figure 10. These figures clearly confirm that using the revised approach helps in presenting a high PAE power amplifier.

![Figure 9. PAE of the conventional and proposed Q-DPA in power sweep](image)

![Figure 10. Output Powers of the conventional and proposed Q-DPA in power sweep](image)

To indicate more about the performance of the Figure 11. and Figure 12 show output powers of $P_{A\text{main}}, P_{A\text{aux}}$, the proposed Q-DPA and its related power gain.
Figure 11. Output Powers of the PA\textsubscript{main}, PA\textsubscript{aux}, and the proposed Q-DPA in power sweep

Figure 12. Power Gain of the proposed Q-DPA in power sweep

Table 1. presents the performance comparison of the conventional DPA and the proposed Q-DPA.

This table clearly leads to drawing this conclusion that utilization of the proposed approach in this technology results in PAE and bandwidth development.

|                | \(P_{out}(\text{dBm})\) | \(P_{out@2^{nd}}(\text{dBm})\) | \(P_{out@3^{rd}}(\text{dBm})\) | Gain (dB) | PAE (%) | FBW (%) |
|----------------|--------------------------|---------------------|--------------------------|-----------|--------|----------|
| This Work      | 33.4                     | -52.5               | -103.6                   | 24.16     | 41.59  | 11       |
| Conventional DPA | 32.3                     | -17                 | -35.1                    | 20.16     | 33.17  | 3.2      |
Results

This paper presents a conventional DPA design and an active feedback-based quasi-DPA in GaAs 0.25µm pHEMT MMIC technology. The comparison between these designs depicts the practicality of the proposed Q-DPA. The proposed PA is designed in a two-stage structure for both the main and auxiliary path of amplification. In the main path, at the first stage, 8 ×150 µm transistor is used and has -1V, 7V for its gate and drain bias, respectively. The second one enjoys a single 8 ×150 µm transistor that is biased in -1V and 7.4 V for its gate and drain. The feedback path comprises a 6×150 µm transistor with -1V and 5V bias voltages for its gate and drain, respectively. The first stage in the auxiliary path enjoys two parallel 8×150 µm transistors that are biased in -1.2V and 7V for its gate and drain biasing, respectively, while the second one contains two parallel 8×150 µm transistors with -1.05V and 7V bias voltages for its gate and drain, respectively. The proposed Q-DPA has the maximum amount for Pout of 33.4 dBm, PAE of 41.6, fractional bandwidth of X, which is the frequency range of 6.6-7.4 GHz, the power gain of 24.16 dB. The feedback path's consumed power is designed so that the theory limit (Eq.3) is satisfied.

Table 2. provides the performance comparison among this work and the state of the art DPA designed in GaAs technology, and the Figures 13. Shows the proposed Q-DPA’s layout.

|                | $P_{out}$ (dBm) | Gain (dB) | Freq (GHz) | PAE (%) | Technology          |
|----------------|-----------------|-----------|------------|---------|---------------------|
| [32]           | 19.4            | N/A       | 62-68      | 28.3    | CMOS 0.045 µm       |
| [33]$^*$       | <27             | ≈14       | 28         | ≈37     | CMOS 0.055 µm       |
|                | <27             | ≈12       | 45         | ≈35     |                     |
| [34]$^{**}$    | 27.2            | <24       | 5.4-6.2    | 24.5    | GaAs pHEMT 0.15 µm  |
| This Work      | **33.4**        | **24.16** | **6.6-7.4**| **41.59**| GaAs pHEMT 0.25 µm |
Conclusion

This work presented the design of a wideband Q-DPA. Considering $Z_{\text{out,main}}$, the main variation in power sweep, which makes realizing a proper load modulation hard, is done in the load modulation’s revised strategy. That modification is grounded on active feedback. The feedback made $PA_{\text{main}}$ to be reached its maximum PAE in the highest level of power. Therefore there is no concern about making a delicately ideal load modulation by which increased PAE can be obtained.

To draw an accurate conclusion, a comparison between the proposed DPA and that of the conventional DPA in which a $\lambda/4$ TL-based load modulation is done is made in this work. Both PAs are designed in GaAs pHEMT 0,25 $\mu$m MMIC technology in which $Z_{\text{out,main}}$ shows a considerable variation in power sweep. Besides, $Z_{\text{out,main}}$’s trajectories in power and frequency sweeps are not similar. That’s why an accurate load modulation considering the trajectory of $Z_{\text{out,main}}$ in power sweep cannot be pragmatic for that of $Z_{\text{out,main}}$ in frequency change. Therefore, no wideband DPA can be expected. $P_{\text{out}}, \text{PAE}$, the bandwidth of those above mentioned PAs verify that modification in load modulation approach in case of different trajectories of $Z_{\text{out,main}}$ in
power, and frequency sweeps for a high PAE wideband performance is needed. Because the active feedback in our work consumes power, the dissipated power of that device should be controlled. In this work, this task is done analytically. Besides, PAfeedback’s bias and size selection are also made to provide desired Pout,feedback, and satisfy the limit of consummated power.

**Appendix**

Amount of the elements of the conventional load modulation based DPA (Figure 4):

- \( L_2=2 \, \text{nH}, L_3=1.85 \, \text{nH}, L_4=0.76 \, \text{nH}, L_5=0.7 \, \text{nH}, L_6=3.2 \, \text{nH}, L_7=3 \, \text{nH}, L_8=0.88 \, \text{nH}, L_9=5.4 \, \text{nH}, L_{10}=1.6 \, \text{nH}, L_{11}=0.5 \, \text{nH}, L_{12}=1 \, \text{nH}, L_{13}=0.72 \, \text{nH}, L_{14}=1.8 \, \text{nH}, L_{15}=1.2 \, \text{nH}, L_{16}=0.78 \, \text{nH}, L_{17}=1.1 \, \text{nH}, L_{18}=0.45 \, \text{nH}, L_{19}=1.4 \, \text{nH}, \text{nH}, L_{21}=2.6 \, \text{nH}, L_{22}=0.68 \, \text{nH}, L_{23}=0.36 \, \text{nH}, L_{24}=0.28 \, \text{nH}, L_{25}=1.65 \, \text{nH}, L_{26}=1.77 \, \text{nH}, L_{27}=1 \, \text{nH}, L_{30}=1.5 \, \text{nH}, L_{31}=0.82 \, \text{nH}, L_{32}=0.5 \, \text{nH}, L_{33}=0.43 \, \text{nH}, L_{34}=0.25 \, \text{nH}, L_{35}=0.43 \, \text{nH}, L_{36}=0.25 \, \text{nH}, L_{37}=1.85 \, \text{nH}, L_{38}=1.12 \, \text{nH}, L_{39}=0.7 \, \text{nH}. \)

- \( C_2=0.2 \, \text{pF}, C_3=0.2 \, \text{pF}, C_4=0.46 \, \text{pF}, C_5=0.93 \, \text{pF}, C_6=0.3 \, \text{pF}, C_7=0.3 \, \text{pF}, C_8=0.55 \, \text{pF}, C_9=0.3 \, \text{pF}, C_{10}=0.3 \, \text{pF}, C_{11}=1 \, \text{pF}, C_{12}=0.3 \, \text{pF}, C_{13}=0.3 \, \text{pF}, C_{14}=0.93 \, \text{pF}, C_{15}=1 \, \text{pF}, C_{16}=0.58 \, \text{pF}, C_{17}=0.83 \, \text{pF}, C_{18}=0.95 \, \text{pF}, C_{19}=0.3 \, \text{pF}, C_{20}=0.3 \, \text{pF}, C_{21}=0.7 \, \text{pF}, C_{22}=1.9 \, \text{pF}, C_{23}=2.2 \, \text{pF}, C_{24}=0.2 \, \text{pF}, C_{25}=0.2 \, \text{pF}, C_{26}=1.45 \, \text{pF}, C_{27}=1.8 \, \text{pF}, C_{28}=0.84 \, \text{pF}, C_{30}=1.4 \, \text{pF}, C_{31}=0.3 \, \text{pF}, C_{32}=0.3 \, \text{pF}, C_{33}=2 \, \text{pF}, C_{34}=0.3 \, \text{pF}, C_{35}=0.3 \, \text{pF}, C_{36}=0.55 \, \text{pF}, C_{37}=0.5 \, \text{pF}, C_{38}=0.73 \, \text{pF}, C_{39}=2 \, \text{pF}, C_{40}=2.26 \, \text{pF}, C_{41}=1 \, \text{pF}, C_{42}=1.5 \, \text{pF}, C_{43}=0.3 \, \text{pF}, C_{44}=0.3 \, \text{pF}, C_{45}=2.5 \, \text{pF}, C_{46}=0.3 \, \text{pF}, C_{47}=0.3 \, \text{pF}, C_{48}=3 \, \text{pF}, C_{49}=0.6 \, \text{pF}, C_{50}=0.18 \, \text{pF}. \)

- \( R_1=100 \, \Omega, R_2=20 \, \Omega, R_3=20 \, \Omega, R_4=20 \, \Omega, R_5=20 \, \Omega. \)

Amount of the elements of the Proposed DPA (Figure 5):

- \( L_2=2 \, \text{nH}, L_3=1.85 \, \text{nH}, L_4=0.76 \, \text{nH}, L_5=0.7 \, \text{nH}, L_6=3.2 \, \text{nH}, L_7=3 \, \text{nH}, L_8=0.88 \, \text{nH}, L_9=5.4 \, \text{nH}, L_{10}=1.6 \, \text{nH}, L_{11}=0.5 \, \text{nH}, L_{12}=1 \, \text{nH}, L_{13}=0.72 \, \text{nH}, L_{14}=1.8 \, \text{nH}, L_{15}=1.2 \, \text{nH}, L_{16}=4 \, \text{nH}, L_{17}=6 \, \text{nH}, L_{18}=5 \, \text{nH}, L_{19}=3.3 \, \text{nH}, L_{21}=0.2 \, \text{nH}, L_{22}=1.98 \, \text{nH}, L_{23}=2.6 \, \text{nH}, L_{24}=0.68 \, \text{nH}, L_{25}=0.36 \, \text{nH}, L_{26}=0.28 \, \text{nH}, L_{27}=0.65 \, \text{nH}, L_{28}=3.5 \, \text{nH}, L_{29}=1.65 \, \text{nH}, L_{30}=1.97 \, \text{nH}, L_{31}=0.75 \, \text{nH}, L_{32}=1.5 \, \text{nH}, L_{33}=0.82 \, \text{nH}, L_{34}=0.53 \, \text{nH}, L_{35}=0.43 \, \text{nH}, L_{36}=0.25 \, \text{nH}, L_{37}=0.9 \, \text{nH}, L_{38}=4 \, \text{nH}, L_{39}=1.85 \, \text{nH}, L_{40}=1.12 \, \text{nH}, L_{41}=0.7 \, \text{nH}. \)

- \( C_2=0.2 \, \text{pF}, C_3=0.2 \, \text{pF}, C_4=0.46 \, \text{pF}, C_5=0.93 \, \text{pF}, C_6=0.3 \, \text{pF}, C_7=0.3 \, \text{pF}, C_8=0.55 \, \text{pF}, C_9=0.3 \, \text{pF}, C_{10}=0.3 \, \text{pF}, C_{11}=1 \, \text{pF}, C_{12}=0.3 \, \text{pF}, C_{13}=0.3 \, \text{pF}, C_{14}=0.93 \, \text{pF}, C_{15}=1 \, \text{pF}, C_{16}=0.58 \, \text{pF}, C_{17}=0.83 \, \text{pF}, C_{18}=0.95 \, \text{pF}, C_{19}=0.3 \, \text{pF}, C_{20}=0.3 \, \text{pF}, C_{21}=0.7 \, \text{pF}, C_{22}=1.9 \, \text{pF}, C_{23}=2.2 \, \text{pF}, C_{24}=0.2 \, \text{pF}, C_{25}=0.2 \, \text{pF}, C_{26}=1.45 \, \text{pF}, C_{27}=1.8 \, \text{pF}, C_{28}=0.84 \, \text{pF}, C_{30}=1.4 \, \text{pF}, C_{31}=0.3 \, \text{pF}, C_{32}=0.3 \, \text{pF}, C_{33}=2.9 \, \text{pF}, C_{34}=0.3 \, \text{pF}, C_{35}=0.3 \, \text{pF}, C_{36}=0.55 \, \text{pF}, C_{37}=0.5 \, \text{pF}, C_{38}=0.73 \, \text{pF}. \)
pF, $C_{39}=2$ pF, $C_{40}=2.26$ pF, $C_{41}=1$ pF, $C_{42}=1.5$ pF, $C_{43}=0.3$ pF, $C_{44}=0.3$ pF, $C_{45}=3.5$ pF, $C_{46}=0.3$ pF, $C_{47}=0.3$ pF, $C_{48}=3$ pF, $C_{49}=0.6$ pF, $C_{50}=0.18$ pF.

- $R_1=100$ Ω, $R_2=20$ Ω, $R_3=20$ Ω, $R_4=20$ Ω, $R_5=20$ Ω.

Amount of the elements of the Feedback path (Figure 6):

- $L_1=3.6$ nH, $L_2=2.6$ nH, $L_3=1.3$ nH, $L_4=0.64$ nH, $L_5=0.64$ nH, $L_6=0.5$ nH, $L_7=0.47$ nH, $L_8=1$ nH, $L_9=0.7$ nH, $L_{10}=2.26$ nH, $L_{11}=2.45$ nH

- $C_1=1.5$ pF, $C_2=0.86$ pF, $C_3=1.8$ pF, $C_4=2.45$ pF, $C_5=1.3$ pF, $C_6=1.88$ pF, $C_7=0.81$ pF, $C_8=0.85$ pF, $C_9=0.28$ pF, $C_{10}=0.28$ pF, $C_{11}=0.28$ pF, $C_{12}=0.28$ pF, $C_{13}=0.92$ pF, $C_{14}=0.24$ pF, $C_{15}=0.58$ pF

- $R_3=20$ Ω

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