Investigation on Hybrid Energy Storage Systems and Their Application in Green Energy Systems

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Abstract: Power systems all over the world have been under development towards microgrids integrated with renewable energy-based distributed generation. Due to the intrinsic nature of output power fluctuations in renewable energy-based power generation, the use of proper energy storage systems and integrated real-time power and energy control schemes is an important basis of sustainable development of renewable energy-based distributed systems and microgrids. The aim of this paper is to investigate the characteristics and application features of an integrated compound energy storage system via simulation and a small-scale hardware system implementation. This paper first discusses the main components, working principles and operating modes of the proposed compound energy storage system. Then, a detailed design example composed of supercapacitors, batteries, and various controllers used in two typical application scenarios, peak demand shaving and power generation smoothing, of a grid-connected microgrid is systematically presented. Finally, an experimental setup with proper power converters and control schemes are implemented for the verification of the proposed control scheme. Both simulation and implementation results prove that the proposed scheme can effectively realize desired control objectives with the proposed coordinated control of the two energy storage devices.

Keywords: microgrid (MG); renewable energy (RE); distributed generation (DG); energy storage system (ESS); compound energy storage system (CESS)

1. Introduction

In recent years, with the increase of the global population and rapid economic development, human demand for energy is rapidly increasing and the global warming effects caused mostly by conventional power generation has become a major issue that countries all over the world must face together [1,2]. In addition, the use of conventional fossil fuels will soon yield problems, such as the rapid reduction in reserves and the unaffordable cost for the emission of greenhouse gases, which highlights the importance of developing alternative energy sources. Therefore, low-carbon energy technologies, such as nuclear energy and renewable energy (RE), have become the development goals of countries all over the world. Relatively mature RE power generation technologies include wind turbine (WT), solar photovoltaic (PV), hydropower, biomass energy, and geothermal energy generation, among which WT generation and solar PV generation are the mainstream of development. The projection in International Energy Outlook 2019 shows that, by 2050, the proportion of global RE power generation will increase to nearly 50%, as shown in Figure 1, and the proportion of WT and solar PV together in RE power generation will increase to over 70%, as shown in Figure 2 [2]. The advantages of RE include: 1) Inexhaustible and widely distributed; and 2) no fuel and extremely environmentally friendly. However, the grid connected with a large
number of such unstable generation systems will have a negative impact on the supply power quality (PQ) and reliability of existing power systems. Therefore, in order to reduce the unavoidable impact caused by the fluctuation of RE generation, such generation systems are often combined with energy storage systems (ESSs), forming a hybrid generation system in which ESSs absorb the fluctuation of power generation, achieving power smoothing and optimal energy management [3].

![Figure 1. Record and projection of world electricity generation and shares by source [2].](image)

![Figure 2. Record and projection of world RE electricity generation and shares by source [2].](image)

There are two types of ESS, i.e., power-type energy storage (PTES) units, such as SC, flywheel, and superconducting magnetic energy storage, and energy-type energy storage (ETES) units, such as various types of batteries. Since these two ESSs have their own characteristics. If proper function allocation can be carried out according to certain criteria to form a compound energy storage system (CESS), the greatest overall benefit can be obtained [4]. Take the power-smoothing control in RE based power generation and energy balancing optimization control in an MG as an example, applying CESS can bring great benefits: the PTES unit can be used to smooth power fluctuations with large amplitude and high frequency in the active distribution network of the MG, which fully demonstrates its advantages of large power output density, high response speed, and considerable number of cycles; the ETES unit can be used to smooth low-frequency power fluctuation and perform real-time power balance and dispatch, which fully demonstrates its advantages of large capacity [5]. Furthermore, the abovementioned CESS can effectively reduce the number of times of charging/discharging and depth of discharge (DoD) of PTES to extend its life. In addition, the optimized configuration of CESS can bring more economical and reliable active distribution network operation.
At present, most of the small and medium-capacity RE power generation systems are equipped with various energy storage devices (ESD) and can be divided into standalone and grid-connected architectures. Although general hybrid RE power generation systems can be easily combined with CESSs to deal with the problem of power generation fluctuations and serve as an interface for power management, real-time power optimization and integrated management and power coordination between subsystems still pose considerable challenges. Therefore, how to design RE-based power generation systems considering both stability and high efficiency with integrated CESS topologies and control schemes has become a popular research topic. Reasonable configuration of CESS and capacity planning is a problem related to customization and system optimization. The usual method is to find the best solution through an algorithm after establishing relevant mathematical models.

In theory, there are a number of possible power interface configurations for HESS. The dual-converter configuration is considered the most flexible and robust one regarding control independency, CTES life, DC bus voltage stability and HESS utilization. This configuration is adopted as the interface topology of the proposed integrated CESS (ICESS) investigated in this paper, as shown in Figure 3.

Figure 3. A dual-converter interfaced CESS and the MG system.

In the past decade, a lot of papers discussing the development and application of CESS have been published in the literature [6]. In [6], a comprehensive review on recently published papers investigating feasible topologies, usable voltage ranges, capacity utilization factors, and application cases of CESS is presented with brief comments. In the aspect of connecting the CESS with the DC bus, passive connection of both battery and supercapacitor to the system is the simplest topology [7]. It has been proved that this simple topology can effectively suppress transient load current induced by pulse load conditions with a relatively low cost; however, the system current will be drawn from or feed into the two storage units based on their respective internal impedances. This significantly limits the transient power handing capability of the supercapacitor unit. To improve the abovementioned shortcoming of passive connection and make better use of the storage units in a passive connected CESS, power electronic converters can be used to allow the real-time power flow to be actively controlled. To balance the complexity of controlling two converters and system cost, in a semi-active CESS topology, only one of the two ESS elements is actively controlled [8]. In a semi-active CESS topology, only one of the storage units in CESS (either battery unit [9] or
super capacitor unit [10]) is interfaced to the DC bus via a bidirectional DC/DC converter. To further improve the overall system performance and enhance the operating flexibility of the CESS a full active CESS topology was proposed [11]. In a full active CESS topology, both the power flow of battery and supercapacitor can be actively controlled via properly designed DC/DC converters. It should be noted that the performance of a full active CESS system is extremely dependent on the performance of power flow control systems designed for the DC/DC converters [12,13]. In regard to CESS applications, Hajiphashi et al. [14] reviewed on CESSs for MG applications regarding power dispatch, RE power smoothing, PQ improvement, and ancillary service. Chen et al. [15] proposed model predictive control-based multi-time-scale optimal dispatch for a flexible traction power supply system based on a back-to-back converter, a battery-SC CESS, and PV generation. In [16], a novel adaptive fuzzy logic controller algorithm was proposed for the power management of a standalone DC MG integrated with solar PV generation and a CESS based on a battery and multiple SCs. It was pointed out that conventional fuzzy logic controllers for MG power sharing might cause over- or under-utilization of the CESS in a standalone MG because it only considered one ESD at a time. A leaderless consensus protocol was utilized in [17] for distributed cooperative control of multiple battery-SC CESSs in a DC MG with multiple PV generators. Nakka and Mishra [18] used droop and virtual inductance control for the power sharing among inverters as well as frequency regulation in an islanded hybrid AC/DC MG with PV generation and a battery-SC CESS. In [19], the fluctuation of grid-connected WT generation output was dealt with a battery-SC CESS and an adaptive SoC range control based on the concept of virtual capacity. Rain flow counting-based capacity planning and optimization of a battery-SC CESS for PV-WT generation smoothing in a complex grid-connected regional integrated power system based on wavelet packet decomposition was studied in [20]. Kumar et al. [21] studied the design, operation, and control of a vast islanded DC MG integrated with solar PV-WT generation, battery-SC CESS, step changing DC loads, and single-phase linear, nonlinear, and three-phase inductive AC loads. In [22], a nonlinear integral backstepping control was proposed for a DC MG with solar PV-WT generation and battery-SC CESS. Only local information was required for the control. Simulation results showed that the proposed control outperformed adaptive sliding mode control. A hybrid AC/DC MG with solar PV-WT generation and battery-SC CESS is controlled with a comprehensive inertial control strategy [23]. Local inertia support and cross-grid inertia support both utilized the rotational kinetic energy of the WT generators (WTGs), yielding satisfactory performance in different operating scenarios regarding load changes. Armghan et al. [24] considered the application of a battery-SC HESS for a grid-connected hybrid AC/DC MG with WT and FC generation for both grid-connected mode and islanding mode.

In the above reviewed papers, software simulation has been commonly used as the investigation method. The schematic design methods of the required controllers for the most powerful full active CESS topology using two actively controlled converters still call for investigation. This paper aims to explore how to appropriately design the required controllers and apply them to different types of ESSs in the CESS for achieving reliable and flexible control requirements. Detailed design procedure of controllers along with both simulation studies and hardware tests are carried out in this paper, in which two typical application scenarios, peak demand shaving and power generation smoothing, of a CESS in a grid-connected microgrid are presented. The complete MG system architecture used includes WT and solar PV power generation units, CESS unit, grid-tie inverter, AC and DC loads, and grid as shown in Figure 3. In the following sections of this paper, the ICESS functions and typical application modes are firstly described in Section 2. The third section presents the controller design of the required power converters. The performance of the designed control system is verified using simulation analyses and hardware implementation tests respectively presented in Sections 4 and 5. Finally, this paper is concluded in Section 6.
2. Functional Planning and Hardware Design of ICESS

2.1. Functional Arrangement

In this paper, a small-scale grid-connected MG including WT and solar PV power generation systems is used as an example for the analysis and demonstration of two advanced ICESS operation modes (Mode 1 and Mode 2). In the following subsections, the two operating modes and the related control schemes used for the real-time energy management of MG will be briefly described.

2.1.1. Operation Mode 1: Peak Demand Shaving

The power demand of general loads varies between peaks and valleys depending on conditions such as day and night and seasons. Therefore, power companies tend to set timely power rates based on different power supply costs, which is called the time price. In terms of user benefits, shifting the electricity consumption during peak hours to off-peak hours according to the time price can greatly save electricity costs; from the perspective of power companies, peak demands can be decreased, reducing the demand in constructing new power plants and supply costs. However, even if the electricity consumption habits can be adjusted, users cannot completely avoid electricity consumption during peak hours. If there are other electricity sources, the more expensive utility power during peak hours can be better avoided. The primary condition for the operation of this operating mode is that the ETES has sufficient power, and the solar PV/WT power generation conditions exist at the same time slot. In this case, the power demand of the loads can be supplied first by solar PV/WT power generation, and the excess power is used to charge the battery; when the power generation is insufficient, the battery will make up for it. In this way, users can avoid using utility power at peak hours and achieve the goal of zero peak power demand.

2.1.2. Operation Mode 2: RE Power Generation Smoothing

Under the change of external power generation conditions, if solar PV/WT power generation is operated in MPPT mode, the output power will inevitably change drastically. The purpose of this control function is to reduce the impact of solar PV/WT power generation on the connected grid and improve the stability and PQ of the MG, which is achieved by fast charging/discharging of the ICESS. In this case, the bidirectional DC-DC converter performs charge and discharge control according to the power smoothing command within the safety SoC range of the ICESS; the grid-tie inverter is responsible for maintaining DC bus voltage at 200 V. In this way, the active power at both ends can be automatically and bidirectionally balanced, achieving the goal of solar PV/WT output power smoothing.

2.2. System Architecture

Figure 4 shows the proposed ICESS hardware system architecture discussed in this paper. The blue blocks are the ICESS units designed and demonstrated. To build an experimental system, a 96 V/14 Ah battery bank, a 117.6 V/14.28F SC bank, a 1 kVA bidirectional single-phase grid-tie inverter, and two 1-kW bidirectional DC-DC buck-boost battery/SC charging/discharging converters are practically constructed with switching frequency of 100 kHz. The solar PV/WT generation with maximum power point tracking (MPPT) functions is emulated using a programmable DC power supply. Related system parameters and specifications are listed in Table 1.
Figure 4. Proposed ICESS hardware architecture.

Table 1. Specifications of proposed ICESS and related equipment.

| Item                                      | Specification                                      |
|-------------------------------------------|----------------------------------------------------|
| DC bus voltage $V_{bus}$                  | 200 V                                              |
| Grid voltage $V_s$                        | 110 Vrms/60 Hz                                     |
| Maximum power output                      | 1 kVA                                              |
| Battery bank                              | 96 V/14 Ah                                         |
| (8 batteries in series)                   | (12 V/14 Ah each)                                  |
| SC bank                                   | 117.6 V/14.28 F                                   |
| (42 SCs in series)                        | (2.8 V/600 F each)                                 |
| Battery charging current $I_b$            | Up to 10 A                                         |
| PV/WTG emulator                           | PV: up to 600 W/35–40 V                            |
| (Programmable DC power supply)            | WTG: up to 300 W                                   |
| (with MPPT)                               |                                                    |
| Single-phase full-bridge grid-tie inverter| 1 kVA/100 kHz                                      |
|                                            | (110 Vrms/up to 10 Arms)                           |
| Bidirectional DC-DC buck-boost battery converter | 1 kW/100 kHz                                   |
| Bidirectional DC-DC buck-boost SC converter | 1 kW/100 kHz                                   |
| Inverter output voltage requirements      | Voltage regulation error < 2%                     |
|                                            | THD < 3% (full load, resistive)                    |
|                                            | Inverter: >92% (resistive load)                    |
| Converter efficiencies                    | DC-DC converters: >92%                             |

3. Controller Design of ICESS Converters

3.1. Single-Phase Full-Bridge Grid-Tie Inverter

Single-phase grid-tie inverters generally adopt half-bridge or full-bridge architecture. The full-bridge architecture is adopted in this paper, as shown in Figure 5, including four power switches (Q1–Q4), an LC low-pass filter (LPF, L, and Cac), and a DC bus capacitor (Cdc). Sinusoidal pulse width modulation (SPWM) is used to control the power switches. Dual-loop control
architecture is chosen. Symbols first appearing in Figure 4 include the following: \( I \) denotes inductor current; \( k_{v1}, k_{v2}, \) and \( k_s \) denote the sensing scales of AC voltage, DC voltage, and AC current, respectively; \( v_{bus} \) and \( v_{bus}^* \) denote the feedback and command signals of DC bus voltage, respectively; \( i_l \) and \( i_l^* \) denote the feedback and command signals of inductor current, respectively; \( v_{\sin(\omega t)} \) denotes the synchronization signal provided by the phase lock loop (PLL) using grid voltage; \( v_{\text{con}} \) and \( -v_{\text{con}} \) denote SPWM control signals; \( R_L \) denotes DC equivalent load. Detailed specifications of this circuit including scaling factors for current and voltage sensors, the carrier amplitude set for SPWM and device values of output filters are listed in Table 2.

![Figure 5. Control architecture of single-phase full-bridge grid-tie inverter.](image)

| Item                              | Value               |
|-----------------------------------|---------------------|
| Carrier amplitude \( v_t \)       | 5 V                 |
| AC voltage sensing scale \( k_{v1} \) | 0.0062 V/V          |
| DC voltage sensing scale \( k_{v2} \) | 0.012 V/V          |
| AC current sensing scale \( k_s \) | 0.05 V/A            |
| DC bus voltage variation limit    | 5%                  |
| DC bus capacitor \( C_{dc} \)     | 663 \( \mu \)F/300–400 V |
| Filter capacitor \( C_{ac} \)     | 10 \( \mu \)F/165–220 Vrms |
| Filter inductor \( L \)           | 97 \( \mu \)H       |

3.1.1. Inductor Current Controller

In Figure 5, both switching legs are controlled using PWM technique. Letting \( k_{\text{pwm}} = V_{\text{bus}}/v_t \), we can express the voltage across the inductor as follows:
According to Equation (1), the grid-tie inverter inductor current control loop can be drawn, as shown in Figure 6, where \( e \) denotes control error, \( \nu_{fb} \) denotes feedback control signal, \( \nu_{ff} \) denotes grid voltage feedforward control signal, \( G_i \) adopts a proportional (P) controller, whose proportional gain is \( k_i \).

\[
L \frac{dI_L}{dt} = k_{pwm} v_{con} - V_s
\]  

(1)

According to Equation (1), the grid-tie inverter inductor current control loop can be drawn, as shown in Figure 6, where \( e \) denotes control error, \( \nu_{fb} \) denotes feedback control signal, \( \nu_{ff} \) denotes grid voltage feedforward control signal, \( G_i \) adopts a proportional (P) controller, whose proportional gain is \( k_i \).

\[
\frac{i_L}{i_L^*} = \frac{k_i k_k k_{pwm}}{sL} = \frac{k_i k_k k_{pwm}}{s + \frac{k_i k_k k_{pwm}}{sL}} = \frac{u_i}{s + u_i},
\]  

(2)

where \( u_i \) denotes the bandwidth of the inductor loop, which is designed at one fifth of the switching frequency here, and thus we can obtain the P controller:

\[
k_i = \frac{u_i \times L}{k_k \times k_{pwm}} = \frac{1}{5} \times 100 k \times 2\pi \times 100 \mu \times \frac{0.05 \times 40}{0.05 \times 40} \approx 6.28.
\]  

(3)

3.1.2. DC Bus Voltage Controller

Considering the unity power factor, we can obtain the equivalent circuit of the grid-tie inverter with the AC power mapped onto the DC side, as shown in Figure 7, where \( I_L \) denotes load current, \( I_c \) denotes capacitor current, \( I_{bus} \) denotes DC bus current, \( v_s \) and \( i_s \) denote grid voltage and current, respectively.

\[
R_{l} \frac{1}{C_{dc}} \frac{1}{V_{bus}} V_s(t) = V_s \sin(\omega t)
\]

\[
i_s(t) = I_s \sin(\omega t)
\]  

Figure 7. Equivalent circuit of grid-tie inverter with AC power mapped onto the DC side.

In Figure 6:
According to Equation (4), the grid-tie inverter DC bus voltage control loop can be drawn, as shown in Figure 8, where \( i_* \) denotes grid current feedback, and \( G_v \) adopts a type II controller. Here, it is assumed that \( i_* \) is much greater than the voltage loop bandwidth \( (i_v) \).

\[
\begin{align*}
V_{bus} &= \frac{k_{dc}}{C_{dc}} \frac{1}{s + \frac{1}{R_L C_{dc}}} = \frac{k_{dc}}{s + a}.
\end{align*}
\]

(4)

Figure 8. Grid-tie inverter DC bus voltage control loop.

In Figure 7, the transfer function of the plant can be expressed as follows:

\[
H_v(s) = \frac{k_{v2} k_{dc}}{k_{c} C_{dc} (s + a)}.
\]

(5)

Considering the influence of the secondary ripple of the DC bus voltage on the stability of the controller, the gain crossover frequency of the voltage control loop is set to 16 Hz, and the pole and zero of the controller are set at 180 rad/s and 30 rad/s, respectively, yielding the transfer function of the type II controller:

\[
G_v(s) = \frac{k_{2} (s + z)}{s (s + p)} = \frac{154.1 (s + 30)}{s (s + 180)}.
\]

(6)

Figure 9 shows the open-loop Bode plot of the DC bus voltage control loop. The phase margin is 65°.
3.2. DC-DC Buck-Boost Converter

The control interfaces for real-time bidirectional charging/discharging required by the battery and SC banks are identical, adopting the configuration of two-switch buck-boost converter, as shown in Figure 10, including two power switches (Q₁, Q₂), a low-voltage LC filter (L and C_LV) and a high-voltage capacitor (C_HV). This converter uses single-loop control architecture to control the inductor current. In Figure 10, the symbols appearing for the first time are as follows: \( V_b \) denotes ESD voltage, \( I_b \) denotes ESD current (equals to inductor current of the converter), \( i_b \) and \( i_b^* \) denote feedback and command signals of ESD current, respectively. The control command can be determined according to the SoC of the ESDs and system operation strategy for real-time control. Detailed system specifications including the maximum and minimum duty cycles respectively set for the boost and buck operating modes of ICESS converters, output current ripple limitation and filter parameters are listed in Table 3.

![Figure 10. Control architecture of buck-boost battery/SC converter.](image)

| Item                              | Value          |
|-----------------------------------|----------------|
| Maximum duty cycle, \( D \) (boost mode) | 0.85           |
| Minimum duty cycle, \( D \) (buck mode) | 0.15           |
| Current output current ripple limitation | 10%           |
| High-voltage capacitor \( C_{HV} \)       | 463 \( \mu \)F/300-400 V |
| Low-voltage capacitor \( C_{LV} \)      | 8.63 \( \mu \)F/250 Vrms |
| Filter inductor \( L \)              | 200 \( \mu \)H |

When operating in buck mode, the small-signal model of the buck-boost converter including the perturbation (the symbols with hats) can be expressed as follows:

\[
sL\hat{i}_b(s) = D\hat{V}_{bus}(s) + V_{bus} \hat{d}(s) - \hat{v}_b(s). \quad (7)
\]

According to Equation (7), the small signal model can be visualized, as shown in Figure 11:
Figure 11. Small-signal model of battery/SC converter inductor current.

Assuming that there is no perturbation on the DC bus, we can obtain the following:

\[
\frac{\hat{i}_b(s)}{d(s)} = \frac{V_{bus}}{sL(1+sC_{LV}R_L) + R_L} = \frac{V_{bus}}{s^2 + \frac{1}{R_LC_{LV}}s + \frac{1}{LC_{LV}}}. \tag{8}
\]

The inductor current control loop drawn according to Equation (8) is shown in Figure 12, where type II controller is adopted, and K-factor is used for quantification design.

Figure 12. Battery/SC converter inductor current control loop.

In Figure 12, the transfer function of the plant can be expressed as follows:

\[
H_i(s) = k_{PWM} \cdot G_{id} \cdot k_s = \frac{k_s \cdot V_{bus}}{v_i \cdot d(s)} = \frac{k_s \cdot V_{bus}}{v_i \cdot \frac{s + \frac{1}{R_LC_{LV}}}{s^2 + \frac{1}{R_LC_{LV}}s + \frac{1}{LC_{LV}}}} = \frac{10000s + 1.302 \times 10^8}{s^2 + 1.302 \times 10^4s + 5 \times 10^8}.
\]

The crossover frequency is set at one tenth of the switching frequency, which is roughly 62,832 rad/s, and thus the gain and phase at crossover frequency can be obtained:

\[
H_i(\omega_c) = \text{Gain}_i, \angle \text{Angle}_i
= 0.1811\angle -88.36^\circ. \tag{10}
\]

The required phase boost at crossover frequency is as follows:

\[
\text{Phase Boost} = \text{Phase Margin} - \text{Angle}_i - 90^\circ
= 60^\circ - (-88.36^\circ) - 90^\circ = 58.36^\circ.
\]

yielding the K-factor:
Thus, we can obtain the zero and pole of the type II controller:
\[
z = \frac{\omega_ci}{K_{\text{factor}}} = \frac{62832}{3.5291} \approx 17804 \text{rad/s};
\]
\[p = \omega_ci \cdot K_{\text{factor}} = 62832 \times 3.5291 \approx 221740 \text{ rad/s}.
\]
Required gain compensation at crossover frequency is as follows:
\[
k_i = \frac{p}{\text{Gain}_H} = \frac{221740}{0.1811} = 1.2244 \times 10^6.
\]
As a result, the transfer function of the designed controller can be obtained:
\[
G_i(s) = \frac{k_i(s + z)}{s(s + p)} = \frac{1.2244 \times 10^6 (s + 17804)}{s(s + 221740)}.
\]
Figure 13 shows the open-loop Bode plot of the inductor current control loop. As can be seen in Figure 13, the phase margin is 60°.

Figure 13. Open-loop Bode plot of battery/SC converter inductor current control loop.

4. Case Simulation

This section investigates the performance of the proposed ICESS in implementing various advanced system-wide functional operations and power charging/discharging control through the planning and simulation of application cases based on grid-connected solar PV/WT generation systems and the power control of MG.
4.1. Case 1: Peak Demand Shaving (Operation Mode 1)

In this mode, the DC load power demand is set to 300 W. The system calculates the charging/discharging command of the battery in real time according to solar PV and WT generation to achieve peak demand shaving requirements. The goal of this case is to realize the ideal zero power demand from the grid, that is, no electricity from the grid is used during peak hours. Figure 14 shows related power variation scenarios.

![Power change scenarios of case 1](image)

**Figure 14.** Power change scenarios of case 1: (a) Solar PV/WT generation; (b) load power; (c) ICESS power; (d) grid power.

Figures 15 and 16 show the simulation results without and with peak power shaving function, respectively. It can be known from the results that, if the solar PV/WT generation is less than 300 W, the system can discharge the ICESS battery to help supply the load demand; when solar PV/WT generates more than 300 W, the battery can absorb the excess power, and users can completely avoid using the utility power.

![Simulation results without peak demand shaving](image)

**Figure 15.** Simulation results without peak demand shaving: (a) Grid AC voltage and current/DC bus voltage/DC bus current/battery current/SC current; (b) feedback and command signals of grid AC current/DC bus voltage/battery current/SC current.
4.2. Case 2: RE Power Generation Smoothing (Operation Mode 2)

In this mode, the AC power injected to the grid is fixed at 200 W. Figures 17 and 18 show the simulation results without and with RE generation smoothing function, respectively. From the simulation results, it can be known that if the solar PV/WT generation is less than 200 W, the control system can instantly make up the power fed to the grid by discharging the ICESS battery; the excess power generated by solar PV/WT can be absorbed by the battery to achieve the purpose of smoothing the power fed to the grid.
Figure 18. Simulation results with RE generation smoothing: (a) Grid AC voltage and current/DC bus voltage/DC bus current/battery current/SC current; (b) feedback and command signals of grid AC current/DC bus voltage/battery current/SC current.

5. Hardware Implementation

Following the simulation of two typical application scenarios of the proposed ICESS presented in the previous section, considering the hardware circuit specifications and the test conditions of current laboratory equipment, a 1kVA ICESS hardware experimental system has been developed. A Texas Instruments (TI) digital signal processor (DSP) is used to realize fully digital integration and coordinated control of power converters in ICESS. The overall performance of the ICESS system is verified by experimental test results with the same system parameters and operating conditions presented in simulation cases. Figure 19 shows a photo of the complete hardware experiment environment.

Figure 19. Photo of complete hardware setup and experiment environment.
5.1. Case 1: Peak Demand Shaving (Operation Mode 1)

The condition of this case is the same as that of the simulation in Section 4.1, and the experimental results are used to analyze and compare with the simulation results. Figures 20–22 show the measuring results of the implementation. As can be seen from the measured waveforms, the same performance of peak demand shaving control as that of simulation is observed. In Figure 21, with the function of peak demand shaving being activated, the control goal of this control case, realizing an ideal zero power demand (Ch2) from the grid, is perfectly achieved. As shown in Figure 22, both the output currents of the battery (Ch3) and supercapacitor (Ch4) banks are stably regulated as commanded.

![Figure 20](image1.jpg)  
**Figure 20.** Implementation results without peak demand shaving: Grid AC voltage and current/DC bus voltage/DC bus current.

![Figure 21](image2.jpg)  
**Figure 21.** Implementation results with peak demand shaving: Grid AC voltage and current/DC bus voltage/DC bus current.
Figure 22. Implementation results with peak demand shaving: Grid AC voltage and current/battery current/SC current.

5.2. Case 2: RE Generation Smoothing (Operation Mode 2)

For comparison purposes, the system parameters and operating condition of this case are the same as that of the simulation case presented in Section 4.2, and the experimental results are used to check with the simulation results. Figures 23–26 shows the measured results of the implementation. As can be seen in 23–26, the same result of effective RE generation smoothing is observed. Figure 23 shows the waveforms of disturbed grid AC voltage and fluctuating current, DC bus voltage, and current injected to DC bus without smoothing control. The corresponding set of waveforms with the power smoothing control function being activated are shown in Figure 25. As can be seen in Figure 25, the waveforms of the AC voltage and current are perfectly stable and the designed real-time tracking control of the currents of the battery (Ch3) and supercapacitor (Ch4) banks in ICESS shown in Figure 26 is achieved.
Figure 24. Implementation results without RE generation smoothing: Grid AC voltage and current/battery current/SC current.

Figure 25. Implementation results with RE generation smoothing: Grid AC voltage and current/DC bus voltage/DC bus current.
6. Conclusions

This paper has briefly introduced the importance of ESDs in new power systems and focused on the relevant system characteristics, applications, and power control technologies of CESS. According to the operating conditions of the system, the advanced power control functions and application scenarios of the proposed ICESS have also been introduced. To fill the gap of lacking detailed design examples, especially with hardware verification of CESS systems, a review of the connecting topology and potential application of CESS, system modeling of converters, and systematic design method for controllers of various ESD converters have been presented in this paper. The overall system performance of the proposed SC-battery-based ICESS with the proposed control schemes has been simulated and analyzed with computer software through two typical control cases, peak demand shaving and power generation smoothing, of a grid-connected microgrid. In this paper, the control chip, TMS320F28335, is used as the control kernel to cooperate with the laboratory equipment to carry out the 1-kVA ICESS small-capacity experimental hardware verification. The simulation and implementation results of two typical control cases can prove that the proposed ICESS operation mode and designed digital controllers are feasible and effective. Evaluated from the application aspect, the proposed ICESS is very suitable for small and medium-sized DG systems, especially for optimal real-time control of power flow and energy efficiency. Further investigation on applying intelligent controllers with learning capability and capable of taking into account the states of charge of battery and supercapacitor banks to CESS is a potential future research direction on this topic.

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