Cumulative Fatigue Damage Balancing for Modular Multilevel Converter

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Abstract: With many advantages, modular multilevel convert (MMC) has been extensively used in high and medium voltage power transmission projects. The thermal performance and reliability of components in MMC are key issues in system operation. However, in the current research on the reliability of MMC components, there are few methods to improve service lifetime expectancy of the components. This paper proposes a balance control algorithm, based on the cumulative fatigue damage of components, feedback to the control terminal for allocating the sub-module (SM) operating state and generating trigger pulses. Finally, the effectiveness of the proposed algorithm is verified and discussed in case studies. It is found that this algorithm is able to improve the aging degree of components in the meanwhile, the improvement in MMC reliability comes at the cost of slightly increasing capacitor voltage fluctuations and total harmonic distortion (THD). Due to the limitation of capacitor thermal performance, SM capacitor banks become a weak link in MMC reliability.

Keywords: modular multilevel converter; cumulative fatigue damage; thermal stresses; reliability improvement; lifetime balancing control

1. Introduction

MMC is used in many medium and high voltage power occasions, which kept evolving with its unique modular structure and other technical advantages, and becomes the most popular voltage source converter [1,2]. MMC can realize DC/AC and AC/DC conversion. As long as the quantity of power modules is increased, the voltage capacity can be greatly improved, which can be well applied to high-level power transmission and distribution projects [3,4].

In China, MMC operating lifetime requirement is 45 years [5], but it is still doubtful whether it can be realized, due to its short operation time, single testing method, and lack of data. In fact, the survey found that the annual failure rate of the HVDC system is 20 times that of conventional transmission systems, 90% of which are caused by power electronic system failure [6]. Because the MMC circuit is constructed based on a large number of vulnerable SMs, its operation is susceptible to a certain SM failure. In engineering practice, although SMs on a bridge arm are connected in series, each SM of bridge arm may have certain differences as a result of various reasons [7,8], such as the installation position, capacitance tolerance, the surrounding environment, etc. From the datasheet of the capacitor manufacturer [9], it can be found that the capacitance value has an error of ±10%. That is to say,
components of the same model are subjected to dissimilar degrees of stress in each SM, which results in their differences about junction temperature and lifetime.

The current MMC converter control strategy has two principal purposes: controlling the output voltage and improving the waveform quality. The control strategy mainly focuses on the modulation algorithm and the SM capacitor voltage control. At present, modulation algorithm principally includes pulse width modulation (PWM) and nearest level modulation (NLM) [10–12]. In fact, the output waveform of MMC is getting closer and closer to the modulated sine wave, by controlling the operation state of SM (inserted, bypassed). SM capacitor voltage control is mainly to ensure the balance of the SM voltage [13–15]. This is a precondition for the normal operation of the converter, which is closely related to the output voltage quality of the converter. Two capacitor voltage balancing methods are: (1) The principle of the capacitor voltage complete sorting strategy (2) The capacitor voltage balancing strategy sorted by state.

In the last five years, several studies have been conducted by researchers to improve MMC thermal performance. Frederik Hahn et al. proposed an active thermal balancing algorithm. It reduced the difference in the junction temperature spread among SMs in all operating conditions without deteriorating the performance of the system [16]. Mohammad Kazem Bakhshizadeh et al. had studied the effect of circulating current in the MMC on the loss and the thermal load of power semiconductor devices, and presented a new control strategy using circulating current to limit the magnitude of thermal cycling [17]. Jing Sheng et al. used symmetrical modulation of active bypass based on half-bridge SM (HBSM) and active thermal control strategy based on full-bridge SM (FBSM), which decreased the junction temperature of the most stressed device in HBSM and FBSM [18]. R. Picas et al. provided a new algorithm to achieve even distribution of switching losses and conduction losses in MMC, which is at the cost of slightly increasing capacitor voltage ripple [19]. Although these methods can improve MMC thermal performance, they are all indirect processes to achieve extended component lifetime. From the existing methods, the thermal performance of MMC can be improved by controlling the components’ junction temperature and power loss. Nonetheless, the cumulative fatigue damage of each SM in MMC is a blind spot during operation. They did not quantify component lifetime extension in their analysis and research. Because in most methods, component lifetime is only the final result rather than control target.

Now with the large-scale utilization of MMC, people urgently need a real time way to judge and decide the next operation based on the current SM lifetime. The operation state distribution of each SM is also not uniform. Some SMs have been inserted for a long time, while others have been in a bypassed state. Owing to the uneven distribution of SM operation state, it is found that the lifetime of SMs is greatly different. This can be said to be a waste of resources for some SMs with low utilization time. As is well known, the component that fails first is the key to the lifetime of the system. If the lifetime of the component with the shortest lifetime can be extended, this is of great significance to the system reliability. In addition, it is still a challenge to effectively improve capacitor thermal performance and reliability.

For this purpose, the contributions of this paper are mainly as follows: First of all, a lifetime balancing algorithm based on MMC cumulative fatigue damage is proposed. The algorithm combines the real time rain-flow counting algorithm to feed back the SM fatigue damage to the control terminal in real time. Fatigue damage of components will no longer be a blind spot for control. Secondly, the difference among the cumulative fatigue damage balancing algorithm and other algorithm is compared and analyzed, as well as the issues to be considered when putting it into practical application. In addition, the selection of algorithm parameters is briefly introduced. The cumulative fatigue damage balancing algorithm can coordinate the lifetime of multiple components, and it is found that the capacitor becomes the weak link, which limits the reliability of the MMC.

The rest of the paper is structured as follows. The Section 2 introduces the SM fatigue damage analysis. The Section 3 describes the principle and process of the novel control algorithm. The Section 4 analyzes and verifies the effectiveness of the novel proposed algorithm. The conclusion is at the end of the article.
2. SM Fatigue Damage Analysis

2.1. SM Fatigue Damage Analysis Flow

The SM fatigue damage analysis first needs to collect the mission profile of MMC operation [20,21]. Including power loss, device junction temperature, device related thermal parameters, etc. Among them, some parameters are not directly available. The junction temperature of insulated gate bipolar transistor (IGBT) and diode can be obtained by the Foster thermal circuit model, and the hot spot temperature of the capacitor can be got by the equivalent thermal circuit. Then the thermal cycles of IGBT and diode are calculated by the rain-flow counting algorithm [22,23]. For capacitors, the duration of their hot spot temperature needs to be recorded and the final cumulative fatigue damage and lifetime are calculated and analyzed by the Miner formula. The fatigue damage analysis flowchart is shown in Figure 1.

![Figure 1. SM fatigue damage analysis.](image)

2.2. IGBT, Diode Fatigue Damage Analysis

During normal operation of MMC, the electrical characteristics of SM components cannot reach zero power loss. It takes a certain time when the component is turned on or off, and there is a saturation voltage in the component during the on-state. In the off state, current is still present in the device [24]. The power loss of the SM device mainly includes the IGBT conduction loss, switching loss, diode conduction loss, and reverse recovery loss [25]. These parameters need to be calculated by the datasheet provided by the manufacturer. The calculation formula of power loss is as follows:

\[ P_{T,\text{loss}} = P_{T,\text{cond}} + P_{T,\text{sw}} \]  
(1)

\[ P_{D,\text{loss}} = P_{D,\text{cond}} + P_{D,\text{rec}} \]  
(2)

where, \( P_{T,\text{cond}} \) and \( P_{T,\text{sw}} \) are on-state loss and switching loss of the IGBT, respectively. Similarly, the total loss of the diode \( P_{D,\text{loss}} \) is the sum of on-state loss \( P_{D,\text{cond}} \) and reverse recovery loss \( P_{D,\text{rec}} \).

Equations (1) and (2) are conducted in detail based on circuit theory in [21,26].

In order to obtain component junction temperature in real time, the junction temperature calculation method based on thermal network analysis is widely used at present. Here, a fourth-order Foster thermal circuit model is used [26,27]. The equivalent thermal circuit model is shown in Figure 2. The power losses of the IGBT and diode are used as the current source of the Foster model. The heat transfer process between the PN junction and the device case is represented by thermal impedances \( Z_{\text{thjC-T}} \) and \( Z_{\text{thjC-D}} \). The thermal impedances of IGBT and diode \( Z_{\text{thjC-T}} \) and \( Z_{\text{thjC-D}} \) can be equivalent to thermal resistance and thermal capacitance in parallel. And \( R_{\Omega} \) and \( \tau_{\Omega} \) represent the thermal resistance, and thermal capacitance of the IGBT, \( R_{Di} \) and \( \tau_{Di} \) represent the thermal resistance and thermal capacitance of the diode, respectively. The heat transfer process between the device case and the heat sink can use the thermal impedance \( Z_{\text{thCH-T}} \) and \( Z_{\text{thCH-D}} \). \( Z_{\text{thsa}} \) is thermal impedance of the heat sink. \( T_{\text{sa}} \) represents the heat sink junction temperature. According to the thermal equivalent
network of IGBT module, the steady-state average junction temperature of IGBT and diode, $T_{T,j}$ and $T_{D,j}$, can be expressed as

$$T_{T,j} = P_{T,loss} \left( \sum_{i=1}^{4} R_{T,ici} + R_{T,ci} \right) + T_h$$

(3)

$$T_{D,j} = P_{D,loss} \left( \sum_{i=1}^{4} R_{D, dici} + R_{D,cd} \right) + T_h$$

(4)

$$T_h = \left( P_{T,loss} + P_{D,loss} \right) R_{ha} + T_a$$

(5)

![Foster thermal model for insulated gate bipolar transistor (IGBT) and Diode.](image)

**Figure 2.** Foster thermal model for insulated gate bipolar transistor (IGBT) and Diode.

Specific thermal resistance parameters can be found in the datasheet provided by the manufacturer [28], see Table 1 for details.

| Device | Parameter | Value       |
|--------|-----------|-------------|
| IGBT   | $R_i$ (K/kW) | 1.631 | 4.286 | 2.089 | 0.588 |
|        | $\tau_1$ (ms) | 2.986 | 48.79 | 364.8 | 3593 |
| Diode  | $R_i$ (K/kW) | 2.441 | 4.23  | 5.055 | 2.253 |
|        | $\tau_1$ (ms) | 2.434 | 36.51 | 116.2 | 1641 |

Table 1. Specifications of the Foster model.

After the junction temperature sequence of the device is obtained, the fatigue damage is calculated by the rain-flow counting algorithm. The detailed steps of the real time rain flow algorithm can refer to the literature [22]. The number of power cycles to failure $N_f$ can be approximated by (6). $N_f$ is related to the average junction temperature $T_{jm}$ and the fluctuation of the junction temperature $\Delta T$ [18]. Where the constants $\alpha, \beta$ have to be fitted by cycling tests.

$$N_f = \alpha \cdot (\Delta T)^\beta \cdot e^{E_a / (k_B T_{jm})}$$

(6)

The cumulative fatigue damage ($DT$) of components can be obtained by adding the stresses experienced by devices under different conditions through the Miner rule [29].

$$DT = \sum_{i=1}^{\infty} \frac{n_i}{N_{f,i}}$$

(7)

In this equation, $N_{f,i}$ is the number of cycles to failure corresponding to the $i$th thermal cycle, and $n_i$ is the number of the $i$th thermal cycles. The device lifetime is the reciprocal of the cumulative damage for the whole year.

2.3. Capacitor Fatigue Damage Analysis

Unlike IGBTs and diodes, the lifetime of the capacitor is related to its hot spot temperature. According to the loss characteristics of the capacitor [27,29], the capacitor power loss is the sum of the losses caused by the different frequencies’ currents [23,24].
Figure 3a is a simplified equivalent circuit model of a capacitor. It consists of an ideal capacitor $C$, an equivalent series resistance (ESR, $R_{ES}$), and an equivalent series inductance (ESL, $L_{ES}$). Figure 3b is a thermal model of a capacitor. This model describes the relationship between power loss and hot spot temperature. Among them, $R_{ha}$ represents the thermal resistance of the capacitor, and the thermal capacitance $C_{hc}$ and $C_{ca}$ can ignore their influence when calculating the steady-state capacitance hot spot temperature.

![Thermal model of capacitor](image)

**Figure 3.** Thermal model of a capacitor: (a) Simplified equivalent circuit model and (b) thermal equivalent model.

According to Figure 3b, the hot spot temperature of the capacitor under steady-state conditions can be expressed as (8).

$$T_h = P_{c,loss}R_{ha} + T_a$$  \hfill (8)

$$R_{ha} = R_{hc} + R_{ca}$$  \hfill (9)

The lifetime model of the capacitor can be used as follows [30]:

$$L(T_h) = L_0 \left( \frac{V}{V_0} \right)^{-n} \cdot \frac{t_0 - T_h}{T_0 - T_h} \cdot 2^{-\frac{1}{m}}$$  \hfill (10)

In the formula, $V$ is the actual voltage of the capacitor; $V_0$ is the test voltage. $L(T_h)$ is the predicted lifetime of the capacitor at a specific hot spot temperature $T_h$; $L_0$ is the lifetime under test conditions; $T_h$ is the actual hot spot temperature of the capacitor (in K); $T_0$ is the hot spot temperature of the capacitor under the test condition. $\alpha$ is the voltage stress index. For MPPF-Caps, the value of $n$ is 7–9.

This paper uses the cumulative damage theory to quantify the lifetime of SM capacitor under variable operating conditions: it is considered that capacitor age under electrothermal stress, and the fatigue damage accumulates with use time. In a small time interval $\Delta t$, which is the temperature sampling period, it is considered that the capacitor’s electrothermal stress is unchanged. According to Miner rule, the cumulative fatigue damage of the capacitor throughout the year is the sum of the fatigue damage caused by the impacts at all sampling points.

$$D = \sum_{j=1}^{N_j} \frac{\Delta t}{L[T_h(t)]}$$  \hfill (11)

### 2.4. Device Fatigue Damage Analysis

The three-phase MMC specified in Table 2 is chosen as the case study. Each phase of the MMC is divided into two upper and lower bridge arms, and a bridge arm consists of $N$ SMs. One SM can be regarded as a discrete voltage source, while SM and equivalent inductors are connected in series on the bridge arm. Each phase of the bridge arm outputs from the midpoint connected by the upper
and lower arms. By inserted and bypassed $N$ SMs in the upper and lower arms, the required voltage waveform is outputted.

Table 2. Specifications of the MMC system.

| Description          | Value | Unit |
|----------------------|-------|------|
| MMC rated power      | 20    | MW   |
| DC-link voltage      | 2000  | V    |
| SMs per arm          | 20    | -    |
| SM capacitance       | 0.013 | F    |
| Arm inductor         | 0.004 | H    |
| Arm resistance       | 0.1   | Ω    |
| Grid nominal frequency | 50  | Hz   |
| Grid voltage         | 1000  | V    |
| Grid inductance      | 0.0046| H    |
| Grid resistance      | 0.045 | Ω    |

In order to analyze the fatigue damage of SMs, a 21-level MMC simulation model was built in MATLAB/Simulink. MMC acts as an inverter to realize the function of DC/AC. The modulation of the model is NLM, which uses a capacitor-voltage balancing algorithm. MMC is symmetrical in three phases, each of which is 120 degrees different from each other, so the bridge arm of phase A is taken as an example. The other phases can refer to the same analysis method. IGBT is 5SNA 1800E330400 from ABB, and capacitor is Type 947D polypropylene film DC-link capacitors from Cornell Dubilier [9]. MMC is considered in steady-state operation, and the rated power is transmitted from DC to AC side. The power factor $\cos \phi$ is set to 0.95.

Simulink simulation results show that the device reaches its thermal steady-state in about 2 s. Figure 4 depicts the junction temperature fluctuation of all SMs, it can be seen that the thermal stresses on the 20 SMs are different even for the same components. The thermal performance of D1 and D2 is particularly noticeable. From the enlarged view, Some SM D1s can reach a maximum junction temperature $72 \, ^\circ\text{C}$. However, the highest junction temperature of some SM D1s is less than $68 \, ^\circ\text{C}$. D2 also has a maximum junction temperature $63.8 \, ^\circ\text{C}$ for some SMs, and around $60 \, ^\circ\text{C}$ for some SMs. For IGBT, T2 suffers from larger junction temperature fluctuations and average junction temperature. The overall junction temperature of T2 is 15–20 °C higher than T1. The reason for this phenomenon is not only related to the MMC mission profile, but also related to the operation state of the SM. However, in voltage balancing algorithms, the single factor that determines the operation state is the capacitor voltage amplitude. MMC control generally only involves the balance of the capacitor voltage, and rarely considers the balance of the SM operation state distribution.

![Graph](image)

(a) T1 junction temperature

Figure 4. Cont.
According to the simulation results, Figure 5 shows that for the same component in the first 10 s, 20 SMs have different degrees of fatigue damage. It can be seen that the differences between SMs of D1, D2 are large. At the same time scale, the cumulative fatigue damage of T2 is greater than that of T1. This also corresponds to the obvious differences in the junction temperature diagrams.

**Figure 4.** Components of SMs junction temperature.
the SMs of D1, D2 are large. At the same time scale, the cumulative fatigue damage of T2 is greater than that of T1. This also corresponds to the obvious differences in the junction temperature diagrams. Due to the unbalanced stress on the components, this will eventually lead to a certain difference in their lifetime. In order to more intuitively see the dissimilarity among components, Figure 6 uses the expected lifetime as an indicator to illustrate the cumulative fatigue damage of components. T2 is the most easily damaged component. The shortest lifetime is 25.63 years, and the longest lifetime is 46.44 years. What's even more remarkable is that the disparity between the shortest and longest lifetime of the component is huge. For example, the range of D1 is 85.37 years, and the range of D2 is 964.3 years.

Since it is the hot spot temperature that affects the capacitor lifetime, the average junction temperature within the sampling time 5 ms is taken as the hot spot temperature of \( \Delta t \). Capacitor hot spot temperature and cumulative fatigue damage are shown in Figures 4e and 5e. It can be seen that
the junction temperature difference of the capacitor is not obvious. Capacitors have a life span of 35.32–36.37 years.

According to the results of components and SM life expectancy analysis in Figures 6 and 7, it is found that there is a distinct lifetime imbalance among components. The T2 life expectancy is the shortest of the five components. From the simulation results, the SM life expectancy is determined by T2. In other words, the lifetime of the first damaged SM is 25.63 years, and the average lifetime of the SMs is 29.13 years. The shortest lifetime component, with the largest cumulative fatigue damage at the same time, is the key to affect the system lifetime. For other components with a longer lifetime, if they can be put into service more frequently, they will bear more of the stress and reduce the lifetime gap between them. This will extend the life expectancy of MMC.

![Image](image_url)  
**Figure 7.** SM lifetime expectancy.

3. Cumulative Fatigue Damage Balancing Algorithm

3.1. Algorithm Principle

Firstly, the temperature of the device is continuously collected and processed. And the real-time rain-flow counting algorithm is used to calculate the thermal stress of each component. Cumulative fatigue damage is also constantly updated. Then, MMC control terminal changes the inserted order of SMs according to each SM fatigue damage degree. The control system reduces the priority of the operation for the components with larger cumulative fatigue damage, and increases the priority of the components with less fatigue damage. Cumulative fatigue damage is inversely proportional to lifetime. This allows shorter lifetime components to have a longer rest time, while longer lifetime components can be fully utilized. By reasonably allocating SMs operation time, the utilization time of components is improved. Ultimately, achieve the effect of extending the components lifetime.

During normal operation of the MMC, the current path in the SM is shown in Figure 8. The capacitor in the SM is connected in series with T1, D1, and in parallel with T2, D2. From the path of current flow, it can be seen that when current flows T1 or D1, the capacitor is in the state of charging or discharging, respectively. Components connects in series withstand the impact of current at the same time, and the component junction temperature also increases. The SM has four operating states, in half of which the capacitor is in the inserted state. This means that the capacitor is subject to more stress than other
component due to its high utilization frequency. This also explains why the hot spot temperature and fatigue damage of each SM capacitor have little difference.

| Inserted(S=1) | Bypassed(S=0) |
|---------------|---------------|
| ![Insertion Diagram] | ![Bypass Diagram] |

**Figure 8.** Current paths in an SM during normal MMC operation.

Although the capacitor is one of the components in the SM, it is known from the previous analysis that their cumulative fatigue damage does not differ much. Therefore, the focus should be on thermal performance rather than cumulative fatigue damage imbalance. For this purpose, the thermal performance of the capacitor can be improved by appropriate modification control algorithm. The control method can be used to change the junction temperature to capacitor hot spot temperature by referring to the thermal balancing algorithm. The thermal performance of the capacitor is indirectly controlled by T1 and D1. This paper focuses on the method to balance the cumulative fatigue damage of the other four components.

In Figure 9, \( d \) is the number of SMs that change the operating state, \( n_{on} \) is the number of SMs that need to be inserted currently, and \( n_{on\text{-old}} \) is the number of SMs that need to be inserted at the previous moment. According to the operating principle of the SM, when the operation state of \( d \) SMs needs to be changed in the next control period.

![Algorithm Flowchart]

**Figure 9.** Algorithm flowchart.
If \( d \) is less than 0 and the bridge arm current is greater than 0, the objective function \( m_3 \) is calculated, and \( d \) SMs are inserted. The current flows through T1, and the capacitors of the SMs are discharged. If \( d \) is less than 0 and the bridge arm current is less than 0, the objective function \( m_4 \) is calculated, and \( d \) SMs are bypassed. The current flows through D2. If \( d \) is greater than 0 and the bridge arm current is greater than 0, the objective function \( m_1 \) is calculated, and \( d \) SMs are inserted. The current flows through D1, and the capacitors of the SMs are charged. If \( d \) is greater than 0 and the bridge arm current is less than 0, the objective function \( m_2 \) is calculated, and \( d \) SMs are passed. The current flows through T2. According to its objective function (12)–(15), which of the \( N \) SMs is determined to be in the \( n_{on} \) SMs. The specific workflow of the SM is shown in Figure 9.

When the MMC runs to time \( t \), it calculates the value of \( d \) at this time, which indicates that there are \( d \) SMs at this time that needs to change the working state. At this moment, it is necessary to determine the SM that changes the working state according to the value of the objective function (12)–(15) of the algorithm. In formulas, \( v_c \) is the capacitor voltage, \( DT \) is the cumulative fatigue damage of IGBT and diode, and \( \alpha \) is the weight. Considering the situation of the capacitor voltage and fatigue damage, the objective function values of the 20 SMs are sorted. The smaller the value of the objective function, the higher the priority of the SM to change the working state. Because the smaller the objective function, the smaller the difference between the capacitor voltage and the voltage boundary value, the smaller cumulative fatigue damage, and the less use of the component. Then, the first \( d \) SMs, with the smallest objective function value, are selected to switch the working components according to MMC operation requirements.

\[
\begin{align*}
    m_1 &= (v_c - v_{c,\min}) + \alpha_1 DT_{D1} \\
    m_2 &= (v_{c,max} - v_c) + \alpha_2 DT_{T2} \\
    m_3 &= (v_{c,max} - v_c) + \alpha_3 DT_{T1} \\
    m_4 &= (v_c - v_{c,\min}) + \alpha_4 DT_{D2}
\end{align*}
\]

3.2. Algorithm Control Flow

The control strategy based on cumulative fatigue damage balancing combines the fatigue damage analysis of components. It feedbacks the cumulative fatigue damage of each SM to the MMC control terminal in real time. It can be seen that on the basis of Figure 1, a feedback channel is connected from the component fatigue damage to the MMC control terminal.

After the power loss of the device is transmitted to the thermal model, the component junction temperature can be obtained. The junction temperature sequence of IGBT and diode is calculated by the real time rain-flow counting algorithm. The hot spot temperature of the capacitor can be got by the thermal model, and the system records the duration of the hot spot temperature. The cumulative fatigue damage of IGBT, diode, and capacitor are constantly updated after the component lifetime model is determined. The fatigue damage value of the component is transmitted to the algorithm solver. Then, comparing the number of SMs to be inserted at this moment with the number of SMs to be inserted at the previous moment, and determining the operation state of the SM. Finally, MMC control terminal completes the allocation of the SM’s operating state and generate trigger pulses, in the light of the current direction of the bridge arm, the SM’s capacitor voltage and the device fatigue damage.

4. Case Study

4.1. Parameter Settings

To avoid more distractions, the following aspects should be considered: (1) capacitor rated voltage and voltage protection; (2) IGBT collector-emitter voltage and voltage protection; (3) the current cumulative fatigue damage of components.

To determine the weight \( \alpha \) in the objective function, this paper makes the following assumptions. Because T2 is the most easily damaged device, the main consideration for coefficient selection is the
lifetime of T2. When the coefficient $a$ is set to a different value, we can see the life expectancy of T2 from Figure 10a. It can be seen from the figure that when the coefficient is very small, the effect of improving the lifetime of T2 is not obvious. The control parameter (cumulative fatigue damage) did not play a major role. As the coefficient increases, the lifetime of T2 also increases. When $a$ is greater than 10,000, because the cumulative fatigue damage is already in an absolutely large proportion, it is meaningless to continue to increase, and it is found that there is no significant change in the lifetime. So, here we focus on the situation when $a = 100, 1000, 10000$. Figure 10b shows the lifetime expectancy of five components when weight $\alpha$ is 100,1000,10,000. As can be seen from the figure, the average lifetime expectancy of five components is not much different. When $\alpha = 100$, D1 and D2 minimum lifetime expectancy is equivalent to that of $\alpha = 1000$. T1’s minimum lifetime expectancy is better than the other two. When $\alpha = 1000$, the maximum lifetime expectancy of T1 is less than the other weights, while the minimum lifetime expectancy is greater than or equal to the other weights. When weight $\alpha = 10,000$, the minimum lifetime expectancy of all components is less than other weights, except for D2. So, effect of $\alpha = 10,000$ is not as good as other weights. Since T2 is the most easily damaged component, the minimum T2 lifetime expectancy at $\alpha = 1000$ is greater than the other weights, which means that it is better to improve the MMC lifetime at $\alpha = 1000$.

![Figure 10. Weight comparison: (a) T2 lifetime expectancy; (b) Lifetime of five components.](image)

Based on this, this paper sets $\alpha = 1000$ for simulation. If the value is not appropriate, the effect of the algorithm will be affected. When the MMC is just put into operation, the $DT$ value of each component is very small. The MMC is mainly based on the capacitor voltage balance. As time increases, the proportion of $DT$ will increase, and the value of the weight will determine $DT$ to what extent is gradually working. This is consistent with the actual situation. At the beginning, because the equipment is brand new, the probability of component failure is low. At this time, the balance of capacitor voltage is more important. As the use time increases, people are more concerned about the reliability of MMC, and the influence of $DT$ will gradually increase.

4.2. Cumulative Fatigue Damage Balancing

In order to verify the correctness of the proposed active cumulative fatigue damage algorithm, a 21-level MMC three-phase simulation model is built in MATLAB/Simulink. The parameters of the MMC simulation model are set in Section 2.

Figure 11 shows the T1, T2, D1, D2, C fatigue damage of SMs. Under the proposed algorithm control, the fatigue damage of four components is decreased. Compared with Figure 5, it can be seen that the fatigue damage of the four components has been reduced to different degrees, expect for the capacitor. At the same time, T2 changed from $0.6-1.2 \times 10^{-8}$ to $4-8 \times 10^{-9}$, and D1 changed from $2.3-8.5 \times 10^{-9}$ to $2-6.5 \times 10^{-9}$. The effect of algorithm control on T2, D1 is intuitive, and the max
fatigue damage is reduced by 34.61% and 21.54%, respectively. This shows that the use of control algorithms can effectively increase components’ lifetime, which has a positive impact on the reliability of SMs. The maximum and minimum cumulative fatigue damage of SMs with and without the control algorithm are discussed, respectively, for purpose of comparing the control algorithm results more clearly. In Figure 12, red, blue, green, and cyan represent the four selected SMs. The solid line represents that none algorithm control is adopted, and the dashed line indicates that the control method is adopted. It can be seen that the control method has significantly reduced the fatigue damage of the most easily damaged SM (red line), T1 SM 10, T2 SM 18, D1 SM 15 and D2 SM 7 respectively reduced by 18.13%, 38.71%, 37.43%, 74.76%. After the control, the fatigue damage of the SM with the least fatigue damage (solid blue line) increases, indicating that by changing the SMs operation priority, the utilization time of vulnerable components could be reduced, and the utilization time of idle components could be increased. As MMC continues to operate, it can be found that the component with the largest fatigue damage has changed. The most vulnerable component is the green dashed line, at this stage, they are T1 SM 14, T2 SM 1, D1 SM 14, D2 SM 20. Due to the balancing effects of the algorithm, the component with the most fatigue damage will always change. It makes the stress of the component is no longer periodic and the component participates in the assigned task more reasonably.

Compared with Figure 7, Figure 13 shows that lifetime expectancy of 20 SMs has been significantly improved. In this case, the SM of minimum lifetime expectancy is SM 20, whose lifetime expectancy is 31.13 years. The average lifetime expectancy of the SM is 31.53 years. At the SM level, the SM lifetime increased by 21.46%.

4.3. Capacitor Thermal Performance Control

From the analysis of the capacitor thermal performance and control mechanism in the second section, it can be seen that the capacitors’ junction temperature and cumulative fatigue damage are not significantly different. So, the cumulative fatigue damage algorithm has no practical significance. In addition, the voltage fluctuation range of the SM is limited, and the hot spot temperature of the capacitor does not change much, which makes the capacitor a weak link in the reliability of MMC. From a practical application point of view, consider indirectly controlling the capacitor hot spot temperature by controlling T1 and D1 through thermal balancing method [7]. Since the thermal performance difference in capacitor is small, a SM is randomly selected from the 20 SMs for reference analysis.
respectively reduced by 18.13%, 38.71%, 37.43%, 74.76%. After the control, the fatigue damage of the SM with the least fatigue damage (solid blue line) increases, indicating that by changing the SMs operation priority, the utilization time of vulnerable components could be reduced, and the utilization time of idle components could be increased. As MMC continues to operate, it can be found that the component with the largest fatigue damage has changed. The most vulnerable component is the green dashed line at this stage, they are T1 SM 14, T2 SM 1, D1 SM 14, D2 SM 20. Due to the balancing effects of the algorithm, the component with the most fatigue damage will always change. It makes the stress of the component is no longer periodic and the component participates in the assigned task more reasonably.

Figure 12. Comparison of maximum and minimum cumulative fatigue damage: (a) T1 maximum and minimum cumulative fatigue damage; (b) D1 maximum and minimum cumulative fatigue damage; (c) T2 maximum and minimum cumulative fatigue damage; (d) D2 maximum and minimum cumulative fatigue damage.

Compared with Figure 7, Figure 13 shows that life time expectancy of 20 SMs has been significantly improved. In this case, the SM of minimum lifetime expectancy is SM 20, whose life time expectancy is 31.13 years. The average lifetime expectancy of the SM is 31.53 years. At the SM level, the SM lifetime increased by 21.46%.

Figure 13. SM lifetime expectancy.

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It can be seen from Figure 14 that after the balance algorithm is adopted, the hot spot temperature of the capacitor is reduced. Within the same two sampling time scales, the controlled hot spot temperature of the capacitors decreased by 2.27 °C and 2.35 °C. After the control, the capacitor hot spot temperature has decreased, which shows that controlling T1, D1 can change the thermal performance of the capacitor. This indicates that the thermal performance of the capacitor can be controlled by some methods. However, because T2 is the weakest link, and T1, D1 and the capacitor are interacted with each other. It is difficult for the five components to coordinate and control, so the control of the capacitor can be temporarily ignored.

Figure 14.
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![Capacitor hot spot temperature](image)

**Figure 14.** Capacitor hot spot temperature.

### 4.4. Thermal Balancing and Fatigue Damage Balancing

This paper compares and analyzes the lifetime expectancy results of thermal balancing algorithm [16] and the cumulative fatigue damage balancing algorithm. The lifetime expectancy results are shown in Figure 15. The lifetime expectancy of 5 components and 20 SMs is represented by sectors with different radii. The orange in the figure represents the thermal balancing algorithm, and the blue represents the cumulative fatigue damage algorithm. And the dashed semicircle represents the average lifetime expectancy of 20 components. The minimum and maximum lifetime expectancy are marked on the graph.

For T1, the average lifetime expectancy, minimum and maximum lifetime expectancy of the fatigue damage balancing algorithm are greater than the thermal balancing algorithm. It indicates that the effect of fatigue damage balancing algorithm is slightly better than the thermal balancing algorithm, and T1 has a greater lifetime expectancy. For T2, the average lifetime expectancy of the two algorithm is basically equal. But, the minimum lifetime expectancy of the fatigue damage balancing algorithm is slightly larger than thermal balancing algorithm, and the standard deviation of fatigue damage balancing algorithm is 8.12. This illustrates that the lifetime expectancy of 20 T2 using the fatigue damage balancing algorithm is more balanced. For diodes, the D1 and D2 average lifetime expectancy of the fatigue damage balancing algorithm is greater than the thermal balancing algorithm, and the minimum lifetime expectancy of the thermal balancing algorithm D1 is greater than the fatigue damage balancing algorithm.
In terms of the algorithm application, the thermal balancing algorithm only needs to collect the component junction temperature, while the cumulative fatigue damage balancing algorithm needs to process and calculate the junction temperature, so the fatigue damage balance algorithm takes longer time. But, for the control side of the thermal balancing algorithm, the component’s cumulative fatigue damage degree is unknown. To achieve the improvement of component lifetime, by improving component’s thermal behavior is only an indirect method. There are blind spots and limitations for the realization of the ultimate goal. The cumulative fatigue damage algorithm directly feeds the fatigue damage to the control terminal, grasps the usage of each component in real time, and adjusts the component operation state. In theory, this method can more directly and effectively control the components, and finally, achieve the effect of extending components’ lifetime.

4.5. System Efficiency and Application Challenges

It can be known from the previous analysis that the algorithm can increase the device service lifetime, but whether it affects the operation requirements of the system itself also needs analysis and research. MMC main function is to approximate the required voltage waveform through the capacitor voltage. So, the capacitor voltage is an important reference parameter. In this regard, Figure 16 indicates that with the balancing algorithm control, the range of the capacitor voltage fluctuation has increased, and the charge and discharge of the capacitor voltage has been expanded from the previous 910–1190 V to 900–1240 V. The results show that the capacitor voltage fluctuation ceiling increases and the lower limit decreases. The fluctuation range of capacitor voltage is larger than before. But, the increase of capacitor voltage fluctuation basically does not affect the MMC performance. The total harmonic distortion (THD) of the cumulative fatigue damage balancing algorithm is 4.34%, which is higher than that of the capacitor voltage balancing algorithm. The THD of the capacitor voltage balancing algorithm and the thermal balancing algorithm are 3.53% and 4.29%, respectively.
In terms of simulation efficiency, based on the Simulink platform, it takes 0.013 ms to call the capacitor voltage balancing control algorithm, while the cumulative fatigue damage balancing algorithm takes 2.68 ms. This means that the realization of this algorithm has more stringent requirements for the control platform. When the number of SMs $N = 20$, the control cycle is usually 2 ms. FPGA can meet this requirement, and with the continuous update of technology, the algorithm can be called in a shorter time in the future.

Table 3 shows a comparison between the cumulative fatigue damage balance algorithm and the capacitor voltage balance algorithm. In terms of cost, it requires more storage space and more computing power than the voltage balancing and thermal balancing algorithm. This also means that it requires a higher configuration of hardware and software. Compared with the voltage data obtained from the voltage sensor, the algorithm also needs to calculate the junction temperature and cumulative fatigue damage in real time. This requires additional equipment investment and larger memory. Fortunately, the real time rain-flow algorithm only needs to collect the extreme points in the junction temperature rather than the whole junction temperature waveform. This takes a lot of pressure off the storage devices.

Table 3. Comparisons of balancing methods.

| Method                                    | Total Harmonic Distortion | Voltage Fluctuation | Service Lifetime | Hardware | Software |
|-------------------------------------------|---------------------------|---------------------|------------------|----------|----------|
| Capacitor voltage balancing [13]         | Lowest                    | Lowest              | Short            | Lowest   | Lowest   |
| Thermal balancing [7]                    | Middle                    | Highest             | Middle           | Middle   | Middle   |
| Cumulative fatigue damage balancing      | Highest                   | Middle              | Long             | Highest  | Highest  |

In practical engineering applications, the cumulative fatigue damage balancing algorithm is suitable for MMC projects with high reliability requirements and stricter requirements for SMs service lifetime. It can sacrifice less than 1% of THD to maximize the lifetime of SMs. In addition, it should be applied to small-scale MMC projects, due to equipment and cost reasons.

5. Conclusions

The fatigue damage distribution of the SMs and their power semiconductors in an MMC has been investigated. It has been demonstrated that the fatigue damage, due to various reasons, is unbalance
among the SMs so that the SM lifetime is different. For balancing the SM lifetime, a cumulative fatigue damage balancing algorithm has been proposed to reduce the difference of SM cumulative fatigue damage and lifetime. The control algorithm enables the system to grasp the reliability of each SM in real time and feed it back to the control terminal for allocating the SM’s operating state and generating trigger pulses. Finally, the proposed model is verified in the case study.

Finally, three important conclusions can be drawn as follows:

1. By balancing the fatigue damage, SM resources are fully utilized so that the MMC lifetime can be effectively extended. In the case, the lifetime of the SM increased by 21.46%.
2. The thermal performance of the capacitor can be improved indirectly through the control of T1 and D1. If the capacitor is added into the control link, the control difficulty will be increased, and it is difficult to coordinate the five components. So, if the weakest component is not capacitor, it can be temporarily ignored.
3. The cumulative fatigue damage among components is achieved by sacrificing a certain degree of capacitor voltage fluctuations, which may have a certain impact on the capacitor output voltage. In addition, the algorithm has higher requirements on the hardware and software of the control system, and the cost is also higher.

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