ABSTRACT
We found that transistors consisting of the gate insulation film exhibit bi-directional transfer characteristics, and when the gate voltage varies from negative to positive, drain current represents the values of positive and negative current, respectively. If the gate voltage is 0 V, it is confirmed that tunneling occurs and the resistance is zero. The drain voltage is low, the resistance is reduced. As the drain voltage increases, the resistance increases. We observed a shift in the \( I_{DS} - V_{GS} \) curve. The current generated through the gate insulation is the surface current caused by the antimatters, and quantum anomalous Hall effect of \( I_{DS} - V_{GS} \) transmission characteristics was observed even in the absence of applied magnetic field. It is shown that \( R = 0 \) is a phase-isolator with excellent insulation properties of the SiOC thin film. The reason for the increased conductivity of the 100 \(^\circ\) C heat-treated thin film is due to the spin current, and the thin films at other temperature is due to the surface current.

Keywords: Phase isolator, SiOC, Magnetoresistance, Quantum anomalous Hall effect

1. Introduction

Electromagnetic energy is generally applicable in nearly all electronic devices regardless of its size of scale down. Smaller sizes face technological limitations, where commonly known physical phenomena are not observed. The physical world can be understood through classical physics and quantum physics, and particles and mass are the starting points for the general physical phenomenon in which physics begins [1-4]. The electron and spin are the same amount of energy, but the sources of energy are very different. The electron is a particle and comes from electrical energy, the spin is an importer and comes from magnetic energy. It is traditional physics that electrons move, and if electrons fail to move, they must be interpreted in quantum physics. This is consistent with the difference between omic and magnetic resistance. Particles have mass, and as a result, they follow laws of energy conservation. There is something that has the energy that is non massive antiparticle, but it is a photon (light). Light does not have mass but carries energy. To explain this, antiparticles were introduced. The spin current is not a particle but has energy because it has no mass. Light, photon, and proton are called antimatters, and because they can be interpreted mathematically by applying energy conservation laws only when they have mass, the mass of the antimatter was applied to the mass of electrons. As a result, matter analysis is general physics, and antimatter analysis is quantum physics. Classical physics cannot explain the behavior of very small particles and we need to employ quantum physics [5-8]. Electromagnetic resistance could be either positive or negative. Positive resistance can be described using ohmic resistance characteristics. However, nothing is known about negative resistance. Surface current consists of spin current and charge current. The source of the current is in electrons and spins, and the direction of electrons and spins in a two-dimensional structure is in the opposite direction because they originate from electricity and magnetism. The resistance of electrical energy is positive ohmic resistance. The resistance of magnetic energy is negative magnetic resistance. Magnetoresistance is a phenomenon caused by a Schottky contact and potential barrier. This study focuses on the electrical features that occur in the negative resistance regime [9-12].
Figure 1 shows the correlation between magnetic resistance and conductivity in phase isolators, that phase-isolators is materials with spin currents in spite of insulators. The top figure shows current-voltage transfer characteristics as an electrical properties, and the bottom figure illustrates the relationship between magnetic resistance in an insulator and conductivity in a circle as fermi plane. If there is a positive resistance as shown in Figs. 1(a) and 1(b), the characteristics of n-type or p-type semiconductor characteristics are unipolar transfer characteristics above the threshold voltage. When resistance decreases, carriers tunnel, and when resistance increases, conductivity decreases. As shown in Figs. 1(c) and 1(d), the resistance becomes negative by the Schottky contact and the magnetoresistance appears. Quantum anomalous spin Hall effects explain the principle that magnetic resistance caused by a phenomenon in which spin current occurs without a magnetic field is made in a phase isolator. Smaller magnetoresistance results in higher conductivity and bi-directional transmission characteristics [13-15]. As the resistance becomes lower and lower, quantum tunneling by surface current occurs.

In this study, transistor using low-k (low dielectric constant materials) SiOC as gate isolators were manufactured to investigate the...
3. Results and discussion

causes of the current generation without channels. We performed X-ray diffraction (XRD) analysis to observe the phase transition in the SiOC thin film. The origin of surface current in phase-isolators and the causes of superior transfer characteristics in transistors using SiOC where phase transition occurs were investigated.

2. Experimental details

The SiOC as a gate insulator was prepared on the Si wafer by employing oxygen flow at the rate of 20 sccm using magnetron sputtering system at 250 RF power for 10 min. The source-target was the SiOC fabricated from Nano Technology, Inc., Korea. The as-deposited SiOC films were annealed at various temperatures in a vacuum with 3 mTorr. To make the transistors, the MIS (metal/insulator/Si) structured electrodes were made with aluminum to measure electrical properties using mask patterns of source, drain, and gate electrodes. The electrical characteristics of the device were measured using a semiconductor parameter analyzer (HP4155A).

3. Results and discussion

Figures 2(a)–(f) shows the transfer characteristics of $I_{DS}$-$V_{GS}$ as electrical properties of transistors due to temperature change in the region of $2 \leq V_{DS} \leq 10$ V. Depending on the characteristics of the phase-isolator, the current is $+I_{DS}$ when it is $-V_{GS}$, and $-I_{DS}$ for $+V_{GS}$, indicating bi-directional transfer characteristics. A significant change of a sudden increase in current takes place between 110 and 120 °C. The transfer characteristic at 110 °C was obtained because the electron polarization was the lowest at 110 °C due to the crystallinity by the low electron polarization during the heat treatment of SiOC, and the plot at 120 °C showed that the transfer characteristic indicates an increase in the charge current, where the phenomenon of electron polarization is not found.

Figure 3(a) shows an XRD pattern showing the crystallization of SiOC thin films. Most of the SiOC thin films exhibit amorphous properties, while SiOC thin films that are annealed at 110 °C are highly crystallized [16]. This implies that the phase state changes only post the 110 °C annealing process. $I_{DS}$-$V_{GS}$ curves were investigated in detail to understand changes in the electrical properties upon phase change at 110 °C.

Figure 4 shows the $I_{DS}$-$V_{GS}$ transfer characteristics of transistors according to temperature changes in the 0.001 V $\leq V_{GS} \leq$ 1 V region to observe the junction characteristics of SiOC thin films with various annealing temperatures. The upper panels show the transfer charac-
Figure 4. Transfer characteristics of $I_{DS}-V_{DS}$ of transistors with temperature variation in the range $0.001 \leq V_{DS} \leq 1 \, V$, upper panels represent high current at micro scale and bottom panels represent low current at nano scale, (a) 60, (b) 70, (c) 100, (d) 110, (e) 120, (f) 130, and (g) 150 °C.

Figure 5. Transfer characteristics of $I_{DS}-V_{DS}$ of transistors with $V_{DS} = 0.001 \, V$, (a) higher $I_{DS}$ at 100 °C annealed transistor than that at 110 °C annealed transistor, (b) Schottky contact at 110 °C annealed transistor, and (c) magnetic resistance characteristics of transistors annealed above 110 °C.

...and the Schottky contact was formed in a 110 °C heat-treated devices, and the transistor with the Schottky contact had electrically superior transfer characteristics.

Figure 6 investigated the transfer characteristics of $I_{DS}-V_{DS}$ to compare the 90 °C transistor and the 110 °C transistor with the Schottky contact. As shown in Figs. 6(a) and 6(b), 110 °C transistor had better $I_{DS}-V_{DS}$ transfer characteristics than that of 90 °C heat treated units. Figure 6(c) compared the $I_{DS}-V_{DS}$ characteristics of 100 and 110 °C elements by plotting them on a log scale. A 100 °C annealed device with a tunneling effect exhibited excellent transmission characteristics.

Figure 7 represents the logarithmic plots of the transfer characteristics of $I_{DS}$ measured in the $0.0001 \leq V_{DS} \leq 1 \, V$ region to investigate the switching characteristics of the transistors. Compared to the transmission characteristics of the 100 °C annealed transistor's $V_{DS} = 1 \, V$, it looks different. For further investigation, only the $I_{DS}$ curves for $V_{DS} = 1 \, V$ were classified and compared as shown in Fig. 8.

Figure 8 shows the logarithmic plots of heat-treated transistors above 90 °C at $V_{DS} = 1 \, V$. The switching characteristics of the transistors treated at 100 °C, forming the Schottky contact were found to be the best. At temperatures beyond 100 °C, the $I_{DS}$ current showed a decreasing magnetic resistance characteristic. The magnetic resistance characteristic of phase-isolator implies a quantum anomalous Hall phenomenon, which is a phenomenon where current flows even without magnetic fields. The quantum hole effect has a singularity of supercurrent. XRD and Snook Joint were used as a way to find out the uniqueness of supercurrent, and it was shown that super current was flowing in a 100 °C heat-treated thin film with relatively high current flow. The bi-directional transfer characteristic of transistors is a quantum tunneling phenomenon. Tunneling phenomena include electronic tunneling and quantum tunneling.
high concentration doping does not have magnetic resistance characteristics, indicating one-way transfer characteristics. Quantum tunneling, however, showed bi-directional transfer characteristics as it has magnetic resistance characteristics. A phase insulator with magnetic resistance due to a potential barrier has a spin current flowing only on the surface [10]. Surface current is characteristic of quantum anomalous Hall phenomena. Here, the quantum anomalous Hall phenomena and quantum tunneling phenomena are the same. The properties of transistors using the phase-isolated SiOC thin film were superior in the case of Schottky contacts.

The reason for observing bi-directional transfer characteristics in transistors that use gate insulation membrane without channels is the flow of the surface current of phase isolation. As the characteristics of the phase-isolator allow for magnetic resistance, and in thin-film transistors, the gate insulation has magnetic resistance, the electrical efficiency of SiOC thin film where phase transition occurs was observed to increase. The tunneling effect or superconducting phenomenon occurs where resistance is reduced. Resistance and conductivity are inversely related to each other, and positive and negative resistance are symmetrical to each other.

4. Conclusions

Low polarity SiOC thin film is known as low-k material with good insulation. Thin film transistors with bi-directional transfer characteristics were created using low-k SiOC with gate insulation, but channels were not used. The reason why transistors operate is because low-k SiOC has phase-insulated properties and thus, surface current flows. In particular, we observed that the switching characteristics of transistors in SiOC thin films with 110 °C heat treatment were better. The XRD analysis of SiOC thin film showed that spin current occurs where the Schottky connection is the best. This is because XRD can be used as a way to find out the specificity of thin films that meet the conditions of super current flow.

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