Memristors With Controllable Data Volatility by Loading Metal Ion-Added Ionic Liquids

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We demonstrate a new memristive device (IL-Memristor), in which an ionic liquid (IL) serve as a material to control the volatility of the resistance. ILs are ultra-low vapor pressure liquids consisting of cations and anions at room temperature, and their introduction into solid-state processes can provide new avenues in electronic device fabrication. Because the device resistance change in IL-Memristor is governed by a Cu filament formation/rupture in IL, we considered that the Cu filament stability affects the data retention characteristics. Therefore, we controlled the data retention time by clarifying the corrosion mechanism and performing the IL material design based on the results. It was found out that the corrosion of Cu filaments in the IL was ruled by the comproportionation reaction, and that the data retention characteristics of the devices varied depending on the valence of Cu ions added to the IL. Actually, IL-Memristors involving Cu(II) and Cu(I) show volatile and non-volatile nature with respect to the programmed resistance value, respectively. Our results showed that data volatility can be controlled through the metal ion species added to the IL. The present work indicates that IL-memristor is suitable for unique applications such as artificial neuron with tunable fading characteristics that is applicable to phenomena with a wide range of timescale.

Keywords: conductive bridge RAM, data retention characteristics, AI devices, fading memory, ionic liquids, reservoir devices

INTRODUCTION

Memristors, which were proposed as the fourth fundamental elements of electric circuitry in 1971 (Chua, 1971), have been extensively investigated in memory and neuromorphic devices since the reports of the TiO$_2$ memristor in 2008 (Strukov et al., 2008; Yang et al., 2008). A memristor is a two-terminal passive device whose resistance changes with the amount of charge passing through it and is expected to advance electronics and electrochemical research (Sun B. et al., 2019). As the mechanism of resistance change in the memristor, not only the movement of oxygen vacancies (Sawa, 2008; Akinaga and Shima, 2010) but also the electrochemical metallization has been proposed (Gan et al., 2019). Here we propose an IL-Memristor in which ionic liquids (ILs) are introduced in a solid device as a new memristive material. ILs are ultra-low vapor pressure liquids comprised cations and anions at room temperature (Wasserscheid and Welton, 2002; Hallett and Welton, 2011) and can be used as chemical reaction field because of their wide potential window (Arimoto et al., 2008), making them suitable candidates for electronic devices.
(Harada et al., 2015a,b, 2016; Saito and Iwasa, 2015; Kinoshita et al., 2017; Yamaoka et al., 2017). Because ILs are stable in vacuum, they can be incorporated into existing microfabrication processes. The stability of the metal in the IL impacts the data retention characteristics in electrochemical metallization type memristors. Expanding the range of device applications is possible by controlling the metal filament stability in IL-Memristor. The stable metal filament have been applied to a non-volatile memory, such as conducting-bridge RAM, which are expected to become next-generation memory devices because of their simple structure and low power consumption (Waser et al., 2009, 2016; Valov et al., 2011). Besides, the stable filament has been used to demonstrate electronics synapse devices, in which a long-term potentiation (LTP) and long-term depression (LTD) as well as spike-timing-dependent plasticity (STDP) in the biological synapse are successfully emulated (Jeong et al., 2016; Shi et al., 2018). Recently, the unstable filament also attracts considerable attention because the resultant temporal resistance change is applicable to the emerging devices for the human brain inspired computing (Hasegawa et al., 2011; Deng et al., 2015; Ascoli et al., 2016; Zhang et al., 2018; Midya et al., 2019; Wang et al., 2019; Zhu et al., 2020).

As an influencing factor on the Cu filament stability, we focused on the comproportionation reaction of Cu in IL reported by Murase et al. (2001). We directly detected the formation of Cu(I) from Cu(II) in IL dropped on a thin Cu film using X-ray photoelectron spectroscopy (XPS) by taking advantage of ultra-low vapor pressure of ILs in the vacuum. We used this reaction to control the data retention characteristics of the IL-Memristor. As expected from the XPS measurement results, the data retention time was more than 10 times longer in Cu(I)-doped IL-Memristor than that in Cu(II)-doped IL-Memristor although reproducible resistance change was observed in both devices. These results indicate that IL-Memristor with controllable data volatility can be produced through changing metal ion species in ILs.

MATERIALS AND METHODS

Device Fabrication

Figure 1A shows the SEM image of IL-Memristor. Figure 1B shows the cross-sectional view of IL-Memristor for lines A-B in Figure 1A. A Ta (1 nm)/Pt (20 nm)/Ta (1 nm) film was deposited on a SiO$_2$ substrate by sputtering, followed by chemical vapor deposition of SiO$_2$ (30 nm). Here, the Ta layer acts as the adhesion

![FIGURE 1](image_url)

**FIGURE 1** | (A) The SEM image of IL-Memristor and (B) the cross-sectional view of IL-Memristor for lines A-B in (A). Photographs of (C) the W needle used to deliver IL microdroplet and (D) IL microdroplet on the IE and pore pattern. A metal mask for Cu AE patterning is also shown in (C,D). IL is filled in a pore structure microfabricated in the 30 nm thick SiO$_2$ layer between Cu and Pt electrodes. In IL-Memristor, IL is used as a location for the Cu ion transport.
layer between the SiO$_2$ and Pt layers, and the Pt layer acts as the inert electrode (IE). A pore structure with an area of 1 × 1 μm, which determined the device size, was microfabricated in the SiO$_2$ layer by conventional photolithography and dry etching. As shown in Figures 1C,D, we used a W needle attached to a precision positioner to supply the IL microdroplet on the microfabricated IE and pore structure. The Cu active electrode (AE) patterns were prepared with the mask-through sputtering process using a metal mask. The thickness of Cu AE is 50 nm. The stacking structure of the present device is represented as Cu (50 nm)/IL (30 nm)/Pt (20 nm). We confirmed that the pore was successfully filled with IL from the results of the electrical measurement, which is explained in more detail in the Supplementary Material. 1-Butyl-3-methylimidazolium bis(trifluoromethyl sulfonyl)amide ([bmim][Tf$_2$N]) was used as the IL (Harada et al., 2015a). Cu(I) was introduced into this IL by electrolysis (Abedin et al., 2007; Qiu et al., 2010). The introduction of Cu(II) was conducted by dissolving Cu(Tf$_2$N)$_2$ metal salts. Hereafter, Cu(I)-doped IL and Cu(II)-doped IL are denoted as Cu(I)-IL and Cu(II)-IL, respectively. In addition to that, the device using Cu(I)-IL and Cu(II)-IL are represented as Cu(I)-IL-Memristor and Cu(II)-IL-Memristor, respectively.

**Experimental Procedure**

Optical microscopy of the Cu pattern in the IL was performed in the atmosphere at room temperature. In XPS measurements, ULVAC-PHI Quantera II applying a monochromatic Al Kα X-ray source (1486.6 eV) was used. The photoelectron take-off angle in XPS was 45°. Cu patterns deposited on SiO$_2$ substrates were introduced into the IL and observed by an optical microscope. After 1 h, the IL was sequentially washed away by acetone and ethanol, and the Cu pattern height was measured using a surface profiler. We measured current–voltage ($I$–$V$), data retention, and fading characteristics using a semiconductor parameter analyzer (Agilent B1500A). For the data retention measurement, which is explained in more detail in the Supplementary Material, we measured current–voltage ($I$–$V$), data retention, and fading characteristics using a semiconductor parameter analyzer (Agilent B1500A). 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FIGURE 2 | (A) Schematic illustration and (B) photograph of Cu patterns immersed in the IL droplet. Magnified image of Cu thin film patterns (C,F) immediately after, (D,G) 15 min after, (E,H) 60 min after they were introduced into Cu(I)-IL and Cu(II)-IL, respectively. The thickness of Cu pattern is 50 nm. When Cu pattern is in Cu(I)-IL, the appearance of Cu pattern exhibited almost no change with time. On the other hands, Cu pattern gradually disappeared in Cu(II)-IL. The red and blue horizontal lines in (E,H) correspond to the scan position for the surface profiles in Figures 3A,B.
FIGURE 3
The surface profiler results for (A) Cu(I)-IL and (B) Cu(II)-IL dropped onto the Cu pattern. The red curve in (A) is the surface profile measured along the red horizontal line in Figure 2E. The blue curve in (B) correspond to the surface profile along the blue horizontal line in Figure 2H. The black lines are the surface profiles before the Cu patterns are immersed in the IL. Note that the spiky peak shape at the edge of the Cu pattern plotted by the black line in (A) marked with an asterisk (*) is thought to be a side-wall fence structure formed during the mask-through sputtering process. The profiles for the Cu patterns dipped in IL were measured after removing IL by acetone and ethanol.

FIGURE 4
Schematic illustrations of (A) IL/Cu and (B) IL/SiO$_2$ used for XPS measurements. The size of the substrate and IL drop (roughly 5 mm in diameter) is much larger than the detection area of the XPS instrument (100 µm in diameter).

XPS spectra was conducted in order to investigate the Cu valence state in the IL in more detail. Here, the main peak was labeled as Peak 1, and the satellite structure was split into two peaks labeled as Peak 2 and Peak 3. The peak position for Peak 1 in IL/Cu was 935.75 eV, whereas that in IL/SiO$_2$ was 936 eV. The lower binding energy value for Peak 1 in IL/Cu implied that not only Cu(II) but also Cu(I) was present in the IL on the Cu thin film. Although the constituent metal element is identical in the ionic material, the core-level binding energy becomes higher when the valence of the metal element (cation) increases because an increasing number of valence electrons is attracted by the neighboring anions. For instance, in the case of the Cu–O binary system (Cu$_2$O and CuO), the main peak position shifts to a higher binding energy when the valence of Cu is increased from Cu(I) in Cu$_2$O to Cu(II) in CuO (Moretti and Beck, 2019). By contrast, Peak 1 shifted to a lower binding energy in IL/Cu compared to that in IL/SiO$_2$, which can be related to the valence state decrease in Cu [i.e., the formation of Cu(I)]. Regarding the satellite structure in the Cu 2p$_{3/2}$ spectra, a strong satellite structure was observed in both samples, indicating that Cu(II) was involved in the IL. This is reasonable because Cu(Tf$_2$N)$_2$ dissolved in [bmim][Tf$_2$N] has the divalent ions of Cu(II). However, the characteristics of the satellite structure of IL/Cu differ from that of IL/SiO$_2$. In the case of IL/SiO$_2$, the area ratio of Peak 3 to Peak 2, i.e., Peak 3/Peak 2, was $\sim$0.97, whereas the value of Peak 3/Peak 2 in IL/Cu was $\sim$1.1. The larger Peak 3/Peak 2 value in IL/Cu than that in IL/SiO$_2$ is
due to the Cu(I) formation, which may be accompanied by the weak satellite structure in Cu 2p$_{3/2}$ spectrum, as observed for Cu(I) in Cu$_2$O (Barreca et al., 2007; Wang et al., 2007).

Considering the above Cu 2p$_{3/2}$ XPS spectra measurement results, the possible chemical reaction for Cu dissolution is

\[ \text{Cu(Tf}_2\text{N)}_2 + \text{Cu} \rightarrow 2\text{Cu(Tf}_2\text{N).} \]  

(1)

From the viewpoint of the Cu valence state, Cu dissolution is induced by the following comproportionation reaction:

\[ \text{Cu(II)} + \text{Cu} \rightarrow 2\text{Cu(I).} \]  

(2)

Two other signs of Cu(I) formation in IL/Cu were obtained. One was the Cu LMM Auger electron spectrum (Figure 6). The intensity of the Cu LMM Auger electron spectrum in IL/Cu is much larger than that in IL/SiO$_2$. A similar change in the Cu LMM Auger spectrum was observed when Cu(I) was introduced in the [MAP][Tf$_2$N] by the electrolysis of Cu (Qiu et al., 2010).

Another was the shape of the N 1s XPS spectra in IL/Cu (Figure 7A) and IL/SiO$_2$ (Figure 7B). The N 1s XPS spectrum can be separated into two peaks (Peaks 4 and 6 in Figure 7A) for IL/Cu, whereas it can be separated into three peaks (Peaks 4–6 in Figure 7B) for IL/SiO$_2$. Peaks 4, 5, and 6 correspond to N in bmim cation, N in Tf$_2$N anion having the interaction with metal cations, and N in free Tf$_2$N anion (Caporali et al., 2016). Peak 5 in IL/Cu disappeared in Ag(I) containing [bmim][Tf$_2$N], whereas it was observed in [bmim][Tf$_2$N] with divalent metal cations, such as Cu(II), Ni(II), and Zn(II) (Caporali et al., 2016). As observed in the Cu 2p$_{3/2}$ spectra, the binding energy between Cu cation and Tf$_2$N anion was weakened because of the formation of Cu(I), which may have caused the disappearance of Peak 5 in Figure 7A. Such feature in N 1s XPS spectrum, i.e., the disappearance of Peak 5, was also observed in Cu(I)-IL prepared by the electrolysis (see Supplementary Material). Regarding Cu(I) in the present IL, there is a possibility that Cu(I) partly forms the carbene complex with imidazolium cation according to the previous studies on

![FIGURE 5](image_url)
the N-heterocyclic carbenes coordinated to metals (Hapiot and Lagrost, 2008; Hopkinson et al., 2014).

**Memory Operation**

The operating mechanism of the IL-Memristor is as follows: when a voltage is applied to the AE, metal ions dissolve in the IL and deposit on the IE to form filaments, resulting in a low resistance state (LRS). Afterward, by applying a negative voltage to the AE in LRS, filaments are ruptured, resulting in a high resistance state (HRS). The switching from HRS to LRS is called SET, whereas that from LRS to HRS is called RESET. **Figure 8A** shows the $I-V$ characteristics of the Cu(I)-IL-Memristor and Cu(II)-IL-Memristor, which were plotted using the median values calculated from 500 cycles. It should be noted that the number of DC sweep cycle in the present study for evaluating the statistical distribution in the operating voltages is comparable to or larger than those in the previous reports (Yan et al., 2017, 2019a). As indicated by the blue and red arrows in **Figure 8A**, the voltage values when the filament formation/rupture occurs are represented as $V_{\text{SET}}/V_{\text{RESET}}$, respectively. **Figure 8B** shows the cumulative probabilities of the operating voltage ($V_{\text{SET}}$ and $V_{\text{RESET}}$) for the Cu(I)-IL-Memristor and Cu(II)-IL-Memristor. In **Figures 8A,B**, the blue circles show the Cu(II)-IL-Memristor, and the red triangles show the Cu(I)-IL-Memristor. The $V_{\text{SET}}$ of the Cu(I)-IL-Memristor was lower than that of the Cu(II)-IL-Memristor. The comproportionation reaction affects each memristor differently: in the Cu(I)-IL-Memristor, Cu easily deposits on the IE, whereas in the Cu(II)-IL-Memristor, Cu easily dissolves from the AE. The reduction reaction (Cu deposition) on the IE is more dominant in affecting the SET process than the oxidation reaction (Cu dissolution) on the AE because the $V_{\text{SET}}$ was lower in the Cu(I)-IL-Memristor than in the Cu(II)-IL-Memristor. This result was consistent with previous reports suggesting that the Helmholtz layer formed on the IE surface and the proton-accepting ability of ILs affect the SET process (Harada et al., 2016; Yamaoka et al., 2017). Additionally, the distribution of $V_{\text{RESET}}$ is insusceptible to the Cu valence state in the IL. Assuming that the operating mechanism for the RESET process is mainly based on Joule heating (Tsuruoka et al., 2010; Sun et al., 2014), Cu valence insusceptibility of $V_{\text{RESET}}$ may be because it masks the impact of corrosion by the comproportionation reaction. From **Figure 8B**, it is necessary to point out that the Cu(I)-IL-Memristors still have statistical variabilities in the values of $V_{\text{RESET}}$ and $V_{\text{SET}}$. It is considered that such variabilities strongly affect the device reliabilities such...
as cycle endurance characteristics because it results in the resistance switching failure during the cycle endurance test. Therefore, the suppression of the operating voltage variabilities through the development of materials, device structures, and fabrication processes for IL-Memristor is required. In terms of the device-to-device reproducibility, there is some device-to-device difference in the SET voltage distribution in Figure 8B (see Supplementary Material). However, the impact of the Cu valence on $V_{\text{SET}}$ is qualitatively assured. One of the possible reasons for the device-to-device difference is that the volume of the IL microdroplet involved in each IL-Memristor is uncontrollable at present because it is transferred manually by using a W needle. The improvement of the device fabrication process such as adopting the ink-jet technology is required to confirm the device-to-device reproducibility.

**Data Retention Characteristics**

Figure 9 depicts the data retention characteristics of IL-Memristors. Data retention characteristics generally depend on the current level. To confirm the data retention characteristics at various current values, $I_{\text{comp}}$ was set at 10, 20, and 200 $\mu$A. The Cu(II)-IL-Memristor showed a short data retention time ($<10^3$ s), whereas the Cu(I)-IL-Memristor showed a relatively long data retention time (more than $10^4$ s). This indicates that the Cu filament was corroded by the comproportionation reaction in Cu(II)-IL, as expected from optical microscopy and XPS results of the Cu pattern involved in IL. Importantly, the volatility and non-volatility of the data retention characteristics in IL-Memristor can be controlled by changing metal ion species added to the IL. We found that the Cu(I)-IL-Memristor was more suitable for non-volatile memory applications compared with the Cu(II)-IL-Memristor. As shown in Figure 9, the resistance value of LRS in Cu(I)-IL-Memristor can be increased by decreasing $I_{\text{comp}}$. Therefore, both the low current operation and good data retention are compatibly realized in Cu(I)-IL-Memristor. Although the minimum value of $I_{\text{comp}}$ in the present study is 10 $\mu$A at present, the value of $I_{\text{comp}}$ can be further decreased by decreasing the device area because the device resistance in HRS inversely scales with the device area. This is also favorable because the device size miniaturization is expected to promote the energy-conserving device operation.
Fading Characteristics

As shown in Figure 9, the Cu(II)-IL-Memristor demonstrated data volatility. Recently, such volatile memristors attract considerable attention because of the applicability to the human brain inspired computing. Various materials including solid oxide and sulfides exhibiting voltage resistance change are intensively investigated in order to realize artificial synaptic devices which mimic the information processing function of biological synapse (Hasegawa et al., 2012; Wang et al., 2016; Sun J. et al., 2019; Yan et al., 2019b). In addition, the memristors exhibiting such time-dependent resistance change are developed as the physical reservoir device for the reservoir computing (RC), in which the fading memory function is one of the essential requirements for the device (Tanaka et al., 2019). It has been pointed out that the timescale of the resistance change influences the time period between input signals to the reservoir and the information processing time (Midya et al., 2019). Figure 10A schematically illustrates the measurement method for the fading characteristics of the Cu(II)-IL-Memristor. At first, a SET pulse having a pulse height/width of $h_{\text{SET}}/w_{\text{SET}}$ was applied to the IL-Memristor in HRS to slightly decrease the device resistance. Immediately after the application of the SET pulse, a read pulse with a height of $h_{\text{READ}}$ was applied, and the current was monitored at regular intervals. For Cu(II)-IL-Memristor, $h_{\text{SET}}/w_{\text{SET}}$ was +0.6 V/1 ms, while $h_{\text{READ}}$ was −100 mV. Because the pulse voltage was used in this experiment, the SET process was incomplete although the pulse voltage height was larger than the median value of $V_{\text{SET}}$ in Figure 8, which was evaluated by DC voltage sweep. Figure 10B shows the results of the fading characteristics of the Cu(II)-IL-Memristor, indicating a gradual current decrease. For comparison, fading characteristics of the Cu(I)-IL-Memristor was also evaluated (Figure 10C). For Cu(I)-IL-Memristor, $h_{\text{SET}}/w_{\text{SET}}$ was +1.5 V/10 µs, while $h_{\text{READ}}$ was −100 mV. During the reading process, about 45% increase of the resistance (from 15.6 to 22.8 kΩ) was observed for 100 ms in Cu(II)-IL-Memristor. On the other hands, about 19% increase of the resistance (from 13.6 to 16.3 kΩ) was observed for 800 ms in Cu(I)-IL-Memristor. The faster resistance change in Cu(II)-IL-Memristor suggests that the incomplete filament formation and subsequent dissolution by the comproportionation reaction in Cu(II)-IL lead to the time-dependent resistance change with a timescale of 100 ms. The timescale observed in Cu(II)-IL-Memristor is almost comparable to that observed in a solid diffusive memristor for RC (Midya et al., 2019). Therefore,
the present results indicate that the Cu(II)-IL-Memristor is suitable for AI applications such as RC because of their fading memory characteristics. Moreover, the present results also imply that controlling the time-dependent resistance change timescale depending on the task executed in the reservoir is expected by selecting the appropriate IL in IL-Memristor.

**CONCLUSION**

Cu corrosion in ILs was investigated by XPS. Based on this corrosion mechanism, we fabricated two types of devices, Cu(I)-IL-Memristor and Cu(II)-IL-Memristor, and evaluated their data retention characteristics. The Cu(II)-IL-Memristor showed data volatility because the comproportionation reaction promoted the corrosion of Cu filaments, whereas the Cu(I)-IL-Memristor was non-volatile. Our results show that data volatility and non-volatility can be easily controlled by changing metal ion species added to the IL. Additionally, the fading characteristics of the Cu(II)-IL-Memristor, where the current value gradually decreased over 100 ms, were confirmed. The Cu dissolution in IL and relevant time-dependent resistance change observed in IL-Memristor imply the adaptability of ILs and IL-Memristor to the emerging electronic and information processing technologies such as transient electronics and reservoir computing.

**DATA AVAILABILITY STATEMENT**

The original contributions presented in the study are included in the article/Supplementary Material, further inquiries can be directed to the corresponding author.

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**AUTHOR CONTRIBUTIONS**

HSa, HSh, YN, HA, and KK conceived the experiments. HSh and YH prepared the devices. TN and TI synthesized the ionic liquids. HSa and HSH conducted the observations and measurements. HSa, HSh, HA, and KK analyzed the results. HSa, HSh, and KK wrote the manuscript. All authors contributed to the discussion about the results and commented on the manuscript.

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**SUPPLEMENTARY MATERIAL**

The Supplementary Material for this article can be found online at: https://www.frontiersin.org/articles/10.3389/fnano.2021.660563/full#supplementary-material
