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ABSTRACT

We report on a method for fabricating balanced hole and electron transport in ambipolar organic field-effect transistors (OFETs) based on the co-evaporation of zinc-phthalocyanine (ZnPc) and its fluorinated derivative (F_8ZnPc). The semiconducting behaviour of the OFET can be tuned continuously from unipolar p-type, with a hole mobility in the range of (1.7 ± 0.1) × 10^{-4} cm^2/Vs, to unipolar n-type, with an electron mobility of (1.0 ± 0.1) × 10^{-4} cm^2/Vs. Devices of the pristine ZnPc and F_8ZnPc show a current on/off ratio of 10^5. By co-evaporating the p-type ZnPc with the n-type F_8ZnPc, we fabricate ambipolar transistors and complementary-like voltage inverters. For the ambipolar devices, the optimum balance between the hole and electron mobilities is found for the blend of 1:1.5 weight ratio with hole and electron mobilities of (8.3 ± 0.2) × 10^{-7} cm^2/Vs and (5.5 ± 0.1) × 10^{-7} cm^2/Vs, respectively. Finally, we demonstrate application of the ambipolar devices in a complementary-like voltage inverter circuit with the performance comparable to an inverter based on separate ZnPc and F_8ZnPc OFETs.

I. INTRODUCTION

Organic semiconductors are of great interest for use in electronics. They combine semiconducting properties with established processing techniques like vacuum and solution coating, which require only low substrate temperatures allowing for deposition on flexible, large-area substrates. Over the three decades since the first reports of organic field effect transistors (OFETs) based on polymer and small molecules, the field has advanced such that applications now include organic integrated circuits and flexible active matrix displays. The majority of materials considered for use in OFETs display p-type conduction, that is, they form accumulation layers of holes at the semiconductor-dielectric interface upon negative bias at the gate. N-type organic semiconductors have been reported stable at ambient conditions remain comparatively rare. Phthalocyanines have demonstrated both p-type semiconducting behaviour and n-type behaviour depending on the attached side groups. Nénon et al. achieved ambipolar behaviour by fabricating heterojunctions of copper phthalocyanine (CuPc) and its fully-fluorinated derivative F_16CuPc. They reported that OFETs based on stacked bi-layers of the different CuPc derivatives gave better performance as compared to a 1:1 blend of the two semiconductors. Recent improvements towards balanced ambipolar transport in OFETs has come from Jiang et al. who proposed a method of molecular crystal engineering to tune zinc phthalocyanine (ZnPc) films from p-type to n-type by replacing the hydrogen on the outer polarities of charge to be induced in an OFET, offer an alternative design known as complementary-like logic. In this scheme, ambipolar OFETs replace both the p- and n-type transistors thus circumventing the advanced patterning techniques necessary to deposit two separate materials, simplifying the fabrication process. The electron and hole mobilities in these ambipolar devices must be balanced in order for the resulting logic gates to have symmetrical outputs.
rings with fluorine F_xZnPc (x = 0, 4, 8, 16). The step-like shift in ionisation potential (IP) by fluorination resulted in a smaller Schottky barrier for electron injection at the organic-gold contacts such that ambipolar behaviour was observed for F_xZnPc and n-type behaviour for both F_4ZnPc and F_8MnZnPc.

In this study, we co-evaporate films of ZnPc and F_xZnPc (chemical structures shown in Figure 1) over a range of blend weight ratios to overcome the limitations of only having discrete energy steps given by the fluorination. This builds on recent work by Schwarze et al. who demonstrated that the IP of a thin film based on the co-deposition of ZnPc with one of its fluorinated derivatives can be linearly tuned as a function of blend ratio. We fabricate and electrically characterise ZnPc:F_xZnPc OFETs with a range of blend weight ratios from 1:5 to 5:1 which span unipolar p-type to ambipolar to unipolar n-type behaviour. We investigate the morphology of the blended films by GIWAXS measurements and report that the blends appear less ordered than the single component films at the concentrations used for the voltage inverter. Finally we demonstrate the ambipolar OFETs in a complementary-like voltage inverter.

II. EXPERIMENT

Devices were fabricated via thermal evaporation in vacuum using pre-patterned bottom-gate bottom-contact OFET substrates (Fraunhofer IPMS, Germany) consisting of a highly conductive n-doped silicon gate-electrode covered with 230 nm dielectric layer of thermally grown SiO_2. The gold interdigitated source-drain contacts have a thickness of 30 nm, width of 10 mm and a channel length of 10 µm. Prior to vacuum deposition, the substrates were cleaned in an ultrasonic bath for 10 min in 2.5% Hellmanex solution, DI water, acetone and finally ethanol. The substrates were treated with O_2 plasma for 10 min prior to deposition. Films were evaporated at a base pressure of 10^-6 mbar with a deposition rate between 0.3 to 0.4 Å/s to achieve thicknesses around 21 nm. The samples were transferred to a nitrogen glovebox, without exposure to air, for characterisation.

The electrical characterisation of the OFETs was made using a Keithley 2636B and Keithley 2400 with in-house developed software freely available online. The measurements were carried out at room temperature, in the dark and under a nitrogen atmosphere. Grazing-incidence wide-angle x-ray scattering (GIWAXS) studies were carried out at the Surface and Interface Diffraction beamline (IO7) at the Diamond Light Source (DLS) using a beam energy of 20 keV (0.62 Å) and a Pilatus2M area detector. The samples were probed while inside a vacuum deposition chamber at a pressure of around 10^-3 mbar and with an applied solid angle correction. Images were converted to 2D reciprocal space using the DAWN software package with an applied solid angle correction. The intensities in the 2D reciprocal space map figures are normalised to the first out-of-plane peak.

III. RESULTS AND DISCUSSION

Figure 2 shows the output and transfer characteristics of OFETs fabricated with ZnPc, F_xZnPc and a blend of 1:1.5 weight ratio ZnPc to F_xZnPc. The ZnPc shows unipolar hole transport behaviour whilst the F_xZnPc shows unipolar electron transporting behaviour. At low source-drain voltages, V_{ds}, the F_xZnPc OFET shows a nonlinear current increase in its output which indicates that there may be a barrier for electron injection from the gold electrodes to the semiconductor. The current on/off ratio for each of these single-component devices is ≤ 10^5.

For the blended films we observe conduction of both holes and electrons. With negative gate bias, V_g, holes accumulate at the semiconductor/SiO_2 interface and the device works as a p-type OFET. Whereas for positive V_g, electrons accumulate at the interface and the device operates as an n-type OFET. The output curves for V_g = 0 and -10 V show a marked increase of the source-drain current I_{ds} with increasingly negative V_{ds} which is typical of ambipolar devices due to the contribution of drain-induced holes. The transfer curve shows a reduced I_{ds} for only a narrow range of V_g, giving an on/off ratio in the range of 10^3.

The field-effect mobilities for the devices as a function of blend ratio are shown in Figure 3. The mobilities were calculated in the saturation regime from the gradient of the straight line fit of I_{ds} vs V_{ds} (the linear regime mobilities follow a similar trend as shown in Figure S1 in the supplementary material). The measured hole
mobility for the ZnPc (represented by the 0 % blend in Figure 3) was \( (1.7 \pm 0.1) \times 10^{-4} \) cm\(^2\)/Vs which is in good agreement with measurements made using a similar geometry in the literature.\(^{19}\) The electron mobility of \( \text{F}_8\text{ZnPc} \) (100 % blend) was found to be \( (1.0 \pm 0.1) \times 10^{-4} \) cm\(^2\)/Vs. At a blend ratio of 1:1.5, ambipolar behaviour is observed and the hole and electron mobility were found to be \( (8.3 \pm 0.2) \times 10^{-7} \) cm\(^2\)/Vs and \( (5.5 \pm 0.1) \times 10^{-7} \) cm\(^2\)/Vs respectively. This decrease of over two orders of magnitude can be partly attributed to increasing injection barriers for both holes and electrons as the IP and EA of the blended semiconductor shift away from the work function of the gold source-drain electrodes.\(^{20,21}\) Additionally, these barriers could be embedded in the semiconductor blend itself as well as at the contacts. The distribution of transport states in the blends is expected to be broader than that of the single component films due to the offset between the highest occupied molecular orbitals of the ZnPc and \( \text{F}_8\text{ZnPc} \).\(^{13}\) This increases energetic disorder which in turn negatively impacts the mobility.\(^{22}\) Further disorder may arise through structural inhomogeneities - trap states can localise at grain boundaries resulting in band bending and barriers.\(^{23}\) This can severely impact charge transport as reported by Bolognesi et al.\(^{24}\) who observed a drop of three orders of magnitude in field-effect mobility as the grain size in pentacene thin-films decreased.

Grazing Incidence Wide Angle X-ray Scattering (GIWAXS) measurements are carried out to investigate the crystalline structure of the samples. Figures 4a, 4b and 4c show the 2D reciprocal space maps of the ZnPc, 1:1.5 ZnPc:F\(_8\)ZnPc weight ratio and \( \text{F}_8\text{ZnPc} \) films. The peaks indicated by arrows in Figure 4a are ascribed to the (200) peak of the herring-bone like \( \alpha \)-phase of ZnPc.\(^{11,25}\) Figures S2a and S2b in the supplementary material show the near out-of-plane and in-plane (at \( Q_z = 0.025 \) Å\(^{-1} \))
All films display two preferred orientations indicated by the position of the (200) peak, either fully out-of-plane or fully in-plane. For each configuration, the out-of-plane peak appears more strongly than the in-plane peaks. This indicates that the edge-on orientation is preferred over the face-on configuration.\(^{25}\) ZnPc (Figure 4a) exhibits greater crystalline ordering than the F\(_8\)ZnPc as indicated by the visible higher order reflections and strong (200) reflections. Furthermore, the F\(_8\)ZnPc appears more powder-like suggesting a greater variation of crystallite orientations as compared to the ZnPc. The ZnPc:F\(_8\)ZnPc blend (Figure 4b) shows the weakest reflection intensities with the in-plane peaks appearing only faintly, indicating less crystalline ordering than in the neat films.

Line cuts were made through the (200) peaks both in-plane and along the near out-of-plane direction to determine the d-spacing of the films. The location of the peaks were fitted with Gaussian functions. The results from the near out-of-plane line cuts, Figure S2a in the supplementary material, are used due to their stronger signal. The location of the peaks and the corresponding d\(_{200}\)-spacings are displayed in Table I. The d-spacing of ZnPc is consistent with previously reported values for thermally evaporated thin films.\(^{26}\) The d-spacing of F\(_8\)ZnPc is slightly larger than that of ZnPc which is in agreement with trends reported for similar fluorinated phthalocyanines.\(^{11,13}\) The periodicity of the ZnPc:F\(_8\)ZnPc blend being closer to the periodicity of the pristine F\(_8\)ZnPc could indicate that the scattering signal from the blend arises primarily due to ordered F\(_8\)ZnPc within a host matrix of disordered ZnPc. Alternatively, the ZnPc and F\(_8\)ZnPc could co-crystallise, with the fluorinated molecules inducing a stacking structure similar to that of pristine F\(_8\)ZnPc. Reports of intermixing at a molecular level in systems of CuPc:F\(_{16}\)CuPc are consistent with this latter picture however further work will be needed to verify this co-crystallisation.\(^{11,27}\)

Finally the OFETs were placed in series, as shown in the inset to Figures 5 and 6, to build voltage inverters, also known as NOT gates.

### Table I

| Material       | Peak (Å\(^{-1}\)) | d\(_{200}\)-spacing (Å) |
|----------------|-------------------|-------------------------|
| ZnPc           | 0.51              | 12.3                    |
| ZnPc:F\(_8\)ZnPc | 0.48              | 13.1                    |
| F\(_8\)ZnPc     | 0.49              | 12.8                    |

**Figure 5** shows a voltage inverter based on complementary logic incorporating a pair of p-type (ZnPc) and n-type (F\(_8\)ZnPc) transistors with a supply voltage \(V_{dd} = 50\) V. At voltages below a threshold of around 44 V, the p-type OFET is on whilst the n-type OFET is off giving an output voltage almost equal to the supply rail. Above 44 V, the p-type OFET switches off and the n-type OFET switches on outside the supply rails.

**Figure 6** shows a voltage inverter based on complementary logic incorporating a pair of p-type (ZnPc) and n-type (F\(_8\)ZnPc) transistors with a supply voltage \(V_{dd} = 50\) V. At voltages below a threshold of around 44 V, the p-type OFET is on whilst the n-type OFET is off giving an output voltage almost equal to the supply rail. Above 44 V, the p-type OFET switches off and the n-type OFET switches on outside the supply rails.
on, resulting in the low state for \( V_{\text{out}} \). The threshold voltage (\( V_T \)) of an inverter is given by

\[
V_T = \frac{V_{dd} + V_{Tp} + (\mu_n/\mu_p)^{1/2} V_{Tn}}{(1 + (\mu_n/\mu_p)^{1/2})}
\]

where \( V_{Tp} \) and \( V_{Tn} \) are the threshold voltages for the p-type and n-type OFETs respectively. Taking an average of the threshold voltages over four devices for each material, the calculated threshold voltage of the inverter shown here is (38 ± 11) V which is in agreement with the experimental data (see Table II). The inverter has a gain of 5 with a good noise margin.

As the ambipolar OFETs show both n- and p-type behaviour, complementary-like logic circuits can be realised. Figure 6 shows the transfer characteristics of a voltage inverter driven with \( V_{dd} = 50\text{V} \) composed of two ZnPc:FzZnPc OFETs with a blend ratio of 1:1.5. The threshold voltage is calculated as (48 ± 2) V which appears to be in fairly good agreement with the experimental data (Table II). Below the threshold voltage, the output is around 40V and above falls to 10V. The dependence of \( V_{\text{out}} \) on \( V_{\text{in}} \) at low and high values of \( V_{\text{in}} \) can be attributed to the lower on/off ratio in the blended transistors as compared to the pristine ZnPc and FzZnPc devices. Consequently there is always a leakage current flowing through the inverter as neither transistor can be fully switched off.

The ambipolar voltage inverter shows a considerable hysteresis as compared to the complementary inverter. This hysteresis is observed when measuring the individual OFETs of the blends at a scan rate of 5 V/s (see Figure S3 in the supplementary material). We have therefore taken averages over forward and backward scans for fitting the mobilities and threshold voltages. Trap related hysteresis has been widely reported in OFETs, however further time-dependent measurements are necessary here in order to clarify the depth and origin of these traps.

### IV. CONCLUSION

In summary, we report a method for fabricating ambipolar OFETs with balanced hole and electron transport based on the co-evaporation of ZnPc and its fluorinated derivative FzZnPc. OFETs of the pristine p-type ZnPc show a mobility of (1.7 ± 0.1) \( \times 10^{-4} \text{cm}^2/\text{Vs} \) measured in the saturation regime. FzZnPc OFETs show an electron mobility of a similar magnitude around (1.0 ± 0.1) \( \times 10^{-4} \text{cm}^2/\text{Vs} \). The current on/off ratio of the OFETs in both cases is 10^2. By co-evaporating these two p- and n-type semiconductors, we demonstrate tunable p-type to ambipolar to n-type behaviour dependent on the weight ratio of ZnPc to FzZnPc. We find the optimum balance between the hole and electron mobility in the blend of 1:1.5 weight ratio. In this blend, the hole and electron mobility are (8.3 ± 0.2) \( \times 10^{-7} \text{cm}^2/\text{Vs} \) and (5.5 ± 0.1) \( \times 10^{-7} \text{cm}^2/\text{Vs} \) respectively. As these transistors show both p- and n-type behaviour, we fabricate complementary-like voltage inverters which show comparable performance to the inverters based on separate p- and n-type OFETs. The OFET tunability demonstrated here to obtain balanced ambipolar transport could be extended to other systems of organic semiconductors, which permit a shift in their band energies when blended with their halogenated derivatives.

### SUPPLEMENTARY MATERIAL

See supplementary material for field-effect mobilities extracted in the linear regime, line cuts from the GIWAXS images and hysteresis curves of the OFETs.

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