Analysis of a DC-DC Flyback Converter Variant for Thermoelectric Generators with Partial Energy Processing

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Abstract: This paper presents a theoretical analysis of a DC-DC flyback converter variant applied in energy harvesting based on thermoelectric generators. The main contribution of the article is the analysis and obtaining the equations of the behavior of the converter with a rearrangement of the elements of the traditional flyback converter in such a way that the converter only processes part of the energy while the other part is delivered directly to the load. This is achieved by connecting the secondary of the flyback in series with the load, and this assembly, in turn, is placed in parallel with the primary and the voltage source. This configuration means that the topology can only be a boost topology; however, there are benefits such as partial power processing (R2P2) and reduced stress on converter components in both voltage and current; all this leads to increase the efficiency. A Low Frequency Averaging Analysis (LFAA) was used to determine the behavior of the proposed circuit, and a simple equivalent circuit to analyze was obtained. In order to validate the theoretical analysis, a circuit was simulated in Spice and implemented in an 18 W prototype. Experimental results showed that the converter has an efficiency of 92.65%. Moreover, the rearranged flyback processed only 56% of the input power.

Keywords: DC-DC flyback converter; partial energy processing; R2P2; thermoelectric generators

1. Introduction

In recent years, due to their application in eco-friendly energy collection systems, thermoelectric generators (TEG) have been studied [1]. TEGs do not generate acoustic noise, require little maintenance, and have a lifespan of 20 years, due to the lack of moving parts [2].

A TEG is a device that works with the principle of the Seebeck effect, which indicates that a voltage is created when two different conductors or semiconductors have a different temperature between them [3].

Some uses of TEGs in aerospace applications were reported [4]: automotive systems [5,6], eco-friendly stoves [7], battery charging [8], industrial processes [9], portable thermoelectric generators for power supply of ultra-low-power sensors [10], and sensing devices in remote pipelines [11].
There are different works related to DC-DC converters applied to thermoelectric generators for increasing the efficiency of these collection systems [12–15], in which the system is provided with maximum power point tracking control (MPPT) achieving efficiencies above 90% only by adjusting the working cycle. There are different papers in which flyback converters are proposed and used [16–18], either with extra components that modify their operation or jobs in which combinations of flyback and boost converters are used for different applications, including jobs with solar panels [19–22]. However, although it has promising results, flyback converters have been little used in applications related to energy collection systems using thermoelectric generators.

In [23–27], converter variations are shown where an adjustment is made in the operation of the circuit to change the energy processing in such a way that it is not processed completely, since they follow the principle of reduced redundant energy processing (R²P²). Part of the energy is adapted to the output, and the other part goes directly to the load.

In [28], a circuit is shown where the components of the flyback converter are rearranged to make a power supply for LED applications that reduces voltage. Following this same idea, the modification proposed in this article consists of making a boost converter following the same principle of rearrangement of the flyback converter. This is done by connecting the secondary winding in series with the load and in turn placing it in parallel with the primary winding and the voltage source, in this case a TEG.

In this way, the galvanic isolation is eliminated and makes the source deliver a part of the energy directly to the load and the missing part is delivered by the converter. This configuration brings benefits such as increasing the efficiency of the entire system, reducing the voltage and current and voltage in each component. For the analysis of the converter, an averaged analysis was carried out, which resulted in an equivalent circuit being very easy to evaluate and thus determine its behavior and design method.

Similar articles have been found such as [29] which reports a fairly similar circuit; however, it does not make a rigorous analysis of the behavior of the converter, and to differentiate this article, an analysis of the circuit is made in two stages. The first analysis is when the converter is used for general purpose applications where the internal resistance of the source is too small to be neglected and the second analysis is when the converter is used for energy harvesting systems based on thermoelectric generators where the internal resistance of the source consumes a considerable part of the power (26% approximately) and reaches its maximum power transfer when the load is equal to the internal resistance of the source.

This paper is organized as follows: Section 2 presents and explains the operation and mathematical analysis of the converter in two cases, first when there is no internal resistance in the source and second when there is internal resistance in it. Section 3 focuses on converter design and simulation in Spice. Section 4 shows the results obtained, and finally, in Section 5, the main conclusions of this document are presented.

2. Analysis of the Flyback Converter
2.1. Without Internal Resistance in the Source

The difference between the basic flyback converter and the topology proposed is shown in Figure 1. This DC-DC converter consists of a variation of the flyback converter in which the primary winding is placed in series with the secondary winding and the load, thus eliminating galvanic insulation. This configuration makes the topology only boost topology; however, it gains benefits such as partial power processing, as the input voltage source directly delivers some of the power to the load, and the other part is handled by the flyback converter; this improves the overall efficiency of the system.
Figure 1. (a) Basic flyback and (b) modified flyback with partial energy processing.

Another advantage that is presented is that the voltage and current stresses in the converter are considerably reduced depending on the gain, due to the secondary having an output voltage equal to the difference between the output voltage and the input voltage and only providing the missing part.

In order to carry out the analysis of the rearranged flyback converter, an average analysis was used. The main idea is to assessment if the topology complies with the corresponding regulations, knowing its efficiency and evaluating its feasibility of implementation.

For this kind of analysis, it is needed to have some considerations like: the primary side can be represented as a loss-free resistance ($R_f$), this resistance represents the average power delivered to the primary side of the flyback converter ($P_{fi}$), and the energy “consumed” by this resistance is transferred to the secondary side of the flyback converter that is modeled as a direct current voltage source ($V_{fo}$), as shown in Figure 2.

Figure 2. Flyback model in DCM for the low frequency averaging analysis (LFAA).

Applying the last considerations in the circuit of Figure 1b, an equivalent circuit was obtained which is shown in Figure 3. Where $V_{in}$ is the input Voltage, $I_{in}$ is the input Current, $V_{fo}$ is the average output voltage in the secondary side of the flyback converter, $I_{Rf}$ is the average input current in the primary side of the flyback converter, $R_f$ loss-free resistance representing the primary side of the Flyback converter, $V_o$ represents the output voltage of the load, $I_o$ is the output current. This equivalent circuit is easier to evaluate than a conventional analysis in high frequency.
Figure 3. Equivalent circuit in low frequency averaged analysis.

In the second mesh, it can define the averaged current flowing by the resistance of the primary \((I_{Rf})\), and by adding currents in the upper node the input current \((I_{in})\), it was calculated.

\[
I_{Rf} = \frac{V_o - V_{fo}}{R_f} = \frac{V_{in}}{R_f} \quad (1)
\]

\[
I_{in} = I_o + I_{Rf} = I_o + \frac{V_{in}}{R_f} \quad (2)
\]

The average output power \((P_{fo})\) and average input power \((P_{fi})\) of the flyback converter can be related considering the efficiency of the converter \((\eta_f)\), as shown in (3).

\[
P_{fo} = \eta_f P_{fi} \quad (3)
\]

where \(P_{fo}\) and \(P_{fi}\) can be defined as

\[
P_{fo} = V_{fo} I_o \quad (4)
\]

\[
P_{fi} = I_{in}^2 R_f \quad (5)
\]

Replacing (5) y (4) in (3) \(R_f\) the following was obtained:

\[
R_f = \frac{V_{fo} I_o}{\eta_f I_{in}^2} = \frac{(V_o - V_{in}) I_o}{\eta_f I_{Rf}^2} \quad (6)
\]

Finally, (2) is replaced in (7) clearing the resistance value of the primary flyback \((R_f)\), as shown in (7).

\[
R_f = \eta_f \frac{(V_o - V_{fo})^2}{V_{fo} I_o^2} = \eta_f \frac{V_{in}^2}{(V_o - V_{in}) I_o} \quad (7)
\]

With the equations defined for \(R_f\) and \(V_{fo}\) it is possible to determine the power that the flyback converter will process. The part of energy processed by the converter with respect to the total power is called \(Q\), which is defined in (8).

\[
Q = \frac{P_{fi}}{P_{fo}} = \frac{1}{\eta_f} \frac{P_{fo} - P_{fi}}{P_{fo} + P_{fi}} = \frac{1}{\eta_f} \frac{P_{fo} - P_{fi}}{P_{fo} + P_{fi}} = \frac{1}{\eta_f} \frac{1}{M - 1} \quad (8)
\]

where \(M\) is the gain defined as \(V_{o}/V_{in}\).
The power flow diagram of a conventional flyback compared with the rearranged flyback are shown in Figure 4, where $P_{in}$ is the input power, $P_o$ is the output power.

![Flyback Diagram](image)

**Figure 4.** Power flow diagrams of the conventional flyback converter (a) and the proposed circuit (b).

In this diagram, it is easier to understand the operation of the proposed converter, in which it is observed as part of the input power $P_{in}$ supplied directly to the load, while the other fraction is processed by the flyback converter. Therefore, the total efficiency with respect to the conventional flyback will be improved.

The range of $Q$ must be $0 < Q < 1$; if $Q$ is greater than 1, there is no point in implementing the topology since instead of having benefits, low efficiency and greater electrical size would be obtained concerning an isolated basic flyback.

The total efficiency is calculated according to (9).

$$\eta_{T} = \frac{P_o}{P_{in}} = \frac{P_o}{P_{in}(P_o-P_{fo})+P_{fo}} = \frac{1}{\frac{P_o}{P_{in}} - \frac{1}{M + \eta_f - 1}}$$

To better observe the behavior of Equations (8) and (9), both were plotted for the efficiency values of the flyback converter of 0.9, 0.7, and 0.5, and the results are shown in Figure 5 and Figure 6.
Figure 5 provides a graph of the behavior of processed energy $Q$ with respect to gain $M$. It is inferred that the proposed converter will process less energy when the gain is small and the efficiency of the converter is higher.

Figure 6 provides a graph of the behavior of total efficiency $\eta_t$ with respect to gain $M$. It is observed that, for very high gains, the efficiency of the flyback converter and the total efficiency tend to be equal. However, for small gains, it is observed that the total efficiency is considerably improved concerning flyback efficiency.

2.2. With Internal Resistance in the Source

When using energy harvesting systems with low efficiency like TEGs, it is necessary to consider the internal resistance due to it is too large with respect to the power they can supply. The value of the internal resistance ranges from 0.9 Ohms to 1.43 Ohms for each TEG depending of the material. For this reason, it is necessary to consider the internal resistance of the TEG’s in the proposed converter model. The internal resistance ($R_i$) is represented as a resistance in series with the voltage source.

The converter circuit matched with the new resistance $R_i$ is shown in Figure 7. This change also brings about changes in the equivalent simplified circuit and in the energy flow diagram, which are shown in Figure 8 and Figure 9, respectively.

Figure 7. Modified flyback converter for thermoelectric generators.

Figure 8. Equivalent circuit in low frequency averaged analysis.
With the new considerations of the TEG as a source in the equivalent circuit shown in Figure 8, some of the equations of the averaged model will also change. The equations of the input current ($I_{in}$) and current of the flyback resistance ($I_{Rf}$) can no longer be a function of the input voltage ($V_{in}$), so the output voltages $V_o$ and $V_{fo}$ are used to define them.

$$I_{Rf} = \frac{V_o - V_{fo}}{R_f}$$  \hspace{1cm} (10)

$$I_{in} = I_o + I_{Rf} = I_o + \frac{V_o - V_{fo}}{R_f}$$  \hspace{1cm} (11)

For the power ratios that correspond to the flyback converter, they will remain the same as in the previous case. The average input power ($P_{fi}$), the average output power ($P_{fo}$), and the efficiency of the flyback converter ($\eta_f$) were shown in (5), (4), and (3), respectively.

By substituting (4), (5), and (10) in (3), an expression is obtained to calculate the value of $R_f$.

$$R_f = \frac{\eta_f (V_o - V_{fo})^2}{V_{fo} I_o}$$  \hspace{1cm} (12)

Using the outer network in the equivalent circuit in Figure 8, an expression can be obtained to calculate the value of $V_{in}$.

$$V_{in} = R_s I_{in} + V_o - V_{fo}$$  \hspace{1cm} (13)

Substituting (12) in (11) and (11) in (13), a quadratic equation was obtained to determine $V_{fo}$ as a function of known variables, as shown in (14).

$$V_{in} + V_{fo} = R_s \left( I_o + \frac{V_o - V_{fo}}{\eta_f (V_o - V_{fo})^2} \right) + V_o$$

$$V_{fo}^2 + \left( V_{in} - 2V_o + R_s I_o \left( \frac{1 - \eta_f}{\eta_f} \right) \right) V_{fo} - \left( V_{in} - V_o - R_s I_o \right) V_o = 0$$

$$V_{fo} = V_o - R_s I_o \left( \frac{1 - \eta_f}{2\eta_f} \right) - V_o \pm \sqrt{\frac{V_{in} - 2V_o + R_s I_o \left( \frac{1 - \eta_f}{2\eta_f} \right)}{\eta_f} - \frac{R_s I_o V_o}{\eta_f}}$$  \hspace{1cm} (14)

With (12) and (14), the values to have $R_f$ and $V_{fo}$ are calculated in the equivalent circuit shown in Figure 8. The expression is also calculated to determine the energy processed by
the Flyback converter with respect to the total power \((Q)\) using the power flowchart shown in Figure 9.

\[
Q = \frac{P_{fi}}{P_{in} - P_{Rs}} = \frac{1}{1 + \eta_f \left(\frac{V_o}{V_{fo}} - 1\right)}
\]  

(15)

Using the same power flow scheme, the total efficiency of the circuit is calculated considering the internal resistance \(R_s\) of the thermoelectric generator.

\[
\eta_f = \frac{P_o}{P_{in} - P_{Rs}} = \frac{1}{1 + \frac{V_{fo}}{V_o} \left(\frac{1}{\eta_f} - 1\right)}
\]  

(16)

With the variables already determined of \(V_{Rs}, V_{fo}\), and \(P_{fi}\), it is possible to calculate the elements of the flyback converter with the conventional equations for the discontinuous conduction mode. Since the conventional flyback converter is a circuit that has already been widely studied in the literature, no emphasis will be placed on the equations for calculating the components.

The gain of the flyback converter \((M_f)\) is defined as:

\[
M_f = \frac{V_{fo}}{V_{in}}
\]  

(17)

If a duty cycle \((D)\) and a discharge cycle \((Desc)\) are proposed to ensure discontinuity, the ratio of turns \((n)\) can be solved from the conventional flyback converter gain equation as shown in (18).

\[
n = \frac{D}{M_f \cdot Desc}
\]  

(18)

In order to calculate the inductance of the primary \((L_1)\), the power that it must handle for each switching is considered, and (19) is obtained, and in turn, it is reflected with the ratio of turns \(n\) to obtain the inductance of the secondary \((L_2)\).

\[
L_1 = \frac{V_{Rs}^2 \cdot D^2}{2 f_s \cdot P_{fi}}
\]  

(19)

\[
L_2 = \frac{L_1}{n^2}
\]  

(20)

Finally, the capacitors will be calculated with respect to the voltage, power, and frequency at which they are working as shown in (21) and (22).

\[
C_1 = \frac{P_{Rs}}{4\pi f_s V_{fo}^2 \Delta V}
\]  

(21)

\[
C_2 = \frac{P_s}{4\pi f_s V_o^2 \Delta V}
\]  

(22)

3. Design of the Rearranged Flyback Converter

This section shows the components selected for the implementation of the rearranged flyback.

In order to corroborate the behavior of the proposed converter, it was decided to work with TEGs manufactured by “Custom Thermoelectric” with Model: 1261G-7L31-10CX1. Each TEG used has an open-circuit voltage \((V_o)\) of 5.325 V when subjected to a hot
side temperature of 200 °C and 50 °C on the cold side. The internal resistance of each TEG at that temperature is 1.1 Ω.

For this experiment, 4 TEGs connected in series were used. Therefore, the input is defined as a voltage source of 21.3 V (V_{in}) and total internal resistance in series of 4.4 Ω (R_s). For the load, a resistive load of 48.22 Ω (R_o) of 19.29 W (P_o) was selected, supplied with a voltage of 30.5 V (V_o).

In order to calculate the components of the converter, some design parameters must be proposed, which are shown in Table 1, among them, it should be noted that the discharge cycle ensures the discontinuity of the converter.

Table 1. Specifications.

| Symbol | Quantity | Value  |
|--------|----------|--------|
| R_s    | Total internal resistance of TEG’s | 4.4 Ω  |
| Desc   | Discharge cycle | 0.25   |
| V_{in} | Open circuit voltage of TEG’s | 21.3 V |
| P_o    | Output power | 19.29 W |
| V_o    | Output voltage | 30.5 V |
| R_o    | Load resistance | 48.22 Ω |
| f_s    | Switching frequency | 100 kHz |
| D      | Duty cycle | 0.75   |
| η      | Proposed efficiency of the converter | 90%    |

Table 2 shows the values obtained from the equations of the proposed model with the values of Table 1, where P_{Rs} is the power consumed by the total internal resistance of TEGs, P_{io} is the input power provided by the TEG’s without considering P_{Rs}.

Table 2. Data obtained from model equations.

| Symbol | Quantity | Value  |
|--------|----------|--------|
| I_o    | Output current | 0.6325 A |
| P_{io} | TEG’s input power | 27.88 W |
| P_{Rs} | Power consumed in Rs | 7.537 W |
| I_{io} | TEG’s input current | 1.390 A |
| L_1    | Primary inductor | 64.62 µH |
| L_2    | Secondary inductor | 6.65 µH |
| R_f    | Loss-free resistance of equivalent circuit in low frequency | 22.975 Ω |
| V_f    | Flyback output voltage on the equivalent circuit in low frequency | 14.959 V |
| V_{si} | Loss-free resistance voltage | 15.541 V |
| I_{si} | Loss-free resistance current | 0.676 A |
| P_{fi} | Flyback input power | 10.512 W |
| P_{fo} | Flyback output power | 9.461 W |
| Q      | Processed power in flyback | 51.67% |
| M_f    | Flyback converter gain | 0.9625 |
| M      | Converter gain | 1.4319 |
| P_{o-P_{fo}} | Power delivered directly to the load | 9.93 W |

Figure 10 shows the circuit simulated in Spice using the component values in Table 2. The Spice version used was OrCAD Lite v17.2-2016. Moreover, the three parts of the total circuit are also identified, which are TEGs consisting of V_{in} and R_s, modified flyback converter made up of L_1, L_2, the switch S_1, diode D_1, and capacitors C_1 and C_2. The third part is the load resistance, represented by R_o.
Figure 10. Simulation of the flyback converter in PSpice.

Figure 11, Figure 12 and Figure 13 show the simulation results obtained in Spice. In the first figure, the waveforms of the input and output voltages ($v_{in}(t)$, $v_{o}(t)$), input and output currents ($i_{in}(t)$, $i_{o}(t)$), and currents of the primary and secondary inductors ($i_{L1}(t)$, $i_{L2}(t)$) are observed. It can be seen that the flyback converter works in the conventional way and that it is working in discontinuous conduction mode.

Figure 12 shows the input and output voltages of the flyback converter ($v_{in}(t)$, $v_{o}(t)$), the output voltage ($v_{o}(t)$) and voltage of the TEGs ($v_{in}(t)-v_{Rs}(t)$), input current ($i_{Rs}(t)$), current of the primary inductors ($i_{L1}(t)$), and current that is delivered directly to the load ($i_{Rs}(t)-i_{L1}(t)$). In this image, it is better observed how partial energy processing works and its benefits. The output voltage ($v_{o}(t)$) is made up of the sum of the voltage of the TEGs ($v_{in}(t)-v_{Rs}(t)$) and the output voltage of the flyback converter $v_{o}(t)$, each contributing around 15 V. This makes the converter gain almost unity (0.9625). Finally, the third graph shows how the input current $i_{Rs}(t)$ is not pulsating even though the current of the primary inductor ($i_{L1}(t)$) has the classic triangular shape; this is compensated since the current that flows directly to the load is pulsating and is complemented by the inductor current.

Figure 13 shows the average powers consumed or contributed by each element, the input power ($P_{in}$), power consumed by $R_s$ ($P_{Rs}$), and power delivered by the TEGs ($P_{in}-P_{Rs}$); input and output power of the flyback converter ($P_{in}$, $P_{o}$); and output power ($P_o$) and power that is delivered directly to the load ($P_o$). In the first graph, the behavior of the TEGs model is observed, since in reality the power supplied by these would be $P_{in}-P_{Rs}$. In the second graph, the good efficiency in the flyback converter can be observed since it does not reduce its power much. And finally, the third graph shows the power that was not processed by the flyback converter and that is delivered directly to the load, which corresponds almost ideally to the 9.83 W that were calculated.
4. Experimental Results

A laboratory prototype has been built to carry out experimental tests and evaluate the performance of the proposed converter. A MOSFET IRF540N model and a U1520 diode model were used. The prototype for experimental tests is shown in Figure 14.
The values of the components used in the prototype are shown in Table 3. In addition, a snubber was used to suppress the harmonics and voltage spikes of the MOSFET, which is made up of a U1520 diode ($D_{\text{snubber}}$), a ceramic capacitor ($C_{\text{snubber}}$) of 5 nF, and a resistance ($R_{\text{snubber}}$) of 55 Ω. The results of the simulation are presented and compared with theoretical and experimental in Table 4.

**Table 3. Components Used in Prototype.**

| Symbol       | Quantity                                   | Value     |
|--------------|--------------------------------------------|-----------|
| $R_s$        | Total internal resistance of TEGs          | 4.4 Ω     |
| $L_1$        | Primary inductor                           | 72.995 µH |
| $L_2$        | Secondary inductor                         | 5.932 µH  |
|              | Core                                       | B66311    |
|              | Coil former                                | EF20      |
|              | Number of threads in $L_1$                 | 10        |
|              | Number of threads in $L_2$                 | 12        |
| $n$          | Transformation relation                    | 3.5       |
| $N_p$        | Number of turns of primary                 | 32        |
| $N_s$        | Number of turns of secondary               | 9         |
|              | Wire gauge                                 | 33 awg    |
| $C_1$        | Input capacitor                            | 10 µF     |
| $C_2$        | Output capacitor                           | 10 µF     |
| $R_o$        | Load resistance                            | 51.8 Ω    |
| $M_1$        | MOSFET                                     | IRF540 N  |
| $D_1$        | Diode                                      | U1520     |
| $R_{\text{snubber}}$ | Snubber resistance                      | 55 Ω     |
| $D_{\text{snubber}}$ | Snubber Diode                           | U1520     |
| $C_{\text{snubber}}$ | Snubber capacitor                        | 5 nF     |
Table 4. Summary of results of the topology.

| Parameter                        | Ideal       | PSpice      | Prototype  |
|----------------------------------|-------------|-------------|------------|
| Input Voltage $V_{in}$           | 21.3 V      | 21.3 V      | 21.3 V     |
| Internal resistance $R_s$        | 4.4 Ω       | 4.4 Ω       | 4.4 Ω      |
| Input current $I_{in}$           | 1.309 A     | 1.3754 A    | 1.272 A    |
| Input power $P_{in}$             | 27.88 W     | 29.296 W    | 27.09 W    |
| Power consumed in $R_s$          | 7.537 W     | 8.4514 W    | 7.036 W    |
| Primary inductor $L_1$           | 64.62 µH    | 64.62 µH    | 72.995 µH  |
| Secondary inductor $L_2$         | 6.65 µH     | 6.65 µH     | 5.932 µH   |
| Flyback input power $P_f$        | 10.512 W    | 11.945 W    | 11.3 W     |
| Flyback output power $P_o$       | 9.461 W     | 10.733 W    | 10.08 W    |
| Load $R_o$                       | 48.22 Ω     | 48.22 Ω     | 51.8 Ω     |
| Output voltage $V_o$             | 30.5 V      | 30.514 V    | 30.71 V    |
| Output current $I_o$             | 0.6325 A    | 0.6328 A    | 0.6053 A   |
| Output power $P_o$               | 19.29 W     | 19.31 W     | 18.58 W    |
| Processed power in flyback $Q$   | 51.67%      | 57.3%       | 56.34%     |
| Flyback efficiency $\eta_f$      | 90%         | 89.85%      | 89.2%      |
| Real system efficiency $\eta_s$  | 94.82%      | 92.638%     | 92.65%     |

In order to perform the characterization of the behavior of the proposed converter, tests were carried out with a voltage source and a variable resistance to emulate the behavior of the TEGs. This was done because it is difficult to keep the desired temperature constant in the TEGs.

Figure 15 shows the graph obtained from the input values, the input voltage ($V_{in}$) is 21.3 V at 4.0 V/div, the input current ($I_{in}$) is 1.272 A at 300 mA/div, the power supplied by the source ($P_{in}$) is 27.09 W at 4.0 W/div, PWM voltage ($V_{pulse}$) is 14.78 V at 10 V/div. In this figure, it can be seen how the input current is not pulsating, unlike a conventional flyback converter.

In order to simulate the internal resistance of TEGs, a rheostat set to 4.4 Ω was used. The voltage in the resistance ($V_{Rs}$) is 5.59 V at 1 V/div, the current through it ($I_{in}$) is 1.256 A at 300 mA/div, and the power consumed ($P_{Rs}$) is 7.036 W at 2 W/div. As shown in Figure 16.
Figure 16. Internal resistance voltage graph.

Figure 17 shows the output voltage ($V_o$) that is 30.71 V at 5 V/div, the current through the load ($I_o$) is 605.3 mA at 300 mA/div, and the power consumed ($P_o$) is 18.58 W at 4 W/div. In addition to that, a ripple of 0.61% was obtained in the output voltage and 0.82% in the case of the current.

Figure 17. Output voltage and current graph.

The flyback input voltage in the flyback converter ($V_{fi}$) is 16.37 V at 3 V/div, the average current through the primary side ($I_{L1}$) is 701.9 mA at 500 mA/div, the maximum peak current is 1.64 A, and the average input power in the flyback converter ($P_{fi}$) is 11.3 W at 10 W/div; this is seen in Figure 18.
Finally, Table 4 shows a comparative table of all the important variables for the calculations, simulation, and implementation. The slight variations that can be observed are due to implementation details such as the value of the inductances that are not exact or losses due to coupling. In a quantitative way, the percentage of error can be seen in Table 5 for each parameter. The largest errors were obtained in the inductances, since it is difficult to construct them when the calculation of the number of turns is not a whole number.

| Parameter                      | Ideal-PSpice | PSpice-Prototype | Ideal-Prototype |
|-------------------------------|--------------|------------------|-----------------|
| Input Voltage $V_{in}$        | 0.0%         | 0.0%             | 0.0%            |
| Internal resistance $R_s$     | 0.0%         | 0.0%             | 0.0%            |
| Input current $I_{in}$        | 5.1%         | 7.5%             | 2.8%            |
| Input power $P_{in}$          | 5.1%         | 7.5%             | 2.8%            |
| Power consumed in $R_s$       | 12.1%        | 16.7%            | 6.6%            |
| Primary inductor $L_1$        | 0.0%         | 13.0%            | 13.0%           |
| Secondary inductor $L_2$      | 0.0%         | 10.8%            | 10.8%           |
| Flyback input power $P_f$     | 13.6%        | 5.4%             | 7.5%            |
| Flyback output power $P_{fo}$ | 13.4%        | 6.1%             | 6.5%            |
| Load $R_o$                    | 0.0%         | 7.4%             | 7.4%            |
| Output voltage $V_o$          | 0.0%         | 0.6%             | 0.7%            |
| Output current $I_o$          | 0.0%         | 4.3%             | 4.3%            |
| Output power $P_o$            | 0.1%         | 3.8%             | 3.7%            |
| Processed power in flyback $Q$| 10.9%        | 1.7%             | 9.0%            |
| Flyback efficiency $\eta_f$   | 0.2%         | 0.7%             | 0.9%            |
| Real system efficiency $\eta_s$| 2.3%     | 0.0%             | 2.3%            |

In Figure 19, the output voltage of the flyback converter ($V_o$) is 14.76 V at 4 V/div, the average current through the secondary side ($I_{L2}$) is 681.1 mA at 2 A/div, the maximum peak current is 6.6 A, and the ($P_{fo}$) flyback average output power is 10.08 W at 25 W/div.
There are different references that present similar results to this work. For example, reference [30] presents an efficiency of 92.6% for an input power of 30 W using a Buck–Boost converter with an open-circuit voltage measurement technique. Reference [15] shows an efficiency of 88.7% for an input power of 30 W using a Boost converter with Incremental Conductance technique. Reference [16] presents an efficiency of 90.126% for an input power of 24.04 W using a flyback converter with Incremental Conductance technique. This paper shows an efficiency of 92.65% for an input power of 20.054 W using the proposed flyback converter with a constant duty cycle of 75%. Efficiency is expected to improve when using an MPPT control technique.

5. Conclusions

This work presents a theoretical analysis of a converter which is based on a variant of the flyback converter and is used for energy harvesting applications based on thermoelectric generators, providing advantages such as partial power processing and less voltage and current stress in the converter.

A prototype was built that delivered 18 W at a resistive load and had an efficiency of 92.65%. The main advantage of this converter was verified, which is the partial power processing, since 56.34% (11.3 W) of the energy was processed by the flyback converter while 43.66% (8.7 W) of the energy is delivered directly to load. Since in this rearrangement the flyback converter processes less energy, it was obtained that the voltage in the components is lower than in a conventional flyback. The output voltage of the flyback converter ($V_{fo}$) was 14.959 V, which is just a part of the output voltage ($V_o$) which is 30.5 V. Furthermore, this operation allows the efficiency of the proposed converter to always be higher than that of the conventional flyback converter. If only the efficiency of the flyback converter is considered ($\eta_f$), this is 89.2%. However, the efficiency of the complete system ($\eta_s$) thanks to the rearrangement of the elements is 92.65%. The main disadvantages of the converter are that it has no magnetic isolation and that it is only a boost topology.

For future works, the efficiency of the system is expected to be increased by using a Maximum Power Point Tracking control technique for the proposed convert.

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References

1. Lv, S.; He, W.; Jiang, Q.; Hu, Z.; Liu, X.; Chen, H.; Liu, M. Study of different heat exchange technologies influence on the performance of thermoelectric generators. Energy Convers. Manag. 2018, 156, 167–177, doi:10.1016/j.enconman.2017.11.011.

2. Sudharshan, K.; Kumar, V.P.; Barshilia, H.C. Performance evaluation of a thermally concentrated solar thermo-electric generator without optical concentration. Sol. Energy Mater. Sol. Cells 2016, 157, 93–100.

3. Maran, A.L.O.; Henao, N.C.; Silva, E.A.; Schaeffer, L.; Junior, O.H.A. Use of the Seebeck Effect for Energy Harvesting. IEEE Lat. Am. Trans. 2016, 14, 4106–4114.

4. Cao, Q.; Luan, W.; Wang, T. Performance enhancement of heat pipes assisted thermoelectric generator for automobile exhaust heat recovery. Appl. Therm. Eng. 2018, 130, 1472–1479.

5. Kumar, T.K.; Kumar, S.A.; Ram, K.K.; Goli, K.R.; Prasad, V.S. Analysis of thermo electric generators in automobile applications. Mater. Today Proc. 2020, doi:10.1016/j.matpr.2020.08.081.

6. Sornke, K.; Filipowicz, M.; Żołądek, M.; Kot, R.; Mikrut, M. Comparative analysis of selected thermoelectric generators operating with wood-fired stove. Energy 2019, 166, 1303–1313.

7. Dalala, Z.M.; Hamdan, Z.S.; Al-Taani, H.; Al-Addous, M.; Albatayneh, A. Battery Charging Application with Thermoelectric Generators as Energy Harvesters. Acad. Res. Community Publ. 2019, 3, 248.

8. Ebbing, D.G.; Krumm, A.; Pfeiffelmann, B.; Gottschald, J.; Bruchmann, J.; Benim, A.C.; Ádám, M.; Labs, R.; Herbertz, R.R.; Stunz, A. Development of a System for Thermoelectric Heat Recovery from Stationary Industrial Processes. J. Electron. Mater. 2016, 45, 3433–3439.

9. Francisco, L.; De Pascali, C.; Sgavlo, V.; Grazioli, A.; Masieri, M.; Siciliano, P. Modelling, fabrication and experimental testing of an heat sink free wearable thermoelectric generator. Energy Convers. Manag. 2017, 145, 204–213.

10. Ma, Q.; Fang, H.; Zhang, M. Theoretical analysis and design optimization of thermoelectric generator. Appl. Therm. Eng. 2017, 127, 758–764.

11. Yu, H.; Chen, M.; Wu, C.; Tang, K.-T.; Wang, G. A batteryless and single-inductor DC-DC boost converter for thermoelectric energy harvesting application with 190mV cold-start voltage. In Proceedings of the 2018 IEEE International Symposium on Circuits and Systems (ISCAS), Florence, Italy, 27–30 May 2018; pp. 1–4.

12. Moreno, J.M.; Vazquez, A.S.M.; Barragan, C.A.B.; Gonzalez, J.M.V.; Rosas, J.C.A. Radio Frequency Energy Harvesting System Making Use of 180° Hybrid Couplers and Multiple Antennas to Improve the DC Output Voltage. IEEE Lat. Am. Trans. 2020, 18, 604–612.

13. Chen, I.-C.; Liang, C.-W.; Tsai, T.-H. A Single-Inductor Dual-Input Dual-Output DC-DC Converter for Photovoltaic and Piezoelectric Energy Harvesting Systems. IEEE Trans. Circuits Syst. II Express Briefs 2019, 66, 1763–1767.

14. Twaha, S.; Zhu, J.; Yan, Y.; Li, B.; Huang, K. Performance analysis of thermoelectric generator using dc-dc converter with incremental conductance based maximum power point tracking. Energy Sustain. Dev. 2017, 37, 86–98.

15. Bhattacharya, A.; Majumdar, S. Design of a 22 W (0.7 A) Current Controlled DC-DC Flyback Converter Operating in DCM Mode. In Emerging Trends in Electrical, Communications, and Information Technologies; Springer: New York, USA, 2020; pp. 247–258.

16. CIPesce, C.; Riedemann, J.; Pena, R.; Jara, W.; Maury, C.; Villalobos, R. A Modified Step-Up DC-DC Flyback Converter with Active Snubber for Improved Efficiency. Energies 2019, 12, 2066.

17. Boeke, U. High efficiency flyback converter technology. In Proceedings of the 2007 Power Conversion Conference-Nagoya, Nagoya, Japan, 2–5 April 2007; pp. 1268–1273.

18. Eshkevari, A.L.; Mosallanejad, A.; Sepasian, M.S. Design, modelling, and implementation of a modified double-switch flyback-forward converter for low power applications. IET Power Electron. 2019, 12, 739–748.

19. Fatihabadi, H. Novel solar-powered photovoltaic/thermoelectric hybrid power source. Renew. Energy 2020, 146, 426–434.

20. Gioria, V.T.; Vina, V.T. Highly Efficient Boost-Flyback coupled magnetic integrated converter for photovoltaic energy. Int. J. Circuits Electron. 2018, 3, 14–18.

21. Somiruwan, G.; Gunawardena, L.; Nayanasiri, D.; Li, Y. High-Step-Up Boost Converter Based on Coupled Inductor, Voltage Lift and Clamp Cells. In Proceedings of the 2019 IEEE Applied Power Electronics Conference and Exposition (APEC), Anaheim, CA, USA, 17–21 March 2019; pp. 2305–2310.

22. Camponogara, D.; Ferreira, G.F.; Campos, A.; Costa, M.A.D.; Garcia, J. Offline LED driver for street lighting with an optimized cascade structure. IEEE Trans. Ind. Appl. 2013, 49, 2437–2443.

23. Tse, C.K.; Chow, M.H.L.; Cheung, M.K.H. A family of PFC voltage regulator configurations with reduced redundant power processing. IEEE Trans. Power Electron. 2001, 16, 794–802.
25. Morales-Saldaña, J.A.; Loera-Palomó, R.; Palacios-Hernández, E.R.; González-Martínez, J.L. Modelling and control of a DC–DC quadratic boost converter with R 2 P 2. *IET Power Electron.* 2014, 7, 11–22.

26. Zogogianni, C.G.; Tatakis, E.C.; Vekic, M.S. Non-Isolated Reduced Redundant Power Processing DC/DC Converters: A Systematic Study of Topologies With Wide Voltage Ratio for High-Power Applications. *IEEE Trans. Power Electron.* 2019, 34, 8491–8502.

27. Zogogianni, C.G.; Tatakis, E.C.; Porobic, V. Investigation of a Non-isolated Reduced Redundant Power Processing DC/DC Converter for High-Power High Step-Up Applications. *IEEE Trans. Power Electron.* 2018, 34, 5229–5242.

28. Ponce-Silva, M.; Salazar-Pérez, D.; Rodríguez-Benítez, O.M.; Vela-Valdés, L.G.; Claudio-Sánchez, A.; De León-Aldaco, S.E.; Cortés-García, C.; Saavedra-Benítez, Y.I.; Lozoya-Ponce, R.E.; Aquí-Tapia, J.A. Flyback Converter for Solid-State Lighting Applications with Partial Energy Processing. *Electronics* 2020, 10, 60.

29. Zapata, J.W.; Kouro, S.; Carrasco, G.; Renaudineau, H. Step-Up Partial Power DC-DC Converters for Two-Stage PV Systems with Interleaved Current Performance. *Energies* 2018, 11, 357.

30. Montecucco, A.; Knox, A.R. Maximum Power Point Tracking Converter Based on the Open-Circuit Voltage Method for Thermoelectric Generators. *IEEE Trans. Power Electron.* 2014, 30, 828–839.