Demonstration of Shor encoding on a trapped-ion quantum computer

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(Dated: April 6, 2021)

Fault-tolerant quantum error correction (QEC) is crucial for unlocking the true power of quantum computers. QEC codes use multiple physical qubits to encode a logical qubit, which is protected against errors at the physical qubit level. Here we use a trapped ion system to experimentally prepare \( m \)-qubit GHZ states and sample the measurement results to construct \( m \times m \) logical states of the \([m^2, 1, m] \) Shor code, up to \( m = 7 \). The synthetic logical fidelity shows how deeper encoding can compensate for additional gate errors in state preparation for larger logical states. However, the optimal code size depends on the physical error rate and we find that \( m = 5 \) has the best performance in our system. We further realize the direct logical encoding of the \([9, 1, 3] \) Shor code on nine qubits in a thirteen-ion chain for comparison, with 98.8(1)% and 98.5(1)% fidelity for state \(|\pm\rangle_L \), respectively.

I. INTRODUCTION

Fault-tolerant logical qubit encoding and fault-tolerant operations are required for executing quantum algorithms of sufficient depth to solve relevant problems [1–3]. Fault-tolerant operations, such as state preparation, syndrome measurement, error correction, logical gates, and measurements are designed such that any physical-level error they introduce is corrected at the logical level [4]. When the physical error rate is below a certain threshold, the logical error can be made arbitrarily small by concatenation, i.e., using multiple layers of encoding [5], or taking advantage of natural robustness within the system [6]. The optimal method for fault-tolerant quantum computation is unknown and current methods offer trade-offs between encoding rate, threshold [7, 8], and the number of available fault-tolerant gates [9–11]. The same is true for near-term quantum error correction where only a limited amount of protection from physical-level errors will be available.

The Shor code [12] protects against all physical single-qubit Pauli errors. While the canonical \([9, 1, 3] \) code is based on triple modular redundancy, larger \([m^2, 1, m] \) Shor codes can be generated using \( m \)-modular redundancy, where \( m \) is the number of physical qubits in each module. The Shor code, together with the rotated surface code [13] and the Bacon-Shor subsystem code [14], is an example of a compass code [15]. The surface code has high memory and circuit-level thresholds, and treats phase- and bit-flip errors equivalently [16–18]. The Bacon-Shor code has no asymptotic threshold with \( m \) for either \( X \) or \( Z \) errors [19]. The Shor code on the other hand has a memory threshold of 50% for \( Z \) errors and no threshold for \( X \) errors as \( m \) increases. In practice, this means that, for any physical error rate, there is an optimal size for the Shor and Bacon-Shor codes [19]. These optimal codes can then be concatenated in a modular fashion to further improve performance [20]. Theoretical investigations comparing the 17-qubit rotated surface code [13] to a compass code on a 3×3 qubit lattice find the latter to have much better performance in a realistic ion trap error model [21]. In this paper, we find the optimal size \( m \) of the Shor code that can be implemented on a particular trapped-ion quantum computer and investigate how measurements on a few qubits can predict the performance of larger systems.

Trapped ions are a promising platform for realizing a large-scale fault-tolerant quantum computer due to their long coherence time [22], high connectivity [23, 24], high-fidelity single- and two-qubit gates [25, 26], and scalable architectures [27, 28]. Also, different components needed for fault-tolerance have been successfully demonstrated on trapped ions, such as logical state preparation [29, 30], single-qubit logical operations [29–31], quantum error-detection with stabilizer readout [30, 32], magic state preparation [30] and multiple rounds of feedback-correction [33]. Here we prepare \( m \)-qubit GHZ states on a trapped-ion system and extrapolate the logical error rate classically in order to emulate state preparation and measurement of an \([m^2, 1, m] \) Shor code, where \( m = 3, 4, 5, 6, 7 \). This emulation yields the optimal code size for our current system. We then compare the emulated \( m = 3 \) results to the full 3×3 code state preparation on nine physical qubits.

The structure of the paper is as follows. In Section II we review the Shor code and describe the methods used to study \([m^2, 1, m] \) codes. In Section III we outline the experimental setup. In Section IV we present the experimental results on scaling. In Section V we demonstrate the logical basis state preparation of \( m = 3 \) with 9 qubits. Lastly in Section VI we discuss the implications of these results for realizing fault-tolerant quantum computing.
II. THE SHOR CODE

An \([m^2, 1, m]\) Shor code uses \(m \times m\) physical qubits to encode a single logical qubit with distance \(m\), i.e., any two orthogonal logical states differ by at least \(m\) bit- or phase-flips. It is constructed from the concatenation of an \(m\)-bit repetition code that corrects \(X\) errors with an \(m\)-bit repetition code that corrects \(Z\) errors [12]. Since all Pauli errors can be described as combinations of \(Z\) and \(X\) errors, measuring the stabilizers returns one of the potential syndromes, which give the location and type of the physical errors. These can then be remedied by applying suitable \(X\) and/or \(Z\) correction operations. For the \([9, 1, 3]\) Shor code only a single physical error can be diagnosed unambiguously, since it has distance 3.

State preparation starts by fault-tolerantly creating a logical basis state, followed by fault-tolerant logical gates to generate a desired logical state \(|\psi\rangle_L\). For an \([m^2, 1, m]\) Shor code, the logical basis is given by \(|\pm\rangle_L = |\text{GHZ}^\pm_m\rangle^\otimes m\), where \(|\text{GHZ}^\pm_m\rangle = \frac{1}{\sqrt{2}}(|0\rangle^\otimes m \pm |1\rangle^\otimes m\rangle).\) Since the \(|\pm\rangle_L\) are product states of \(|\text{GHZ}^\pm_m\rangle\), we can prepare and measure many copies of a single \(|\text{GHZ}^\pm_m\rangle\) and randomly sample from these copies to artificially construct results corresponding to an \(m \times m\) logical state. For example, with \(m = 3\), the logical states are \(|+\rangle_L = \frac{1}{\sqrt{2}}(|000\rangle + |111\rangle)^\otimes 3\) and \(|-\rangle_L = \frac{1}{\sqrt{2}}(|000\rangle - |111\rangle)^\otimes 3\). The circuit for encoding the \(|+\rangle_L\) separates into three independent sub-circuits for creating 3-qubit GHZ states, see Fig. 1. This is also the case for state preparation of the Bacon-Shor subsystem code [30]. This up-sampling allows us to study an \(m \times m\)-qubit Shor code with only \(m\) qubits. However, some physical errors that come with larger system sizes such as cross-talk and others [34] are underestimated.

For a \([9, 1, 3]\) Shor code, there are eight stabilizers, six \(Z\) stabilizers, which detect \(X\) errors, and two \(X\) stabilizers, which detect \(Z\) errors [21]. Therefore the code is better at detecting \(X\) errors than \(Z\) errors. To detect a single bit-flip within any GHZ sub-group, we measure the \(Z\) stabilizers \(Z_jZ_{j+1}\) for the physical qubit index \(j = 1, 2, 4, 5, 6, 7\). To detect a single phase-flip error, we measure the \(X\) stabilizers \(X_1X_2X_3X_4X_5X_6\) and \(X_1X_2X_5X_6X_7X_8\). These error detection measurements can be done in a non-destructive way by projecting the parity onto an ancilla qubit and measuring it without disturbing the code qubits [35]. In our experiment we directly perform the projective measurement on the physical qubits and perform the error detection or correction procedure in post-processing. While not realizing a full error correction scheme, we still observe how logical errors scale with the number of physical qubits and gates used for state preparation.

III. TRAPPED ION SETUP

We carry out this experiment on a chain of trapped ions in a linear Paul trap. Two states in the hyperfine-split \(^2S_{1/2}\) ground level of \(^{171}\text{Yb}^+\), \(|F = 0, m_F = 0\rangle\) and \(|F = 1, m_F = 0\rangle\), form the qubit. The ions are laser-cooled close to the motional ground state and initialized to \(|0\rangle\) via optical pumping. Coherent operations are performed with two counter-propagating Raman laser beams, derived from a pulsed laser at 355 nm. The difference between relevant frequency components is stabilized to the energy splitting of the qubit. One of the Raman beams is split into an array of individual addressing beams, each of which is tightly focused onto exactly one ion, while the other is a global beam that illuminates the entire chain. We have frequency, amplitude, and phase control over each individual beam to selectively apply single-qubit and two-qubit gate operations. Detection is done via state-dependent fluorescence, where each ion is imaged onto one channel of a photo-multiplier tube array. Detailed performance of the system has been described elsewhere [36, 37]. For this work, we have extended the setup to operate on up to thirteen ions, at most nine of which act as qubits.

The native gate set consists of single-qubit rotations around any axis \(\vec{n}\) in the \(x\)-\(y\) plane, \(R_{\phi}^j(\theta) = e^{-i\vec{n}_j \cdot \sigma_j \theta / 2}\), rotations around the \(z\)-axis applied as classical phase shifts, \(R_\phi^z(\theta) = e^{-i\sigma_z \theta / 2}\), and two-qubit entangling gates \(X_1X_4(\theta) = e^{i\sigma_x^1 \sigma_x^4 \theta}\) between any pair. These entangling gates are executed via spin-motion coupling based on the Mølmer-Sørensen scheme [38–40]. We decouple the spin from the harmonic motion of all the modes by implementing a series of amplitude and frequency modulated pulses [41, 42]. The fidelity for both single- and two-qubit gates is mainly limited by beam misalignment, beam-pointing instabilities, imperfect Stark-shift compensation and axial micromotion for all but the center ion. We do not have the ability to apply a quartic axial potential in order to space the ions equally. As a re-
result, the alignment of the equally-spaced individual addressing beams worsens for larger numbers of ions in the trap. We mitigate this effect to some degree by using up to two additional ions at the each end of the chain. For five qubits, we trap seven ions; for seven qubits, we trap nine ions; for nine qubits, we trap thirteen ions. Trap imperfections also cause an unwanted axial radio-frequency (RF) field component that leads to axial micromotion. Therefore, in our setup, the single-qubit and two-qubit gate fidelities tend to decrease with the number of ions. The average fidelity for single-qubit gates (except $R_{y}$) in our experiment is 99.0(5)% after correcting for state-preparation-and-measurement (SPAM) error. Typical fidelities for two-qubit gates are 99% for a five-qubit system, 98.5% for a seven-qubit system and 98% for a nine-qubit system.

IV. SCALING OF THE SHOR CODE

We use the circuit in Fig. 2 to prepare $|{\text{GHZ}}^{\pm}_{m}\rangle$ states using our native gate set. The results of measuring in the Z-basis for $m = 3$ are shown in Fig. 3 (a). To measure in the X-basis, we apply $H^\otimes m$ before detection which creates an equal superposition of all even- and odd-parity computational states for $|{\text{GHZ}}^{\pm}_{m}\rangle$ and $|{\text{GHZ}}^{-}_{m}\rangle$, respectively, i.e. $X^\otimes m = 1$ and $-1$. The results for $m = 3$ are shown in Fig. 3 (b). The probability of measuring the $|{\text{GHZ}}^{\pm}_{m}\rangle$ in the Z or X basis, shown in Fig. 4 (a), is given by summing the relevant measured state populations,

$$F_{z} = P_{00...0} + P_{11...1},$$

$$F_{x}^{\pm} = \sum_{s \text{ even odd}} P_{s}.$$  

We sample a group of $m$ experimental shots from $|{\text{GHZ}}^{\pm}_{m}\rangle$ to construct an artificial shot corresponding to the measurement of an $m \times m$ logical state $|\pm\rangle_{L}$, which we read out by majority voting. For even $m$, ties are assigned randomly. Repeating this $N/m$ times, where $N$ is the total number of experimental repetitions, we arrive at the fidelities $F_{L}^{\pm}$ for $m = 3, 4, 5, 6, 7$ shown in Fig. 4 (b) and Table I.

For large $N$ the up-sampled logical fidelities are given by

$$F_{L}^{\pm} = \sum_{k=(m+1)/2}^{m} \binom{m}{k}(F_{x}^{\pm})^{k}(1-F_{x}^{\pm})^{m-k}$$  

(3)

for odd $m$, and

$$F_{L}^{\pm} = \sum_{k=(m+2)/2}^{m} \binom{m}{k}(F_{x}^{\pm})^{k}(1-F_{x}^{\pm})^{m-k}$$

$$+ \frac{1}{2} \binom{m}{m/2}(F_{x}^{\pm})^{m/2}(1-F_{x}^{\pm})^{m/2}$$  

(4)

for even $m$, due to the random assignment of ties.

Assuming depolarizing errors dominate, the physical qubit fidelity is roughly $f = F_{x}/m$ [19] and therefore the logical fidelity is

$$F_{L}^{\pm} = \sum_{k=[m/2]}^{m} \binom{m}{k}(mf)^{k}(1-mf)^{m-k}.$$  

(5)

Fig. 5 plots the dependence of the logical error rate, which is $1 - F_{L}^{\pm}$, on the physical error rate, which is $1 - f$, for different code sizes $m = 3, 5, 7, 9$ as given by Eq. (5). There is a cross-over point in the physical error rate where deeper encoding compensates for the larger number of gate errors that can arise when preparing larger GHZ states. The experimental results presented in Table I and Fig. 4 follow the estimated fidelity given by Eqs. (3) and (4).

Although the fidelity to prepare five-qubit GHZ states is lower than that of the three-qubit GHZ states, the up-sampled logical states for the [[25, 1, 5]] code has a higher...
fidelity than that for the up-sampled \([9,1,3]\) code after majority voting. This hints at the onset of fault-tolerance, since it demonstrates that deeper encoding can compensate for the increase in physical errors caused by employing more qubits and gates, leading to a lower logical error than a shallower code. This increase is not replicated when going to \([49,1,7]\), which shows that the state preparation errors have increased substantially as seen in the drop in the fidelity of GHZ\(_m^\pm\) (Fig. 4). The random assignment of ties leads to a lower probability for \( m = 4,6\) in Fig. 4 (b).

The additional errors in the logical state preparation and measurement (SPAM) process mainly come from an increase in single- and two-qubit gate errors for longer ion chains as discussed in Section III, and read-out errors because of cross-talk between photo-multiplier tube channels, i.e. physical SPAM errors. Physical read-out cross-talk accounts for \(1 - 5\%\) infidelity in the Z -measurements, depending on \(m\). The rest is from two-qubit gates, which corresponds to an average of 0.9% error per gate for \(m = 3\), 1.3% for \(m = 4\), 1.6% for \(m = 5\), 1.7% for \(m = 6\) and 2.2% for \(m = 7\). These errors come from the increased beam mismatch discussed in Section III.

The Shor code can also be used as an error detection code, where non-unanimous votes are discarded rather than corrected, which leads to a finite yield. The fidelity and yield of the logical states after this procedure are presented in Table II. The fidelities are higher than for the correction scheme since all \(m\) qubits have to flip for a logical error to occur. For large \(N\), the yield can be estimated by \((F_x^\pm)^m + (1 - F_x^\pm)^m\). The optimal code size has increased to 6, which is a valid size since ties don’t play a role in the detection code.

Using Eq. (3) we can estimate the minimal \(|\text{GHZ}_m^\pm\rangle\) fidelities needed in order for the up-sampled \(m \times m\)-qubit logical state to have the same fidelity as that of the \(3 \times 3\)-qubit logical state. For \(m = 5\), it is 0.93; for \(m = 7\) qubits, it is 0.895, which translates to an average infidelity of 1.7% per two-qubit gate.
Table II. Fidelity of logical states of \([m^2, 1, m]\) after discarding non-unanimous results (Error detect) and the success probability (Yield). Data are taken with \(N = 20000\) shots.

| \(m\) | Prepare | Error detect | Yield |
|------|---------|-------------|-------|
| 3    | +       | 0.99995(1)  | 0.00005(1)  | 0.898(4)  |
|      | −       | 0.00003(1)  | 0.99997(1)  | 0.906(4)  |
| 4    | +       | 0.99999(1)  | 0.00001(1)  | 0.804(6)  |
|      | −       | 0.000009(1) | 0.999991(1) | 0.812(6)  |
| 5    | +       | 0.999998(1) | 0.000002(1) | 0.717(3)  |
|      | −       | 0.000003(1) | 0.999997(1) | 0.690(4)  |
| 6    | +       | 0.9999996(2)| 0.0000004(1)| 0.594(5)  |
|      | −       | 0.0000010(1)| 0.9999990(1)| 0.581(5)  |
| 7    | +       | 0.999997(1) | 0.000003(1) | 0.373(6)  |
|      | −       | 0.000002(1) | 0.999998(1) | 0.371(6)  |

Table III. Fidelity of state preparation and measurement for three sets of \([\text{GHZ}_3^\pm]\) labeled as 1, 2, 3 and the logical state \(|\pm_L\rangle\) of \([9,1,3]\) code on a thirteen-ion chain.

| Prepare | Measure | + | − |
|---------|---------|---|---|
| Logical  | +       | 0.988(1)  | 0.012(1)  |
|         | −       | 0.015(1)  | 0.985(1)  |
| |GHZ}_3^\pm\rangle_1 | +       | 0.942(1)  | 0.058(1)  |
| |GHZ}_3^\pm\rangle_2 | +       | 0.942(1)  | 0.058(1)  |
| |GHZ}_3^\pm\rangle_3 | +       | 0.070(1)  | 0.921(1)  |
|         | −       | 0.068(1)  | 0.932(1)  |

V. LOGICAL QUBIT ENCODING

We also perform the full \([9,1,3]\) encoding with nine qubits in a 13-ion chain. In this experiment we directly generate and read out logical states \(|\pm_L\rangle\) of the \([9,1,3]\) Shor code. We also characterize the individual \([\text{GHZ}_3^\pm]\) states with measurements in the X-basis. The results are presented in Table III and Fig. 6.

The fidelity of the logical states \(|\pm_L\rangle\) is at the level of the physical state \(|\mp\rangle\) and hence falls short of the average performance of the physical qubit \(|\pm\rangle\). Using Eq. (3), we estimate that the fidelity of each \([\text{GHZ}_3^\pm]\) state must be increased to 0.968 in order to achieve the same logical fidelity as our physical qubit \(|\pm\rangle\). It is worth noting that the fidelities of the \([\text{GHZ}_3^\pm]\) triplet on nine qubits are very similar to each other. This indicates a high level of uniformity among the qubits and gates.

Notice that the fidelities of both the \([\text{GHZ}_3^\pm]\) states and logical \(|\pm_L\rangle\) states are lower than their corresponding counterparts in the up-sampled experiment with three qubits in a chain of seven ions (see Fig. 4). The additional errors reduce the fidelity compared to the up-sampled version by about 1%.

VI. DISCUSSION & OUTLOOK

Our preparation and sampling of \([\text{GHZ}_m^\pm]\) states to synthetically construct \(m \times m\) logical Shor code states shows experimentally that deeper encoding can compensate for additional physical errors from logical state preparation. For our specific setup, the \([\text{GHZ}_m^\pm]\) state projects the best logical fidelities. The increase in physical errors with larger \(m\) we observe is due to hardware limitations, most of which can be solved by better engineering. For example, detection cross-talk can be eliminated by independent photon-detectors [30, 43, 44]. The beam-ion alignment can be improved by traps with more control electrodes, such as micro-fabricated surface traps [45] or blade traps with more segments [46]. Alternatively, near-perfect ion addressing can be achieved with integrated optics [47, 48] or beam steering using a micro-electromechanical system of mirrors [49]. Axial RF stray fields, and hence axial micromotion is also greatly reduced in precision-fabricated surface or 3D traps [50, 51].

The comparison of the emulated 9-qubit Shor state to a direct preparation of the 9-qubit Shor code states shows a 2% decrease in the fidelity of individual \([\text{GHZ}_m^\pm]\) states and a 1% decrease in the SPAM logical fidelity. This result points toward future work where parts of quantum error corrected codes can be used as benchmarks for system scalability and uniformity. Recent work has emphasized that physical errors are not uniform over the Pauli operators [15, 52–54]. Although the \(m \times m\) Shor code has no threshold for one type of Pauli error, it matches the classical threshold for the other Pauli error. This makes the Shor code an exciting choice for quantum memories.
with asymmetric errors. Further work on asymmetric Shor codes and how they interact with bias-preserving gates is warranted [55].

ACKNOWLEDGMENTS

We are grateful to T. Yoder for helpful discussions and comments on the manuscript. We also thank S. Deb-
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