Applicability of Well-Established Memristive Models for Simulations of Resistive Switching Devices

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Abstract—Highly accurate and predictive models of resistive switching devices are needed to enable future memory and logic design. Widely used is the memristive modeling approach considering resistive switches as dynamical systems. Here we introduce three evaluation criteria for memristor models, checking for plausibility of the $I$-$V$ characteristics, the presence of a sufficiently non-linearity of the switching kinetics, and the feasibility of predicting the behavior of two anti-serially connected devices correctly. We analyzed two classes of models: the first class comprises common linear memristor models and the second class widely used non-linear memristive models. The linear memristor models are based on Strukov’s initial memristor model extended by different window functions, while the non-linear models include Pickett’s physics-based memristor model and models derived thereof. This study reveals lacking predictivity of the first class of models, independent of the applied window function. Only the physics-based model is able to fulfill most of the basic evaluation criteria.

Index Terms—Memristor, memristive system, resistive switching, ReRAM, complementary resistive switch, modeling, SPICE

I. INTRODUCTION

Redox-based resistive switches (ReRAM) are an emerging class of two terminal non-volatile devices considered the key components for future memories and logic circuits [1-3]. There are two important subclasses of redox-based bipolar resistive switches. The first one, which is called electrochemical metallization (ECM) or conductive bridge memory (CBRAM), is based on the formation of Cu or Ag filaments [4]. The second one is based on formation of oxygen-deficient filaments in transition metal oxides, so called valence change mechanism (VCM) [5]. Typical VCM devices comprise materials such as SrTiO$_x$, TaO$_y$, HfO$_z$, or TiO$_y$. The field of resistive switching has experienced a substantial progress in 2008 by Strukov et al. suggesting to consider TiO$_x$-based devices as memristive systems [6, 7], or generalized memristors for short [8, 9]. TiO$_x$-based devices are prototypical for the whole class of VCM devices [10], thus modeling all VCM devices as memristive devices should be feasible [9]. The memristive modeling approach enables to capture the complex dynamic behavior of resistive switches by means of a simple ordinary differential equation system which is directly implementable for circuit simulations [6]. The availability of accurate circuit models of resistive switches is the fundament for proper circuit design and further development of novel computer-architectural approaches, as suggested in [11-14], for example.

In general, a memristive system reads

$$\dot{x} = h(x, I)$$

$$V = R(x, I) \cdot I$$

where $x$ is the inner state variable which is multidimensional in general.

On the basis of Strukov’s initial memristor model [7], a whole class of models using different kinds of state variable boundaries, i.e. window functions, has been derived [15-20]. These models are widely used due to simplicity and ease of use [13, 21-22]. In this paper, we evaluate these models with respect to generic properties of resistive switches as well as simulation robustness.

Furthermore, we consider a second class of models which is based on a more complex modeling approach by Pickett et al. and implemented in SPICE by Abdalla et al. [23, 24]. On basis of this physics-oriented modeling approach [25] some simplified and generalized memristive models have been derived [26-29]. We include these models to our comparison and evaluate the properties of these resistive switch models, too.

A comprehensive review of SPICE implementations of several above mentioned models can be found in [30] which was the starting point for our analysis. From the circuit engineer’s point of view a model should be as simple as possible, but as complex as required to reproduce essential properties of the device. First of all, to enable a reasonable comparison, we introduce three evaluation criteria extracted from experimental data: 1) the $I$-$V$ characteristics and robustness of the models against parameter input and input signals, 2) the device switching kinetics, and 3) the
applicability to simulate complementary resistive switching behavior, i.e. the anti-serial connection of two elements.

II. EVALUATION CRITERIA

In memristive modeling a resistive switch is considered a dynamical system (equations (1)-(2)). Thus, it should be possible to simulate the device behavior for a wide range of input signals. This is the main strength of the memristive modeling approach compared to using models with built-in fixed threshold voltages [31, 32], which are only valid for certain input signals.

The first evaluation criterion considers the I-V characteristics of bipolar resistive switches (Fig. 1a) which exhibit some distinct features that should be reproduced by a suitable model (compare [5], for example).

Typically, during the SET operation an abrupt increase in current is observed for ECM and VCM-based ReRAM cells. The RESET operation, however, differs for these two classes: VCM devices often show a gradual RESET whereas ECM devices exhibit an abrupt change. Furthermore, the I-V characteristics are asymmetric with respect to the origin. In addition, the SET and RESET voltages increase when the sweep rate of the voltage sweep is increased. Additionally, we know from experiment that a wide range of excitation signals will lead to resistive switching device behavior. In so far, a suitable memristive model should offer certain robustness against changes in the input voltage amplitude and variations of initial values, e.g. the initial value of the state variable.

The second criterion is related to the switching kinetics. In experiments a strong non-linear relationship between SET time \( t_{\text{SET}} \) and pulse height \( V_p \) is observed (see Fig. 2). Here we selected device data from four typical VCM devices: strontium titanate [34], tantalum oxide [35], hafnium oxide [36], and titanium oxide [37]. A common fingerprint of all VCM devices is the decrease of \( t_{\text{SET}} \) by several orders of magnitude by only increasing the pulse amplitude \( V_p \) by a factor of two. Hence, our second criterion is the check for such an exponential dependency. Fulfilling this criterion is essential to enable simulation of typical applications using memristive devices (either memory or logic applications) which are conducted by fast pulses.

A third criterion arises from the need for multi-element simulations for mapping real-world circuits. Two-element circuits are the simplest application, and therefore, are well suited for a basic analysis. Here, we consider an anti-serial connection of two memristive devices, known as complementary resistive switch (CRS) [38]. Typical I-V characteristics of VCM-type CRS cells can be found in [33, 39], for example (see Fig. 1b). One distinctive feature of the anti-serial connection is the presence of an overall low resistive state (‘ON state’) when applying a voltage sweep. The existence of this ON state region in a two element simulation can be used as a further check for model consistency [40].

III. MEMRISTOR MODELS

A. Linear model and window function extensions

In [7], the following device equations were suggested:

\[
\begin{align*}
\dot{x} &= h(I) = K_I \cdot I \\
V &= R(x) \cdot I = \left( R_{\text{LRS}} - R_{\text{HRS}} \right) \cdot x + R_{\text{HRS}} \cdot I.
\end{align*}
\]

Here, \( K_I \) is a constant, \( R_{\text{LRS}} \) is the resistance of the low resistive state (LRS) and \( R_{\text{HRS}} \) is the resistance of the high resistive state (HRS). The state variable \( x \), which represents the position of the boundary between low conductive and high conductive region, is normalized by the switching layer thickness of \( D = 10 \text{ nm} \).

Note that the equation system (3)-(4) is mathematically not a linear system. However, we call this model a ‘linear model’ for the following reasons: first, the state variable \( x \) influences the resistance \( R(x) \) linearly. Second, the voltage \( V \) is directly proportional to the current \( I \) for a certain \( R(x) \). Third, \( x \) depends linearly on the current \( I \). In models from section B, which we call ‘non-linear models’, at least one of these relations is non-linear.

To prevent nonphysical values for \( x \), the state variable must be limited to the layer thickness, thus \( 0 \leq x \leq 5 \text{ nm} \) holds. In common SPICE implementations, this bounding is realized by a window function \( f(x,I) \). Thus, equation (3) can be rewritten as:

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Fig. 1. (a) Exemplary I-V characteristic of a TaO\(_x\)-based resistively switching device. The input signal is a triangular voltage sweep of amplitude -2.5 V/3V (see inset). Note that for a symmetric voltage amplitude (e.g., -3 V/3V) the characteristic does not change significantly. Initially, the device is in HRS state and switches to state LRS at \( <1> \). In \( <2> \), \( <3> \) the device stays in the LRS and switches back to HRS at \( <4> \). This is again the initial HRS state. Note that the switching polarity depends on the actual material composition.

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Fig. 2. Set time \( t_{\text{SET}} \) of the switching from HRS to LRS versus applied pulse height \( V_p \). Menzel data for SrTiO\(_3\) [34], Nishi data for TaO\(_x\) [35], Yu data for HfO\(_2\) [36], and Alibart data for TiO\(_2\) [37].

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\[ x = h(x,I) = K_I \cdot I \cdot f(x,I). \] (5)

In this study, we consider four different window function implementations: Benderli’s model [15], Joglekar’s model [16], Biolek’s model [17] and Shin’s model [18], which are illustrated in Fig. 3, a, c, e and g, respectively. Note that there are several other window function-based models, like Prodromakis’s model [19] or Corinto’s and Ascoli’s model [20, 21] which are not included in this study. We think it will be a worthwhile future task to evaluate these too.

In case of Biolek’s model and Shin’s model, the window function also depends on the sign of current \( I \), compare Fig. 3, e, g. However, we can rewrite Biolek’s window function as follows:

\[ f(x,I) = \begin{cases} 1 - (x)^{7p} & \text{for } I \geq 0 \\ 1 - (x-1)^{7p} & \text{for } I < 0 \end{cases}. \] (6)

Similarly, Shin’s window reads:

\[ f(x,I) = \begin{cases} \sigma(1-x) & \text{for } I \geq 0 \\ \sigma(x-1) & \text{for } I < 0 \end{cases}. \] (7)

Here, \( \sigma(\cdot) \) is the step function. Thus, when only either purely positive or negative input signals are considered, the window function is only a function of \( x \).

The simulation parameters for all models are selected according to [7]: \( K_I = 10^4 \text{A}^{-1}\text{s}^{-1} \), \( R_{LRS} = 100 \text{Ω} \) and \( R_{HRS} = 16 \text{ kΩ} \). In Fig. 3c Joglekar’s window function is shown. It has the same shape as the Benderli’s window function, but can be parameterized with \( p \) to vary the gradient.

To illustrate the impact of \( p \), three curves, for \( p = 1, 7 \) and 50, are depicted. For the \( I-V \) simulations in Fig. 3d we applied \( p = 1 \).

The Biolek’s window function offers a similar parameterization as Joglekar’s window function, and window curves for \( p = 1, 7 \) and 50 are shown in Fig. 3e. However, the function behaves quite different due to the involved step function \( \sigma(\cdot) \) which enables a sudden upward transition of the window function from a value close to zero towards a value close to unity if the sign of the current \( I \) changes and when \( x \) is in proximity of its limits (see Fig. 3e). In Fig. 3e the solid line is valid for positive currents (SET direction) and the dashed line for negative currents (RESET direction). Shin’s window function (Fig. 3g) can be considered an edge case of Biolek’s window function for \( p \rightarrow \infty \).

In order to evaluate these models against our first criterion we simulated their \( I-V \) characteristics. For this, symmetric triangular input voltage signals with sweep rates of 10 V/s, 30 V/s and 100 V/s (see inset in Fig. 3b) are used. The resulting \( I-V \) characteristics using Benderli’s and Joglekar’s window function are depicted in Fig. 3b and Fig. 3d, respectively.

Both models exhibit completely symmetric \( I-V \) characteristic with respect to the origin due to their symmetric window functions, i.e., one switching event (SET) occurs after reaching the maximum voltage level (\( <2> \) in Fig. 3b and Fig. 3d) and the other one (RESET) occurs before reaching the maximum absolute voltage level (\( <4> \) in Fig. 3b and Fig. 3d). Therefore, the symmetry is an inherent property of these two models and as a consequence they cannot reproduce the asymmetry of bipolar resistive switching, as analytically proved [20]. Keep in mind that Benderli’s and Joglekar’s model represent memristors with window depending on state only. Fingerprint of these types of models is a symmetric \( I-V \) characteristic with respect to the origin (compare [20, 41]). Furthermore, Benderli’s and Joglekar’s model do not show an abrupt SET transition which limits their applicability (cf. Fig. 3b, and d). In contrast, the simulated \( I-V \) curves using Biolek’s and Shin’s window function show an abrupt SET transition. In addition, the \( I-V \) characteristics are asymmetrical with respect to the origin. However, one should note that the characteristics in Fig. 3f, h defer strongly from characteristics of typical VCM devices (see Fig. 1a or [5]) anyway: for example, the current in LRS for negative voltages is very low.

All models offer the general trend of higher SET voltages for increasing sweep rates.
amplitudes become larger. This is due to the form of the window function which gives rise to convergence problems (called “terminal-state problem” in [19]), and limits applicability of the model. To enable proper simulation, parameters and input signal must be carefully adjusted for these window functions. This problem was solved in the Biolek and Shin model by resetting the window function when the sign of the input signal changes.

In the following, we evaluate the models with respect to the reproducibility of the experimentally observed switching kinetics, i.e. the second evaluation criterion. Thus, we apply voltage pulses of height $V_{\text{device}} = V_p$ to each model being initially in the high resistive state (HRS). For positive voltages the models are SET to the low resistive state (LRS) after the time $t_{\text{SET}}$.

From Fig. 4a, we can see that the basic trend observed in experiments, i.e., decreasing $t_{\text{SET}}$ for increasing pulse height, is also observed for the four simulation models. But, the dependency is much less pronounced as observed in experiments. Compared to experimental data shown in [37] for titanium oxide, we can clearly see that actual dependency differs greatly (several orders of magnitude).

This mismatch can be directly assigned to the $R$-$x$ dependency in model equation (4), which is not sufficiently non-linear. Furthermore, we can show that independent of the applied window function, all models offer the same kinetics (cf. Fig. 4b). By normalizing the $t_{\text{SET}}$ values by a certain point (here: $V_{p1} = 0.7$ V), all curves collapse to a single line, showing this feature directly. By inserting equations (4) into equation (5) we obtain:

$$\frac{dx}{dt} = K\frac{V(t)}{R(x)} f(x),$$

(8)

where we removed the dependence of $f(\cdot)$ on $I$, since for $t \in [t_s, t_p]$ the current has a unique sign and thus $f(\cdot)$ depends only on $x$. Equation (8) is a differential equation offering two variables, $x$ and $t$. Integration by parts results in:

$$\int_0^{t} \left( (R_{\text{HS}} - R_{\text{GRS}}) \cdot x + R_{\text{GRS}} \right) \frac{1}{K'} f(x) \, dx = \int_0^{t} V(t) \, dt.$$

The bounds of integration are: $t_0 = 0$ s and $t_1 = t_{\text{SET}}$, for the right side and $x_0 = 0$ and $x_1 = 0.5$ (our assumed SET condition) for the left side of equation (9). Furthermore, we consider a voltage pulse of amplitude $V_p$, thus $V(t) = V_p$ holds. Finally, the equation reads:

$$\int_0^{t} \left( (R_{\text{HS}} - R_{\text{GRS}}) \cdot x + R_{\text{GRS}} \right) \frac{1}{K'} f(x) \, dx = V_p \int_0^{t} \frac{dx}{t_{\text{SET}}^p},$$

which is equivalent to

$$K' = V_p \cdot t_{\text{SET}}.$$

(10) The left hand side of the equation (10) is constant for every model and is called $K_2$ in equation (11). Note that the value of $K_2$ is specific to the applied model, but cancels out when normalizing values with respect to a certain pulse height $V_{p1}$ offering a set time $t_{\text{SET},p1}$. This procedure is done for each model independently, and results in the graph shown in Fig. 4b. Thus, for all models of this kind the normalized SET time only depends on the pulse height $V_p$ and not on the window function (Fig. 4b):

$$t_{\text{SET,normalized}} = \frac{t_{\text{SET}}}{t_{\text{SET},p1}} = \frac{V_{p1}}{V_p}.$$  

(12)

The resulting dependency is

$$t_{\text{SET}} = \frac{1}{V_p}.$$  

(13)

So, the models are not capable to show the required exponential dependency.

From these considerations it is clear that a simple addition of a window function is not appropriate to introduce realistic device dynamics to the initial memristor model.

Next we consider our third criterion which is the anti-serial connection of two elements. Here, the results reveal even more striking mismatches between simulation and real device behavior. Due to the anti-serial connection of both cells A and B, $\dot{x}_A = -\dot{x}_B$ holds if $f(x_A) = f(x_B)$, where we dropped the dependence on $I$. Therefore, any change of state variable in cell A is canceled out by the change of state in cell B. Thus, the total resistance of both elements is constant all the time (see Fig. 5a) which is not the case in reality, as we know from CRS cells (compare Fig. 1b, where $x_{OA} = x_{OB} = 0$, i.e. $x_A(t=0) = 0$ and $x_B(t=0) = 1$). For Shin’s window function we could show this property already in [42]. Biolek’s window (Fig. 3e) shows the same behavior (compare Fig. 5a) while Benderli’s window and the Joglekar’s window only offer a straight line for symmetrical initial conditions, e.g., $x_{OA} = x_{OB} = 0.001$ ($0 < x_{OA}(t=0) = x_{OA}$ and $x_B(t=0) = x_{OB}$).

(For the sake of completeness, one must say that for very carefully adjusted input signals and highly asymmetric initial states also Shin and Biolek could show different behavior than Fig. 5a.) For Benderli’s window and the Joglekar’s window one can force a non-ohmic device behavior by starting from different initial states and considering a smooth window function. For simulation shown in Fig. 5b the model with Joglekar’s window ($p = 1$) is used and $x_{OA} > x_{OB}$ is assumed.
Due to the asymmetry of the initial values the SET process in element A starts earlier, leading to a increased current $<1>$ in Fig. 5b. Note that a similar result was observed in [22] which is in accordance to CRS behavior at first glance. However, for the negative voltage cycle the increased current (point $<4>$) occurs after reaching the maximum absolute voltage which does not correspond to real device behavior at all. For $x_{0A} < x_{0B}$ the observed behavior becomes even more unusual since the resistance (chordal resistance) is increased in a certain regime (grey line curves at points $<1>$, $<4>$ in Fig. 5c) – the opposite behavior than observed in experiments. In consequence, the simulation result strongly depends on the initial states, which is unfavorable according to the first criterion. However, adjusting the initial states is not suited to reproduce real device behavior for those models. Pay attention that a strong dependency on initial states is a commonly observed incident for memristor models (compare e.g. [14]).

The curves run-through is denoted by the arrows. (a) For symmetrical initial conditions ($x_{0A} = x_{0B} = 0.001$), no change of the overall resistance is observed. (b) Simulation for $x_{0A} > x_{0B}$ ($x_{0A} = 0.001$ and $x_{0B} = 0.0001$). (c) Simulation for $x_{0A} < x_{0B}$ ($x_{0A} = 0.0001$ and $x_{0B} = 0.001$). The portion of the loop relative to the regime of increased resistance is marked by a grey line color.

In conclusion, although highly attractive due to ease of use, none of the above studied models is suited to reproduce the basic resistive switch properties for arbitrary input signals. But these models could be modified to meet the requirements. The window function can have a positive impact on the accuracy, but cannot fix the basic physical equations. Correspondingly, simulation results obtained from these models, e.g. [12-13, 43], should be reconsidered using more sophisticated models.

However, one should keep in mind that additional model complexity allows a very high predictivity for a variation of parameter inputs. But this complexity might also give rise to convergence issues.

Next, we consider the improved physics-related approach by Pickett [23, 24].

### B. Pickett’s model and generalized sinh models

In [23] and [44] an improved modeling approach towards TiO$_2$ device modeling was suggested and published as a SPICE model by Abdalla and Pickett in [24]. In this model the state variable $w$ corresponds to a tunneling gap, and a highly non-linear current-voltage relationship, i.e., a tunneling current equation, was introduced. By trial and error modification the basic $I$-$V$ curve could be fitted to a measured $I$-$V$ curve considering the non-linear switching kinetics of the device. The equations read:

$$I = \frac{J_A}{\Delta w} \left\{ \phi_1 \exp \left\{ -Q \sqrt{\phi_2 \left( \phi_1 + c \left( \phi_1 \right) \right) \exp \left\{ -R \sqrt{\phi_1 + c \left( \phi_1 \right) \right) \right\} \right\} \right\}$$  \hspace{1cm} (14)

$$w = \begin{cases} f_a \sinh \left( \frac{W}{w_a} \right) & \exp \left\{ -\frac{w - w_a}{w_a} \right\} \mathcal{I} > 0 \\ -f_a \sinh \left( \frac{W}{w_a} \right) & \exp \left\{ \frac{w - w_a}{w_a} \right\} \mathcal{I} < 0 \end{cases}$$  \hspace{1cm} (15)

with

$$\phi_1 = \phi - c \left( \phi_1 \right) \left( \frac{w_1 + w_2}{w} \right) - \frac{1.15 \Delta w}{\Delta w} \left( \frac{w_1}{w} \right) \left( \frac{w_1}{w_2} \right)$$  \hspace{1cm} (16)

$$\phi_2 = \phi_1 - c \left( \phi_1 \right) \left( \frac{w_1 + w_2}{w} \right) - \frac{1.24 \Delta w}{\Delta w} \left( \frac{w_1}{w} \right)$$  \hspace{1cm} (17)

$$w_a = w_1 + w_2 \left( \frac{9.24}{3 \phi_1 + 4 \phi_2 - 2 \phi_1} \right) \text{ with } w_1 = \frac{1.24 \Delta w}{\phi_1}$$  \hspace{1cm} (18)

$$\lambda = c \ln \left( \frac{1}{2 \pi} \right) \text{ with } \lambda = \frac{c}{2 \pi h}$$  \hspace{1cm} (19)

$$V_i = V_{device} - I \cdot R$$  \hspace{1cm} (20)

The values of the used constants can be found in [24].

In Fig. 6a the simulation results for different voltage sweep rates are depicted. Note that an additional external serial resistance ($2.4 \, k\Omega$) of the measurement setup is considered in the simulations. Thus, the voltage at the device ($V_{device}$) is the difference of the applied voltage and the voltage drop at series resistor. This configuration is the reason for the sudden voltage decrease at the device (also called ‘snapback’) which occurs during SET (step $<4>$ in Fig. 6a). To stay conform to simulations in [24], SET occurs for negative voltages while the RESET voltage is positive.

This model is able to reproduce the measured $I$-$V$ curve very well. Unfortunately, the model simulation is very sensitive to changes of the input signal, as the authors of [24] have pointed out (compare also [21]). For example, it is not possible to obtain the same shape of the curve when applying a symmetric input signal ($\pm 5$ V for example). This issue

Fig. 5. Anti-serial connected device model simulations using Joglekar’s window. A triangular input voltage signal of sweep rates of 10 $V/s$ was used. The curves run-through is denoted by the arrows. (a) For symmetrical initial conditions ($x_{0A} = x_{0B} = 0.001$), no change of the overall resistance is observed. (b) Simulation for $x_{0A} > x_{0B}$ ($x_{0A} = 0.001$ and $x_{0B} = 0.0001$). (c) Simulation for $x_{0A} < x_{0B}$ ($x_{0A} = 0.0001$ and $x_{0B} = 0.001$). The portion of the loop relative to the regime of increased resistance is marked by a grey line color.
strongly limits applicability of this model for circuit simulations where arbitrary external stimuli may occur. However, the model features highly non-linear switching kinetics, similar to the reported experimental data of [37] for TiO$_2$-based devices (see Fig. 7). This is a major advantage compared to models described in section III.A.

Note that if there is more than one slope in the kinetics plot, different equations are required for modeling each voltage regime; compare [45] for example.

Furthermore, combining two elements anti-serially leads to characteristics offering an ON regime, as required for simulating CRS cells (Fig. 6b). Note that the self-crossing occurring in the ON regime is also observable in Fig. 1b (compare also [33]).

Pickett’s and Yang’s approach has inspired several other groups to derive generalized models using a hyperbolic sine current relationship offering generic application for memristive devices by simplifying the differential equations. First, we want to mention two models without any built-in thresholds, introduced by Laiho [26] and Chang [27]. For comparison we also considered a generalized model with built-in thresholds, the model of Yakopcic [29]. SPICE codes as well as applied parameters for all models can be found in [30].

Laiho’s model equations are:

\[
I = \begin{cases} \alpha \cdot x \cdot \sinh(B \cdot V) & V \geq 0 \\ \alpha \cdot x \cdot \sinh(B \cdot V) & V < 0 \end{cases}
\]

\[
x = \begin{cases} C_1 \cdot \sinh(D_1 \cdot V) \cdot f(x) & V \geq 0 \\ C_2 \cdot \sinh(D_2 \cdot V) \cdot f(x) & V < 0 \end{cases}
\]

Here \(f(x)\) is the Biolek window function which was added in [30] to limit the range of the state variable to reasonable values. The parameters for this model were selected by the authors with respect to \(I-V\) data from [46], an Ag-based ECM device. We simulated this model for three different sweep rates in Fig. 6c and conducted also pulse simulations to obtain the kinetics data (see Fig. 7). The \(I-V\) characteristics in Fig. 6c fit well the data in [46]. From Fig. 7, one can see that the extracted kinetics data for this device model offers a relatively weak non-linearity. (Note that the non-linearity of most ECM devices is typically much larger [4].) By combining two elements anti-serially we obtain a characteristic offering very low-conductive ON windows (minimal resistance > 50 MΩ) (Fig. 6d).

Curiously, the model predicts a decrease of current level for higher sweep rates – an unexpected behavior for ECM-based CRS cells [47]. Although equations (21)-(22) offer ease of use, predictions from this model should be treated with care. For more details on physics-based modeling of ECM devices we refer to [48, 49]. Note that Menzel’s ECM model fulfills all three evaluation criteria, see [48] for criteria one and two, and [47] for anti-serially connected cell simulations.

Chang’s model is another generalized sinh model using a more complex set of equations:

\[
I = (1-x) \cdot \left[ 1 - \exp(\beta V) \right] + x \cdot \gamma \sinh(\delta V)
\]

\[
x = \lambda \eta \sinh(\eta V)
\]

Here, the underlying material system for parameter extraction was a VCM-type WO$_3$ device. The model offers the general trend of higher SET voltages for increasing sweep rates (Fig. 6e). The non-linearity of the kinetics is moderate, in parts similar to data from Menzel for strontium titanate VCM devices, but less steep than Alibart’s data (Fig. 7). Moreover, the corresponding anti-serial connection of two devices (Fig. 6f) offers a conceivable behavior for this specific device. An experimental cross-check for our evaluation criteria might verify the model predictions.

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Another approach to accommodate highly non-linear kinetics in resistive switches is to reintroduce a built-in threshold. Either current thresholds (e.g., Kvatinsky’s TEAM model [28]) or voltage thresholds as in Yakopcic’s model [29], which we consider here:

\[ i = \begin{cases} \alpha, x \cdot \sinh(b \cdot V) & V \geq 0 \\ \alpha, x \cdot \sinh(b \cdot V) & V < 0 \end{cases} \]

(25)

where \( f(x) \) is a window function and \( g(V) \) is the function implementing the threshold behavior. Functions \( f(x) \) and \( g(V) \) read:

\[ f(x) = \begin{cases} \exp\left(\beta \left( x - x_p \right) \right) & x \geq x_p \\ \exp\left(\beta \left( x + x_p - 1 \right) \right) & x \leq x_p < 1 \\ 1 & 1 \leq x_p < x < \infty \end{cases} \]

(26)

and

\[ g(V) = \begin{cases} A_{\text{on}} \cdot \left( \exp(V) - \exp(V_{\text{th,on}}) \right) & V > V_{\text{th,on}} \\ -A_{\text{off}} \cdot \left( \exp(-V) - \exp(V_{\text{th,off}}) \right) & V < -V_{\text{th,off}} \\ 0 & -V_{\text{th,off}} \leq V \leq V_{\text{th,off}} \end{cases} \]

(27)

For simulation in Fig. 6g,h the threshold was \( V_{\text{th}} = V_{\text{th,on}} = V_{\text{th,off}} = 1.2 \) V. The \( I-V \) behavior in Fig. 6g offers the correct trend, i.e., \( V_{\text{SET}} \) and \( V_{\text{RESET}} \) increase for larger sweep rates. For 100 V/s the exponential decay of the window function (equation (27)) determines the \( \text{RESET} \) process, leading to a gradual OFF switching. In complementary connection this effect is visible for 100 V/s, too. The impact of the assumed voltage threshold becomes also visible in CRS configuration: independent of the applied sweep rate the SET process starts above \( V_{\text{th}} \). There are two major limitations of this approach one should be aware of. The first one lies in the form of equation (25). As for Laiho’s model, if \( x = 0 \) occurs in one device, there is no current flow through the device at all (see Fig. 6h). This property will limit the applicability for simulation of parasitic currents in arrays, for example. Another limitation is inherent for any threshold-based approach where the sub-threshold kinetics is not specified: the device’s actual sub-threshold behavior cannot be modeled (see Fig. 7), thus realistic pulse simulations are not possible. An advantage of this model is the flexibility to fit the model to a wide range of \( I-V \) curves, i.e. adjust the kinetics in the supra-threshold regime. Note, for the given parameter set, the kinetics is similar to Chang’s model (compare Fig. 7).

IV. CONCLUSION

We have analyzed the applicability of existing memristive models for simulation of resistive switches. Our results are:

1) A good way to obtain a realistic dynamic model is to fit \( I-V \) characteristic and to incorporate switching kinetics data, as done for Pickett’s model or Menzel’s ECM model. However, simulation robustness can be an issue for highly complex models as we see for Pickett’s model.

2) The derivation of simplified models with better simulation stability is a justifiable first-order approach, as we see for Chang’s and Yakopcic’s model.

3) Using a generic set of equations which does not directly correspond to the actual real device physics leads to low predictivity, as we see for Laiho’s model in complementary configuration.

4) If the basic equations do not reflect the actual device physics well, as we see for the basic memristor equations, with or without window functions, low-predictivity is given at all. However, if only a certain restricted regime of operation shall be modeled also very simple models can be applied.

5) A check for our three evaluation criteria, the \( I-V \) characteristics, the non-linearity of the switching kinetics and the complementary switching behavior of two devices, is a suitable test for model consistency. Finally, further development of physics-based memristive models is crucial and statements drawn from low-predictive models should be reconsidered carefully.

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