NO MORE 996: UNDERSTANDING DEEP LEARNING INFERENCE SERVING WITH AN AUTOMATIC BENCHMARKING SYSTEM

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ABSTRACT

Deep learning (DL) models have become core modules for many applications. However, deploying these models without careful performance benchmarking that considers both hardware and software’s impact often leads to poor service and costly operational expenditure. To facilitate DL models’ deployment, we implement an automatic and comprehensive benchmark system for DL developers. To accomplish benchmark-related tasks, the developers only need to prepare a configuration file consisting of a few lines of code. Our system, deployed to a leader server in DL clusters, will dispatch users’ benchmark jobs to follower workers. Next, the corresponding requests, workload, and even models can be generated automatically by the system to conduct DL serving benchmarks. Finally, developers can leverage many analysis tools and models in our system to gain insights into the trade-offs of different system configurations. In addition, a two-tier scheduler is incorporated to avoid unnecessary interference and improve average job compilation time by up to 1.43x (equivalent of 30% reduction). Our system design follows the best practice in DL clusters operations to expedite day-to-day DL service evaluation efforts by the developers. We conduct many benchmark experiments to provide in-depth and comprehensive evaluations. We believe these results are of great values as guidelines for DL service configuration and resource allocation.

1 INTRODUCTION

Deep learning (DL) tools are transforming our life, as they are incorporated into more and more cloud services and applications (Tran et al., 2015; Bahdanau et al., 2014; Gatys et al., 2016; Silver et al., 2017). Facing this ever-increasing DL deployment demand, many companies, tech-giants or startups, are engaging in a fierce arm race to develop customized inference hardware (Jouppi et al., 2017), software (Baylor et al., 2017) and optimization tools (Chen et al., 2018) to support DL services. Hence, there is a huge need to concurrently develop systems to study and benchmark their performance as guidelines for deploying high-performance and cost-effective DL services.

Unlike conventional web frameworks and techniques, deep learning (DL) techniques as well as their inference hardware and software platforms, are still evolving at a rapid pace. To address this challenge, an easy-to-use and highly deployable benchmark system is needed to assist DL developers in their daily performance evaluation tasks and service configuration. Also, the system must provide out-of-the-box methodologies to assess and understand the intricate interactions among DL models, hardware, and software across a variety of configurations (e.g., batch size and layer number) (Wang et al., 2020). The obtained results can be used as guidelines for future design.

Though existing benchmark studies have made substantial contributions to understand DL inference performance (Reddi et al., 2020; Ignatov et al., 2019; Coleman et al., 2017), they still do not adequately address the aforementioned challenges. MLPerf inference benchmark (Reddi et al., 2020), as the current state-of-the-art solution, lacks the much-needed configurability, as it leaves the implementation details to developers. As a result, developers have to spend days or even weeks preparing a benchmark submission for a fair comparison. In addition, the results (Reddi et al., 2020) only showed the importance of hardware and software configuration but provided very limited insights and prescriptive guidelines for DL service configuration and resource allocation. Moreover, benchmarking performance using only one or two models optimized for specific inference tasks (e.g., a well-trained ResNet50 model (He et al., 2016) for image classification) provides a very limited understanding of the impact of hyper-parameters (e.g., layer number and batch size) on the performance or resource utilization for a wide range of inference applications.

In this work, we propose an automatic and complete DL serving benchmark system (as shown in Figure 1) to address these needs and narrow gaps of existing systems. We design our system following the best practice in DL clusters design.
The goal is to provide an end-to-end solution that frees DL developers from tedious and potentially error-prone benchmarking tasks (e.g., boilerplate code writing, data collection and workload generation). In addition to the models chosen by users, the system can effortlessly generate and iterate models with different hyper-parameters (e.g., different layer types and different number of layers) to adequately explore the design space. The system also provides an extensive set of analysis tools and models to help developers choose the best configuration for their applications under constraints like latency, cloud cost, etc. Besides, we implement a two-tier scheduler in the system to improve service efficiency.

For the logical clarity and comprehensiveness of benchmarking evaluations, we designate impact factors for DL inference into three tiers, as shown in Figure 2. For the hardware tier, we select five representative platforms for evaluation. For the software tier, we choose four representative online serving infrastructures. For the pipeline tier, we simulate the real-world workload and examine a specific inference service with three types of transmission technologies. We use metrics such as tail latency, cloud cost and resource usage to measure their performance, and analysis models like Roofline (Williams et al., 2009) and Heat maps to profile DL serving system properties.

In summary, the main contributions of this paper are as follows,

- We build an automatic and distributed benchmark system in DL clusters to streamline DL serving inference benchmark for developers.
- We conduct a comprehensive performance analysis on three tiers with many DL applications under various workloads, providing users insights to trade off latency, cost, and throughput as well as configure services.
- We use generated models to study the sensitivity of hardware performance to model hyper-parameters and derive valuable insights for system design.

- We implement a scheduler to ensure safe benchmark progress and reduce the average benchmark job completion time.

In the remainder of this paper, we first introduce the background knowledge of DL inference serving and our motivation in Section 2. Then, we detail three benchmark tiers as well as their benchmarking metrics in Section 3. Next, we present the system implementation and the employed methodologies in Section 4. We employ our system to perform benchmark tasks and evaluate its performance in Section 5. Finally, we briefly introduce the related work in Section 6 and summarize our paper in Section 7.

## 2 Background and Motivation

In this section, we introduce the typical workflow of building DL services including pre-deployment, post-deployment and online serving. We illustrate how current benchmark studies can not fully address the challenges of the new workloads, thus motivating us to build a new benchmark system.

### 2.1 Deep Learning Service Pre-Development

After receiving models from data scientists, engineers need to perform many optimization and service configuration tasks before models go to production, as shown in Figure 3. First, models alone with their artifacts such as weight files and corresponding processors (e.g., image resize), will be stored and versioned (Chard et al., 2019). Developers may try to re-implement processors with a production language such as Java and test them. Second, engineers will optimize models (e.g., INT8 conversion or model compression (Han et al., 2015)) but maintaining good accuracy. As such, engineers need to check every new model to ensure that it meets both accuracy and latency requirements. Third, the users will choose a serving system such as Tensorflow-
Serving (TFS) (Baylor et al., 2017) to bind their models as a service. Different serving systems perform differently over a wide range of hardware choices. Even more, these systems have many new features such as dynamic batching (Crankshaw et al., 2017), resulting in a complex configuration with varied performance. Developers need to perform rigorous evaluations for a service. Finally, a service can be dispatched to a cluster to serve customers.

**Observation 1:** A benchmark task for building a service often takes tens of iterations. Also, developers need to consider the trade-off among many impact factors (IFs) under SLOs (Service-Level-Objectives) or budget to make a judicious choice. To efficiently complete the task, developers need a simple, easy-to-use and customizable system.

**Observation 2:** The benchmark tasks need a dedicated and isolated runtime environment. As AutoML techniques are becoming popular, these tasks will consume more resources, resulting in long delays in getting benchmark results. This motivates us to propose a way to perform a benchmark with efficient use of resources.

### 2.2 Deep Learning Service Post-Development

Upon finishing the pre-development, the developers still need to 1) study the accelerator (e.g., TPU (Jouppi et al., 2017) and FPGA) characteristic for upgrading them; 2) design better resource allocation mechanisms for DL serving in clusters; 3) monitor DL services to diagnose performance issues immediately. The automation of these tasks has been studied extensively. For instance, while many engineers manually write DL operators, TVM (Chen et al., 2018) automatically generate operators to run models on specific hardware efficiently. Also, to increase the GPU resource utilization, MPS (NVIDIA, 2020a) and Salus (Yu & Chowdhury, 2019) provide supports to share a GPU among multiple models. All of these studies require a lot of benchmark efforts. However, current benchmark studies ignore these post-deployment activities and provide very few supports for developers.

**Observation 3:** Resource usage under different scenarios is not fully studied, which limits the development of resource allocation methods for DL services. Developers need to analyze resource usage with varied settings to improve resource utilization.

**Observation 4:** Current studies for simple isolated models can not be easily generalized to analyze system bottleneck (e.g., compute- or memory-bound). Also, only benchmarking these models can not provide help to understand the hyper-parameter influence on both DL hardware and model performance. More effective analysis methodologies should be incorporated.

### 2.3 DL Inference Serving Workflow

In this section, we depict how a DL Service process users’ requests and point out the limitations of current studies. As shown in Figure 4, customers first send requests from mobile or website. These requests will be pre-processed either on the client- or server-side to meet a format requirement of a model service. The requests are then forwarded to a frontend in a server for further dispatched. Next, the request will be fed into a backend where a model or multiple models is loaded by a serving platform (e.g., TFS). Once the inference is completed, the predictions will be post-processed (either in a server or a client) and displayed to the end-users.

**Observation 5:** The end-to-end design of the pipeline determines the performance of a service. As such, it is very useful to have a detailed benchmark to locate the bottleneck.

**Observation 6:** Since a service processes hundreds of thousands of requests daily (Hazelwood et al., 2018), the mitigation of tail latency under varied arrival rates, is critical. Serving software usually includes functions to address the issue. Developers should have a tool to analyze this.

**Observation 7:** Existing DL serving systems (Crankshaw et al., 2017; Baylor et al., 2017) provides several mechanisms such as dynamic batching to improve the performance of a service. A benchmark system should explore these advanced features.

### 3 Benchmark Tiers

In this section, we describe three benchmark tiers: hardware, software, and pipeline as shown in Figure 2.
3.1 Tier 1 - Hardware

For the first tier, our system mainly focuses on inference hardware performance as well as the interactions between hardware and models under varied configurations and scenarios. In addition to the latency and throughput measurement capabilities provided in previous research, we extensively study the cost, hardware performance sensitivity to model hyper-parameters, and their bottleneck with different types of models. We next detail the metrics.

Latency & Throughput. These two metrics are widely used to measure hardware. In general, online real-time services require low latency and offline processing systems prefer high throughput. Unlike CPUs, the new accelerators such as TPU and GPUs encourage batch processing to improve resource utilization. We extensively studied the property with our system.

Cost. When developers implement a DL service, they must consider their budget. To support this, our system provides tools to measure energy, CO2 emission (Anthony et al., 2020), and cloud cost under different devices and cloud providers with an aim to present a comprehensive study.

Sensitivity of Model Hyper-parameters. DL models have many hyper-parameters such as layer types (e.g., LSTM (Hochreiter & Schmidhuber, 1997)) and the number of layers. Our system can generate models to study their influences to provide insights.

Memory & Computation. The performance of a model on a device is decided by both computation and memory capacity from the device. We explore both of them with real-world and generated models under a wide range of hyper-parameters.

3.2 Tier 2 - Software

In the second tier, we study the impacts of software such as formats and serving platform. Here, the system provides complete support for serving platforms such as TFS (Olston et al., 2017) and Trion Inference Serving (NVIDIA, 2020b), which have not been fully investigated in the previous study. Users can easily extend our systems to support more platforms with provided APIs. We will study the following features.

Tail Latency. A well-designed serving platform can effectively mitigate the effect of tail latency which is critical for online services. We provide a detailed study of the performance under varied request arrival rates.

Resource Usage. Serving software may bring overhead or save resources by their dedicated design. Understanding the behavior will lead to better resource allocation.

Advanced Features. To improve resource utilization, serving software provide some useful functions like dynamic batching. Our system can help to study their impacts.

3.3 Tier 3 - Pipeline

In this tier, we aim to look into each stage to identify the bottleneck of a service under various conditions. The system supports to explore the performance of each stage.

Latency per Stage. The DL serving pipeline often consists of five stages: pre-processing, transmission, batching, DL inference, and post-processing. In addition to the DL inference stage, the other stages also have varied performance under different conditions (e.g., transmission under varied networking conditions). We will discuss this in detail. Meanwhile, the cold start that refers to the time interval (milliseconds to seconds) to start a system is varied for different systems. It is also a critical metric as it decides the provisioning time.

Summary. The huge configuration space leads to many trade-offs including but not limited to Latency versus Throughput, Accuracy versus Latency, Cost versus Quality and Sharing versus Dedicate. With the help our system, DL developers can inspect to these trade-offs for better configuration and future upgrading.

4 SYSTEM DESIGN AND IMPLEMENTATION

Design Philosophy. We aim to design an automatic and distributed benchmark system which can be 1) operated independently to perform benchmark tasks; 2) incorporated into DL lifecycle management (Zaharia et al., 2018) or DL continues integration (Zhang et al., 2020; Karlaš et al., 2020) systems to further improve the automation of DL service development and evaluation; 3) and connected to a monitor system for DL service diagnose. Accordingly, our system can serve users involved at various stages of a DL service pipeline: data scientists, DL deployment engineers, hardware engineers, and AIOps engineers, by significantly reducing the often manual and error-prone tasks.

4.1 System Overview

As shown in Figure 1, our centralized benchmark system uses a Leader/Follower architecture. The leader server manages the whole system by accepting users’ benchmark submissions and dispatching benchmark tasks to specific follower workers, guided by a task scheduler. The leader server also generates the corresponding requests and workloads for later evaluation. Meanwhile, users can choose either to register their own models to our system or use the different iterations of canonical models generated by our system to benchmark. Next, the follower workers, which can be any
Table 1. Five hardware platforms we used for experiments.

| ID | Platform (Arch) | Version | Memory | Peak TFLOPS (FP32/FP16) | Memory Bandwidth (GB/s) | AWS (Instances) | Google Cloud (Instances) |
|----|-----------------|---------|--------|-------------------------|-------------------------|------------------|-------------------------|
| C1 | CPU Intel Xeon | E502698 v4 | 128 GB | -                       | -                       | -                | -                       |
| G1 | GPU (Volta)     | Tesla V100 | 32 GB  | 15.7 (31.4)             | 900                     | 4                | 4                       |
| G2 | GPU (Turing)    | GeForce 2080Ti | 11 GB  | 14.25 (28.5)           | 616                     | -                | -                       |
| G3 | GPU (Turing)    | Tesla T4  | 16 GB  | 8.1 (16.2)             | 300                     | 7                | 3                       |
| G4 | GPU (Pascal)    | Tesla P4  | 8 GB   | 5.5 (11.0)             | 192                     | -                | 3                       |

Figure 5. The benchmarking system architecture. A management plane running in the leader server is designed to control and monitor the system and users’ benchmarking tasks. We divide an inference benchmarking into four stages and provide a set of functions to complete it automatically.

of the idle servers in a cluster, execute users’ benchmark tasks according to the submissions’ specifications. Specifically, it loads and serves a model with infrastructure such as TensorFlow-Serving (TFS) and then invokes the corresponding pre-processing and post-processing functions to complete a service pipeline. Finally, users can start clients to simulate real-world workload by sending requests to the DL service to evaluate its performance.

For evaluation, a metric collector is utilized as a daemon process to obtain detailed performance information via a probe module. At the same time, a logging module is enabled to record the running systems’ status, including both hardware and software. All of this information will be sent to a PerfDB (performance database). After evaluation, users can aggregate the performance data and use our build-in analysis models to extract insights. We also implement a recommender and a leaderboard for service configuration and resource allocation.

4.2 System Implementation Detail

Our implementation of the system (shown in Figure 5) follows the following three best-practices:

Modularity. Modularity can provide 1) a seamless extension to support evolving DL applications, 2) a natural adaptation to existing model management and serving systems, and 3) easy customization from developers.

Scalability. As cluster-based model training and deployment have become the de facto practice for both industry and academia, our system supports all the computation and management functionalities required by cluster computing, with the aim to keep user manual efforts (e.g., manually moving model and result files) to the minimum.

System Integrity and Security. Since starting a benchmarking task without proper coordination with the leader can potentially interrupt the running service, the system shall have the capability to monitor environment status so as to decide whether a task can be executed or to schedule such a task.

4.2.1 Management Plane

The management plane, designed to control all benchmark tasks in users’ clusters, consists of four functional blocks: task manager, monitor, scheduler, and other utility functions. The goal is to provide an interface for users’ fine-grained control inputs.

Task Manager. The task manager accepts users’ benchmark submission and logs related information, such as the user name, task ID, and submission timestamp. The task manager then dispatches the task to a specific worker according to the task specification and starts the related procedures. In our implementation, we use MongoDB as the backend. The developers can easily replace the backend with their preferred database such as MySQL.

Monitor. This functional block collects and aggregates system resource usage. Specifically, we use two backends, cAdvisor (Google, 2018) and Node Exporter (NVIDIA, 2018), for logging the status of serving container (e.g., CPU usage) and hardware status (e.g., GPU usage), respectively.

Scheduler. We aim to design a multi-tenant system to organize a team’s daily benchmark tasks and avoid potential conflict and interference. As such, we implement a scheduler for these benchmark jobs to minimize the average job completion time (JCT) and improve efficiency. Specifically, we design a simple baseline method (as discussed in Section 4.3.2), upon which developers can extend to design their own algorithms per the workload profiles.

Utility Functions. Currently, we have two functions, a
sharing manager and a configuration recommender. The sharing manager helps users configure MPS, which is the de facto software to share multiple DL models with one GPU, to support a sharing benchmark. The recommender helps users to make a simple decision for service configuration. Users need to input an SLO (e.g., latency), and the system will return the top 3 configurations.

4.2.2 Stage 1 - Generate

In the first stage, our system prepares requests, workloads (sending patterns), and models for benchmarking according to users’ specifications. From their submission (a YAML file), the system first chooses to call either real-world or generated models and then prepares the corresponding requests and workloads to perform tasks. We next describe the four main functions of this stage.

Model Repository. As each team can produce tens or even hundreds of models, this module is designed to help them organize different versions of models. It has four APIs, including register, update, search, and delete, based on MongoDB with GridFS. Both model weights and basic information such as the model name and the dataset used can be stored in the repository.

Canonical Model Generator. The module contains a trainer and an exporter to generate canonical models under different hyper-parameter, including the batch size, the neuron number, and the layer number (see appendix for details). We build models by repeatedly stacking the following four most commonly used blocks (layers), a fully-connected layer (FC), a residual block (CNN), an LSTM layer (RNN), and an attention block (Transformer (Vaswani et al., 2017)), respectively, to obtain four groups of models. They are trained with Tensorflow and exported to a format that can be served by Tensorflow-Serving. Different from those isolated real-world models registered by users, the canonical models can help explore the sensitivity of hardware performance to model hyper-parameters, which can not only benchmark target platforms but also profile their properties.

Request Generator. To save developers’ preparation time, the module stores many kinds of data selected from widely used datasets such as ImageNet (Deng et al., 2009) and also has an interface for users to upload their own test data.

Workload Generator. Since the requests must be sent by following a pattern for benchmarking, we implement this workload generator. We have built many modes in our system to meet the diversity testing scenarios (including both online services and offline services). Developers can further customize all these modes. For instance, we have a pattern to simulate request arrival processes that follow a Poisson Distribution and a specified arrival rate.

4.2.3 Stage 2 - Serve

In the second stage, the system performs users’ benchmark tasks according to the schedule. To expedite the execution, we implement several unified functionalities, including a pre-processor, a post-processor, and a model server.

Pre-processor & Post-processor. We collect and implement many out-of-the-box processing functions for different DL models in our system. For instance, for image classification models, we have image resizing and tensor conversion functions. For text classification models, we provide a set of tokenizer methods. These functions can be called individually or jointly according to model specifications. For instance, when analyzing videos, we need to build a pre-processing pipeline that consists of video decoding and image resize functions. The post-processor also has a variety of functionalities. For example, the post-processor can match a prediction class ID to a label in a database for classification tasks.

Following our design, developers can extend the system by implementing their own processors. Our system also provides the flexibility for users to offload these functions to the client-side or directly run them on the server-side.

Model Server. The benchmarking of DL serving software infrastructure has not been thoroughly investigated by the current benchmark studies. As such, we provide the module to bridge the gap. The serving software can transform models into services and expedite the model development. We adopt two kinds of DL serving infrastructure into our system. The first is the DL-specific software, with Tensorflow-Serving (TFS) and Trion Inference Serving (TrIS) as two representatives. The second is a general web framework (e.g., FastAPI and Flask) with an optimized runtime (e.g., ONNX Runtime (Microsoft, 2019)). We wrap many of them in our system so that developers can start a service for benchmarking with ease.

4.2.4 Stage 3 - Collect

In this stage, three modules are implemented to ensure both fine-grained performance collection and reproducibility. All of the information will be sent to the performance database.

Prober. The module examines the DL serving pipeline to obtain the performance of each stage. As shown in Figure 4, to evaluate the performance of a serving pipeline with multiple stages, the prober sets endpoints at the boundaries of each stage. Then it triggers the metric collector to obtain the measurement results corresponding to each of them. The results help developers detect the performance bottlenecks in an inference pipeline.

Metric Collector. This module contains a series of evaluation metrics as well as their implementation, as described
in Section 3. This module is designed to be composable - it can either be fully automated during the benchmarking process or be called selectively to meet users’ demand.

**Logger.** This module records the system runtime information and parameter settings. As we discussed in Section 3, the DL inference performance is affected by model, hardware, input size, etc. To ensure the benchmarking results’ reproducibility, we use a logger to track all the above information during evaluations. The information falls into two categories: runtime environment information and evaluation settings. Runtime environment information includes hardware types, serving software names, etc. Evaluation settings include model names, training frameworks, etc.

### 4.2.5 Stage 4 - Analyze

In the last stage, users can get initial benchmark results from a database and use analysis models and tools built in our system to gain insights. A leaderboard is provided for users to check results.

**PerfDB.** We design a performance database to store benchmark results and log information from the collection stage. The database runs a daemon process in the leader server to collect data from those follower workers in a cluster. Same with the model repository, it exposes many functions for results management MongoDB as the backend.

**Aggregator.** The module is used to aggregate results from PerfDB for a comprehensive analysis. We adopt many analysis models (e.g., Roofline) to our system to draw more insightful conclusions. The models will be described in Section 4.3.1.

**Leaderboard.** In the leaderboard, developers can sort results by different metrics such as energy computation and cloud cost to configure a high-performance and cost-effective DL service. We also provide a visualizer for presenting the results.

### 4.3 System Methodology

This section introduces adopted analysis models in our system and a simple scheduling method for benchmark tasks.

#### 4.3.1 Analysis Model

**Roofline.** Roofline (Williams et al., 2009) model is widely used to study the performance of applications running on hardware devices. We use Roofline to estimate two core capacity metrics of an accelerator: computation and memory bandwidth. Compared to a simple percent-of-peak estimate, it evaluates the quality of attained performance and explores a performance bound more effectively. As DL models’ performance is affected by many hyper-parameters (e.g., neuron numbers), we use generated canonical models to exploit the performance as well as the interactions between hardware and models instead of simple real-world models.

**CDF Plots.** Cumulative distribution function (CDF) plots can give the probability that a performance metric (e.g., latency) is less than or equal to its target (e.g., latency SLO). With the plot, we compare the capability to process real-world workloads of varied software.

**Heat Maps.** Heat maps are powerful tools to understand the performance (e.g., utilization) sensitivity to models’ hyper-parameters. We rely on heat maps to measure how system performance varies with the hyper-parameters to help developers better understand the interactions between models and systems.

**Other Plots.** We also design and upgrade some basic bar plots to summarize the performance obtained from our system. Compared to a table that lists results, our plots highlight the insights and give intuitive guidelines for service configuration.

#### 4.3.2 Scheduler Design

Our system invokes a scheduling agent in a two-tier manner to perform benchmarking tasks efficiently. In the first tier, a newly submitted job will be dispatched by the leader server to a relatively idle follower worker with adequate resources to execute the job. The second tier is to determine the order of job execution on a follower worker. Specifically, suppose the system has a set of jobs \( J \) waited to be processed in a scheduling interval. For each job \( j \in J \), the total time to process a job is \( t_j = \text{waiting} + \text{processing} \). The optimization target is to minimize \( T \) where \( T = \sum_{j \in J} t_j \).

**Algorithm 1 Benchmark Job Scheduling**

```plaintext
Input: Jobs \( J \), Workers, \( W_1, \ldots, W_k \)
for all jobs \( j \in J \) do
    repeat
        Select an idle worker \( W_{\min} \) with the shortest queue
        Enqueue \( j \) to \( W_{\min} \)
        Remove \( j \) from \( J \)
    until \( J \) is empty
end for
for all Workers, \( W_1, \ldots, W_k \) do
    Re-order jobs in an ascending way
end for
Execute Jobs
```

To solve the problem, we implement a global load balancer (LB) to decide the job placement and a scheduler at every follower worker to allocate resources, as shown in Algorithm 1. First, workers will publish their current queue length (i.e., the time to process all waiting jobs) to the leader server. Then LB distributes a job to a worker, minimizing the waiting time. Next, the worker will re-order jobs in
an ascending way. Finally, workers will execute the jobs sequentially.

5 Evaluation Results

In this section, we describe the experimental settings and the evaluations of our system. We also illustrate the importance of benchmarking scheduling via a simple case study.

5.1 Experimental Setup

We deploy our system to a server in a private cluster and study five types of hardware, four types of software platforms, and a pipeline with three scenarios. In the process, we have registered many real-world models and generated hundreds of models for the benchmark study.

**Hardware Platforms.** The specifications of five hardware platforms are listed in Table 1, including one CPU platform as the reference and four GPU platforms (V100, 2080Ti, T4, and P4). These GPUs have many different architecture designs (e.g., Volta and Turing), thus providing us a wide range of computational capabilities. We also survey instances that provide the GPUs in cloud providers.

**Software Platforms.** As shown in Figure 6, we investigate four serving infrastructures in our experiments. Tensorflow-Serving (TFS) is the default serving platform for Tensorflow SavedModel format, which is converted from Tensorflow models. Trion Inference Server (TrIS) supports many formats like TensorRT format that can be converted from many other formats. Both TorchScript (converted from PyTorch) and ONNX (converted from PyTorch) have no stable serving infrastructure during our implementation, so we use their default optimized runtime torch.jit and ONNX runtime, respectively, to load models and wrap them as services with FastAPI (a popular web framework (FastAPI, 2019)) by following the official document. To use these platforms, our system first converts newly trained models into optimized and serialized formats. We next use gRPC APIs test them with docker containers. All of them have been adopted to our system to free developers from engineering works.

**Pipelines.** We build a pipeline (shown in Figure 4) that consists of a service broker that accepts users’ requests and dispatches them to a backend with a running DL service, as our testbed in the system. We test the whole pipeline in three network scenarios, including LAN, 4G LTE, and Campus WIFI.

5.2 Hardware Platform Characterization

We present hardware benchmarking results with both real-world and generated models in this section. More results can be found in the leaderboard at our website (we omit it here due to the anonymous review). Unless otherwise stated, all experiments are conducted with TensorFlow SavedModel with TFS 2.3.0.

![Figure 6. Four serving software infrastructures under our test. Our system accepts models trained with PyTorch and Tensorflow. Then it converts the models into many serialized and optimized serving formats such as ONNX and TensorRT. These serving model will be bound with a serving infrastructure like Tensorflow-Serving for benchmarking.](image)

![Figure 7. The latency and throughput comparisons of different hardware. We showcase two representative models with varied batch sizes. We also show the speedup of GPU/CPU under the latency SLO.](image)

**Latency & Throughput.** We first plot how latency changes with different batch sizes and type of hardware for two inference models (Bert-Large and ResNet50), in Figure 7a and 7b, respectively. The batch size for the CPU is fixed at one. The plots show that for latency, GPU platforms perform better than CPU for small batch sizes (less than eight). When the batch size becomes large, the latency becomes much longer for two types of GPU. A larger batch size often form better than CPU for small batch sizes (less than eight).

![Figure 7. The latency and throughput comparisons of different hardware. We showcase two representative models with varied batch sizes. We also show the speedup of GPU/CPU under the latency SLO.](image)
different inference models on one type of GPU (V100). The inference models (see appendix for more details) include OD (object detection), GAN (CycleGAN (Zhu et al., 2017)), TC (text classification) and IC (image classification). The evaluation shows a wide range of speedup ratios, from 3.6x to 47.4x. We use the model latency with CPU as each service’s SLO and input them to our system. The system can recommend the best batch size and the speedup ratio of each service as a reference.

![Image](https://example.com/image1.png)

(a) ResNet50 energy and CO2 emission.  
(b) ResNet50 cloud cost.

**Figure 8.** The three cost comparison across different GPUs.

**Cost.** We examine three kinds of costs, including energy consumption, CO2 emission, and cloud cost. Figure 8a shows the energy consumption and CO2 emission per request of the ResNet50 model in a batch-processing manner. In this scenario, more powerful GPUs consume more energy and emit more CO2. We note that most energy is consumed with the batch size one. This can be attributed to the overhead associated with context start, which can be amortized with larger batch sizes.

For cloud cost, we use hourly rates of different GPU instances from Google Cloud Platform and AWS. Our focus is the benchmark comparison provided by our system instead of ranking providers, so we use [C1, C2] and [I1, I2, I3] as labels for providers and instances, respectively. From the results plotted in Figure 8b, we observe that 1) for the same devices (V100), different providers have different hourly rates; 2) GPU devices’ costs vary with computational capability. Though T4 (I3) GPU is more powerful than P4 (I2) GPU, it has a lower price; and 3) as the batch size increases, more images can be processed hourly. As a result, the cost per request decreases. With this capability provided by our system, users can choose the best cloud providers and instances for their services.

**Performance Sensitivity.** We evaluate performance sensitivity to hyper-parameters such as batch size and the layer number with generated models on a V100 GPU. Every time we select two parameters and keep the others fixed. Figure 9 shows two examples. For a CNN type model, GPU utilization increases with both batch size and model depth, indicating that a GPU exploits parallelism within the batch size and depth of this kind of models. For a transformer model, the model’s depth has more impact, indicating that a transformer model with more layers will utilize GPU resources more. Since more hardware accelerators (e.g., TPU) are emerging, we provide a powerful tool for hardware engineers to explore the hyper-parameter influences.

![Image](https://example.com/image2.png)

(a) CNN models.  
(b) Transformer models.

**Figure 9.** The GPU utilization with varied hyper-parameters of different models.

**Computation and Memory Bandwidth.** The performance of a GPU is decided by both its computation capacity and memory bandwidth. We apply Roofline models, which represent the relationship between the model’s operational intensity and operations per second, to give a quantitative comparison. The ceiling line (in red) shows the theoretical bandwidth and computation capability of a GPU (V100). For real-world CNN models (shown in Figure 10a), we observe that two lightweight models (MobileNet (Howard et al., 2017)) are more memory-bound and the other heavy models are more compute-bound. This is in line with that in real-world practice, the performance improvement of MobileNet does not significantly align with its small numbers of parameters. It can not fully utilize the computation of GPUs. With our system, data scientists can easily understand their models for optimization. Figure 10b presents the Roofline analysis of generated models. The model operations per second increases while the arithmetic intensity increases. Larger batch sizes make MLP models more compute-bound, whereas more layers and more neurons incur a memory-bound. This insight can not be obtained from isolated real-world models. Hardware engineers can apply our system to analyze the inference performance of both newly proposed model structures or accelerators.
Figure 11. The tail latency of four systems under varied workloads. The batch size, arrival rate, and serving software influence tail latency a lot.

5.3 Software Platform Characterization

We prepare several benchmark submissions to evaluate software platforms. They serve models with a range of up to 5 minutes. The workload generator is used to simulate Poisson Distribution with varied arrival rates to understand software performance. We also explore the advanced features like the dynamic batching specific to DL serving.

Tail Latency. We compare the tail latency across varied software and arrival rates. We use TFS with ResNet50 as a case study. As shown in Figure 11a, the larger batch size accounts for a longer tail latency though it can save service providers’ cost. Both Figure 11b and Figure 11c indicate that TFS cannot adequately handle spike load.

We further compare different four platforms for a same image classification service (based on ResNet50) on a V100 GPU as shown in Figure 11d. TrIS from NVIDIA performs best with no surprise since it includes many GPU optimization techniques. ONNX Runtime with a simple web framework performs better than TFS, a dedicated serving system for DL inference. In conclusion, when configuring a latency-intensive service, developers need to take the impact of batch size and the served software (on latency) into consideration. Both academia and industry have invested efforts in serving software development, and our system can make things easier.

Advance Feature: Dynamic Batching. Two representative systems, TFS and TrIS, provide dynamic batching settings. To use the feature, developers need to set a lot of hyper-parameters like the maximum batch size and maximum queue time. We use the two software to serve a ResNet50 model and send requests concurrently. In this case, TrIS can utilize the feature and improve the throughput steadily while TFS performs even worse than no dynamic batching in a small concurrency. This reminds us that before starting to use the feature, engineers should understand their scenarios first and tune the two parameters accordingly.

Resource Usage. Understanding the resource utilization pattern often leads to better resource allocations. We test two services (BERT with an arrival rate of 30 requests/seconds and batch size 1 and ResNet50 with 160 requests/second and batch size 1). The results are shown in Figure 13. We observe that the GPU utilization is dynamic with varied workloads and tends to be under-utilization with a low arrival rate (even it loads a heavy model like BERT). This gives developers a large enough room for optimization.

Figure 13. The GPU utilization with different serving software under varied workloads

5.4 Inference Pipeline Decomposition

We simulate a real-world DL service by building a simple pipeline and sending requests using three networking transmission technologies. We use the ResNet50 and TFS as the inference model and the serving system, respectively.

The results in Figure 14a show that the transmission time is very comparable to inference time for small batch sizes. As the batch size increases, the inference time accounts for a much larger portion of the total time. The results (Figure 14b) also show that for the same service, 4G LTE has the longest end-to-end latency. This indicates that for a mobile application, sending requests to cloud DL service can incur high latency.
5.5 Case Study: Task Scheduling

To the best of our knowledge, current benchmarking tasks are often carried out in an error-to-prone fashion. Since tasks need to be executed in an idle server, the system status needs to be checked. If this check is neglected, all tasks located in a worker will crash. As there are no specific scheduling methods to address these issues before this work, we implement two methods as the baselines, a round-robin (RR) load balancer (LB) with First-Come-First-Serve (FCFS) and an LB with Short-Job-First (SJF). The results show that our scheduler, queue aware (QA) LB with SJF, can reduce the average job-completion-time JCT by 1.43 (equivalent of 30% reduction). Now we assume that the processing time of every benchmark task is determined before they are executed. We leave the study of a scheduler for jobs with stochastic processing time for future work.

6 RELATED WORK

The system’s design follows the best practice of previous benchmarking works and we provide a highly efficient cluster system. We categorize previous studies into two classes: micro-benchmark and macro-benchmark. Micro-benchmark focuses more on low-level operators. DeepBench (Baidu, 2017) is designed to study the kernel-level operations such as GEMM across multiple hardware devices, including both cloud servers and edge nodes. AI matrix (Alibaba, 2018) gets inspiration from DeepBench and further extends it to benchmark both layers (e.g., RNN) and models (e.g., Faster-RCNN). These studies focus on small sets of impact factors and can not simulate the scenarios in practice. Macro-benchmark such as Fathom (Adolf et al., 2016) and AI Benchmark (Ignatov et al., 2019) collect a set of models and study their performance on the mobile side. DawnBench (Coleman et al., 2017) and MLPerf Inference (Reddi et al., 2020) support more scenarios and provide competition for both industry and academia to measure their systems. In comparison, our work provides full support for developers to speed up their benchmark process and configure new services with ease. Our system also explores a wider space to evaluate DL systems for a comprehensive study. ParaDNN (Wang et al., 2019) also uses analysis models like Roofline and Heat maps to study DL platforms, but it focuses on training workload and lack of configurability. In general, we implement the system to complement existing inference benchmark suits and obtain more comprehensive analysis results.

7 SUMMARY AND FUTURE WORK

With the rapid development of deep learning (DL) models and the related hardware and software, benchmarking for service configuration and system upgrade becomes developers’ day-to-day tasks. Previous work focuses on the isolated DL model benchmark study and leaves tedious and error-prone tasks to developers. In this work, we design and implement an automatic benchmarking system to address these issues. Our system streamlines the task executions and explores a wide range of design space. It provides many analysis models to gain insights for resource allocation and service operations. A scheduler is implemented to improve efficiency. We conduct many experiments to demonstrate the functions of our system and provide many practical guidelines. We plan to continue upgrading the system and design more APIs to give users more flexibility to adopt our system into their own DL services.
REFERENCES

Adolf, R., Rama, S., Reagen, B., Wei, G.-Y., and Brooks, D. Fathom: Reference workloads for modern deep learning methods. In 2016 IEEE International Symposium on Workload Characterization (IISWC), pp. 1–10. IEEE, 2016.

Alibaba. Ai matrix. https://github.com/alibaba/ai-matrix, 2018. Accessed: 2020-09-02.

Anthony, L. F. W., Kanding, B., and Selvan, R. Carbontracker: Tracking and predicting the carbon footprint of training deep learning models. arXiv preprint arXiv:2007.03051, 2020.

Bahdanau, D., Cho, K., and Bengio, Y. Neural machine translation by jointly learning to align and translate. arXiv preprint arXiv:1409.0473, 2014.

Baidu. Benchmarking deep learning operations on different hardware. https://github.com/baidu-research/DeepBench, 2017. Accessed: 2020-09-02.

Baylor, D., Breck, E., Cheng, H.-T., Fiedel, N., Foo, C. Y., Haque, Z., Haykal, S., Ispir, M., Jain, V., Koc, L., et al. Tfx: A tensorflow-based production-scale machine learning platform. In Proceedings of the 23rd ACM SIGKDD International Conference on Knowledge Discovery and Data Mining, pp. 1387–1395, 2017.

Chard, R., Li, Z., Chard, K., Ward, L., Babuji, Y., Woodard, A., Tuecke, S., Blaiszik, B., Franklin, M., and Foster, I. Dlhub: Model and data serving for science. In 2019 IEEE International Parallel and Distributed Processing Symposium (IPDPS), pp. 283–292. IEEE, 2019.

Chen, T., Moreau, T., Jiang, Z., Zheng, L., Yan, E., Shen, H., Cowan, M., Wang, L., Hu, Y., Ceze, L., et al. {TVM}: An automated end-to-end optimizing compiler for deep learning. In 13th {USENIX} Symposium on Operating Systems Design and Implementation (OSDI 18), pp. 578–594, 2018.

Coleman, C., Narayanan, D., Kang, D., Zhao, T., Zhang, J., Nardi, L., Bailis, P., Oluokiton, K., Ré, C., and Zaharia, M. Dawnbench: An end-to-end deep learning benchmark and competition. Training, 100(101):102, 2017.

Crankshaw, D., Wang, X., Zhou, G., Franklin, M. J., Gonzalez, J. E., and Stoica, I. Clipper: A low-latency online prediction serving system. In 14th {USENIX} Symposium on Networked Systems Design and Implementation (NSDI 17), pp. 613–627, 2017.

Deng, J., Dong, W., Socher, R., Li, L.-J., Li, K., and Fei-Fei, L. Imagenet: A large-scale hierarchical image database. In 2009 IEEE conference on computer vision and pattern recognition, pp. 248–255. Ieee, 2009.

FastAPI. Fastapi framework, high performance, easy to learn, fast to code, ready for production. https://fastapi.tiangolo.com/, 2019. Accessed: 2020-06-03.

Gatys, L. A., Ecker, A. S., and Bethge, M. Image style transfer using convolutional neural networks. In Proceedings of the IEEE conference on computer vision and pattern recognition, pp. 2414–2423, 2016.

Google. cadvisor: Analyzes resource usage and performance characteristics of running containers. https://github.com/google/cadvisor, 2018. Accessed: 2020-06-03.

Han, S., Mao, H., and Dally, W. J. Deep compression: Compressing deep neural networks with pruning, trained quantization and huffman coding. arXiv preprint arXiv:1510.00149, 2015.

Hazelwood, K., Bird, S., Brooks, D., Chintala, S., Diril, U., Dzhulgakov, D., Fawzy, M., Jia, B., Jia, Y., Kalro, A., et al. Applied machine learning at facebook: A datacenter infrastructure perspective. In 2018 IEEE International Symposium on High Performance Computer Architecture (HPCA), pp. 620–629. IEEE, 2018.

He, K., Zhang, X., Ren, S., and Sun, J. Deep residual learning for image recognition. In Proceedings of the IEEE conference on computer vision and pattern recognition, pp. 770–778, 2016.

Hochreiter, S. and Schmidhuber, J. Long short-term memory. Neural computation, 9(8):1735–1780, 1997.

Howard, A. G., Zhu, M., Chen, B., Kalenichenko, D., Wang, W., Weyand, T., Andreetto, M., and Adam, H. Mobilenets: Efficient convolutional neural networks for mobile vision applications. arXiv preprint arXiv:1704.04861, 2017.

Ignatov, A., Timofte, R., Kulik, A., Yang, S., Wang, K., Baum, F., Wu, M., Xu, L., and Van Gool, L. Ai benchmark: All about deep learning on smartphones in 2019. In 2019 IEEE/CVF International Conference on Computer Vision Workshop (ICCVW), pp. 3617–3635. IEEE, 2019.

Jouppi, N. P., Young, C., Patil, N., Patterson, D., Agrawal, G., Bajwa, R., Bates, S., Bhatia, S., Boden, N., Borchers, A., et al. In-datacenter performance analysis of a tensor processing unit. In Proceedings of the 44th Annual International Symposium on Computer Architecture, pp. 1–12, 2017.

Karlaš, B., Interlandi, M., Renggli, C., Wu, W., Zhang, C., Mukunthu Iyappan Babu, D., Edwards, J., Lauren, C.,
Xu, A., and Weimer, M. Building continuous integration services for machine learning. In *Proceedings of the 26th ACM SIGKDD International Conference on Knowledge Discovery & Data Mining*, pp. 2407–2415, 2020.

Microsoft. Onnx runtime: cross-platform, high performance scoring engine for ml models. [https://github.com/microsoft/onnxruntime](https://github.com/microsoft/onnxruntime), 2019. Accessed: 2020-05-07.

NVIDIA. Nvidia data center gpu manager (dcgm) is a suite of tools for managing and monitoring tesla gpus in cluster environments. [https://developer.nvidia.com/dcgm](https://developer.nvidia.com/dcgm), 2018. Accessed: 2020-10-02.

NVIDIA. Multi-process service. [https://docs.nvidia.com/deploy/pdf/CUDA_Multi_Process_Service_Overview.pdf](https://docs.nvidia.com/deploy/pdf/CUDA_Multi_Process_Service_Overview.pdf), 2020a. Accessed: 2020-06-28.

NVIDIA. The triton inference server provides a cloud inferencing solution optimized for nvidia gpus. [https://github.com/NVIDIA/triton-inference-server](https://github.com/NVIDIA/triton-inference-server), 2020b. Accessed: 2020-05-07.

Olston, C., Fiedel, N., Gorovoy, K., Harmsen, J., Lao, L., Li, F., Rajashekhari, V., Ramesh, S., and Soyke, J. Tensorflow-serving: Flexible, high-performance ml serving. *arXiv preprint arXiv:1712.06139*, 2017.

Reddi, V. J., Cheng, C., Kanter, D., Mattson, P., Schmuelling, G., Wu, C.-J., Anderson, B., Breughe, M., Charlebois, M., Chou, W., et al. Mlperf inference benchmark. In *2020 ACM/IEEE 47th Annual International Symposium on Computer Architecture (ISCA)*, pp. 446–459. IEEE, 2020.

Silver, D., Schrittwieser, J., Simonyan, K., Antonoglou, I., Huang, A., Guez, A., Hubert, T., Baker, L., Lai, M., Bolton, A., et al. Mastering the game of go without human knowledge. *nature*, 550(7676):354–359, 2017.

Tran, D., Bourdev, L., Fergus, R., Torresani, L., and Paluri, M. Learning spatiotemporal features with 3d convolutional networks. In *Proceedings of the IEEE international conference on computer vision*, pp. 4489–4497, 2015.

Vaswani, A., Shazeer, N., Parmar, N., Uszkoreit, J., Jones, L., Gomez, A. N., Kaiser, L., and Polosukhin, I. Attention is all you need. In *Advances in neural information processing systems*, pp. 5998–6008, 2017.

Wang, Y., Wei, G.-Y., and Brooks, D. A systematic methodology for analysis of deep learning hardware and software platforms. *Proceedings of Machine Learning and Systems*, 2020:30–43, 2020.

Williams, S., Waterman, A., and Patterson, D. Roofline: an insightful visual performance model for multicore architectures. *Communications of the ACM*, 52(4):65–76, 2009.

Yu, P. and Chowdhury, M. Salus: Fine-grained gpu sharing primitives for deep learning applications. *arXiv preprint arXiv:1902.04610*, 2019.

Zaharia, M., Chen, A., Davidson, A., Ghodsi, A., Hong, S. A., Konwinski, A., Murching, S., Nykodym, T., Ogilvie, P., Parkhe, M., et al. Accelerating the machine learning lifecycle with mlflow. *IEEE Data Eng. Bull.*, 41(4):39–45, 2018.

Zhang, H., Li, Y., Huang, Y., Wen, Y., Yin, J., and Guan, K. Mlmodelci: An automatic cloud platform for efficient mlaas. *arXiv preprint arXiv:2006.05096*, 2020.

Zhu, J.-Y., Park, T., Isola, P., and Efros, A. A. Unpaired image-to-image translation using cycle-consistent adversarial networks. In *Proceedings of the IEEE international conference on computer vision*, pp. 2223–2232, 2017.