Millimetre Wave Kinetic Inductance Parametric Amplification using Ridge Gap Waveguide

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Abstract
We present the design and simulation methodology of a superconducting ridge-gap waveguide (RGWG) as a potential basis for mm-wave kinetic inductance travelling wave parametric amplifiers (KI-TWPAs). A superconducting RGWG was designed using Ansys HFSS to support a quasi-TEM mode of transmission over a bandwidth of 20–120 GHz with its internal dimensions optimised for integration with W-band rectangular waveguide. A design of an impedance loaded travelling wave structure incorporating periodic perturbations of the ridge was described. A method to simulate the nonlinear kinetic inductance via user-defined components in Keysight’s ADS was outlined, which yielded the power dependent S-parameters and parametric signal gain. A RGWG with a 30 nm NbTiN coating and 5 μm conductor spacing, corresponding to a kinetic inductance fraction $\alpha \sim 60\%$ was used for the description of a KI-TWPA with 900 perturbations equivalent to a physical length 25 cm that achieved more than 10 dB of signal gain over a 75–110 GHz bandwidth via 4-wave mixing (4WM).

Keywords Millimetre wave · W-band · Kinetic inductance · Travelling wave · Ridge gap waveguide · Parametric amplifier

1 Introduction

Kinetic inductance travelling-wave parametric amplifiers (KI-TWPAs) promise high gain, large bandwidth and potential for quantum limited noise performance [1]. Currently, most KI-TWPAs [2–4] have been fabricated for sub-20 GHz and mK temperature operation. However, there is interest in extending this to higher frequency applications where quantum limited amplification is needed such as dark matter detection [5] and high frequency quantum computing [6]. At mm-wave energy scales these superconducting circuits are less sensitive to thermal background noise due to
the higher photon energies involved. Hence, these paramps may achieve their low noise figures at temperatures of 1 K, which are accessible via higher heat lift fridges such as adsorption coolers. Additionally, even at temperatures of 4 K KI-TWPAs are competitive with their transistor-based counterparts while producing an order of magnitude smaller heat load [7]. Recently, narrowband parametric amplification has been achieved at 30 GHz [8] and at 95 GHz [9], while the highest reported frequency in a travelling wave design [10] is approaching 30 GHz.

At mm-wave frequencies KI-TWPAs based on conventional transmission lines such as co-planar waveguides and microstrips face challenges in connecting their μm size traces to external feeding structures without suffering from large dielectric losses. A potential method to alleviate dielectric losses at frequencies over 100 GHz is to use more exotic transmission lines that incorporate a hollow gap between conducting surfaces instead of one filled with dielectric and can be coupled to waveguide feeds via low loss transitions. One such structure is ridge gap waveguide (RGWG)[11] which is readily scalable to frequencies of a few hundred GHz [12] and can support quasi-TEM mode of transmission over an octave of bandwidth. Coating these RGWGs with a thin superconducting (SC) film such as NbTiN and maintaining a conductor separation in the micrometer to sub-micrometer range can help achieve kinetic inductance fractions in excess of 90%, which makes these transmission line structures potential candidates for mm-wave KI-TWPAs.

2 High Kinetic Inductance Ridge Gap Waveguide

RGWG as shown in Fig. 1 consists of two parallel conducting plates where one of these is textured with a wave-guiding ridge that is surrounded by a periodic pin surface. This surface, often referred to as a bed of nails, on its own is able to stop the propagation of parallel plate modes that may exist in the hollow enclosed cavity if

![Fig. 1](image-url)
the nails were absent. This protecting characteristic is exhibited over a certain band-width that depends on the length of the nails and the gap to the top surface [11]. A wave guiding channel may be created between these nails, where the nails act as soft magnetic boundaries at the edges of the channel, creating a type of groove gap waveguide (GGWG). Finally, adding a central ridge to this groove allows the propagation of a TEM mode with fields constrained to the volume between the top of the ridge and the opposing lid.

RGWG can be fabricated using conventional metal milling or silicon micromachining techniques [12]. In both cases a SC coating is necessary to provide a non-linear kinetic inductance, which is required for parametric processes to take place. The extent to which this nonlinearity dominates over the total structure inductance is dictated by the kinetic inductance fraction, $\alpha$, which is dependent on the waveguide geometry and film properties. It is possible for $\alpha \rightarrow 1$ to be achieved when the SC coating is thin enough such that $t \ll \lambda_L$, where $\lambda_L$ is the London penetration depth, and the hollow gap is small such that $h < 1 \mu m$, which can be achieved via a deposition of a thin spacer along the outer perimeter of one of the involved bodies.

RGWG design begins with the engineering of the pin surface which creates the stopband for any global parallel plate or cavity modes that may exist. It was found that the pin surface protecting bandwidth was maximised when the nail width and separation were equal. A nail width of 0.5 mm was chosen with consideration to the mechanical fragility of the nails. Following this, the GGWG and RGWG are considered, which are dictated by the groove width, $a$, and ridge width $w$, respectively. The groove width was set to match the long wall of W band rectangular waveguide and three rows were chosen to provide sufficient isolation to the device edges in the GGWG region that would be needed in the transition between rectangular waveguide and RGWG via a taper. The GGWG mode fields decay by $\sim 30$ dB per row of nails. The upper cut-off of the RGWG mode can be affected by $w$ especially when $w \rightarrow a$, since the added presence of a high KI coating can cause higher order RGWG modes to propagate. The main tool for designing and simulating these structures was Ansys HFSS. The superconducting coating was included in S-parameter simulations via frequency dependent surface impedance boundaries [13] that are imported from a calculation of the Zimmermann conductivity [14]. Specifically, the eigenmode solver of HFSS was used to calculate the dispersion properties of pin surface and RGWG, which is shown in Fig. 1.

A thin SC coating enhances the linear kinetic inductance and sets the critical current of the line which needs to be reached for maximum non-linearity. Since the top of the ridge is easily accessible, the thin SC coating can be applied to this surface. A thin coating ($< \lambda_L$) on the textured surface deposited by a technique such as magnetron sputtering may cause current restrictions or discontinuities on sharp edges of the pins and ridges. These coating irregularities may give rise to undesired dissipative behaviour in regions of high current flow where the critical current may be surpassed locally. This may be alleviated by depositing a much thicker coating.

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[1] https://www.ansys.com/products/electronics/ansys-hfss.
on the textured surface or using a deposition technique such as atomic layer deposition, which can produce a thin conformal coating on all edges. For this work, NbTiN coatings with a critical temperature $T_c \sim 12.5$ K and resistivity in the range of 200 $\mu\Omega$ cm are considered. This coating would allow paramp operation at $\sim 1 - 4$ K with gain and noise figures comparable to equivalent transistor-based amplifiers [7]. At these temperatures dissipation should be low due to the $\exp(-T_c/T)$ scaling of the SC surface resistance [15].

The linear kinetic inductance, $L_{k0}$, introduced via the SC coating scales with the dimensions and physical properties of the film. The kinetic inductance scales non-linearly with current [16] and when expanded to second order can be expressed as $L_k(I) = L_{k0} \left[1 + (I/I_s)^2\right]$, where $I_s$ is the scaling current which is dependent on the SC material properties and the geometry of the trace.

For parametric processes to be explored, it is first necessary to estimate the values of the low current $K_I$ and scaling current. The value of $L_{k0}$ per square can be estimated [13] using $L_{k0} = \frac{\mu_0 \lambda_L}{4} \tanh(\frac{t}{\lambda_L})$. To determine the extent to which the $K_I$ non-linearity dominates over the total inductance the $K_I$ fraction can be calculated using, $\alpha = L_k/(L_g + L_k)$, where $L_g = \mu_0 h$ is the geometric inductance for a RGWG assuming a parallel plate (separated by $h$) approximation.

The scaling current is sensitive to a number of factors [17] including the substrate roughness and composition, film geometry, deposition conditions and operating temperature which means it can vary over orders of magnitude for a given SC material. Analytic solutions of the scaling current usually only set the upper bound for this value. The scaling current, which is of the order of the critical current, $I_c$, takes on different solutions based on the current distribution on the SC trace. For a conductor of thickness $t \ll \lambda_L$ and width $w \ll \lambda_\bot$, where $\lambda_\bot = 2\lambda_L^2/t$ is the perpendicular penetration depth, the current density can be assumed to be constant across the film [18]. This low temperature scaling current can be estimated [10, 19] using $I_s = \frac{w\kappa_s}{3\sqrt{3\pi\xi(0)\mu_0}} \sqrt{N_0\Delta^2/\mu_0\lambda_L^2}$, where $\kappa_s$ is constant of order 1 [20] and $N_0$ is the number density at the Fermi level. However, if the trace is wider than the perpendicular penetration depth $w \gg \lambda_\bot$, the current distribution becomes non-uniform and accumulates at the edges of the film [17]. In this work, the SC traces are in the 100s $\mu$m which means solutions of the wide film critical current should be explored. In the case of a pristine film with perfect edges the critical current [17] in SI units is given by

$$I_c(T) = \frac{2\Phi_0}{3\sqrt{3\pi\xi(0)\mu_0}} \left(\frac{w}{\lambda_\bot(0)}\right)^{1/2} (1 - T/T_c),$$ (1)

where $\mu_0$ is the permeability of free space, $\Phi_0$ is the magnetic flux quantum and $\xi(0)$ is the zero temperature Ginzburg-Landau coherence length. The perfect edge stipulation is important since $I_c$ in a wide film is dependent on the pinning properties that arise at the edges of the trace [17]. Therefore, Eq. 1 provides only the upper limit for the wide film critical current. The scaling current should be measured experimentally by measuring the kinetic inductance induced frequency shift of a resonant cavity with ac or dc current [2].
In the presence of a strong pump tone and weak signal, a four photon mixing process can take place [21], referred to as four wave mixing (4WM). These tones are related by
\[ \omega_p = \omega_s + \omega_i, \]
where \( \omega_p, \omega_s, \omega_i \) are the frequencies of the pump, signal and idler tones, respectively.

For sufficient parametric gain the RGWG needs to be extended into an electrically long structure spanning hundreds of wavelengths. In addition, the phase relationship between the pump, signal and idler tones should be considered such that the dispersion difference is
\[ \Delta\beta = \beta_s + \beta_i - 2\beta_p = 0, \]
where \( \beta_p, \beta_s, \beta_i \) are the propagation constants of the involved tones. The nonlinear SC line introduces a power dependent phase shift, which must be compensated for by introducing anomalous dispersion [2, 3, 22, 23] in order to satisfy the condition for wideband exponentially scaling gain. If this condition is not met, the gain will scale quadratically with propagation length.

The simplest dispersion engineering method involves the perturbation of the transmission line every \( \lambda_{\text{per}}/2 \) such that stopbands are created at integer values of frequency \( f_{\text{per}} \) near which anomalous dispersion is introduced. By positioning the pump tone near these dispersion features, the nonlinear phase slippage may be partially or even fully cancelled out.

Dispersion engineering is achieved in RGWG by employing periodic widening of the ridge as shown in Fig. 2. The impedance of the loaded and unloaded sections is estimated using
\[ Z = \sqrt{\frac{L_g + gL_k0}{C}}, \]
where \( C \) is the capacitance and \( g \) is a geometric factor accounting for the SC coating of both parallel surfaces of the RGWG. More accurate impedance values were extracted from HFSS simulations, these were within \( \sim 5\% \) of the analytic solutions. The impedance of the perturbations was \( \sim 20\% \) lower than the main line, which introduced a weak dispersion feature near the
f_{\text{per}} that becomes stronger as the number of repeating units is increased. As a result, a travelling wave structure spanning $450\lambda_{\text{per}}$ was investigated. Such an electrically long structure can be simulated directly in HFSS to show the stopband structure, however, HFSS is unable to take into account the current dependency of the KI nor does it support harmonic balance simulations to evaluate mixing. Keysight’s ADS is an industry standard amplifier design tool that is able to address these aspects of the paramp design process.

An equivalent circuit model of the nonlinear RGWG was created, which is described in Fig. 2. This circuit takes into account the nonlinear inductance via a general symbolically defined device (SDD) component, which requires a voltage definition in the form $V = -\frac{d}{dt}L_k(I)I$. These repeating unit cells were nested within subcircuits to make up a total of $N = 900$ cells. A large signal S-parameter (LSSP) simulation within ADS was used to generate the nonlinear S-parameters and dispersion properties as shown in Fig. 3. The dispersion of the TWPA was calculated via $\beta = \frac{-\arg(S_{21})}{L}$, where $L$ is the device length.

The parametric gain of the dispersion engineered RGWG can be estimated using the simulated dispersion data and the analytic solutions from the coupled mode equations (CME) that describe the amplitudes of the propagating waves. The CME solutions used only take into account the amplitude of the first pump harmonic and the associated mixing products but do not take into account the transmission of the travelling-wave structure. These CMEs can be extended to include higher order pump harmonics, which provide a more accurate gain description. Alternatively, the parametric gain can be simulated using the harmonic balance (HB) simulator of ADS with the repeating circuit shown in Fig. 2, which accurately describes the gain of such a paramp to a chosen number of input tone

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Fig. 3 Left: Transmission coefficient and effective dispersion from a simulation of 900 $\lambda/2$ cells in ADS, corresponding to a physical propagation length of 25 cm. The first 2 stopbands at $f_{\text{per}} \sim 95$ GHz and $f_{\text{per}} \sim 190$ GHz are shown. The unperturbed dispersion $\beta$ was found by fitting a second order polynomial to the perturbed data. Right: The simulated signal gain from the HB simulator of ADS. The $-9$ dBm pump tone is positioned at 93 GHz, while the signal is at $-80$ dBm. The CME solution [24] uses the dispersion properties extracted from the 900 cell LSSP simulation to calculate the phase mismatch between the 3 involved tones. The calculated CME gain matched the simulated ADS gain at $I/I_c \sim 0.19$.
harmonics. The simulated gain of the 25 cm RGWG paramp and is shown in Fig. 3, which shows a 10 dB signal gain over the whole of W-band. This was achieved by sweeping the pump tone frequency and input power close to the first stop band generated by the periodic structure such that the phase mismatch $\Delta \beta$ is minimised and maximum gain is achieved.

The simulated gain shows promise that the fabricated RGWG KI-TWPA could provide sufficient gain as a front end amplifier. A logical prototype could be a series resonator of similar dimensions which would confirm the feasibility of the high KI RGWG at W-band and would allow the characterisation of the scaling current. The designed RGWG had a conservative conductor spacing of $h = 5 \, \mu m$ and a 30 nm NbTiN film coating, which resulted in a KI fraction of $\alpha \sim 60\%$. By reducing $h$ to 1 $\mu m$, via a shorter deposition of the spacer layer, the value of $\alpha$ may reach $> 90\%$, which would significantly reduce the required TWPA length.

The main practical challenges of the RGWG TWPA include the fabrication of the long structure, and the matching of the $6 \, \Omega$ RGWG impedance to the chosen feedlines. The structure could be fabricated on a 4 inch wafer by reducing the groove width $a$ to 700 $\mu m$ and meandering the RGWG with a single row of nails between meanders to provide isolation between parallel lines. Transitioning to W band rectangular waveguide could be achieved via a slow tapering of the height of the ridge leading to the waveguide flange. In addition, the ridge could be reduced in width to < 50 $\mu m$ which would increase the impedance to > 50 $\Omega$ and ease the impedance mismatch. The use of an impedance taper would unavoidably cause significant gain ripples. However, such a tapered structure should still be able to demonstrate wideband parametric gain, confirming its feasibility and paving the way for further development.

4 Conclusion

This work proposed a low loss structure for high frequency KI-TWPAs in the form of RGWG. This structure is scaleable to frequencies of a few 100 GHz with feature sizes in the hundreds of $\mu m$ range, meaning that it can be fabricated with high speed metal milling techniques or silicon micromachining and coated in a superconducting film. It was shown via superconducting HFSS simulations that this structure can have over an octave of bandwidth for single mode quasi-TEM transmission, which would be ultimately limited by external factors such as the transition to the chosen waveguide feeds. It was shown via ADS HB simulations that a NbTiN coated RGWG can be the basis of a TWPA and provide over 10 dB of parametric gain over the whole of W-band (75–110 GHz) with a physical length of 25 cm. This paper sets out the design methods and simulation pipeline for such KI RGWGs and their TWPA counterparts while including an accessible method to simulate the expected parametric gain.

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