Development of a power electronics converter dynamics toolbox for MATLAB

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Abstract

Providing the required load’s power using Switch Mode Power Supplies (SMPS), leads to smaller and more efficient converters. Correct operation of a SMPS needs some form of control. Applying the well-known controller design techniques like root locus and Bode needs a dynamical model for system under control. Power electronics converters are dynamical variable structure systems. Extraction of small signal dynamical equations by hand is cumbersome, time consuming and error prone. Development a software to automate the small signal model extraction process of power electronics converters is the aim of this paper. Parameter’s change and uncertainty’s effects on overall system performance can be studied easily using this software. Multi graph property of developed software, allow drawing results of different simulations on the same graph. This makes comparison possible. Available commercial softwares cannot calculate the algebraic transfer functions while developed software can do this job. Contact correspondence author to receive the software.

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1. Introduction

Power electronics is the science and art of control and efficient conversion of electric power using solid state semiconductor devices. Power range can vary from milliwatts up to several megawatts. Switch Mode Power Supply (SMPS) is among the most common technologies in use. SMPS’s working principle is based on storing energy temporarily in passive elements like inductors and capacitors and releasing it to output at different voltage level. SMPS are composed of two parts, i.e., power and control circuitry.

Power circuitry, is responsible for providing required and suitable form of power for load. Control circuitry, provides control signals required for power circuitry’s switches, i.e. signals which make the switches on and off. When a switch is made on (off), a new current path is added to (removed from) circuit. In fact, SMPS is an example of variable structure systems. Fig. 1 shows a buck converter and its equivalent circuit for switch is closed and opened.

In order to obtain a dynamical model of variable structure systems, averaging techniques which takes average of sub circuit’s state space model has been suggested (Middlebrook and Cuk, 1976). Obtaining a dynamical model for plant is the first step toward designing a controller in model base controller design techniques like Bode diagram and root locus. In this paper a MATLAB® base toolbox is designed to extract converter’s dynamical model. Available commercial softwares give only converter's frequency response diagram, while developed software can give either frequency response diagram or the algebraic transfer function. Effect of uncertainties in system parameters can be studied easily with the aid of this toolbox. Developed toolbox can analyze buck, boost, buck-boost, Cuk, SEPIC, fly back, forward and full bridge topologies.

2. Previous works

Modeling is the process of formulating a mathematical description of the system. Generally speaking, a dynamical system can be described in two forms, i.e., input-output equation, and state space equation.

Input-output equation gives no information about the internal details of system. State space
MATLAB®/Simulink®, PSpice and NI Multisim are general purpose circuit simulation's programs. Either analogue or digital simulations can be done. Although PSIM can be used to simulate any electronic circuits, it is designed specifically for use in power electronics and motor drive simulations. SABER is a nonlinear dynamic system simulation program. Dynamical equation of power electronics converters is nonlinear in nature. So, after extraction of circuit’s dynamical equations SABER can be used to solve these equations. Simulink and power system toolbox of MATLAB provide an environment to analyze power electronics circuits by drawing the schematic (Asadi and Abut, 2016). MATLAB/Simulink can be used to extract small signal Bode plot of converters. It gives no information about system's poles and zero locations. In other words, converter’s transfer function can’t obtained in closed algebraic form

\[ H(s) = \frac{k}{s^n + a_{n-1}s^{n-1} + \cdots + a_1s + a_0} \]

at the time of this writing, there is no power electronics converter dynamic toolbox available for MATLAB.

There is a rich literature on converter modeling. Here is a quick review of some of them: Mahery et al. (2012) proposed a new method based on Laplace transforms for mathematical modeling of buck-boost in CCM. Ghadimi et al. (2007) presented a detailed small signal and transient analysis of a full bridge pulse width modulator (PWM) converter based on average model. Emadi (2004) presented a modular approach for modeling based on generalized state space averaging. Modabbernia et al. (2013) presented a complete state space averaging model for buck-boost converter.

3. CCM, DCM, and state space averaging (SSA)

SMPS is nothing more than passive elements, semiconductor switches and a controller. In steady state, converters voltages and currents are periodic. An inductor current that remains positive throughout the switching period is known as continuous current. Conversely, in discontinuous current, inductor current’s return to zero during each switching period. Table 1, compares two modes of operation:

| Table 1: Comparison of CCM and DCM |
|------------------------------------|
| Continuous Current Mode (CCM) | Discontinuous Current Mode (DCM) |
| Voltage gain is independent of load | Voltage gain is a function of load and design parameters |
| Input current is continuous and non-pulsating | Input current is pulsating |
| Efficiency is higher in comparison with DCM | Commutation of controlled switch is made with zero current which reduce commutation losses |
| Inductor size can be reduced considerably in comparison with CCM |

Generally a CCM converter is designed when the load isn’t variable. A variable load may cause converter to enter DCM for light loads. So, this must be taken into account for controller design. Assuming that converter works in CCM next step is to find converter’s dynamical equation.

Fig. 1: Buck converter, equivalent circuit for switch close and switch open are shown in b and c respectively.

Various techniques can be found in literature to obtain a linear continuous time invariant (LTI) model of a DC-DC converter. The most well-known methods are: Current injected approach, circuit averaging and state space averaging (Middlebrook and Cuk, 1976; Sokal et al., 1991; Mohan et al., 2007; Vorperian, 1990). Averaging and small signal linearization is key steps of these methods.

State space averaging described in Middlebrook and Cuk (1976) is appropriate to describe converters that work in CCM while is less suitable for converters work in DCM. The current injected method (Sokal et al., 1991; Mohan et al., 2007) can do the job of modeling in either CCM or DCM. Circuit averaging gained a lot of attention recently due to its generality (Hren and Slibar, 2005).

To avoid time and financial losses converter's performance must be ensured before realization. This job can be done with the aid of simulation softwares. Some of the most important softwares are: PSpice®, NI Multisim®, PSIM®, SABER® and MATLAB®.
The two set of state equations: one set for switch close and one set for switch open, are averaged over the switching period. Variables are perturbed around their steady state operating point and small signal dynamical model is extracted.

Assume a general state space model of following form for a CCM converter:

\[
\begin{align*}
\dot{x} &= A_1x + B_1u \\
\dot{v}_o &= C_1^T x \\
\dot{x} &= A_2x + B_2u \\
\dot{v}_o &= C_2^T x
\end{align*}
\] (1) (2)

Where \(x\) is state vector, i.e. capacitors' voltage and inductors' current, \(u\) is control input and \(v_o\) is output voltage of converter. Applying State Space Averaging (SSA) to Eqs. 1 and 2 leads to: \(\text{(Erickson and Maksimovic, 2001)}\):

\[
\begin{align*}
\dot{x} &= [A_1D + A_2(1-D)]\dot{x} + [(A_1 - A_2)x + (B_1 - B_2)v_o]\tilde{d} \\
\dot{v}_o &= [C_1^T + C_2^T(1-D)]\dot{x} + [(C_1 - C_2)^T x]\tilde{d}
\end{align*}
\] (3) (4)

where tilde \((\dot{x}, \tilde{d}, \tilde{v}_o)\) shows small signal variables and \(D\) shows steady state duty ratio.

Table A in appendix, shows state space of Eqs. 1 and 2 for different type of DC-DC converters. Equations in Table B and Eqs. 3 and 4 make the working principles of developed software.

4. Designed software

Fig. 2 shows the main menu of software. Figs. 3, 4, and 5, show some sections of developed software.
Determining the parameters of the circuit is all what must be done by user. There is no need to draw circuit's schematic. Non idealities like equivalent series resistance of inductors and capacitors, voltage drop of diodes, on resistance of MOSFET's and internal resistance of input sources are taken into account.
Obtaining the dynamical model of converter in presence of these non-idealities is quite cumbersome for pencil-and-paper analysis. Assume a SEPIC converter with the following parameters:

\[ \text{Vin} = 9\text{V}, \text{rinternal} = 0.9\text{H}, L_1 = 1.2 = 90\mu\text{H}, rL_1 = rL_2 = 10\mu\text{H}, C_2 = 80\text{mF}, rC_1 = rC_2 = 15\text{mH}, \text{VDIode_on} = 0.7, r\text{Diode_on} = 0.05 \Omega, r\text{MOSFET} = 40 \text{m} \Omega, \text{RLoad} = 3\Omega \]

After entering component’s values and selecting the desired transfer function (for example \( H(s) = \frac{V_{o2}(s)}{d(s)} \)), following results are obtained in Fig. 6.

![Bode Diagram](image)

**Fig. 6:** Bode diagram and pole zero diagram of selected transfer function

| Topology  | Steady state inductor’s current | Steady state capacitor’s voltage |
|-----------|---------------------------------|----------------------------------|
| Buck      | \( V_s + V_d(\frac{D}{1}) \)    | \( R_s x (D-1) + (R_i + R_d + R_m) \) |
| Boost     | \( V_s + V_d(\frac{D}{1}) \)    | \( R_s x (D-1) + (R_i + R_d + R_m) \) |
| Buck-Boost| \( V_s + V_d(\frac{D}{1}) \)    | \( R_s x (D-1) + (R_i + R_d + R_m) \) |

**Table 2:** Steady state values in presence of component’s non-idealities

Effect of load change on \( \frac{dV_{o2}}{ds} \) transfer function, i.e. transfer function from control input to output voltage is shown in Fig. 7.

![Bode Diagram](image)

**Fig. 7:** Effect of load’s change on converter’s transfer function

Beside this Graphical user interface (GUI), a collection of text base programs are developed to analyze converters symbolically in presence of components non idealities such as diode’s voltage drop and capacitor’s ESR. **Table 2** shows some of the obtained results for basic converters:

5. Conclusion

Computers play an important role in designing and analyzing modern power electronics converters. In this paper software has developed to extract small signal dynamical model of famous DC-DC topologies. Developed software can give the algebraic transfer function of converter which makes it different from available commercial softwares. Developed software can be used to design the control loop of converters.

List of symbols

- \( i \) current
- \( v \) voltage
- \( i_{Lj} \) j'th inductor current
- \( v_{Cj} \) j'th capacitor voltage
- \( i_{lm} \) magnetizing inductance
- \( N_1 \) primary turns number of transformer
- \( N_2 \) secondary turns number of transformer
- \( r_{L1}, r_{L2} \) primary winding’s resistance
- \( r_{N2}, r_{sec} \) secondary winding’s resistance
- \( R_L \) inductor value
- \( C \) capacitor value

\( r_L (r_c) \) series resistance of inductor (capacitor)
\( r_d \) diode’s forward bias series resistance
\( v_p \) diode’s forward voltage drop
\( r_{sw} \) MOSFET’s on resistance
\( v_s \) input source’s internal resistance
\( V_s \) input source’s voltage

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Appendix

Table A: Different type of DC-DC converters and their large signal state space model

| Converter type | Dynamical equation for switch closed | Dynamical equation for switch opened |
|----------------|--------------------------------------|--------------------------------------|
| Buck           | $\frac{di}{dt} = \frac{1}{L} (ir + r_m + r) i_i - \frac{1}{C} v_c - \frac{1}{R} v_i$ | $\frac{di}{dt} = \frac{1}{L} (ir + r_m + r) i_i - \frac{1}{C} v_c - \frac{1}{R} v_i$ |
| Boost          | $\frac{di}{dt} = \frac{i_c}{C} - \frac{i}{R_c} v_c$ | $\frac{di}{dt} = \frac{i_c}{C} - \frac{i}{R_c} v_c$ |
| Buck-Boost     | $\frac{di}{dt} = \frac{i_c}{C} - \frac{i}{R_c} v_c$ | $\frac{di}{dt} = \frac{i_c}{C} - \frac{i}{R_c} v_c$ |
| Cuk            | $\frac{dv}{dt} = \frac{1}{C} v_c - \frac{1}{R_c} v_c$ | $\frac{dv}{dt} = \frac{1}{C} v_c - \frac{1}{R_c} v_c$ |
| SEPIC          | $\frac{dv}{dt} = \frac{1}{C} v_c - \frac{1}{R_c} v_c$ | $\frac{dv}{dt} = \frac{1}{C} v_c - \frac{1}{R_c} v_c$ |
| Fly back       | $\frac{dv}{dt} = \frac{1}{C} v_c - \frac{1}{R_c} v_c$ | $\frac{dv}{dt} = \frac{1}{C} v_c - \frac{1}{R_c} v_c$ |
| Forward        | $\frac{dv}{dt} = \frac{1}{C} v_c - \frac{1}{R_c} v_c$ | $\frac{dv}{dt} = \frac{1}{C} v_c - \frac{1}{R_c} v_c$ |
| Full Bridge    | $\frac{dv}{dt} = \frac{1}{C} v_c - \frac{1}{R_c} v_c$ | $\frac{dv}{dt} = \frac{1}{C} v_c - \frac{1}{R_c} v_c$ |

where $R_T = \frac{r_m}{N_{pri}}$, $r = r + 2 r_m + r_{sec}$, and $r_i = r_i + 2 r_i$.
Table B: Schematic of DC-DC converters (Equivalent series resistance of inductors and capacitors are not shown)

| Converter Type | Schematic Diagram |
|---------------|-------------------|
| Buck          | ![Buck Schematic](image) |
| Boost         | ![Boost Schematic](image) |
| Buck-Boost    | ![Buck-Boost Schematic](image) |
| Cuk           | ![Cuk Schematic](image) |
| SEPIC         | ![SEPIC Schematic](image) |
| Fly Back      | ![Fly Back Schematic](image) |
| Forward       | ![Forward Schematic](image) |
| Full Bridge   | ![Full Bridge Schematic](image) |