Performance Guaranteed Network Acceleration via High-Order Residual Quantization

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Abstract

Input binarization has shown to be an effective way for network acceleration. However, previous binarization scheme could be regarded as simple pixel-wise thresholding operations (i.e., order-one approximation) and suffers a big accuracy loss. In this paper, we propose a high-order binarization scheme, which achieves more accurate approximation while still possesses the advantage of binary operation. In particular, the proposed scheme recursively performs residual quantization and yields a series of binary input images with decreasing magnitude scales. Accordingly, we propose high-order binary filtering and gradient propagation operations for both forward and backward computations. Theoretical analysis shows approximation error guarantee property of proposed method. Extensive experimental results demonstrate that the proposed scheme yields great recognition accuracy while being accelerated.

1. Introduction

Methods to accelerate learning and evaluation of deep network could be roughly divided into three groups. The simplest method is to perform network pruning (i.e., by rounding off near-zero connections) and re-train the pruned network structure \cite{10, 18, 20}. To achieve more structural compression rate, structural sparsity approximation techniques are later developed to morph larger sub-Networks into shallower ones \cite{1, 2, 27}. However, this type of method is not a general plug-in solution. Namely, for different networks with different network structures, expert knowledge is required to design the corresponding proper approximation network. Recently, a new set of solutions called network binarization was proposed \cite{4, 5, 21}. The idea behind network binarization is simple: transform the floating weights of network as well as the corresponding forward or backward data flow to binary, therefore both computation and network storage could be reduced. For example, BinaryConnect-Network \cite{4} shows great performance on datasets like CIFAR-10 and SVHN, but does not perform well enough on large-scale datasets (e.g., ImageNet). Binary-Weights-Network (BWN) \cite{21} reduces the network storage by $\sim 32\times$ and reach a state-of-art result on ImageNet dataset.

To further speed up network computation, input image is also binarized via thresholding operation. However, while network evaluation speed is reduced dramatically by a factor of $\sim 58\times$, the recognition accuracy on ImageNet drop from 56.6\% to 27.9\% (BNN \cite{5}) and 44.2\% (XNOR \cite{21}), due to large approximation error. Motivated by this limitation, in this work, we propose a High-Order Residual Quantization (HORQ) framework. The basic idea of this proposed framework is straightforward: previous input binarization operation, which simply performs positive and negative thresholding, could be considered as a very coarse quantization of floating numbers. In contrast, we propose a much more precise binary quantization method via recursive thresholding operation. Namely, after one time of thresholding operation, we could calculate the residual er-
ror and then perform a new round of thresholding operation to further approximate the residual. Thus, we could obtain a series of binary maps corresponding to different quantization scales. Based on these binary input tensors (stacked binary maps of different magnitude scales), we have developed efficient binary filtering operations for forward and backward computation. Experiments well demonstrate that our new proposed input binary quantization scheme not only outperforms the original XNOR-Networks [21], but also possesses great speedup ratio. At the same time, theoretically, we provide error analysis for our approximation scheme.

The rest of this paper is organized as follows. Some related works are demonstrated and compared in Section 3. In Section 3, we propose the High-Order Residual Quantization method and HORQ-Net. Section 4 covers the experiment part and analysis on storage and computation.

2. Related Work

Standard implementation of DCNNs is inefficient in memory storage and consumes considerable computational resources. Many works tried to accelerate and simplify DCNN. We divide these works into three categories:

Parameter Pruning It is believed many deep learning models are over-parameterized with significant redundancy [9]. To simplify DCNN, a widely used method is to remove parameters with little information. An early method called weight decay [29] is firstly used in pruning a network. OBD (Optimal Brain Damage [18]) provides a method using second-derivative information to remove the unimportant weights from a neural network under the assumption that the Hessian matrix of the problem is diagonal. OBS (Optimal Brain Surgeon [10]) furthers the idea of OBD and achieves a better experimental results. However, their methods need to compute the second-derivatives, which increases the computational complexity significantly.

Collins et al. [3] proposed a method using sparsity-inducing regularizer during the training of CNNs. Then Han et al. [9] proposed an approach to apply parameter pruning to a memory-efficient structure. Related approaches can be found in [25] and [28]. They firstly find similar neurons during the training process and then combine or remove these neurons. These methods are based on a pre-trained neural network. Our High-Order Residual Quantization method does not rely on a pre-trained network. Therefore, our method has an advantage of easy training.

Model Compression Another approach to simplify neural networks is called model compression [1]. The original idea of model compression is to train a compact artificial neural network to mimic a full-version pre-trained complex model. The full-version model is used to label the large unlabeled data set and the compact network is trained on this ensemble labeled data set. Compared with other compression methods, this method simply trains a network with fewer hidden units, thus the performance is limited.

Some other methods also consider the similar approach but develop many other skills. Jaderberg et al. [12] proposed a method for accelerating convolutional networks in linear case and later, Zhang et al. [27] proposed a method for accelerating convolutional networks in nonlinear case. These two methods minimize the reconstruction error of the responses (linear and nonlinear respectively) under the assumption that the convolutional filters can be low-rank approximated along certain dimensions. Methods in [22], [7] and [13] approximate a weight filter with a set of separable smaller filters. These methods rely on the low-rank assumption and also need a pre-trained network. They are network-dependent. In contrast, our method is general and is a plug-in solution.

Network Quantization This part is most related to our method. It is obvious that operations in high precision are much more time-consuming than those in binary values (e.g., +1, −1). Training a DCNN with binary weights can significantly accelerate the computation since if the weight filters are replaced with binary values, the convolutional operation can be simply replaced by additions and subtractions. EPB (Expectation BackPropagation [24]) shows that network with binary weights and binary activations is capable of achieving high performance. BC (BinaryConnect [4]) extends the idea of EBP and later, Courbariaux et al. [5] proposed BinaryNet (BN) which is a further extension of BC. BC constrains weights to +1 and −1 and BN further constrains activations to +1 and −1 thus the input (except the first layer) and the output of each layer are all binary values. They both achieve a state-of-art result in small-scale data sets (e.g., MNIST and CIFAR-10). According to Rastegari et al. [21]. BC and BN are not very successful on large-scale data set. Therefore, Rastegari et al. [21] proposed Binary-Weights-Networks (BWN) and XNOR-Networks, which use a different binary method compared with BC and BN. The most innovative point of their method is to compute the scaling factor. BWN uses binary weights and shows better performance than BC on ImageNet. XNOR, using both binary weights and binary input, further improves the efficiency of the computation. The accuracy of this network drops largely due to the information loss during quantization. This inspires us to propose the High-Order Residual Quantization method, which reduces the information loss during quantization. We compare our HORQ method with XNOR and BN. Our method outperforms these previous methods.

3. HORQ Network

In this chapter, we propose a new binary quantization method named High-order Residual Quantization (HORQ) which realizes the binarization of both input and weights
in a neural network. The most innovative point of HORQ is that we recursively make use of the residual (defined in Section 3.2). Then we can obtain a series of binary inputs in different magnitude scales. We perform convolution operation on input in different scales and combine the results. This method manages to reduce the information loss during binary quantization.

We start with some notations. We use \( (I, W, \alpha) \) to represent a convolutional neural network where \( I \) represents the set of input tensors, \( W \) represents the set of weight filters and \( \alpha \) represents the convolution operation. We use \( I_l \in I \) to represent the input tensor of the \( l \)th layer and \( W_l \in W \) to represent the weight filters of the \( l \)th layer. We use \( c, w, h \) to represent channel, width and height so that \( I_l \in \mathbb{R}^{c \times w \times h} \) and \( W_l \in \mathbb{R}^{c \times c_{in} \times w \times h} \).

### 3.1. XNOR-Network Revisited

In this section, we will briefly revisit the method proposed by Rastegari et al. [21]. They proposed two kinds of binary-neural-Networks named BWN and XNOR. BWN uses binary weights to speed up the computation. XNOR is based on BWN and realizes the binarization of input data in a convolutional layer. Here is a brief explanation of the quantization method used in XNOR and BWN:

Consider one convolution layer of the neural network with \( I \in I \) being the input tensor and \( W \in W \) being a weight filter. The core operation of this layer can be represented as \( I \ast W \). The idea of BWN is to constrain a convolutional neural network with binary weights. Rastegari used \( \alpha B \) to approximate \( W \) where \( \alpha \in \mathbb{R}^+ \) is a scaling factor and \( B \in \{-1, +1\}^{c \times w \times h} \) is a binary filter:

\[
I \ast W \approx (I \oplus B) \alpha \tag{1}
\]

Here, \( \oplus \) represents a binary convolution operation with no multiplication. To find suitable \( \alpha \) and \( B \), Rastegari [21] solved the following optimization problem:

\[
\alpha^*, B^* = \arg\min_{\alpha, B} J(B, \alpha) = \arg\min_{\alpha, B} \|W - \alpha B\|^2 \tag{2}
\]

It is easy to find the solution to Equation 2:

\[
\begin{align*}
B^* &= \text{sign}(W) \\
\alpha^* &= \frac{1}{n} \|W\|_1
\end{align*} \tag{3}
\]

Using the optimal estimation (Equation 3), one can train the CNN according to Algorithm 1 proposed by [21].

The idea of XNOR is based on BWN. BWN only replaces real value weights with binary values while XNOR is designed to replace real value inputs with binary values in addition to binary weights. XNOR uses \( \beta H \) to approximate the input tensor \( X \): \( X \approx \beta H \) and they solve the following optimization problem:

\[
\alpha^*, B^*, \beta^*, H^* = \arg\min_{\alpha, B, \beta, H} \|X \odot W - \alpha \beta H \odot B\|^2 \tag{4}
\]

**Algorithm 1** Training an L-layers CNN with binary weights:

**Input:** A minibatch of inputs and targets \( (I, Y) \), cost function \( C(Y, \hat{Y}) \), current weight \( W_t \) and current learning rate \( \eta_t \)

**Output:** Updated weight \( W_{t+1} \) and updated learning rate \( \eta_{t+1} \)

1. Binarizing weight filters:
   2. for \( l = 1 \) to \( L \) do
   3. for \( k = 1 \) to \( c_{out} \) do
   4. \( A_{lk} = \frac{1}{n} \|W_{lk}\|_k \)
   5. \( B_{lk} = \text{sign}(W_{lk}) \)
   6. \( \hat{W}_{lk} = A_{lk}B_{lk} \)
   7. \( \hat{Y} = \text{BinaryForward}(I, B, A) \)
   8. \( \frac{\partial C}{\partial W} = \text{BinaryBackward}(\frac{\partial C}{\partial Y}, \hat{W}) \)
   9. \( W_{t+1} = \text{UpdateParameters}(W_t, \frac{\partial C}{\partial W}, \eta_t) \)
   10. \( \eta_{t+1} = \text{UpdateLearningrate}(\eta_t, t) \)

As showed in [21], an approximate solution to this problem is:

\[
\begin{align*}
\beta^* H^* &= \frac{1}{n} \|X\|_1 \text{sign}(H) \\
\alpha^* B^* &= \frac{1}{n} \|W\|_1 \text{sign}(W)
\end{align*} \tag{5}
\]

We can use an algorithm similar to Algorithm 1 to train XNOR. More details about the training process of BWN and XNOR can be found in [21]. The experiments in [21] show that XNOR further accelerates the speed but the accuracy drops largely compared with BWN. Thus our purpose is to propose a improved neural networks of which both weights and inputs are binary values and the performance remains a relatively high level both in speed and accuracy. Based on this idea, in the next section, we propose the High-Order Residual Quantization method (HORQ).

### 3.2. High-Order Residual Quantization

In this section, we will explain the HORQ method to quantize the input of a convolutional layer. Using \( H^* \) and \( \beta^* \) in Equation 5 is not precise enough. Our HORQ method calculates the residual error and then performs a new round of thresholding operation to further approximate the residual. This binary approximation of the residual can be considered as a higher-order binary input. We can recursively perform the above operations and finally we can obtain a series of binary maps corresponding to different quantization scales. Based on these binary input tensors, we develop efficient binary filtering operations for forward and backward computation.

The input of a convolution layer is a 4-dimension tensor. If we reshape the input tensor and the corresponding weight filters into matrices, the convolution operation can be con-
The analytical solution to this problem is:

\[ X \approx \beta_1 H_1 \]  

where \( \beta_1 \in \mathbb{R} \) and \( H_1 \in \{+1, -1\}^n \). We can get the result by solving the following optimization problem:

\[ \begin{align*}
\beta_1^*, H_1^* &= \arg\min_{\beta_1, H_1} J(\beta_1, H_1) \\
&= \arg\min_{\beta_1, H_1} \|X - \beta_1 H_1\|^2
\end{align*} \tag{7} \]

The analytical solution to this problem is:

\[ \begin{align*}
H_1^* &= \text{sign}(X) \\
\beta_1^* &= \frac{1}{n} \|X\|_{l_1}
\end{align*} \tag{8} \]

Equation (8) can be considered as an order-one binary quantization (i.e., simple thresholding). Thus we can define the first-order residual tensor \( R_1(X) \) by computing the difference between the real input and first-order binary quantization:

\[ R_1(X) = X - \beta_1 H_1 \]  

Since \( \beta_1 \) and \( H_1 \) can both be determined by \( X \) from Equation (8), \( R_1(X) \) can also be determined by \( X \). We can use \( R_1(X) \) to represent the information loss due to approximation using Equation (6). Notice that \( R_1(X) \) is a real value tensor and we can further quantize \( R_1(X) \) as follow:

\[ R_1(X) \approx \beta_2 H_2 \]  

where \( \beta_2 \in \mathbb{R} \), \( H_2 \in \{+1, -1\}^n \), then we can get the Order-Two Residual Quantization of the input:

\[ X = \beta_1 H_1 + R_1(X) \approx \beta_1 H_1 + \beta_2 H_2 \]  

where \( \beta_1, \beta_2 \) are real value scalars and \( H_1, H_2 \) are binary value tensors. \( \beta_1 H_1 \) is called the first-order binary input, while \( \beta_2 H_2 \) is called the second-order binary input. Using the similar way that we solve the Equation (8), we can solve approximation problem of Equation (11).

Firstly, we solve the corresponding optimization problem:

\[ \begin{align*}
\beta_2^*, H_2^* &= \arg\min_{\beta_2, H_2} \|R_1(X) - \beta_2 H_2\|^2
\end{align*} \tag{12} \]

and the solution to Problem (12) is:

\[ \begin{align*}
H_2^* &= \text{sign}(R_1(X)) \\
\beta_2^* &= \frac{1}{n} \|R_1(X)\|_{l_1}
\end{align*} \tag{13} \]

We can show that our binary approximation method of using Equation (11) is much better than the original method by using Equation (8) both theoretically and experimentally.

We can compare the information loss between these two binary approximation methods. Remember we define \( R_1(X) \) as the residual tensor of approximation using Equation (6). Then it’s natural to define the residual tensor of approximation by Equation (11):

\[ R_2(X) = X - \beta_1 H_1 - \beta_2 H_2 \]

\[ = R_1(X) - \beta_2 H_2 \]  

Notice that \( H_2^* \) and \( \beta_2^* \) minimize \( \|R_1(X) - \beta_2 H_2\|^2 \), therefore:

\[ \|R_2(X)\|_{l_2}^2 = \|(R_1(X) - \beta_2 H_2)\|_{l_2}^2 \]

\[ \leq \|(R_1(X) - \beta_2 H_2)\|_{l_2}^2 \|H_2\|_{l_2}^2 \]

\[ \leq \|R_1(X)\|_{l_2}^2 \|H_2\|_{l_2}^2 \]

\[ \leq \|R_1(X)\|_{l_2}^2 \|H_2\|_{l_2}^2 \]

Thus if we use the \( L2 - \text{norm} \) of the residual tensor to represent the information loss, from the above derivation, we can prove that our Order-Two Residual Quantization by Equation (11) reduces the information loss compared with the approximation using Equation (6) in [21].

It’s straightforward to develop the Order-Two Residual Quantization using Equation (11) into a Order-K Residual Quantization:

\[ X \approx \sum_{i=1}^{K} \beta_i H_i \]  

where

\[ \begin{align*}
R_0(X) &= X \\
R_{i-1}(X) &= X - \sum_{j=1}^{i-1} \beta_j H_j, \quad i = 2, 3, ..., K \\
H_i &= \text{sign}(R_{i-1}(X)), \quad i = 1, 2, ..., K \\
\beta_i &= \frac{1}{n} \|R_{i-1}(X)\|_{l_1}, \quad i = 1, 2, ..., K
\end{align*} \tag{17} \]

We can recursively calculate the residual tensor to get a higher-order input. In fact, if the order becomes higher, the information loss will be more less, while the computational cost will also increase. We find that Order-Two and Order-Three residual quantization are good enough to approximate the input in terms of information loss. In the next section, we will introduce the HORQ network using our Order-Two Residual quantization method.

### 3.3. The HORQ Network

In this section, we proposed HORQ-Net which takes the HORQ binary input and performs high order binary filtering
for forward and backward computation. As for the convolutional layer, suppose the input $X \in \mathbb{R}^{c_{in} \times w \times h_{in}}$ and the convolutional filter $W \in \mathbb{R}^{c_{out} \times c_{in} \times w \times h}$ of this convolution layer are two tensors. Then if we reshape the input tensor and weight tensor into two matrices respectively, the convolution operation can be considered as a matrix multiplication.

**Tensor Reshape** To reshape the weight tensor $W$, we can straighten each filter to a vector shape of $1 \times (c_{in} \times w \times h)$. There are $c_{out}$ filters thus the weight tensor $W$ is reshaped to a matrix $W_r$ shape of $c_{out} \times (c_{in} \times w \times h)$. If we use $Y$ to denote the output of the convolution layer $\langle X, W_r, s \rangle$ then $Y \in \mathbb{R}^{c_{out} \times w_{out} \times h_{out}}$, where $w_{out} = (w_{in} + 2 \times p - w) / s + 1$ and $h_{out} = (h_{in} + 2 \times p - h) / s + 1$, $p$ and $s$ represent the pad and stride parameter respectively. To reshape the input tensor $X$, we can straighten each sub-tensor in $X$ with the same size of a filter to a vector and combine these vectors to a matrix $X_r$. In fact, there are $w_{out} \times h_{out}$ sub-tensors in $X$, thus $X_r$ is in the shape of $(c_{in} \times w \times h) \times (w_{out} \times h_{out})$. Then we can use a matrix production $Y_r = W_r \cdot X_r$ to replace the convolution operation between $X$ and $W$ where $Y_r$ is a matrix shape of $(c_{out} \times (w_{out} \times h_{out}))$. Then we reshape $Y_r$ to $Y$ to complete the whole computation.

\[ X_{\text{out}} \]
\[ X_{\text{in}} \]
\[ W \]
\[ W_{\text{t}} \]
\[ \hat{X} \]
\[ \hat{X}_{\text{t}} \]
\[ Y \]
\[ Y_{\text{t}} \]

Figure 2. This figure shows the tensor reshape process.

**Convolution Using Order-Two Residual Quantization** After the Tensor Reshape process, we get the input $X_r$ and the weight $W_r$ in matrix form. In this part, we show how to use Order-Two Residual Quantization to compute the matrix production between $W_r$ and $X_r$. We firstly quantize the weight matrix $W_r$:

\[
W_{r(i)} \approx \alpha_i B_i \quad (i = 1, 2, ..., c_{out})
\]  
(18)

\[
\begin{align*}
B_i &= \text{sign}(W_{r(i)}) \\
\alpha &= \frac{1}{c_{in} \times w \times h} \|W_{r(i)}\|_1
\end{align*}
\]  
(19)

where $W_{r(i)}$ is the $i$-th row of $W_r$; $W_{r(i)}, B_i \in \mathbb{R}^{1 \times (c_{in} \times w \times h)}$, $\alpha \in \mathbb{R}$.

**Algorithm 2 OrderTwoBinaryConvolution($X, W$)**

**Input**: Input tensor $X \in \mathbb{R}^{c_{in} \times w \times h_{in}}$, Weight tensor $W \in \mathbb{R}^{c_{out} \times c_{in} \times w \times h}$ and convolutional parameters include pad and stride.

**Output**: The convolutional result $Y$ using method of second-order binary approximation.

1: Reshape weight tensor and input tensor:
2: $W_r=\text{ReshapeWeight}(W)$
3: $X_r=\text{ReshapedInput}(X, W)$
4: Binarizing weight matrix:
5: for $k = 1$ to $c_{out}$
6: \begin{equation}
A_k = \frac{1}{c_{in} \times w \times h} \|W_{r(k)}(t)\|_1
\end{equation}
7: \begin{equation}
M_k = \text{sign}(W_{r(k)})
\end{equation}
8: $W_{r(k)} = A_k M_k$
9: Binarizing input matrix:
10: for $k = 1$ to $w_{out} \times h_{out}$
11: \begin{equation}
B_{1k} = \frac{1}{c_{in} \times w \times h} \|X_{r(k)}(l)\|_1
\end{equation}
12: $N_{1k} = \text{sign}(X_{r(k)})$
13: \begin{equation}
R_{1k}(X_{r(k)}) = X_{r(k)} - B_{1k} N_{1k}
\end{equation}
14: \begin{equation}
B_{2k} = \frac{1}{c_{in} \times w \times h} \|R_{1k}(X_{r(k)})\|_1
\end{equation}
15: $N_{2k} = \text{sign}(R_{1k}(X_{r(k)}))$
16: $\hat{X}_{r(k)} = B_{1k} N_{1k} + B_{2k} N_{2k}$
17: $Y_r = \text{BinaryProduction}(\hat{X}_{r(k)}, \hat{W}_{r(k)})$
18: $Y = \text{ReshapeOutput}(Y_r)$

Then, we quantize the input matrix $X_r$ using Order-Two Residual Quantization:

\[
X_{r(i)} \approx \beta_{1(i)} H_{1(i)} + \beta_{2(i)} H_{2(i)} \quad (i = 1, 2, ..., w_{out} \times h_{out})
\]  
(20)

\[
\begin{align*}
H_{1(i)} &= \text{sign}(X_{r(i)}) \\
\beta_{1(i)} &= \frac{1}{c_{in} \times w \times h} \|X_{r(i)}\|_1 \\
R_{1}(X_{r(i)}) &= X_{r(i)} - \beta_{1(i)} H_{1(i)} \\
H_{2(i)} &= \text{sign}(R_{1}(X_{r(i)})) \\
\beta_{2(i)} &= \frac{1}{c_{in} \times w \times h} \|R_{1}(X_{r(i)})\|_1
\end{align*}
\]  
(21)

where $X_{r(i)}$ is the $i$-th column of $X_r$; $X_{r(i)}, H_{1(i)}, H_{2(i)} \in \mathbb{R}^{(c_{in} \times w \times h) \times 1}$; $\beta_{1(i)}, \beta_{2(i)} \in \mathbb{R}$. Thus we can compute the binary convolution via Algorithm 2.

**Training HORQ Network** Algorithm 3 demonstrates the procedure for training a HORQ network using our Order-Two Residual Quantization method. The ordinary procedure includes Forward, Backward and Parameter-Update. We use the binary value of inputs and weights during the Forward and Backward process. For convenience, we only include convolution layers in the Forward process in Algorithm 3. In fact, our High-Order Residual Quantization method can be easily applied to fully-connected layers because the fully connected layer only involves vector inner
product and we can use our HORQ method directly without the Tensor Reshape process.

Algorithm 3 Traning an L-layers HORQ network:

**Input:** A minibatch of inputs and targets \((X, Y)\), cost function \(L(Y, \hat{Y})\), current weight \(W(t) = \{W^l(t)\}_{l=1}^{L}\) and current learning rate \(\eta(t)\)

**Output:** Updated weight \(W^{l+1}\) and updated learning rate \(\eta^{l+1}\)

1. for \(l = 1\) to \(L\) do
2. \(\hat{Y}^l(t) = \text{OrderTwoBinaryConvolution}(X^l(t), W^l(t))\)
3. \(\frac{\partial L}{\partial W} = \text{BinaryBackward}(\frac{\partial L}{\partial Y}, W)\)
4. \(W(t + 1) = \text{UpdateParameters}(W(t), \frac{\partial L}{\partial W}, \eta(t))\)
5. \(\eta(t + 1) = \text{UpdateLearningrate}(\eta(t), t)\)

To train a HORQ-Net, we quantize the input and weight filters and compute the binary convolution layer by layer. The binary convolution is detailed in Algorithm 2. After the Forward-pass, we use the binary weight \(\hat{W}\) and binary input \(\hat{X}\) to do the back propagation. We also use the same way as Courbariaux et al. [5] does to compute the gradient for the sign function \(\text{sign}(\cdot)\). We should notice that we use the real-value weights and inputs when updating the parameters. The reason is that the parameter update is quite small in each iteration. If we update with binary weights, these updates may be eliminated during the binary operation in the next iteration and therefore the network will not be efficiently trained. The similar strategy is also applied in [4][5][21].

4. Experiments

In this section, we will show two main comparison experiments on MNIST and CIFAR-10. We compare HORQ-Net with some of the previous methods. Experiments show that HORQ-Net possesses better performance on image classification tasks.

4.1. MNIST

We test our HORQ-Net on MNIST dataset, which is a benchmark image classification dataset [17] of handwritten digits from 0 to 9. To make this experiment comparable with BC [4] and BNN [5], we also use a MLP with a similar structure. This MLP consists of 3 hidden layers with 4096 Order-Two Residual Quantized connections and a L2-SVM layer with the Hinge loss (Lee et al. [19] showed that L2-SVM is better than Softmax in this dataset). To train this MLP, we do not use any convolution, preprocessing, data-augmentation or pre-training skills. We use ADAM adaptive learning rate method [15]. We use Batch Normalization with a minibatch of size 200 to speed up the training.

We also train a same MLP with only order-one binary connections (XNOR) to compare the final test accuracy.

The results are shown in Figure 3 and Figure 4. We use the same network structure above to train XNOR-Net and HORQ-Net and find that HORQ-Net outperforms XNOR-Net by 0.71% in accuracy. From Figure 3 we also observe that HORQ-Net converges within fewer epochs. Figure 4 shows the hinge loss changes over epoch. Both HORQ-Net and XNOR-Net can converge to a relatively small loss but the hinge loss curve of XNOR-Net is not as smooth as the loss curve of HORQ-Net. Most previous works
4.2. CIFAR-10

We also test our HORQ-Network on CIFAR-10 dataset containing 50000 training images and 10000 testing images. We do not use any preprocessing or data-augmentation skills (which is showed to be a game changer in this data set [8]). In order to show the difference between the performance of methods using Order-Two Residual Quantization (HORQ) and order-one binary approximation (XNOR), firstly we use a shallow convolution neural network. The structure of our CNN is:

\[(32) C_5 - S - MP3 - N - (32) C_5 - S - MP3 - N - (64) C_5 - S - AP3 - 10 FC - SOFTMAX \]  

(22)

Where \(C_5\) is a \(5 \times 5\) convolution layer, \(S\) is a sigmoid activation layer, \(MP3\) is a max-pooling layer with kernel size 3 and stride 2, \(AP3\) is a average-pooling layer with kernel size 3 and stride 2, \(N\) is a LRN layers, \(FC\) is a fully connected layer and SOFTMAX is a softmax loss layer. To train this CNN, we set the size of the minibatch to 50 to speed up the training. We also centralize and standardize the training data.

Since our CNN structure is not as complex as ConvNet [4] (ConvNet has six convolutional layers and two fully connected layers and each layer has more perceptions), our baseline (without using any binary approximation) accuracy is not as high as theirs. But this shallow network makes it easier to compare the performance between HORQ and XNOR under the same initialization, parameter setting and training strategy. We report the final performance in Figure 5 and Figure 6. Using the same network structure, HORQ-Net converges with accuracy drop within 2% compared with our baseline. The accuracy drops \(\sim 5\%\) in XNOR-Net. Besides, HORQ-Net and XNOR-Net converges in a similar speed. Hence this experiment also shows the better performance of HORQ-Net.

4.3. Storage Space Analysis

Generally speaking, our high-order binarization can be applied to any DCNN models. Models with binary weights will take up less storage memory than models with double precision weights. A very deep convolutional neural networks, for example, VGG-16, will occupy nearly 400M storage space using float precision. Figure 7 shows the storage cost of some widely used models with double and binary precision weights.

4.4. Computation Analysis

Consider a convolution operation \((I, W, \ast)\), where input \(I \in \mathbb{R}^{c_{in} \times w_{in} \times h_{in}}\), Weight tensor \(W \in \mathbb{R}^{c_{out} \times c_{in} \times w \times h}\), the total number of operation is \(c_{out} \times c_{in} \times w \times h \times w_{in} \times h_{in}\). Using the current generation CPU, which is capable of per-
form 64 binary operations within one cycle clock, our method of High-Order Residual Quantization of order \( K \) needs \( K \times c_{out} \times c_{in} \times w \times h \times w_{in} h_{in} + (K + 1) \times w_{in} h_{in} = K N_{p} + (K + 1) N_{n} \) operations. Among these operations, \( K N_{p} \) operations are binary-precision operations, which can be sped up, while the other \( (K + 1) N_{n} \) operations are float-precision operations, which cannot be sped up. Thus the speedup ratio can be computed as:

\[
\eta = \frac{c_{out} c_{in} w h w_{in} h_{in}}{64(K c_{out} c_{in} w h w_{in} h_{in}) + (K + 1) w_{in} h_{in}}
\]

\[
= \frac{c_{out} c_{in} w h w_{in} h_{in}}{K c_{out} c_{in} w h + 64(K + 1)}
\]  

(23)

For the case of Order-Two, we can compute the speedup ratio:

\[
\eta = \frac{c_{out} c_{in} w h w_{in} h_{in}}{64(2c_{out} c_{in} w h w_{in} h_{in}) + 3w_{in} h_{in}}
\]

\[
= \frac{c_{out} c_{in} w h}{2c_{out} c_{in} w h + 192}
\]  

(24)

As we can see in Equation 24, the speedup ratio does not depend on the width or the height of the input tensor but on the filter size: \( w \times h \) and the number of channels: \( c_{in} c_{out} \). Firstly, we fix the number of channels: \( c_{in} c_{out} = 10 \times 10 \) to see how filter size influence speedup ratio. Secondly, we fix the filter size : \( w \times h = 3 \times 3 \) and input channels \( c_{in} = 3 \) to see how output channels influence speedup ratio. As we can see from Figure 8, the speedup will not be remarkable if the number of channels and filter size is two small. Thus when we apply the binary method to DCNN, we should avoid quantizing layers with few channels (e.g. first layer with 3 channels). If we set \( c_{in} c_{out} = 64 \times 256, w \times h = 3 \times 3 \), our Order-Two Residual Quantization can reach 31.98x speedup. But in practice, the speedup ratio may be a little bit lower due to the process of memory read and data preprocessing. From Figure 8 we observe that Order-Two and Order-Three Residual Quantization still remain a relatively high speed up ratio (> 20x). Thus our HORQ method of order-two and order-three are very powerful in accelerating the neural network with performance guaranteed.

5. Conclusion

In this paper, we propose an efficient and accurate binary approximation method called High-Order Residual Quantization. We introduce the concept of residual to represent the information loss and recursively compute the quantized residual to reduce the information loss. Using binary weights, the size of network is reduced by \( \sim 32 \times \) and this method provides \( \sim 30 \times \) speed up. This also provides the possibility of running the inference of deep convolutional network on CPU. Our experiments show that the performance of HORQ-net is guaranteed. HORQ-Net outperforms XNOR-Net in MNIST(0.71%) and in CIFAR-10(\( \sim 3\% \)).

6. Acknowledgements

The work was supported by State Key Research and Development Program (2016YFB1001003). This work was also supported by NSFC (U1611461, 61502301), China’s Thousand Youth Talents Plan, the 111 Program, the Shanghai Key Laboratory of Digital Media Processing and Transmissions, and Cooperative Mediant Innovation Center.
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