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Citation for published version:
Ferracin, S, Kapourniotis, T & Datta, A 2019, 'Accrediting outputs of noisy intermediate-scale quantum computing devices', New Journal of Physics, vol. 21, no. 11, 113038, pp. 1-26. https://doi.org/10.1088/1367-2630/ab4fd6

Digital Object Identifier (DOI):
10.1088/1367-2630/ab4fd6

Link:
Link to publication record in Edinburgh Research Explorer

Document Version:
Publisher's PDF, also known as Version of record

Published In:
New Journal of Physics
Accrediting outputs of noisy intermediate-scale quantum computing devices

To cite this article: Samuele Ferracin et al 2019 New J. Phys. 21 113038

View the article online for updates and enhancements.
PAPER

Accrediting outputs of noisy intermediate-scale quantum computing devices

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Keywords: NISQ devices, accreditation, sampling problems

Abstract

We present an accreditation protocol for the outputs of noisy intermediate-scale quantum devices. By testing entire circuits rather than individual gates, our accreditation protocol can provide an upper-bound on the variation distance between noisy and noiseless probability distribution of the outputs of the target circuit of interest. Our accreditation protocol requires implementing quantum circuits no larger than the target circuit, therefore it is practical in the near term and scalable in the long term. Inspired by trap-based protocols for the verification of quantum computations, our accreditation protocol assumes that single-qubit gates have bounded probability of error. We allow for arbitrary spatial and temporal correlations in the noise affecting state preparation, measurements, single-qubit and two-qubit gates. We describe how to implement our protocol on real-world devices, and we also present a novel cryptographic protocol (which we call ‘mesothetic’ protocol) inspired by our accreditation protocol.

1. Introduction

Quantum computers promise to expand our computing capabilities beyond their current horizons. Several commercial institutions [1–3] are taking steps towards building the first prototypes of quantum computers that can outperform existing supercomputers in certain tasks [4–9], the so-called ‘Noisy Intermediate-Scale Quantum’ (NISQ) computing devices [10]. As all their internal operations such as state preparations, gates, and measurements are by definition noisy, the outputs of computations implemented on NISQ devices are unreliable. It is thus essential to devise protocols able to accredit these outputs.

A commonly employed approach involves simulating the quantum circuit whose output we wish to accredit, the target circuit, on a classical computer. This is feasible for small circuits, as well as for circuits composed of Clifford gates [11] and few non-Clifford gates [12, 13]. Classical simulations have been performed for quantum computations of up to 72 qubits, often exploiting subtle insights into the nature of specific quantum circuits involved [14, 15]. Though practical for the present, classical simulations of quantum circuits are not scalable. Worthwhile quantum computations will not be efficiently simulable on classical computers, hence we must seek for alternative methods.

Another approach employed in experiments consists of individually testing classes of gates present in the target circuit. This is typically undertaken using a family of protocols centered around randomized benchmarking and its extensions [16–21]. These protocols allow extraction of the fidelity of gates or cycles of gates and can witness progresses towards fault-tolerant quantum computing [22]. However they rely on assumptions that may be invalid in experiments. In particular, they require the noise to be Markovian and cannot account for temporal correlations [23, 24]. Quantum circuits are more than the sum of their gates, and the noise in the target circuit may exhibit characteristics that cannot be captured by benchmarking its individual gates independently.

This calls for protocols able to test circuits as a whole rather than individual gates. Such protocols have been devised inspired by Interactive proof Systems [25]. In these protocols (which we call ‘cryptographic protocols’) the outputs of the target circuit are verified through an interaction between a trusted verifier and an untrusted prover (figure 1 (a)). The verifier is typically allowed to possess a noiseless quantum device able to prepare [26–33] or
measure [34–38] single qubits, however recently a protocol for a fully classical verifier was devised that relies on the widely believed intractability of a computational problem for quantum computers [39]. Other protocols for classical verifiers have also been devised, but they require interaction with multiple entangled and non-communicating provers [40–44]. Cryptographic protocols show that with minimal assumptions, verification of the outputs of quantum computations of arbitrary size can be done efficiently, in principle.

In practice, implementing cryptographic protocols in experiments remains challenging, especially in the near term. In experiments all the operations are noisy, as in figure 1(b), and the verifier does not possess noiseless quantum devices. Thus, the verifiability of protocols requiring noiseless devices for the verifier is not guaranteed. Moreover, the concept of scalability, which is of primary interest in cryptographic protocols, is not equivalent to that of practicality, which is essential for experiments. For instance, suppose that the target circuit contains a few hundred qubits and a few hundred gates. Cryptographic protocols require implementing this circuit on a large cluster state containing thousands of qubits and entangling gates [26–31, 34] or on two spatially-separated devices sharing thousands of copies of Bell states [40, 41]; or appending several teleportation gadgets to the target circuit (one for each T-gate in the circuit and six for each Hadamard gate) [33]; or building Feynman–Kitaev clock states, which require entangling the system with an auxiliary qubit per gate in the target circuit [35, 39, 43]. These protocols are scalable, as they require a number of additional qubits, gates and measurements growing linearly with the size of the target circuit, yet they remain impractical for NISQ devices.

In this paper we present an accreditation protocol that provides an upper-bound on the variation distance between noisy and noiseless probability distribution of the outputs of a NISQ device, under the assumption that

N1: Noise in state preparation, entangling gates, and measurements is an arbitrary Completely Positive Trace Preserving (CPTP) map encompassing the whole system and the environment (equation (2));

N2: Noise in single-qubit gates is a CPTP map $\mathcal{F}_{SE}$ of the form $\mathcal{F}_{SE} = (1 - r)I + r\mathcal{F}'_{SE}$, with $0 \leq r < 1$, where $I_{SE}$ is the identity on system and environment and $\mathcal{F}'_{SE}$ is an arbitrary (potentially gate-dependent) CPTP map encompassing the whole system and the environment.

Inspired by cryptographic protocols [26–44], our accreditation protocol is trap-based, meaning that the target circuit being accredited is implemented together with a number $\nu$ of classically simulable circuits (the ‘trap’ circuits) able to detect all types of noise subject to conditions N1 and N2 above. A single run of our protocol requires implementing the target circuit being accredited and $\nu$ trap circuits. It provides a binary outcome in which the outputs of the target circuit are either accepted as correct (with confidence increasing linearly with $\nu$) or rejected as potentially incorrect. More usefully, consider running our protocol $d$ times, each time with the same target and $\nu$ potentially different trap circuits. Suppose that the output of the target is accepted as correct by $N_{\text{acc}} > 0$ runs. With confidence $1 - 2\exp(-2d\theta^2)$, for each of these accepted outputs our protocol ensures

$$\frac{1}{2} \sum_{\tau} |p_{\text{noiseless}}(\tau) - p_{\text{noisy}}(\tau)| \leq \frac{\varepsilon}{N_{\text{acc}}/d - \theta},$$

where $\theta \in (0, N_{\text{acc}}/d)$ is a tunable parameter that affects both the confidence and the upper-bound, $p_{\text{noiseless}}(\tau)$ and $p_{\text{noisy}}(\tau)$ are the noiseless and noisy probability distributions of the outputs $\tau$ of the target circuit.
respectively and $\varepsilon \propto 1/v$. Bounds of this type can fruitfully accredit the outputs of experimental quantum computers as well as underpin attempts at demonstrating and verifying quantum supremacy in sampling experiments [4–9].

Crucially, our accreditation protocol is both experimentally practical and scalable: all circuits implemented in our protocol are no wider (in the number of qubits) or deeper (in the number of gates) than the circuit we seek to accredit. This makes our protocol more readily implementable on NISQ devices than cryptographic protocols. Moreover, our protocol requires no noiseless quantum device, and it only relies on the assumption that the single-qubit gates suffer bounded (but potentially non-local in space and time and gate-dependent) noise—condition N2. This assumption is motivated by the empirical observation that single-qubit gates are the most accurate operations in prominent quantum computing platforms such as trapped ions [45, 46] and superconducting qubits [2, 47, 48].

In addition to its ready implementability on NISQ devices, our accreditation protocol can detect all types of noise typically considered by techniques centered around randomized benchmarking and its extensions [16–21]. Moreover it can detect noise that may be missed by those techniques such as noise correlated in time. Mathematically, this amounts to allowing noisy operations to encompass both system and environment (figure 1(b)) and tracing out the environment only at the end of the protocol. This noise model is more general than the Markovian noise model considered in protocols centered around randomized benchmarking [23, 24]. Moreover, by testing circuits rather than gates, our protocol ensures that all possible noise (subject to condition N1 and N2) in state preparation, measurement and gates is detected, even noise that arises only when these components are put together to form a circuit. On the contrary, benchmarking isolated gates can sometimes yield over-estimates of their fidelities [21], and consequently of the fidelity of the resulting circuit. We note that noise of the type N2 excludes unbounded gate-dependent errors in single-qubit gates such as systematic over- or under-rotations, as also is the case for other works [16–21].

Inspired by our accreditation protocol we also present a novel cryptographic protocol, which we call ‘mesothetic verification protocol’. In the mesothetic protocol the verifier implements the single-qubit gates in all circuits while the prover undertakes all other operations. This is distinct from prepare-and-send [26–33] or receive-and-measure [34–38] cryptographic protocols in that the verifier intervenes during the actual implementation of the circuits, and not before or after the circuits are implemented.

Our paper is organized as follows. In section 1 of Results we introduce the notation, in section 2 we provide definitions, in sections 3 and 4 we present our protocol and prove our results and in section 2.5 we present the mesothetic verification protocol.

## 2. Results

### 2.1. Notation

We indicate unitary matrices acting on the system with capital letters such as $U$, $V$ and $W$, and Completely Positive Trace Preserving (CPTP) maps with calligraphic letters such as $\mathcal{E}$, $\mathcal{F}$, $\mathcal{R}$ and $\mathcal{M}$. We indicate the 2 × 2 identity matrix as $I$, the single-qubit Pauli gates as $X$, $Y$, $Z$, the controlled-Z gate as $\mathcal{CZ}$, the controlled-X gate as $\mathcal{C}X$, the Hadamard gate as $H$ and $S = \text{diag}(1, i)$. The symbol $\otimes$ denotes the composition of CPTP maps:

$$\phi_{p=1}^{\mathcal{E}}(\rho) = \mathcal{E}_q \ldots \mathcal{E}_1(\rho), \quad \text{Tr}_E[-]$$

is the trace over the environment, $D(q, \tau) = \text{Tr}[\sigma - q]/2$ is the trace distance between the states $\sigma$ and $\tau$. We say that a noisy implementation $\tilde{\mathcal{E}}$ of $\mathcal{E}$ suffers bounded noise if $\tilde{\mathcal{E}}$ can be written as $\tilde{\mathcal{E}} = (1 - r)\mathcal{E} + r\mathcal{F}$ for some CPTP map $\mathcal{F}$ and number $0 \leq r < 1$, otherwise if $r = 1$ we say that the noise is unbounded [49].

### 2.2. Background

We start by defining our notion of protocol:

**Definition 1 (Protocol).** Consider a system $S$ in the state $\rho_S$. A protocol on input $\rho_S$ is a collection of CPTP maps $\{\mathcal{E}^{(p)}_S\}_{p=1}^q$ acting on $S$ and yielding the state $\rho_{\text{out}} = \phi_{p=1}^{\mathcal{E}^{(p)}_S}(\rho_S)$. When implemented on real devices protocols suffer the effects of noise. Modeling noise as a set $\{\mathcal{F}^{(p)}_{SE}\}$ of CPTP maps acting on system and environment (figure 1(b)), the state of the system at the end of a noisy protocol run is

$$\rho_{\text{out}} = \text{Tr}_E[\phi_{p=1}^{\mathcal{E}^{(p)}_{SE}}(\mathcal{E}^{(p)}_S \otimes \mathcal{F})](\rho_S \otimes \rho_E),$$

where $\rho_E$ is the state of the environment at the beginning of the protocol. We allow each map $\mathcal{F}^{(p)}_{SE}$ to depend arbitrarily on the corresponding operation $\mathcal{E}^{(p)}_S$.

A trap-based accreditation protocol is defined as follows. A single run of such a protocol takes as input a classical description of the target circuit and a number $v$, implements $v + 1$ circuits (the target and $v$ traps) and
returns the outputs of the target circuit, together with a ‘flag bit’ set to ‘acc’ (‘rej’) indicating that the output of the target must be accepted (rejected). Formally,

**Definition 2 (Trap-Based Accreditation Protocol).** Consider a protocol \( \{ \mathcal{E}^{(p)} \}_{p=1}^{q} \) with input \( \rho_{S} \), where \( \rho_{S} \) contains a classical description of the target circuit and the number \( v \) of trap circuits. Consider also a set of CPTP maps \( \{ \mathcal{F}_{S}^{(p)} \}_{p=1}^{q} \) (the noise) acting on system and environment. We say that the protocol \( \{ \mathcal{E}^{(p)} \}_{p=1}^{q} \) can accredit the outputs of the target circuit in the presence of noise \( \{ \mathcal{F}_{S}^{(p)} \}_{p=1}^{q} \) if the following two properties hold:

1. The state of the system at the end of a single protocol run (equation (2)) can be expressed as
   \[
   \rho_{\text{out}} = b \tau_{\text{tar}}^\ast \otimes |\text{acc}\rangle\langle \text{acc}| + \left(1 - b\right)\left(\sigma_{\text{out}}^{\text{tar}} \otimes |\text{acc}\rangle\langle \text{acc}| + \left(1 - \lambda\right)\tau_{\text{out}}^\ast \otimes |\text{rej}\rangle\langle \text{rej}|\right),
   \]
   where \( \sigma_{\text{out}}^{\text{tar}} (\tau_{\text{out}}^\ast) \) is the state of the target circuit at the end of a noiseless (noisy) protocol run, \( \tau_{\text{tar}}^\ast \) is an arbitrary state for the target circuit, \( |\text{acc}\rangle \) is the state of the flag indicating acceptance, \( |\text{rej}\rangle = |\text{acc} \oplus 1\rangle \), \( 0 \leq l \leq 1 \), \( 0 \leq b \leq \varepsilon \) and \( \varepsilon \in [0, 1] \).

2. After \( d \) protocol runs with the same target circuit and \( v \) potentially different trap circuits, if all these runs are affected by independent and identically distributed (i.i.d.) noise, then the variation distance between noisy and noiseless probability distribution of the outputs of each of the \( N_{\text{acc}} \in [0, d] \) protocol runs ending with flag bit in the state \( |\text{acc}\rangle \) is upper-bounded as in equation (1).

Property 1 ensures that the probability of accepting the outputs of a single protocol run when the target circuit is affected by noise (the number \( b \) in equation (3)) is smaller than a constant \( \varepsilon \). The constant \( \varepsilon \) is a function of the number of trap circuits, of the protocol and of the noise model and is to be computed analytically. The quantity \( 1 - \varepsilon \) quantifies the credibility of the accreditation protocol.

Note that Property 1 in the above definition implies Property 2. To see this, assume Property 1 is valid for a given protocol. Suppose that this protocol is run \( d \) times with i.i.d. noise (a standard assumption in trap-based cryptographic protocols [29, 36]) and suppose that \( N_{\text{acc}} > 0 \) protocol runs end with flag bit in the state \( |\text{acc}\rangle \). For each of these \( N_{\text{acc}} \) runs, the state of the system at the end of the protocol run is thus of the form (see equation (3))

\[
\rho_{\text{out},\text{acc}} = \frac{1}{1 - b} \left(1 - b\right)\lambda_{\text{tar}}^\ast + b \tau_{\text{out}}^\ast \otimes |\text{acc}\rangle\langle \text{acc}|.
\]

This yields a bound on the variation distance of the type [50]

\[
\frac{1}{2} \sum_{\tau} |p_{\text{noisless}}(\tau) - p_{\text{noisy}}(\tau)| \leq D\left(\sigma_{\text{out}}^{\text{tar}}, \frac{(1 - b)\lambda_{\text{tar}}^\ast + b \tau_{\text{out}}^\ast}{(1 - b)\lambda + b}\right) \leq \frac{b}{(1 - b)\lambda + b} \leq \frac{\varepsilon}{\text{prob}(\text{acc})},
\]

where in the last inequality we used that \( b \leq \varepsilon \) (Property 1) and that the quantity \( \text{prob}(\text{acc}) = (1 - b)\lambda + b \) is the probability of accepting (equation (3)). Hoeffding’s Inequality ensures that \( \text{prob}(\text{acc}) = N_{\text{acc}} / d \leq \theta \) with confidence \( 1 - 2\exp(-2d\theta^{2}) \) and this yields Property 2.

Bounding the variation distance as in equation (1) requires knowledge of the two numbers \( \varepsilon \) and \( N_{\text{acc}} \), the former obtained theoretically from the protocol and the latter experimentally from the device being tested. \( \varepsilon \) is a property of the protocol, of its input and of the noise model and can be computed without running the protocol. However, different devices running the same target circuit will suffer different noise levels and this is captured by \( N_{\text{acc}} \), which depends on the experimental device being tested. It is important to note that the bound on the variation distance is valid only for the outputs of the \( N_{\text{acc}} \) protocol runs ending with flag bit in the state \( |\text{acc}\rangle \). If a protocol run ends with flag bit in the state \( |\text{rej}\rangle \), Property 1 implies no bound on the variation distance and all rejected outputs must be discarded.

We can now present our accreditation protocol (a formal description can be found in Box 1 in the Methods).

### 2.3. Our accreditation protocol

Our accreditation protocol takes as input a classical description of the target circuit and the number \( v \) of trap circuits. The target circuit (figure 2) must start with qubits in the state \( |+\rangle \), contain only single-qubit gates and \( \mathcal{Z} \) gates and end with a round of measurements in the Pauli-X basis\(^1\). Moreover, it must be decomposed as a

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\(^1\) This does not result in any loss of generality: every experimental architecture has its native input states, entangling gates and measurement basis, but these can always be mapped to \(|+\rangle \) states, \( \mathcal{Z} \) gates and Pauli-X measurements.
sequence of bands, each one containing one round of single-qubit gates and one round of $cZ$ gates. We will indicate the number of qubits with $n$ and the number of bands with $m$.

In our accreditation protocol $v+1$ circuits are implemented, one (chosen at random) being the target and the remaining $v$ being the traps. The trap circuits are obtained by replacing the single-qubit gates in the target circuit with other single-qubit gates, but input state, measurements and $cZ$ gates are the same as in the target (figure 3(a); all single-qubit gates acting on the same qubit in the same band must be recompiled into one gate—for instance, in figure 3(a), the $H^t$-gate and subsequent $S$-gate acting on qubit 1 in band 1 must be implemented as one gate $SH^t$).

Moreover, depending on the random bit $t \in \{0, 1\}$, the traps may begin and end with a round of Hadamard gates. Since $(S \otimes H)cZ(S^\dagger \otimes H) = cX$, the trap circuits are a sequence of (randomly oriented) $cX$ gates acting on $|+\rangle^{\otimes m}$ (if $t = 0$) or $|0\rangle^{\otimes m}$ (if $t = 1$)—figure 3(b). In the absence of noise, they always output $\sigma = 0$.

Our protocol requires appending a Quantum One-Time Pad (QOTP) to all single-qubit gates in all circuits (target and traps). This is described in Routine 1 in the Methods and is done as follows:

- For all bands $j = 1, \ldots, m$ and qubits $i = 1, \ldots, n$, a random Pauli gate is appended after each gate $U_{i,j}$ (figure 4(a)). This yields

$$U_{i,j}' = X_{i}^{\alpha_{i,j}} Z_{i}^{\alpha_{i,j}} U_{i,j},$$

where $\alpha_{i,j}, \alpha_{i,j}' \in \{0, 1\}$ are random bits.

- For all bands $j = 2, \ldots, m$ and qubits $i = 1, \ldots, n$, another Pauli gate is appended before each single-qubit gate. This Pauli gate is chosen so that it undoes the QOTP coming from the previous band (figure 4(b)). Choosing
this Pauli gate requires using the identities
\[(X_1 \otimes I_2)cZ = cZ(X_1 \otimes Z_2),\]  \[(Z_1 \otimes I_2)cZ = cZ(Z_1 \otimes I_2).\]  
This yields

\[U''_{i,j} = X_{i,j}^{\alpha_{i,j}} Z_{i,j}^{\gamma_{i,j}} U_{i,j+1} P_i(\{\alpha_{i,j-1}\} \cup \{\alpha'_{i,j-1}\}),\]  
where \(P_i(\{\alpha_{i,j-1}\} \cup \{\alpha'_{i,j-1}\})\) is a Pauli gate that depends on the QOTP in the previous band.

- A random Pauli-X gate is appended before all the gates \(U''_{i,j}\) in the first band. This yields

\[U''_{i+1,j} = U'_{i+1,j} X_{i}^{\gamma_{i}},\]  
with \(\gamma_i \in \{0, 1\}\) chosen at random.

Overall, replacing each gate \(U_{i,j}\) with \(U''_{i,j}\) yields a new circuit that is equivalent to the original one, apart from the un-recovered QOTP \(\otimes_{j=1}^{n} X^{\alpha_{i,m}} Z^{\gamma_{i,m}}\) in the last band. Since all measurements are in the Pauli-X basis, the Pauli-X component of this un-recovered QOTP is irrelevant, while its Pauli-Z component bit-flips some of the outputs. These bit-flips can be undone by replacing each output \(s_j\) with \(s_j \oplus \alpha_{i,m}\) (a procedure that we call ‘classical post-processing of the outputs’). This allows to recover the correct outputs.

After all the circuits have been implemented and the outputs have been post-processed, the flag bit is initialized to \(|\text{acc}\rangle = |0\rangle\), then it is checked whether all the traps gave the correct output \(\bar{x} = \emptyset\). If they do, the protocol returns the output of the target together with the bit \(|\text{acc}\rangle\), otherwise it returns the output of the target together with the bit \(|\text{rej}\rangle = |1\rangle\). The output of the target is only accepted in the first case, while it is discarded in the second case.

In the absence of noise, our protocol always returns the correct output of the target circuit and always accepts it. Correctness of the target is ensured by the fact that the QOTP has no effect on the computation, as all the extra Pauli gates cancel out with each other or are countered by the classical post-processing of the outputs. Acceptance is ensured by the fact that in the absence of noise all the trap circuits always yield the correct outcome \(\bar{x} = \emptyset\).

We will now consider a noisy implementation of our protocol, explain the role played by the various tools (QOTP, trap circuits etc.) and show that with single-qubit gates suffering bounded noise, our protocol ensures that wrong outputs are rejected with high probability.
2.4. The credibility of our protocol

As per equation (2), we model noise as a set of CPTP maps acting on the whole system and on the environment (figure 5). For simplicity, let us begin with the assumption that all the rounds of single-qubit gates in our protocol are noiseless, i.e. that for all circuits \( k = 1, \ldots, \nu + 1 \) and bands \( j = 1, \ldots, m \), a noisy implementation of the round of single-qubit gates is (see figure 5 for notation)

\[
\tilde{U}_j^{(k)} = E_j^{(k)}(U_j^{(k)} \otimes I_E) \quad \text{with} \quad E_j^{(k)} = I_{SE},
\]

where \( I_{SE} \) is the identity on system and environment. Under this assumption, a first simplification to the noise of type N1 comes from the QOTP, a tool used in many works in verification [25] and benchmarking protocols [20, 51] that also plays a crucial role in our protocol. If single-qubit gates are noiseless, the QOTP allows to randomize all noise processes, even those non-local in space and time, to classically correlated Pauli errors (see lemma 1 in appendix A). A similar result was previously proven in [51] for Markovian noise, and here we show that this result holds also if the noise creates correlations in time.

Having reduced arbitrary non-local noise to Pauli errors via the QOTP, we show (see lemma 2 in appendix B) that our trap circuits detect all Pauli errors with non-zero probability. The reasoning is as follows: since the trap circuits contain only Clifford gates, the noise acting at any point of a trap circuit can be factored to that this result holds also if the noise creates correlations in time.

![Figure 5. Schematic illustration of a noisy implementation of our protocol where all boxes represent CPTP maps.](image)

The above arguments and lemmas can be used to prove that our protocol can detect arbitrary noise in state preparation, measurement and two-qubit gates, provided that single-qubit gates are noiseless:

**Theorem 1.** Suppose that all single-qubit gates in our accreditation protocol are noiseless. For any number \( \nu \geq 3 \) of trap circuits, our accreditation protocol can accredit the outputs of a noisy quantum computer affected by noise of the form N1 with

\[
\varepsilon = \frac{\kappa}{\nu + 1},
\]

where \( \kappa = 3(3/4)^2 \approx 1.7 \).

To prove \( \varepsilon = \kappa / (\nu + 1) \) we write the state of the system at the end of a noisy protocol run as in equation (3). We do this using lemmas 1 and 2. The proof of theorem 1 is in appendix C.
We now relax the assumption of noiseless single-qubit gates and generalize our results to noise of the form $N2$. We assume that all rounds of single-qubit gates suffer bounded noise, i.e. that for all circuits $k = 1, \ldots, \nu + 1$ and bands $j = 1, \ldots, m$, a noisy implementation of the round of single-qubit gates is (see Figure 5 for notation)

$$\tilde{U}_j^{(k)} = \mathcal{E}_j^{(k)}(U_j^{(k)} \otimes I_E)$$

with $\mathcal{E}_j^{(k)} = (1 - r_j^{(k)}) I_{SE} + r_j^{(k)} \mathcal{E}_j^{(k)}$ for some arbitrary CPTP map $\mathcal{E}_j^{(k)}$ acting on both system and environment and for some number $0 \leq r_j^{(k)} < 1$. We refer to the number $r_j^{(k)}$ as ‘error rate’ of $U_j^{(k)}$. Since each $U_j^{(k)}$ is chosen at random (depending on whether circuit $k$ is the target or a trap and on the QOTP) and since noise in single-qubit gates is potentially gate-dependent (condition $N2$), let us indicate with $r_{\text{max}, j}^{(k)}$ the maximum error rate of single-qubit gates in band $j$ of circuit $k$, the maximum being taken over all possible choices of $U_j^{(k)}$.

We can now state theorem 2:

**Theorem 2.** Our protocol with $\nu \geq 3$ of trap circuits can accredit the outputs of a noisy quantum computer affected by noise of the form $N1$ and $N2$ with

$$\varepsilon = g \frac{\kappa}{\nu + 1} + 1 - g,$$

where $\kappa = 3(3/4)^{\nu} \approx 1.7$ and $g = \prod_{j,k} (1 - r_{\text{max}, j}^{(k)})$.

To calculate $\varepsilon$ for the protocol with noisy single-qubit gates we use that $\mathcal{E}_j^{(k)} = (1 - r_{\text{max}, j}^{(k)}) I_{SE} + r_{\text{max}, j}^{(k)} \mathcal{Q}_j^{(k)}$, where $\mathcal{Q}_j^{(k)}$ is a CPTP map encompassing the system and the environment. We can then rewrite the state of the system at the end of the protocol as

$$\rho_{\text{out}}^* = g \rho_{\text{out}} + (1 - g) \tilde{\rho}_{\text{out}},$$

where $\rho_{\text{out}}$ is the state of the system at the end of a protocol run with noiseless single-qubit gates—which by theorem 1 is of the form of equation (3) with $b \leq \kappa/(\nu + 1)$—and $\tilde{\rho}_{\text{out}}$ is a quantum state containing the effects of noise in single-qubit gates. Expressing $\tilde{\rho}_{\text{out}}$ as

$$\tilde{\rho}_{\text{out}} = h \tilde{\gamma}_{1\text{tar}} \otimes |\text{acc}\rangle\langle\text{acc}| + (1 - h) \tilde{\gamma}_{2\text{tar}} \otimes |\text{rej}\rangle\langle\text{rej}|,$$

where $\tilde{\gamma}_{1\text{tar}}$ and $\tilde{\gamma}_{2\text{tar}}$ are arbitrary states for the target and $0 \leq h \leq 1$, we thus have

$$\rho_{\text{out}}^* = g[b \rho_{\text{out}} \otimes |\text{acc}\rangle\langle\text{acc}| + (1 - b)(l \rho_{\text{out}} \otimes |\text{acc}\rangle\langle\text{acc}| + (1 - l) \rho_{\text{out}} \otimes |\text{rej}\rangle\langle\text{rej}|) + (1 - g)[h \tilde{\gamma}_{1\text{tar}} \otimes |\text{acc}\rangle\langle\text{acc}| + (1 - h) \tilde{\gamma}_{2\text{tar}} \otimes |\text{rej}\rangle\langle\text{rej}|].$$

As it can be seen, the probability that the target is in the wrong state and the flag bit is in the state $|\text{acc}\rangle$ is $gb + (1 - g)h \leq g\nu/(\nu + 1) + (1 - g)h$, where we used that $b \leq \kappa/(\nu + 1)$ from theorem 1. This probability reaches its maximum for $h = 1$, therefore we have $\varepsilon = g\nu/(\nu + 1) + 1 - g$. Note that if $r_{\text{max}, j}^{(k)} \leq n_0 \ll 1$, then

$$g \geq \prod_{k=1}^{\nu+1} \prod_{j=1}^{m} (1 - n_0) \approx 1 - m(\nu + 1)n_0 + O(r_0^2).$$

Thus, if $n_0 \ll 1/m(\nu + 1)$, then $g \approx 1$ and $\varepsilon \approx 1.7/(\nu + 1)$.

It is worth noting that our theorem 1 also holds if single-qubit gates suffer unbounded noise, provided that this noise is gate-independent. Indeed, if $\mathcal{E}_j^{(k)} = \mathcal{E}$ does not depend on the parameters in $U_j^{(k)}$ (see figure 5 for notation), using $\mathcal{E}_j^{(k)} C Z_j^{(k)} F_j^{(k)} = C Z_j^{(k)} F_j^{(k)} (w i t h F_j^{(k)} = C Z_j^{(k)} C Z_j^{(1/2)} C Z_j^{(1/2)} C Z_j^{(1/2)} C Z_j^{(1/2)} C Z_j^{(1/2)} C Z_j^{(1/2)})$ we can factor this noise into that of $C Z_j$ and prove $\varepsilon = \kappa/(\nu + 1)$ with the same arguments used in theorem 1. Similarly, we also expect our theorem 2 to hold if noise in single-qubit gates has a weak gate-dependence, as is the case for some of the protocols centered around randomized benchmarking [19]. We leave the analysis of weakly gate-dependent noise to future works.

2.5. Mesothetic verification protocol

In Box 4 in the Methods we translate our accreditation protocol into a cryptographic protocol, obtaining what we call ‘mesothetic’ verification protocol. To verify an $n$-qubit computation, in the mesothetic protocol the verifier (Alice) must possess a device that can receive $n$ qubits from the prover (Bob), implement single-qubit gates on all of them and send the qubits back to the Bob. In appendix D we present theorems D1 and D2, which are the counterparts of theorems 1 and 2 for the cryptographic protocol. In the first two theorems the number $\varepsilon$
is replaced by the soundness $\varepsilon_{cr}$ (see definition 4 in appendix D.2), namely the probability that Alice accepts a wrong output for the target when Bob is cheating.

Our mesothetic verification protocol is different from prepare-and-send [26–33] or receive-and-measure [34–38] cryptographic protocols in that Alice encrypts the computation through the QOTP during the actual implementation of the circuits, and not before or after the implementation. To do this, she must possess an $n$-qubit quantum memory and be able to execute single-qubit gates. Despite being scalable, our protocol is more demanding than those in [27–38], where Alice only requires a single-qubit memory. This suggests the interesting possibility that protocols optimized for experiments may translate into more demanding cryptographic protocols and vice-versa.

Similarly to post-hoc verification protocols [35, 43], our protocol is not blind. Alice leaks crucial information to Bob regarding the target circuit, such as the position of two-qubit gates. This is not a concern for our goals, as verifiability in our protocol relies on Bob being incapable to distinguish between target and trap circuits, i.e. to retrieve the number $v_0$, see lemma D3 in appendix D.2.

Blindness may be required to protect user’s privacy in future scenarios of delegated quantum computing [53]. In appendix D.2 we thus show how to make our protocol blind. This requires recompiling the target circuit into a circuit in normal form with fixed cZ gates, such as the brickwork-type circuit in figure 6. This yields an increase in circuit depth, hence the minimal overheads of our protocol must be traded for blindness.

3. Discussion

We have presented a trap-based accreditation protocol for the outputs of NISQ devices. Our protocol is scalable and practical, and relies on minimal assumptions on the noise. Specifically, our protocol requires that single-qubit gates suffer bounded (but potentially gate-dependent and non-local in space and time) noise.

A single protocol run ends by either accepting or rejecting the output of the target circuit that we seek to accredit. We can then run our accreditation protocol multiple times (with the same target and with the same number of traps), each time keeping the output if the protocol accepts and discarding it if the protocol rejects. After multiple runs with i.i.d. noise, our protocol allows to bound the variation distance between noisy and noiseless probability distribution of the accepted outputs (equation (1)).

Real-world devices can be accredited by running our protocol on them. The accreditation is provided by bounds on the variation distance that rely on $\varepsilon$, which we obtained in theorems 1 and 2, and the acceptance probability $\text{prob}(\text{acc})$ of our accreditation protocol. The latter is estimated experimentally by running our protocol multiple times on the device being accredited.

Some noise models allow to lower-bound the acceptance probability analytically and consequently to upper-bound the variation distance. For instance, if all operations $E_{ij}$ in our protocol suffer bounded noise and have error rates $r_{pn}$ we can write the state of the system at the end of the protocol as

$$\rho_{\text{out}} = \delta \sigma_{\text{out}}^{\text{tar}} \otimes |\text{acc}\rangle \langle \text{acc}| + (1 - \delta) \tau,$$

where $\sigma_{\text{out}}^{\text{tar}}$ is the state of the target at the end of a noiseless protocol run, $\tau$ is an arbitrary state for target and flag and $\delta = \prod \rho_p (1 - r_p) \in [0, 1]$. This yields $\text{prob}(\text{acc}) \geq \delta$ and (see equation (5))

$$\frac{1}{2} \sum_{\tau} |\text{prob}_{\text{noisy}}(\tau) - \text{prob}_{\text{noisy}}(\tau)| \leq \frac{\varepsilon}{\text{prob}(\text{acc})} \leq \frac{\varepsilon}{\delta}. \quad (22)$$

In figure 7 we plot the RHS of the above equation. Plots of this type can be used to seek error rates that will provide the desired upper-bound on the variation distance.
Considering the error rates of present NISQ devices [2, 45–48] we expect that our protocol may provide worthwhile upper-bounds for target circuits with up to \( n \approx 7 \) qubits and \( m \approx 7 \) bands (figure 7(a)). We also expect that larger target circuits may yield upper-bounds that are too large to be useful. In fact, for large target circuits, it is also possible that none of the protocol runs will accept the output of the target, and thus that our protocol will provide no upper-bound. Nevertheless, it is worth remarking that this does not indicate that our accreditation protocol is unable to accredit computations on NISQ devices. On the contrary, by providing large upper-bounds, our protocol reveals that the device being tested suffers high levels of noise and that its outputs are not credible.

Our work leaves several open questions. Our theorem 2 shows that our protocol requires reducing the error rates of single-qubit gates with the size of the target circuit. This requirement is similar to that found in other works [29, 38, 41], and is a known obstacle towards scalable quantum computing. A strategy that has been exploited in previous works is to incorporate fault-tolerance into the existing protocols [29, 41]. Another has been to define verifiable fault-tolerance using notions such as acceptability and detectability [54]. Interfacing fault tolerance with accreditation is an interesting challenge for the future.

Another open question regards the applicability of our accreditation protocol if single-qubit gates suffer unbounded noise. In its current state, the analysis of our protocol does not account for unbounded gate-dependent noise in single-qubit gates, including unitary errors such as over- or under-rotations. The reason is that the QOTCP (which maps coherent errors into classically correlated Pauli errors) is applied at the level of single-qubit gates. Unbounded errors that depend on the gates used to randomize arbitrary noise processes to Pauli errors are an obstacle to other works including cryptographic protocols [29] and protocols based on randomized benchmarking [16–18, 20, 21].

Finally, with the mesoethetic protocol we show how to adapt our protocol to the cryptographic setting. In the mesoethetic protocol the verifier requires an \( n \)-qubit memory and the ability to execute single-qubit gates. This protocol is more demanding than several existing cryptographic protocols [27–38] requiring single-qubit memory for the verifier. An interesting question is whether a mesoethetic protocol can be devised that only requires single-qubit gates and single-qubit memory for the verifier.

4. Methods

4.1. Overhead of our accreditation protocol

Here we count the overhead of our protocol. Our protocol has no quantum overhead, as all circuits have the same size as the one being verified. The classical overhead consists in \( O(nm) \) bits for each of the \( v + 1 \) computations. Specifically, the target computation has an overhead of \( 2nm + n \) bits (the \( 2nm \) random bits \( \alpha_{ij}, \beta_{ij} \) and the \( n \) random bits \( \gamma_i \) in Routine 1), while the traps have an overhead of at most \( 2nm + n + nm \) bits (the \( 2nm + n \) random bits in Routine 1 and at most \( nm \) random bits in Routine 2).

Figure 7. RHS of Inequality 22 for (a) target circuit preparing and measuring \( n \)-qubit GHZ states, with \( n = m = 7 \) (dashed lines) and \( n = m = 10 \) (solid lines) and (b) target circuit implementing a pseudo-random circuit for supremacy experiment [14] with \( n = 62 \) qubits and circuit depth \( m = 34 \). In these plots we assume that all operations are affected by bounded noise. We also assume that single-qubit state preparation, single-qubit measurements and \( cZ \)-gates have error rates \( \sigma_0 \), and that all single-qubit gates have error rates \( \gamma_f / 10 \).
Box 1. Accreditation protocol.

Input:
1. A target circuit that takes as input $n$ qubits in the state $|+>$, contains only single-qubit gates and $cZ$ gates arranged in $m$ bands and ends with Pauli-$X$ measurements (figure 2).
2. The number $v$ of trap circuits.

Routine:
1. Choose a random number $v_0 \in \{1, \ldots, v+1\}$ and define $\{U_{ij}^{(v_0)}\} = \{U_{ij}\}$, where $\{U_{ij}\}$ is the set of single-qubit gates in the target circuit.
2. For $k = 1, \ldots, v+1$: If $k = v_0$ (trap circuit), run Routine 2 and obtain the set of single-qubit gates $\{U_{ij}^{(k)}\}$ for the $k$th trap circuit.
3. For $k = 1, \ldots, v+1$: run Routine 1 and obtain $\{U_{ij}^{(k)}\}$, together with the bit-string $(a_{ij}^{(0)}, \ldots, a_{ij}^{(v_0)})$.
4. For $k = 1, \ldots, v+1$:
   1. Create a state $\rho_{kn} = |+>^{\otimes n}$.
   2. Implement circuit $k$ with single-qubit gates from the set $\{U_{ij}^{(k)}\}$ and obtain output $\tau^{(k)} = (s_{ij}^{(k)}$, $\ldots, s_{ij}^{(k)})$.
5. Initialize a flag bit to $\text{(acc)} = |0\rangle$. Then, for $k = 1, \ldots, v+1$: if $\tau^{(k)} = \emptyset$ and $k \neq v_0$ (trap circuit), set the flag bit to $\text{(rej)} = |\text{acc} \oplus 1\rangle$.

Output:
The output $\tau^{(m)}$ of the target circuit and the flag bit.

Box 2. Routine 1. (Quantum one-time pad).

Input:
A set $\{U_{ij}\}$ of single-qubit gates, for $j = 1, \ldots, m$ and $i = 1, \ldots, n$.

Routine:
1. For $j = 1, \ldots, m$ and $i = 1, \ldots, n$:
   Choose two random bits $\alpha_{ij}$ and $\alpha'_{ij}$. Next, define $U_{ij}' = X^{\alpha_{ij}}Z^{\alpha'_{ij}}U_{ij}$.
2. For $i = 1, \ldots, n$:
   Choose a random bit $\gamma_i$ and define $U_{i1}' = U_{i1}'X^{\gamma_i}$.
3. For $j = 1, \ldots, m-1$:
   Using equations (7) and (8) define $U_{ij+1}'$ so that
   \[ (\otimes, U_{ij+1}')cZ_j(\otimes, U_{ij}') = (\otimes, U_{ij+1}'cZ_j(\otimes, U_{ij})), \]
   where $cZ_j$ is the entangling operation in the $j$th band.

Output:
The set $\{U_{ij}'\}$ and the $n$-bit string $(a_{1,m}, \ldots, a_{n,m})$.

Box 3. Routine 2. (Single-qubit gates for trap circuits).

Input:
The target circuit.

Routine:
1. Initialize the set $\{U_{ij} = I\}$, for $i = 1, \ldots, n$ and $j = 1, \ldots, m$.
2. For all $j = 1, \ldots, m - 1$:
   1. For all $i = 1, \ldots, n$: if in band $j$ of the target circuit qubits $i$ and $i'$ are connected by a $cZ$ gate, set
      \begin{align*}
      U_{ij} &= S_i U_{ij} \quad \text{and} \quad U_{ij} = H_{i'} U_{ij} \quad \text{with probability 1/2}, \\
      U_{ij} &= H_j U_{ij} \quad \text{and} \quad U_{ij} = S_i U_{ij} \quad \text{with probability 1/2}.
      \end{align*}
   Otherwise, set $U_{ij} = S_i U_{ij}$ or $U_{ij} = H_j U_{ij}$ with probability 1/2.
2. For all $i = 1, \ldots, n$: Set $U_{i,i+1} = U_{ij}'$.
3. For all $i = 1, \ldots, n$:
   Choose a random bit $t \in \{0, 1\}$. Next, set $U_{i1} = U_{i1}HP$ and $U_{in} = H'U_{in}$.

Output:
The set $\{U_{ij}\}$.
Box 4. Mesothetic protocol (further details in appendix D.)

Input:
A classical description of the target circuit and the number of traps. (The input is known to both Alice and Bob).

Preliminary Operations:
1. Alice randomly chooses which circuit \( n_0 \in \{1, \ldots, n + 1\} \) will be used to implement the target. Next she defines \( \{U_{ij}^{(n_0)}\} = \{U_{ij}\} \), where \( \{U_{ij}\} \) is the set of single-qubit gates in the target circuit.
2. For \( k = 1, \ldots, n + 1 \): if \( k = n_0 \) (trap circuit), Alice runs Routine 2 and obtains the set \( \{U_{ij}^{(n_0)}\} \) of single-qubit gates for the \( k \)th circuit.
3. For \( k = 1, \ldots, n + 1 \): Alice runs Routine 1 and obtains the set of gates \( \{U_{ij}^{(n_0)}\} \), together with the random bits \( \alpha_{ij}^{(n_0)} \).

Routine:
4. For all \( k = 1, \ldots, n + 1 \), Alice and Bob interact as follows:
   4.1 Bob creates \( n \) qubits in state \(+\).
   4.2 For \( j = 1, \ldots, m \):
      4.2.1 Bob sends all the qubits to Alice. For \( i = 1, \ldots, m \), Alice executes \( U_{ij}^{(n_0)}(\rho_k) = \otimes_{r=1} U_{ij}^{(n_0)}(\rho) U_{ij}^{(r)}(\rho) \), where \( \rho_k \) is the state of the system.
      4.2.2 Bob applies the entangling gates \( Z_j \) contained in the \( j \)th band of the target circuit.
   4.3 For \( i = 1, \ldots, m \): Bob measures qubit \( i \) in the Pauli \( \chi \) basis and communicates the output to Alice. Alice bit-flips the output if \( \alpha_{ij}^{(n_0)} = 1 \), otherwise she does nothing. Next, if \( k = n_0 \) and this output is \( 0 \), Alice aborts the protocol, otherwise she does nothing.
   5. Alice initializes a flag bit to the state \( |\text{acc}\rangle = |0\rangle \). Next, for all \( k = 1, \ldots, n + 1 \) if \( k = n_0 \) (trap circuit) and \( \epsilon_i^{(k)} \oplus \alpha_{ij}^{(k)} = 0 \) for some \( i \in \{1, \ldots, n\} \), Alice sets the flag bit to \( |\text{rej}\rangle = |1\rangle \).

Output:
The outputs of the target circuit and the flag bit.

Acknowledgments
This research was supported by the UK EPSRC (EP/K04057X/2) and the UK Networked Quantum Information Technologies (NQIT) Hub (EP/M013243/1). We acknowledge helpful discussions with Joel Wallman, Elham Kashefi, Dominic Branford, Zhang Jiang, Joseph Emerson.

Notation
In these appendices we will indicate the action of the round of single-qubit gates in a band \( j \in \{1, \ldots, m\} \) of a circuit \( k \in \{1, \ldots, n + 1\} \) as \( U_{ij}^{(k)}(\rho_k) = \otimes_{r=1} U_{ij}^{(k)}(\rho) U_{ij}^{(r)}(\rho) \), where \( \rho_k \) is the state of the system. Similarly, we will indicate the action of a round of \( \chi \) gates on the system as \( \chi_j(\rho_k) = \otimes_{ij} Z_j(\rho) Z_j(\rho) \), where \( Z_j \) is the tensor product of all \( \chi \) gates in band \( j \) in the target circuit, and the action of \( n \)-qubit Pauli operators as \( P \in \{I, X, Y, Z\}^n \), where \( \hat{X}(\rho) = \rho, \hat{X}(\rho) = X(\rho), \hat{Y}(\rho) = Y(\rho) \), \( Z(\rho) = Z(\rho) \) are single-qubit Pauli operators.

In appendix A we provide statement and proof of lemma 1, in appendix B we provide statement and proof of lemma 2, in appendix C we prove theorem 1 and in appendix D we prove soundness of the mesothetic protocol.

Appendix A. Statement and proof of lemma 1
We now present and prove lemma 1, which is as follows:

**Lemma 1.** Suppose that all single-qubit gates in all the circuits implemented in our protocol are noiseless, and that state preparation, measurements and two-qubit gates suffer noise of the type type N1. For a fixed choice of single-qubit gates \( U_{ij}^{(1)}, \ldots, U_{ij}^{(m+1)} \), summed over all the random bits \( \alpha_{ij}, \alpha_{ij}', \gamma \) (see Routine 1), the joint state of the target circuit and of the traps after they have all been implemented is of the form

\[
\rho_{out}(U_{ij}^{(1)}, \ldots, U_{ij}^{(m+1)}) = \sum_{\rho_0, \ldots, \rho_{m+1}} \sum_{\rho_{ij}^{(1)}, \ldots, \rho_{ij}^{(m+1)}} \frac{\prod_{k=0}^{m+1} \prod_{j=1}^{n} \rho_{ij}^{(k)}}{2^{m+1}} \\
\times \otimes_{k=1}^{m+1} \{ |+\rangle \langle +| (Z_j^{(k)} P_{ij}^{(k)}(\rho)^{-1} \sum_{j=1}^{n} (\chi_j P_{ij}^{(k)}(\rho) U_{ij}^{(k)} \otimes P_{ij}^{(k)}(\rho)^{-1})) |+\rangle \langle +| Z_j^{(k)} + |+\rangle \langle +| \}
\]

(A1)
where \( \rho_{in} = \otimes |_{\Psi} \rangle \langle \Psi | \) is a binary string representing the output of the \( k \)th circuit, \( Z^{\oplus} (\rho) = \otimes \rho Z_{a}^{(i)} \) and \( \text{prob}(\mathcal{P}_{0}^{(k)}, \ldots, \mathcal{P}_{m}^{(k)}) \) is the joint probability of a collection of Pauli errors \( \mathcal{P}_{0}^{(k)}, \ldots, \mathcal{P}_{m}^{(k)} \) affecting the system, with \( \mathcal{P}_{i}^{(k)}, \ldots, \mathcal{P}_{m-1}^{(k)} \in \{ Z, X, Y \} \otimes n \) and \( \mathcal{P}_{m}^{(k)} \) with \( \mathcal{P}_{m}^{(k)} \in \{ I, Z \} \otimes n \) for all \( k \).

This lemma shows that if single-qubit gates are noiseless, the QOTP allows to reduce noise of the type \( N_{1} \) to classically correlated Pauli errors. These Pauli errors affect each circuit after state preparation (\( \mathcal{P}_{0}^{(k)} \)), before each entangling operation \( C_{Z} (\mathcal{P}_{j}^{(k)}, \text{for } j \in \{ 1, \ldots, m - 1 \} \) and before the measurements (\( \mathcal{P}_{m}^{(k)} \)). Errors in the \( C_{Z} \) gates can be Pauli-X, Y and Z, while those in state preparation and measurements are Pauli-Z (this is because their Pauli-X components stabilize \( \rho_{in} \) and Pauli-X measurements respectively).

The main tool used in this section is the ‘Pauli Twirl’ [18].

(Pauli Twirl). Let \( \rho \) be a \( 2^{v} \times 2^{v} \) density matrix and let \( P, P' \) be two \( n \)-fold tensor products of the set of Pauli operators \( \{ I, X, Y, Z \} \). Denoting by \( \{ Q_{i} \} \) the set of all \( n \)-fold tensor products of the set of Pauli operators \( \{ I, X, Y, Z \} \),

\[
\sum_{r=1}^{2^{v}} Q_{r} P Q_{r} P' Q_{r} = 0 \quad \forall P \neq P'.
\] (A2)

We will also use a restricted version of the Pauli Twirl, which is proven in [29]

(Restricted Pauli Twirl). Let \( \rho \) be a \( 2^{v} \times 2^{v} \) density matrix and let \( P, P' \) be two \( n \)-fold tensor products of the set of Pauli operators \( \{ I, Z \} \). Denoting by \( \{ Q_{i} \} \) the set of all \( n \)-fold tensor products of the set of Pauli operators \( \{ I, X \} \),

\[
\sum_{r=1}^{2^{v}} Q_{r} P Q_{r} P' Q_{r} = 0 \quad \forall P \neq P'.
\] (A3)

The same holds if \( P \) and \( P' \) are two \( n \)-fold tensor products of the set of Pauli operators \( \{ I, X \} \) and \( \{ Q_{i} \} \) is the set of all \( n \)-fold tensor products of the set of Pauli operators \( \{ I, Z \} \).

\textbf{Proof. (Lemma 1)} We start proving the lemma for the case where we run a single circuit (\( v = 0 \)), and then we generalize to multiple circuits (\( v > 0 \)). Including all purifications in the environment, we can rewrite the noise as unitary matrices acting on system and environment (for clarity we write these unitaries in bold font). Doing this, for a fixed choice of gates \( U_{l}^{\prime}, \ldots, U_{m}^{\prime} \) (which depend on the choice of gates \( U_{l}, \ldots, U_{m} \) and on all the random bits \( \alpha_{ij}, \alpha_{ij}', \gamma_{i} \), see Routine 1), the state of the system before the measurement becomes (figure 8)

\[
\rho(U_{l}^{\prime}, \ldots, U_{m}^{\prime}) = \text{Tr}_{\text{E}}[\mathbf{M} U_{m}^{\dagger} \ldots U_{m-1}^{\dagger} F_{m-1}^{\dagger} \ldots F_{1}^{\dagger} F_{1}^{\dagger} U_{m}^{n} R(\rho_{in} \otimes \rho_{E}) R^{\dagger} U_{l}^{n} U_{l}^{n} F_{1}^{\dagger} \ldots F_{m-1}^{\dagger} (Z_{m} \ldots (Z_{1} F_{1}^{\dagger} \ldots F_{m-1}^{\dagger} U_{m}^{n}) \mathbf{M})],
\] (A4)

where \( \rho_{in} = \otimes |_{\Psi} \rangle \langle \Psi | \) is the initial state of the environment, \( U_{l}^{n} = \otimes U_{l_{ij}}^{n} \) are the gates output by Routine 1, the unitary matrix \( R \) represents the noise in state preparation, the unitary matrix \( \mathbf{M} \) represents the noise in the measurements, \( \bar{Z}_{j} F_{j} \) is the noisy round of entangling gates in a band \( j \) and \( \text{Tr}_{\text{E}}[\cdot] \) is the trace over the environment.

\[\text{Figure 8.} \text{ Noisy implementation of the } 6\text{-qubit target circuit in figure 2. The noise in state preparation is described by the unitary } R \text{, that in the measurements by } M \text{, that in the } C_{Z}\text{-gates in a band } j = 1 \ldots m - 1 \text{ by } F_{j} \text{. All these unitaries act simultaneously on the system and on the environment (initially in the ground state } |e_{0}\rangle \text{).}\]**
For simplicity, we first prove our result for a circuit with \( m = 2 \) bands and generalize to \( m > 2 \) bands later. In this case, defining an orthonormal basis \([|e_0\rangle, |e_1\rangle]\) for the environment, the state in equation (A4) is
\[
\rho(\mathcal{U}_0', \mathcal{U}_0^+)= \sum_p \langle e_p | M U_0^+ e_U^* \mathcal{Z}_F U_0^R \mathcal{R}(\rho_{in} \otimes |e_0\rangle \langle e_0|) U_0^{+\dagger} F_i^* \mathcal{Z}_i U_0^{+\dagger} M^* |e_p\rangle.
\]  
(A5)

Introducing resolutions of the identity on the environment before and after every noise operator, we have
\[
\rho(\mathcal{U}_0', \mathcal{U}_0^+)= \sum_{k_1 k_2 l_1 l_2} \left[ \langle e_p | M U_0^+ e_U^* \mathcal{Z}_F U_0^R \mathcal{R} \langle e_0 | \langle e_0| \right] (\rho_{in}) \\
\times \left[ \langle e_0 | R | e_0\rangle \langle e_0| F | e_0\rangle \langle e_0| R | e_0\rangle \langle e_0| F | e_0\rangle \langle e_0| M^* | e_p\rangle \right],
\]  
(A6)

since \(\sum_1 |e_i\rangle \langle e_i| = I_F\) and \(\langle e_i | V_{ij} | e_j\rangle = V_{ij} \delta_{i,j}\) for every operator \(V_{ij}\) acting only on the system. The operators \(\langle e_p | M | e_0\rangle\), \(\langle e_p | F | e_0\rangle\), \(\langle e_0 | R | e_0\rangle\), \(\langle e_0 | F | e_0\rangle\), \(\langle e_0 | M^* | e_p\rangle\) act only on the system, and can thus be written as in table 1.

In table 1, \(P_{ij} \in \{I, X, Y, Z\} \otimes \overline{\sigma}\) are \(n\)-fold tensor products of Pauli operators acting on the system and \(\eta_{\sigma_{i_0} \mu_{i_1}}^{(R)}\), \(\eta_{\sigma_{i_0} \mu_{i_1}}^{(F)}\), \(\eta_{\sigma_{i_0} \mu_{i_1}}^{(M)}\) are complex numbers. We then obtain
\[
\rho(\mathcal{U}_0', \mathcal{U}_0^+)= \sum_{R,F,M} \left( \eta_{\sigma_{i_0} \mu_{i_1}}^{(R)} \eta_{\sigma_{i_0} \mu_{i_1}}^{(F)} \eta_{\sigma_{i_0} \mu_{i_1}}^{(M)} \right) P_{i_1} U_0^R \mathcal{Z}_F U_0^R P_{i_1} U_0^{+\dagger} \mathcal{Z}_F U_0^{+\dagger} \rho_{in} P_{i_1} U_0^{+\dagger} F_i^* \mathcal{Z}_i U_0^{+\dagger} P_{i_1}.
\]  
(A7)

We will now describe how to apply the Pauli twirl lemmas iteratively, in the order the operations apply on the input. Therefore, we start by showing how to eliminate terms of the sum where \(\mu_1 = \mu_i\). Since \(X\) stabilizes \(|+\rangle\) states, we can rewrite \(\rho_{in}\) as \((\otimes_i X^\alpha_i^+) \rho_{in} (\otimes_i X^\alpha_i^+)\). Moreover, using \(U_0^{+\dagger} = U_i^N (\otimes_i X^\alpha_i^+)\), see Routine 1, the above state becomes
\[
\rho(\mathcal{U}_0', \mathcal{U}_0^+)= \sum_{R,F,M} \left( \eta_{\sigma_{i_0} \mu_{i_1}}^{(R)} \eta_{\sigma_{i_0} \mu_{i_1}}^{(F)} \eta_{\sigma_{i_0} \mu_{i_1}}^{(M)} \right) P_{i_1} U_0^R \mathcal{Z}_F U_0^R P_{i_1} U_i^N (\otimes_i X^\alpha_i^+) P_{i_1} \rho_{in} (\otimes_i X^\alpha_i^+) P_{i_1} (\otimes_i X^\alpha_i^+) U_i^{N\dagger} P_{i_1} \mathcal{Z}_F U_i^{N\dagger} P_{i_1}.
\]  
(A8)

Summing over all possible \(\gamma\) and applying the Restricted Pauli Twirl (the Pauli-X components of both \(P_{i_1}\) and \(P_{i_2}\) stabilize \(\rho_{in}\) and can thus be ignored), we obtain a factor \(\delta_{\mu_1, \mu_i}\), and thus the above state becomes
\[
\rho(\mathcal{U}_0', \mathcal{U}_0^+)= \frac{1}{2^n} \sum_{\{\alpha_{i_1}, [\alpha_{i_1}]\}} \rho(\mathcal{U}_0', \mathcal{U}_0^+)
\]  
\[
= \sum_{R,F,M} \left( \eta_{\sigma_{i_0} \mu_{i_1}}^{(R)} \eta_{\sigma_{i_0} \mu_{i_1}}^{(F)} \eta_{\sigma_{i_0} \mu_{i_1}}^{(M)} \right) P_{i_1} U_0^R \mathcal{Z}_F U_0^R P_{i_1} U_i^N (\otimes_i X^\alpha_i^+) P_{i_1} \rho_{in} (\otimes_i X^\alpha_i^+) P_{i_1} (\otimes_i X^\alpha_i^+) U_i^{N\dagger} P_{i_1} \mathcal{Z}_F U_i^{N\dagger} P_{i_1}.
\]  
(A9)

To operate a Pauli twirl on \(P_{i_1}\) and \(P_{i_2}\), we rewrite \(U_i^N\) as \((\otimes_i X^\alpha_i^+) U_i^N \mathcal{Z}_F U_i^N (\otimes_i X^\alpha_i^+)\), see Routine 1. Summing over \(\alpha_{i_1}\) and \(\alpha_{i_2}\) and using the Pauli Twirl, we obtain \(\delta_{\mu_{i_1}, \mu_i}\), and thus
\[
\rho(\mathcal{U}_0, \mathcal{U}_0^+)= \frac{1}{2^{2n}} \sum_{\{\alpha_{i_1}, [\alpha_{i_1}], [\alpha_{i_2}],[\alpha_{i_2}]\}} \rho(\mathcal{U}_0', \mathcal{U}_0^+)
\]  
\[
= \sum_{R,F,M} \left( \eta_{\sigma_{i_0} \mu_{i_1}}^{(R)} \eta_{\sigma_{i_0} \mu_{i_1}}^{(F)} \eta_{\sigma_{i_0} \mu_{i_1}}^{(M)} \right) P_{i_1} U_0^R \mathcal{Z}_F U_0^R P_{i_1} U_i^N (\otimes_i X^\alpha_i^+) P_{i_1} \rho_{in} (\otimes_i X^\alpha_i^+) P_{i_1} (\otimes_i X^\alpha_i^+) U_i^{N\dagger} P_{i_1} \mathcal{Z}_F U_i^{N\dagger} P_{i_1}.
\]  
(A10)
To operate a Pauli twirl on $P_{\rho \alpha}$ and $P_{\psi}$ we write the state of the system after the measurements:

$$
\rho_{\text{meas}}(U_1, U_2) = \frac{1}{2^n} \sum_{i} \langle i | \rho(U_1, U_2) Z^i | i \rangle Z^i |angle \langle + | Z^i \rangle^\otimes n
$$

$$
= \frac{1}{2^n} \sum_{\rho \in \mathcal{B}_{n}} \left( \langle i | \rho(H_{0,0}^{(R)} H_{0,0}^{(F)} H_{0,0}^{(M)}) \rangle \langle i | \rho(H_{0,0}^{(R)} H_{0,0}^{(F)} H_{0,0}^{(M)}) \rangle \right) 

\times \langle + \rangle^\otimes n \times \langle \otimes | Z^i \rangle^\otimes n \langle + | Z^i \rangle^\otimes n
$$

$$
= \frac{1}{2^n} \sum_{\rho \in \mathcal{B}_{n}} \left( \langle i | \rho(H_{0,0}^{(R)} H_{0,0}^{(F)} H_{0,0}^{(M)}) \rangle \langle i | \rho(H_{0,0}^{(R)} H_{0,0}^{(F)} H_{0,0}^{(M)}) \rangle \right) 

\times \langle + \rangle^\otimes n \times \langle \otimes | Z^i \rangle^\otimes n \langle + | Z^i \rangle^\otimes n
$$

where in the second equality we used $U_2 = (\otimes_1 X_1^\alpha Z_{1,T}^\otimes n) U_2$. We can now rewrite $\langle + \rangle^\otimes n$ as $\langle \otimes | X_1^\alpha Z_{1,T}^\otimes n | + \rangle^\otimes n$ and sum over $\{\alpha_1, \ldots, \alpha_n\}$. Using the Restricted Pauli Twirl (the Pauli-X components of both $P_{\rho \alpha}$ and $P_{\psi}$ stabilize $| + \rangle^\otimes n$ and can thus be ignored), we obtain $\delta_{\rho_{\text{meas}}}$:

$$
\rho_{\text{meas}}(U_1, U_2) = \frac{1}{2^n} \sum_{\alpha_1} \rho_{\text{meas}}(U_1, U_2)
$$

$$
= \frac{1}{2^n} \sum_{\alpha_1} \left( \langle i | \rho(H_{0,0}^{(R)} H_{0,0}^{(F)} H_{0,0}^{(M)}) \rangle \langle i | \rho(H_{0,0}^{(R)} H_{0,0}^{(F)} H_{0,0}^{(M)}) \rangle \right) 

\times \langle + \rangle^\otimes n \times \langle \otimes | Z^i \rangle^\otimes n \langle + | Z^i \rangle^\otimes n
$$

Finally, after the classical post-processing (which replaces the outputs $s_i$ with $s_i \oplus \alpha_{i,2}$), average over $\{\alpha_{i,2}\}$ yields the outcome state

$$
\rho_{\text{out}}(U_1, U_2) = \frac{1}{2^n} \sum_{\alpha_1} \frac{1}{2^n} \sum_{\rho \in \mathcal{B}_{n}} \left( \langle i | \rho(H_{0,0}^{(R)} H_{0,0}^{(F)} H_{0,0}^{(M)}) \rangle \langle i | \rho(H_{0,0}^{(R)} H_{0,0}^{(F)} H_{0,0}^{(M)}) \rangle \right) 

\times \langle + \rangle^\otimes n \times \langle \otimes | Z^i \rangle^\otimes n \langle + | Z^i \rangle^\otimes n
$$

$$
= \frac{1}{2^n} \sum_{\alpha_1} \left( \langle i | \rho(H_{0,0}^{(R)} H_{0,0}^{(F)} H_{0,0}^{(M)}) \rangle \langle i | \rho(H_{0,0}^{(R)} H_{0,0}^{(F)} H_{0,0}^{(M)}) \rangle \right) 

\times \langle + \rangle^\otimes n \times \langle \otimes | Z^i \rangle^\otimes n \langle + | Z^i \rangle^\otimes n
$$

$$
= \frac{1}{2^n} \sum_{\alpha_1} \delta_{\rho_{\text{out}}}
$$

(A11)
where
\[ \phi_{(p_1, p_2, p_3)} = \sum_{p_1, p_2, p_3} \eta_{k_0, k_1, k_2, k_3}^{(p_1)} \eta_{k_0, k_1, k_2, k_3}^{(p_2)} \eta_{k_0, k_1, k_2, k_3}^{(p_3)} \eta_{k_0, k_1, k_2, k_3}^{(p_1)} \eta_{k_0, k_1, k_2, k_3}^{(p_2)} \eta_{k_0, k_1, k_2, k_3}^{(p_3)} = \sum_p \left| \sum_k \eta_{k_0, k_1, k_2, k_3}^{(p_1)} \eta_{k_0, k_1, k_2, k_3}^{(p_2)} \eta_{k_0, k_1, k_2, k_3}^{(p_3)} \right|^2 \geq 0 \]  
(A14)

and \( \sum_{p_1, p_2, p_3} \phi_{(p_1, p_2, p_3)} = 1 \). \( \rho_{\text{out}}(U_1, U_3) \) is therefore a convex combination of quantum states and \( \phi_{(p_1, p_2, p_3)} \) can be seen as the joint probability of Pauli errors \( P_{j_0}, P_{j_1}, P_{j_2} \). We can thus rewrite
\[ \rho_{\text{out}}(U_1, U_3) = \sum_{p_1, p_2, p_3} \frac{\text{prob}(P_{j_0}, P_{j_1}, P_{j_2})}{2^n} \left( |\{Z^+_2 \} \rangle \langle Z^+_1 | + |\{Z^-_2 \} \rangle \langle Z^-_1 | \right) \]
\( \left( \bigotimes_{i=1}^{n} (\bigotimes_{j} Z_j) \right) \),

(B15)

where \( P_{j_0}, P_{j_1}, P_{j_2} \in \{I, Z\}^{\otimes n}, P_j \in \{I, X, Y, Z\}^{\otimes n} \) and \( \text{prob}(P_{j_0}, P_{j_1}, P_{j_2}) \) is the joint probability of Pauli errors \( P_{j_0}, P_{j_1}, P_{j_2} \). This concludes the proof for the protocol with \( n = 0 \) and \( m = 2 \).

The generalization to a protocol with \( n = 0 \) and \( m > 2 \) is straightforward. Starting from the state in equation (A14), one can use the same arguments as for the two-band circuit. To generalize to multiple circuits \( (n > 0) \), we start by noticing that the circuits are implemented in series, hence the noise can only affect one circuit at a time. By the principle of deferred measurements, we can execute all the measurements at the end of the protocol. Moreover, we can prepare the input qubits for all the circuits at the beginning of the protocol. Doing this, the state of the system after all circuits have been implemented becomes
\[ \rho(U_0^{(1)} \ldots U_m^{(n+1)}) = \text{Tr} \left[ (M^{(n+1)} U_m^{(n+1)} Z_{m-1}^{(n+1)} U_{m-1}^{(n+1)} \ldots Z_1^{(n+1)} U_1^{(n+1)} \right] \]
\( \left( M^{(n+1)} U_m^{(n+1)} Z_{m-1}^{(n+1)} U_{m-1}^{(n+1)} \ldots Z_1^{(n+1)} U_1^{(n+1)} \right) \]
\( \left( \rho_{\text{in}} \otimes P_j \right) (R^{(k)} U_i^{(k)}) (F^{(k)}_i Z_i \ldots U_{m-1}^{(k)} F^{(k)}_i Z_i \ldots U_m^{(k)} M^{(k)} \ldots) \]
\( \left( R^{(k)} U_i^{(k)} F^{(k)}_i Z_i \ldots U_{m-1}^{(k)} F^{(k)}_i Z_i \ldots U_m^{(k)} M^{(k)} \ldots \right) \]
\( \left( \rho_{\text{in}} \otimes P_j \right) \),

(B16)

where \( \rho_{\text{in}} = \bigotimes_{k=1}^{m} \bigotimes_{j=1}^{n} |\{Z^+_j \} \rangle \langle Z^+_j | + |\{Z^-_j \} \rangle \langle Z^-_j | \) and \( R^{(k)}, F^{(k)}, M^{(k)} \) are unitary matrices that act only on the qubits in the \( k \)th circuit and on the environment (which is the same for all the circuits) and \( U_i^{(k)} = \bigotimes_{j=1}^{n} U_{i_j}^{(k)} \). Starting from here and using the same arguments as above, one can finally obtain equation (A1).

\section*{Appendix B. Statement and proof of lemma 2}

Lemma 1 shows that in our accreditation protocol the noise of the form N1 can be reduced to classically correlated collections of Pauli errors affecting the circuits. In this appendix we prove lemma 2, which states that all collections of Pauli errors can be detected with probability larger than 1/4. More formally:

\textbf{Lemma 2.} For any collection of Pauli errors affecting a trap circuit, summing over all possible single-qubit gates in the trap circuit (i.e. over all possible sets \( \{U_{i_j}\} \) output by Routine 2), the probability that the trap circuit outputs \( \tau = 0 \) is at most 3/4.

\textbf{Proof.} For a given collection of Pauli errors \( \{P_j\}_{j=0}^{m} \) affecting a trap circuit, the state of the trap circuit after the measurements is of the form
\[ \rho_{\text{out}}^{\text{trap}} (\{P_j\}) = \frac{1}{M_j \times \cdots \times M_{m-1}} \sum_{P_j} P_j M_j \circ \left( \sigma_{j=0}^{m-1} C Z P_j U_i \right) \circ P_0 \rho_{\text{in}}^{\text{trap}}, \]

(B17)

where \( \rho_{\text{in}}^{\text{trap}} = \bigotimes_{k=1}^{m} \bigotimes_{j=1}^{n} |\{Z^+_j \} \rangle \langle Z^+_j | + |\{Z^-_j \} \rangle \langle Z^-_j | \) is the entangling operation in band \( j \), \( P_0, P_n \in \{I, Z\}^{\otimes n} \), \( P_j \in \{I, X, Y, Z\}^{\otimes n} \) for all \( j = 1, \ldots, m \) and \( M_j \) is the number of choices of \( U_i \). Note that each number \( M_j \) depends on the number of qubits connected by a CZ in band \( j \) of the trap circuit, see Routine 2.

In a trap circuit the gate \( U_1 \) in the first band is of the form \( U_1 = Y_1 H \), where \( Y_1 \) implements a gate from \( \{H, S \}^{\otimes n} \) (see step 2.1 of Routine 2) and \( H^c \) is the round of Hadamard gates activated at random (see step 3 of Routine 2). Similarly, for all \( j = 2, \ldots, m - 1 \), \( U_j \) implements a gate belonging to the set \( \{I, HS^c, SH^c\}^{\otimes n} \). These gates undo the gates in previous band and implement new ones (see step 2.1 Routine 2 and figure 3), thus we can write them as \( U_j = Y_{j-1} \ldots Y_1 \) with each \( Y_j \) implementing a gate from the set \( \{H, S \}^{\otimes n} \). Finally, the gate \( U_m \) in the last band is of the form \( U_m = H^c V_{m-1} \), where \( V_{m-1} \) implements a gate from \( \{H, S^c\} \otimes n \) and undoes the gate in band \( m - 1 \) (see step 2.2 of Routine 2). Using this, we obtain
\[ \rho_{\text{out}}^{\text{trap}} (\{P_j\}) = \frac{1}{2} \sum_{j=0}^{n} N_j \times \cdots \times N_{m-1} \sum_{Y_{j-1}, \ldots, Y_1} P_j M_j \circ \left( \sigma_{j=0}^{m-1} C Z P_j U_i \right) \circ H^c P_0 \rho_{\text{in}}^{\text{trap}}, \]

(B18)

where \( N_j \) is the number of possible choices of \( Y_j \).
Using that $\mathcal{V}_j^{-1}cX\mathcal{V}_j = cX_j$ is a tensor product of $cX$ gates, the above state can also be rewritten as

$$p_{\text{out}}^{\text{trap}}(\{P\}) = \frac{1}{2} \sum_{t=0,1} \sum_{N_t \times \cdots \times N_{m-1}} \sum_{V_t, \ldots, V_{m-1}} P_m H^t \circ (c^{m-1}_{m-1} c X_j) V_j^{-1} P_j V_j \circ H^t P_m (\rho_{\text{trap}}^{\text{trap}}).$$  \hfill (B3)

Notice that each $cX_j$ carries an implicit dependency on $V_j$ (the orientation of the $cX$ gates depends on $V_j$, see Figure 3).

The probability that the trap outputs $\sigma = \emptyset$ is

$$\text{prob}(\sigma = \emptyset | \{P\}) = \langle + | + \rangle p_{\text{out}}^{\text{trap}}(\{P\}) | + \rangle^\otimes n. \hfill (B4)$$

To upper-bound this probability by $3/4$, we first consider ‘single-band’ collections of errors, namely collections $\{P\}$ such that $P_j \neq I$ for some $j_0 \in \{0, \ldots, m\}$ and $P_j = I$ for all other $j \neq j_0$. For these collections, we prove that the probability that the output of the trap is the correct one $\sigma = \emptyset$ is smaller than $1/2$:

$$\text{prob}(\sigma = \emptyset | \text{single - band collection}) \leq \frac{1}{2}. \hfill (B5)$$

We prove this in Statement 1.

Next, we consider ‘two-band’ collections of errors. We obtain

$$\text{prob}(\sigma = \emptyset | \text{two - band collection}) \leq \frac{3}{4}. \hfill (B6)$$

We prove this in Statement 2. To obtain this bound, we move the two errors towards each other (i.e. we commute them with all the gates in the middle) and subsequently merge them, rewriting them as a single Pauli operator. The resulting Pauli operator is the identity $I$ with probability $c$, or is a different operator with probability $1 - c$. In the former case, the errors have canceled out with each other, while in the latter they have reduced to a single-band error. Importantly, in Statement 2 we prove that $c \leq 1/2$. This yields

$$\text{prob}(\sigma = \emptyset | \text{two - band collection}) = (1 - c) \text{prob}(\sigma = \emptyset | \text{single - band collection}) + c \text{prob}(\sigma = \emptyset | \text{no error})$$

$$\leq \frac{1 - c}{2} + c = \frac{1}{2} + \frac{c}{2}, \hfill (B7)$$

where we used $\text{prob}(\sigma = \emptyset | \text{no error}) = 1$ and $\text{prob}(\sigma = \emptyset | \text{single - band collection}) \leq 1/2$. Maximizing over $c \in [0, 1/2]$, we find

$$\text{prob}(\sigma = \emptyset | \text{two - band collection}) \leq \max_{0 \leq c \leq \frac{1}{2}} \left( \frac{1}{2} + \frac{c}{2} \right) = \frac{3}{4}. \hfill (B8)$$

Finally, we generalise to collections affecting more than two bands. For three-band collections, we again move two of these Pauli operators towards each other and merge them. Doing this, the three-band collection reduces to a single-band one with probability $c \leq 1/2$ or to a two-band one with probability $1 - c$. Thus, using the above results, we have

$$\text{prob}(\sigma = \emptyset | \text{three - band collection}) = (1 - c) \text{prob}(\sigma = \emptyset | \text{two - band collection}) + c \text{prob}(\sigma = \emptyset | \text{single - band collection})$$

$$\leq \frac{3(1 - c)}{4} + \frac{c}{2} \leq \max_{0 \leq c \leq \frac{1}{2}} \left( \frac{3(1 - c)}{4} + \frac{c}{2} \right) = \frac{3}{4}. \hfill (B9)$$

This argument can be iterated: at any fixed $h$, if $\text{prob}(\sigma = \emptyset | (h - 2) - \text{band collection}) \leq 3/4$ and $\text{prob}(\sigma = \emptyset | (h - 1) - \text{band collection}) \leq 3/4$, then it can be easily shown that $\text{prob}(\sigma = \emptyset | h - \text{band collection}) \leq 3/4$. We now complete the proof by proving Statement 1 and Statement 2.

**Statement 1.** Single-band collections are defined as follows.

$$P_j = I \text{ for } j = j_0 \in \{0, \ldots, m\}, \quad P_j = I \text{ for all } j \neq j_0. \hfill (B10)$$

If $j_0 = 0$, using $cX|++\rangle = |++\rangle$ and $cX|00\rangle = |00\rangle$, we have

$$\text{prob}(\sigma = \emptyset | \{P\}) = \frac{1}{2} \sum_{t=0,1} \sum_{V_t} |+\rangle^{\otimes n} H^t \circ (c^{m}_{m} c X_j) V_j^{-1} P_j V_j \circ H^t P_m (|+\rangle^{\otimes n}) = 0,$$  \hfill (B11)

since $P_0 = I \in \{I, Z\}^{\otimes n}$, and the same happens for $P_m = I$.

If $1 \leq j_0 \leq m - 1$, we have

$$\text{prob}(\sigma = \emptyset | \{P\}) = \frac{1}{2} \sum_{V_t} \sum_{V_t} |+\rangle^{\otimes n} H^t \circ (|+\rangle^{\otimes n} P_m (|+\rangle^{\otimes n}) = 0 \quad \text{for all Pauli operators } P \text{ whose Pauli-Z component is non-trivial, therefore } \sum_{t} |+\rangle^{\otimes n} H^t P^2 H^t + |+\rangle^{\otimes n}/2 \leq 1/2 \text{ for all }$$  \hfill (B12)

where we used again that $cX|++\rangle = |++\rangle$ and $cX|00\rangle = |00\rangle$. Notice that $|+\rangle^{\otimes n} P + |+\rangle^{\otimes n} = 0$ for all Pauli operators $P$ whose Pauli-Z component is non-trivial, therefore $\sum_{t} (|+\rangle^{\otimes n} H^t P H^t + |+\rangle^{\otimes n})/2 \leq 1/2$ for all
\[ P \in \{ I, X, Y, Z \}^\otimes n / I. \] This yields
\[
\text{prob}(\bar{\sigma} = \emptyset|\{P_i\}) \leq \frac{1}{N} \sum_{i=0}^{1} \frac{1}{2} \leq \frac{1}{2},
\] (B13)
where we used that \( V^{-1}_b P_b V_b \) is a Pauli operator for any \( V_b \), as this \( V_b \) implements a gate from the set \( \{ H, S \}^\otimes n \).

**Statement 2.** Two-band collections are defined as follows:
\[
P_j = I \text{ for } j = j_1, \ldots, j_2, \quad P_j = I \text{ for all } j = j_1, j_2.
\] (B14)

We can distinguish four classes of two-band collections:

1. Errors in state preparation and entangling gates, i.e. \( j_1 = 0 \) and \( 1 \leq j_2 \leq m - 1 \).
2. Errors in entangling gates and measurements, i.e. \( 1 \leq j_1 \leq m - 1 \) and \( j_2 = m \).
3. Errors in two different entangling gates, i.e. \( 1 \leq j_1 < j_2 \leq m - 1 \).
4. Errors in state preparation and measurements, i.e. \( j_1 = 0 \) and \( j_2 = m \).

Errors in class 1 yield \( \bar{\sigma} = \emptyset \) with probability at most 3/4. To prove this, we start by rewriting this probability as
\[
\text{prob}(\bar{\sigma} = \emptyset|\{P_i\}) = \frac{1}{2} \sum_{t=0}^{1} \binom{n}{t} \sum_{(\mathcal{C}_x \ldots \mathcal{C}_z)} \left(1 + |{\mathcal{C}_x}^+| + |{\mathcal{C}_y}^+| + |{\mathcal{C}_z}^+|\right)^{t}\otimes n = \frac{1}{2} \sum_{t=0}^{1} \binom{n}{t} \sum_{(\mathcal{C}_x \ldots \mathcal{C}_z)} \left(1 + |{\mathcal{C}_x}^+| + |{\mathcal{C}_y}^+| + |{\mathcal{C}_z}^+|\right)^{t}\otimes n,
\] (B15)
where we used \( |{\mathcal{C}_x}^+| = |{\mathcal{C}_y}^+| = |{\mathcal{C}_z}^+| = |0\rangle \). We now start from the case \( j_2 = 1 \). We then note that (i) if \( n = 1 \) (single-qubit circuit), all \( P_1, P_2 \in \{ I, X, Y, Z \} \) and all \( P_0 = I \) lead to
\[
\text{prob}(\bar{\sigma} = \emptyset|\{P_i\}, 1 \text{ qubit}) = \frac{1}{2} \binom{n}{0} \sum_{(\mathcal{C}_x \ldots \mathcal{C}_z)} \left(1 + |{\mathcal{C}_x}^+| + |{\mathcal{C}_y}^+| + |{\mathcal{C}_z}^+|\right)^{0}\otimes n = \frac{3}{4},
\] (B16)
and (ii) if \( n = 2 \) (two-qubit circuit) and in band 1 the two qubits are connected by \( cZ \), all \( P_1, P_2 \in \{ I, X, Y, Z \} \) and all \( P_0 = I \) lead to
\[
\text{prob}(\bar{\sigma} = \emptyset|\{P_i\}, 2 \text{ qubits}) = \frac{1}{2} \binom{n}{0} \sum_{(\mathcal{C}_x \ldots \mathcal{C}_z)} \left(1 + |{\mathcal{C}_x}^+| + |{\mathcal{C}_y}^+| + |{\mathcal{C}_z}^+|\right)^{2}\otimes n = \frac{3}{4},
\] (B17)
The above inequalities for \( n = 1 \) and \( n = 2 \) can be proven using that \( H \) maps \( \{ X, Y, Z \} \) into \( \{ Z, Y, X \} \) under conjugation and \( S \) maps \( \{ Y, X, Z \} \) into \( \{ Y, X, Z \} \) under conjugation (apart from unimportant global phases). Extension to more than two qubits is as follows: If \( \text{prob}(\bar{\sigma} = \emptyset|\{P_i\}, n \text{ qubits}) \leq 3/4 \), then tensoring one more qubit yields
\[
\text{prob}(\bar{\sigma} = \emptyset|\{P_i\}, n + 1 \text{ qubits}) = \frac{1}{2} \binom{n}{0} \sum_{(\mathcal{C}_x \ldots \mathcal{C}_z)} \left(1 + |{\mathcal{C}_x}^+| + |{\mathcal{C}_y}^+| + |{\mathcal{C}_z}^+|\right)^{0}\otimes n = \frac{3}{4},
\] (B18)
where \( H_{1,\ldots,n+1}, V_{1,\ldots,n+1} P_{1,\ldots,n+1} \) are the components of \( H_{1,\ldots,n+1}, V_{1,\ldots,n+1} P_{1,\ldots,n+1} \) on qubits \( \{1,\ldots,n+1\} \) and \( H_{n+1,\ldots,n+1}, V_{n+1,\ldots,n+1} P_{n+1,\ldots,n+1} \) are the components acting on qubits \( n+1 \). Using that if \( A_0, B_0 \geq 0 \) and then \( \sum A_0 B_0 \leq \sum A \sum B \), we obtain
\[
\text{prob}(\bar{\sigma} = \emptyset|\{P_i\}, n + 1 \text{ qubits}) = \frac{1}{2} \binom{n}{0} \sum_{(\mathcal{C}_x \ldots \mathcal{C}_z)} \left(1 + |{\mathcal{C}_x}^+| + |{\mathcal{C}_y}^+| + |{\mathcal{C}_z}^+|\right)^{0}\otimes n = \frac{3}{4},
\] (B19)
Tensoring two qubits connected by \( cZ \) yields the same bound, and this concludes the proof by induction for \( j_2 = 1 \). If \( j_2 \in \{1,\ldots,m - 1\} \) the proof is similar, but the Pauli operator \( V^{-1}_b P_b \) must be commuted with \( C \mathcal{V}_x \ldots C \mathcal{V}_{j-1} \) (where we remember that each \( C \mathcal{V}_j \) depends on \( V_j \)).
Errors in class 1 yield $\sigma = \emptyset$ with probability at most $3/4$. This can be proven with the same arguments as for errors in class 1.

Errors in class 3 yield $\sigma = \emptyset$ with probability at most $3/4$. To see this, consider first the case where the errors affect neighboring bands ($j_2 = j_1 + 1$), which yields

$$\text{prob}(\sigma = \emptyset | \{P_i\}) = \frac{1}{2} \sum_{i=0,1} \sum_{j_{i+1}} \sum_{\vdots} prob(\sigma = \emptyset | \{P_i\}) \leq \frac{3}{4}$$

As for errors in class 1, this can be shown by proving that the bound holds for the single-qubit case and the two-qubit one, and then using induction. If the errors affect two non-neighboring bands ($j_2 \neq j_1 + 1$), we have

$$\text{prob}(\sigma = \emptyset | \{P_i\}) \leq \frac{3}{4}$$

To prove the inequality, one can commute $V^{-1}_j \cdot \mathcal{H} \cdot V_h$ (which is a Pauli operator) with the entangling operation and use the same arguments as for $j_1 = j_1 + 1$.

Finally, errors in class 4 yield

$$\text{prob}(\sigma = \emptyset | \{P_i\}) = \frac{1}{2} \sum_{i=0,1} \sum_{j_{i+1}} \sum_{\vdots} prob(\sigma = \emptyset | \{P_i\}) \leq \frac{1}{2}$$

To see this, consider first the case $t = 0$, and consider commuting $P_0 \in \{I, Z\}^\otimes n / I$ with all the gates in the circuit. Since all these gates are $cX$ gates with random orientation, the identities

$$cX (Z_t \otimes I_2) = (Z_t \otimes I_2) cX$$
$$cX (I_t \otimes Z_2) = (I_t \otimes Z_2) cX$$
$$cX (Z_t \otimes Z_2) = (I_t \otimes Z_2) cX$$

ensure that every time that a Pauli-$Z$ error is commuted with a $cX$, this error becomes another error, chosen at random from two possible ones—figure 9. This can be used to prove that if $t = 0$, errors in class 4 are detected with probability larger than $1/2$. The same considerations apply to the case $t = 1$, where the identities

$$cX (X_t \otimes I_2) = (X_t \otimes X_2) cX$$
$$cX (I_t \otimes X_2) = (I_t \otimes X_2) cX$$
$$cX (X_t \otimes X_2) = (X_t \otimes I_2) cX$$

must be used instead of identities B23.

\hfill \Box

### Appendix C. Proof of theorem 1

We start by using lemma 1 (appendix 1), which allows to reduce noise of the type N1 to classically single-qubit Pauli errors and to write the joint state of target and trap circuits after all circuits have been implemented (for a fixed choice of single-qubit gates $U^{(k)}$) as

$$\rho_{out}(U^{(1)}, \ldots, U^{(m+1)}) = \sum_{\rho} \rho \sum_{P^{(1)}, \ldots, P^{(m+1)}} prob(P^{(1)}, \ldots, P^{(m+1)}) \frac{2^{n+1}}{2^{n+1}}$$

$$\otimes_{k=1}^{n+1} \rho Z_{i}^{(k)} \rho Z_{i}^{(k)}$$

where $\rho_{out} = \otimes_{i=1}^{n} (|+\rangle \langle +|)$, $Z^{(k)} = (s_1^{(k)}, \ldots, s_n^{(k)})$ is the output of the $k$th circuit, $Z^{(k)}(\rho) = \otimes_{i=1}^{n} Z_{i}^{(k)} \rho Z_{i}^{(k)}$, and

$$prob(P^{(1)}, \ldots, P^{(m+1)})$$

is the joint probability of a collection of Pauli errors $(P^{(1)}, \ldots, P^{(m+1)})$ affecting the system.
with \( P^{(k)}_1, \ldots, P^{(k)}_{m-1} \in \{ I, X, Y, Z \} \otimes n \) and \( P^{(k)}_0, P^{(k)}_m \in \{ I, Z \} \otimes n \) for all \( k \). Summing over all possible choices of single-qubit gates we thus obtain the state of target and traps at the end of the protocol:

\[
\rho_{\text{out}} = \sum_{U_1^{(1)}, \ldots, U_{m+1}^{(1)}} \text{prob}(U_1^{(1)}, \ldots, U_{m+1}^{(1)}) \rho_{\text{out}}(U_1^{(1)}, \ldots, U_{m+1}^{(1)})
\]

\[
= \sum_{U_1^{(1)}, \ldots, U_{m+1}^{(1)}} \text{prob}(U_1^{(1)}, \ldots, U_{m+1}^{(1)}) \sum_{p^{(1)}, \ldots, p^{(m+1)}} \text{prob}(p^{(1)}, \ldots, p^{(m+1)}) \sum_{k=1}^{m+1} \frac{1}{2^{m+1}} \rho_{\text{out}}(U_1^{(1)}, \ldots, U_{m+1}^{(1)})
\]

\[
\times \bigotimes_{k=1}^{m+1} \langle + | | Z^{p^{(k)}} | \rangle \rho_{\text{out}}(U_1^{(1)}, \ldots, U_{m+1}^{(1)}) \rho_{\text{out}}(p^{(k)})(\rho_{\text{in}}) | + \rangle \langle + | Z^{p^{(k)}} | \rangle^*.
\]

(C2)

where \( \text{prob}(U_1^{(1)}, \ldots, U_{m+1}^{(1)}) \) is the probability of single-qubit gates \( U_1^{(1)}, \ldots, U_{m+1}^{(1)} \) being chosen. Crucially, notice that the probability associated to each collection of Pauli errors does not depend on the specific choice of single-qubit gates \( U_1^{(1)} \). We can thus rewrite the above state as

\[
\rho_{\text{out}} = \sum_{p^{(1)}, \ldots, p^{(m+1)}} \text{prob}(p^{(1)}, \ldots, p^{(m+1)}) \sum_{k=1}^{m+1} \frac{1}{2^{m+1}} \rho_{\text{out}}(U_1^{(1)}, \ldots, U_{m+1}^{(1)})
\]

\[
\times \bigotimes_{k=1}^{m+1} \langle + | | Z^{p^{(k)}} | \rangle \rho_{\text{out}}(U_1^{(1)}, \ldots, U_{m+1}^{(1)}) \rho_{\text{out}}(p^{(k)})(\rho_{\text{in}}) | + \rangle \langle + | Z^{p^{(k)}} | \rangle^*.
\]

(C3)

Consider now the state

\[
\rho_{\text{out}}(P^{(1)}_1, \ldots, P^{(m+1)}_m) = \sum_{p^{(1)}, \ldots, p^{(m+1)}} \frac{1}{2^{m+1}} \rho_{\text{out}}(U_1^{(1)}, \ldots, U_{m+1}^{(1)})
\]

\[
\times \bigotimes_{k=1}^{m+1} \langle + | | Z^{p^{(k)}} | \rangle \rho_{\text{out}}(U_1^{(1)}, \ldots, U_{m+1}^{(1)}) \rho_{\text{out}}(p^{(k)})(\rho_{\text{in}}) | + \rangle \langle + | Z^{p^{(k)}} | \rangle^*.
\]

(C4)

corresponding to a fixed collection of Pauli errors \( \{ P^{(1)}_1, \ldots, P^{(m+1)}_m \} \) and assume that the Pauli errors only affect \( \tilde{v} \in \{ 1, \ldots, v + 1 \} \) circuits. In this case, as the Pauli errors do not depend on the single-qubit gates, they do not depend on the random number \( v_0 \) (which labels the position of the target circuit) nor on the random parameters in the trap circuits. Therefore, summing over all choices of single-qubit gates, the probability that the target is among the \( \tilde{v} \) circuits affected by noise is \( \tilde{v}/(v + 1) \).

Next using lemma 2 (which states that summed over all possible choices of single-qubit gates, trap circuits output \( \tilde{v} = 0 \) with probability at most \( 3/4 \)) we have that if \( \tilde{v} = 1 \) trap circuits are affected by noise, they all output \( \tilde{v} = 0 \) with probability at most \( (3/4)^{v-1} \). We thus obtain

\[
\rho_{\text{out}}(P^{(1)}_1, \ldots, P^{(m+1)}_m) = b \tau_{\text{out}} \otimes \sigma_{\text{trap}} \otimes (1 - b)(\tilde{l} \sigma_{\text{out}} \otimes \sigma_{\text{trap}} + (1 - \tilde{l}) \tau_{\text{out}} \otimes \sigma_{\text{trap}}),
\]

(C5)
where \( \tau_{\text{out}} (\sigma_{\text{out}}) \) is the state of the target circuit if the target computation is (is not) among the \( \tilde{v} \) traps affected by noise, \( \tau_{\text{out}} \) is an arbitrary state for the target, \( \sigma_{\text{trap}} \) is the state of the traps when they all output \( \tilde{v} = 0 \), \( \sigma_{\text{trap}} \) is an arbitrary state for the traps orthogonal to \( \sigma_{\text{trap}} \), \( 0 \leq \tilde{v} \leq 1 \) and

\[
\tilde{b} \leq \frac{\tilde{v}}{v + 1} (\frac{3}{4})^{v-1}.
\]

(C6)

For all \( v \geq 3 \) the RHS of the above upper-bound on \( \tilde{b} \) is maximized by \( \tilde{v} = 3 \), which yields

\[
\tilde{b} \leq \frac{\tilde{v}}{v + 1} (\frac{3}{4})^{v-1} = \frac{3}{v + 1} (\frac{3}{4})^{v-1} = \frac{\kappa}{v + 1}, \text{ with } \kappa \approx 1.7
\]

(C7)
As this is true for all collections of Pauli errors \( \{ P^{(1)}_1, \ldots, P^{(m+1)}_m \} \), we can sum over all possible collections (weighted by their respective probabilities) and obtain

\[
\rho_{\text{out}} = \sum_{P^{(1)}_1, \ldots, P^{(m+1)}_m} \text{prob}(P^{(1)}_1, \ldots, P^{(m+1)}_m) \rho_{\text{out}}(P^{(1)}_1, \ldots, P^{(m+1)}_m)
\]

\[
= b \tau_{\text{out}} \otimes \sigma_{\text{trap}} + (1 - b)(l \sigma_{\text{out}} \otimes \sigma_{\text{trap}} + (1 - l) \tau_{\text{out}} \otimes \sigma_{\text{trap}})
\]

(C8)
with \( b \leq \kappa/(v + 1) \) and \( 0 \leq l \leq 1 \). The theorem is finally proven by replacing the state of the traps with that of the flag bit, namely by replacing \( \sigma_{\text{trap}} \) with \( | \text{acc} \rangle \langle \text{acc} | \) and \( \sigma_{\text{trap}} \) with \( | \text{rej} \rangle \langle \text{rej} | \).

Appendix D. Mesothetic verification protocol

D.1. Background

We will now define the notions of protocol and verifiability in the cryptographic setting. We start by defining quantum states as states belonging to the Hilbert space \( \mathbb{H}_{ABC} = \mathbb{H}_A \otimes \mathbb{H}_B \otimes \mathbb{H}_C \), where \( \mathbb{H}_A \) and \( \mathbb{H}_B \) are Alice's
and Bob’s registers and $\mathbb{H}_C$ is a common register used to exchange qubits. The definition of protocol is as follows:

**Definition 3 (Protocol).** We define a $q$-step protocol on input $\rho_{in} \in L(\mathbb{H}_{ABC})$ as a series of CPTP maps $\{\mathcal{E}^{(p)}_{ABC}\}_{p=1}^q = \{\mathcal{E}^{(p)}_{ABC} \circ \mathcal{E}^{(p+1)}_{ABC}\}_{p=1}^q$ yielding the state $\rho_{out} = \mathcal{E}^{(q)}_{ABC}(\rho_{in})$.

In our protocol Alice verifies the correct implementation of the target circuit through the trap circuits. If Bob is honest, all these circuits output a default output and the flag bit is set to $\{acc\} \in \{\{0\}, \{1\}\}$, otherwise it is likely that they return an incorrect output and the flag bit is set to $\{\text{rej}\} = \{\text{acc} \oplus 1\}$. We thus define verifiability as follows:

**Definition 4 (Verifiability).** Consider a protocol $\{\mathcal{E}^{(p)}_{ABC}\}_{p=1}^q$ on input $\rho_{in}$. The protocol is ‘$\delta_{cr}$-complete’ if

$$D(\text{Tr}_{BC}[\mathcal{E}^{(p)}_{ABC}(\rho_{in})], \sigma_{\text{out}} \otimes [\text{acc}]\langle \text{acc} \rangle) \leq 1 - \delta_{cr},$$

where $\sigma_{\text{out}}$ is the correct state of the target at the end of the protocol, $[\text{acc}]\langle \text{acc} \rangle$ is the state of the state of the traps indicating acceptance and $0 \leq \delta_{cr} \leq 1$ is called ‘completeness’.

The protocol is ‘$\varepsilon_{cr}$-sound’ if for any set of CPTP maps $\{\mathcal{E}^{(p)}_{ABC}\}_{p=1}^q$ acting on Bob’s register and on the channel, the state in Alice’s register at the end of the protocol is

$$D(\text{Tr}_{BC}[\mathcal{E}^{(p)}_{ABC} \circ \mathcal{E}^{(p)}_{ABC}(\rho_{in})], I \sigma_{\text{out}} \otimes [\text{acc}]\langle \text{acc} \rangle + (1-I)\sigma_{\text{out}} \otimes \{\text{rej}\}\langle \text{rej} \rangle) \leq \varepsilon_{cr},$$

where $0 \leq \varepsilon_{cr} \leq 1$ is called ‘soundness’, $0 \leq I \leq 1$, $\sigma_{\text{out}}$ is an arbitrary state and $\{\text{rej}\}$ is orthogonal to $[\text{acc}]$.

We thus model Bob’s deviations as arbitrary CPTP maps acting on the system being processed and on Bob’s private register.

We can now explain how to translate our protocol in the cryptographic setting.

**D.2. Mesothetic protocol**

We assume that Alice, the verifier, and Bob, the prover, want to implement interactively a circuit of the type of figure 2, with $n$ qubits, depth $m$ and single-qubit gates $\{U_{i,j}\}$. We start by defining the resources required by Alice:

- Alice’s resources: a device that can receive $n$ qubits from Bob, apply a single-qubit gate to each of them and send the qubits back to Bob. Alice’s device must be able to execute all single-qubit gates contained in the target circuit, together with $X$, $Y$, $Z$ (used for the QOTP). We assume that the overall number of these gates is $G$.

For the protocol to always accept, Bob must possess a quantum computer that can prepare qubits in the $|+\rangle$ state, execute $cZ$ gates, measure in the Pauli-X basis and store qubits.

We can now explain our protocol, which is formally presented in Box 4 in the Methods. Our protocol takes as input a description of the target circuit and the number $\nu$ of trap circuits, both inputs also being known to Bob. Steps 1, 2 and 3 are classical and solely involve Alice’s register, which at the end of step 3 (for a fixed choice of single-qubit gates $U_1^{(1)} \ldots U_m^{(m+1)}$) is in the state

$$\rho_{A_{in}}(U_1^{(1)} \ldots U_m^{(m+1)}) = [\otimes_{k=1}^{\nu+1} \otimes_{j=1}^m \otimes_{i=1}^n |U_{i,j}^{(k)}\rangle \langle U_{i,j}^{(k)}|] \otimes [\otimes_{k=1}^{\nu+1} \otimes_{i=1}^n |\alpha_{i,m}^{(k)}\rangle \langle \alpha_{i,m}^{(k)}|].$$

(D3)

Here, $|U_{i,j}^{(k)}\rangle$ is a classical description of the gate $U_{i,j}^{(k)}$. Since Alice’s device can execute $G$ different gates, this classical description requires at most $\log_2(G)$ bits. Moreover, $|\alpha_{i,m}^{(k)}\rangle$ is a classical description of the bit $\alpha_{i,m}^{(k)}$.

In step 4 Alice and Bob interact as follows. For all circuits $k = 1, \ldots, \nu + 1$, in step 4.1 Bob creates $n$ qubits in the state $|+\rangle$. Then, for each band $j = 1, \ldots, m$, Bob sends all the qubits to Alice, Alice acts on each qubit $i$ with $U_{i,j}^{(k)}$, Alice sends all the qubits back to Bob and Bob implements $cZ_j$. Finally, in step 4.3 Bob measures all the qubits and returns the outputs to Alice. The random but $\alpha_{i,m}^{(k)}$ coming from the QOTP in the last round of single-qubit gates bit-flips the outputs, therefore Bob transmits to Alice the bits $s_{i,j}^{(k)} \oplus \alpha_{i,m}^{(k)}$. Alice post-processes those bits and obtains $s_{i,j}^{(k)}$. At the end of step 4, the states in Alice and Bob’s registers (if Bob is honest) are of the form

$$\rho_{A_{out}}(U_1^{(1)} \ldots U_m^{(m+1)}) = [\otimes_{k=1}^{\nu+1} \otimes_{j=1}^m \otimes_{i=1}^n |U_{i,j}^{(k)}\rangle \langle U_{i,j}^{(k)}|] \otimes [\otimes_{k=1}^{\nu+1} \otimes_{i=1}^n |s_{i,j}^{(k)}\rangle \langle s_{i,j}^{(k)}|]$$

$$\rho_{B_{out}} = \otimes_{j=1}^m \otimes_{i=1}^n Z_{i,j}^{s_{i,j}^{(k)} \oplus \alpha_{i,m}^{(k)}}.$$ (D4)

The protocol ends with Alice checking whether the traps yield the correct output and setting the flag bit to $[\text{acc}]$ or $[\text{rej}]$. 

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We will now prove completeness and soundness for our mesothetic protocol (we refer the reader to the beginning of the appendix for notation). We begin with the assumption that Alice’s device is noiseless. Soundness requires the following Lemmas:

Lemma D1. Suppose that Alice’s device is noiseless. For a fixed choice of single-qubit gates $U_{ij}^{(1)}\ldots U_{m}^{(v+1)}$ by Alice, tracing out Bob’s private register and summing over all the random bits $\alpha_{ij}^{(k)}, \alpha_{ij}^{(k)}$, except $\lambda_{m}^{(k)}$ (see Routine 1), the state in Bob’s register at the end of the protocol is of the form

$$\rho_{\text{B, out}}(U_{1}^{(1)}\ldots U_{m}^{(v+1)}) = \sum_{\mathcal{P}_{1}^{(1)}\ldots \mathcal{P}_{m}^{(v+1)}} \frac{\text{prob}(\mathcal{P}_{1}^{(1)}\ldots \mathcal{P}_{m}^{(v+1)})}{2^{m(v+1)}}$$

$$\otimes_{k=1}^{v+1} \left([\mathcal{Z}_{j}^{k} U_{m}^{(k)} \mathcal{U}_{j}^{(k)}]^{m-1} \mathcal{Z}_{f}^{k}) U_{j}^{(k)} \circ \mathcal{P}_{0}^{(k)}(\rho_{in})] + \gamma_{0}^{(k)} \times (\mathcal{Z}_{i}^{k} U_{i}^{(k)} \mathcal{U}_{i}^{(k)})^{m} + \gamma_{1}^{(k)} \times (\mathcal{Z}_{i}^{k} U_{i}^{(k)} \mathcal{U}_{i}^{(k)})^{m}, \right) \tag{D5}$$

where $\rho_{in} = \otimes_{j=1}^{n} [\mathcal{Z}_{j}^{k} \mathcal{P}_{j}^{(k)}]^{j}$ is Bob’s deviations when in step 4.2 he should execute $\mathcal{Z}_{j}^{k}$, $\mathcal{P}_{j}^{(k)}$ is the output of the $k$th circuit, $Z^{k}(\rho) = \mathcal{Z}_{j}^{k} \mathcal{P}_{j}^{(k)} \mathcal{Z}_{j}^{k}$ and $\text{prob}(\mathcal{P}_{1}^{(1)}\ldots \mathcal{P}_{m}^{(v+1)})$ is the joint probability of a collection of Pauli errors $\mathcal{P}_{1}^{(1)}\ldots \mathcal{P}_{m}^{(v+1)}$ affecting the system, with $\mathcal{P}_{1}^{(k)}\ldots \mathcal{P}_{m}^{(k)} \in \{I, \mathcal{X}, \mathcal{Y}, \mathcal{Z}\}^{n}$ and $\mathcal{P}_{0}^{(k)} = \{I, \mathcal{Z}\}$ for all $k$.

Proof. (Sketch) We start proving the lemma for a protocol with a single circuit ($v = 1$), next we generalize to $v > 1$. For a fixed choice of gates $U_{ij}^{(1)}$ by Alice, the joint state of Alice’s register $A$ and Bob’s register $B$ before the final measurements is of the form

$$\rho_{AB}^{(1)}(U_{1}^{(1)}\ldots U_{m}^{(v+1)}) = \left[\otimes_{j=1}^{n} \mathcal{P}_{j}^{(1)} \mathcal{U}_{j}^{(1)}\right] \otimes \left[\otimes_{k=1}^{v+1} \mathcal{P}_{k}^{(1)} \mathcal{U}_{k}^{(1)}\right] \otimes \left[\alpha_{i,j}^{(1)} \mathcal{U}_{i}^{(1)}\right] \otimes \left(\alpha_{i,j}^{(1)} \mathcal{U}_{i}^{(1)}\right) \otimes (\text{Alice’s register}) \tag{D6}$$

where $\mathcal{E}_{0}$ is Bob’s deviations. Specifically, $\mathcal{E}_{0}$ is Bob’s deviations when he prepares $\rho_{in}$ (step 4.1), for all $j = 1, \ldots, m - 1$, $\mathcal{E}_{j}$ is Bob’s deviations when in step 4.2 he should execute $\mathcal{Z}_{j} F_{j} \mathcal{Z}_{j}^{-1}$, $\mathcal{E}_{m}$ is Bob’s deviation before he measures the qubits (step 4.3). Without loss of generality, suppose now that Bob holds another register, which we call ‘ancillary register’ and denote with $B_{\text{anc}}$. Tensoring $\rho_{in}$ with the state $\sigma_{\text{anc}}$, in the register $B_{\text{anc}}$, for $j \in \{1, \ldots, m - 1\}$, we can rewrite Bob’s deviations $\mathcal{E}_{j}$ as unitaries $F_{j} \mathcal{Z}_{j}^{-1} E$, where $F_{j}$ is a unitary matrix that acts on Bob’s register $B$ and on $B_{\text{anc}}$—for convenience we indicate unitary matrices acting both on Bob’s register $B$ and on $B_{\text{anc}}$ in bold font. Similarly, we can replace $\mathcal{E}_{0}$ with the unitary $R$ and $\mathcal{E}_{m}$ with the unitary $M$. We thus obtain

$$\rho_{\text{ABB}}^{(1)}(U_{1}^{(1)}\ldots U_{m}^{(v+1)}) = \left[\otimes_{j=1}^{n} \mathcal{P}_{j}^{(1)} \mathcal{U}_{j}^{(1)}\right] \otimes \left[\otimes_{k=1}^{v+1} \mathcal{P}_{k}^{(1)} \mathcal{U}_{k}^{(1)}\right] \otimes \left[\alpha_{i,j}^{(1)} \mathcal{U}_{i}^{(1)}\right] \otimes \left(\alpha_{i,j}^{(1)} \mathcal{U}_{i}^{(1)}\right) \otimes (\text{Alice’s register}) \tag{D7}$$

Tracing out Alice’s register and $B_{\text{anc}}$ yields

$$\text{Tr}_{\text{AB}}[\rho_{\text{ABB}}^{(1)}(U_{1}^{(1)}\ldots U_{m}^{(v+1)})] = \text{Tr}_{B_{\text{anc}}}[M U_{m}^H F_{m-1}^H \mathcal{Z}_{m-1} U_{m-1}^H \ldots F_{j} \mathcal{Z}_{j} U_{j}^H R(\rho_{in} \otimes \sigma_{\text{anc}}) R^H U_{j}^H \mathcal{Z}_{j} F_{j} \ldots U_{m}^H \mathcal{Z}_{m} F_{m-1} U_{m-1}^H M^H \otimes M]. \tag{D8}$$

This state is equal to the state $\rho_{\text{B}}$ in equation (A4), provided that we replace $\sigma_{\text{anc}}$ with $\rho_{B_{\text{anc}}}$, with $\text{Tr}_{B_{\text{anc}}}[\mathcal{E}]$, hence the lemma can be proven repeating the same calculations. The reader can verify that the same applies to the case $v > 1$.

Lemma D2. For any collection of Pauli errors affecting a trap circuit, summing over all possible single-qubit gates in the trap circuit (i.e. over all possible sets $\{U_{ij}\}$ output by Routine 2), the probability that the trap circuit outputs $s = 0$ is at most $3/4$.

As Alice chose all the gates in a trap circuit with Routine 2, the proof is the same as that of Lemma 2. Using these two lemmas we now compute the classical state in Alice’s register after all the circuits have been implemented:

Lemma D3. Suppose that Alice’s device is noiseless. Then, for any number $v \geq 3$ of trap computations, the state in Alice’s register at the end of the protocol is of the form (see definition 4 for notation)

$$\rho_{\text{out}} = b \tau_{\text{out}} \otimes (\text{acc}) \otimes (\text{acc}) + (1 - b) \tau_{\text{out}} \otimes (\text{acc}) + (1 - b) \tau_{\text{out}} \otimes (\text{rej}) \otimes (\text{rej}), \tag{D9}$$

where $\tau_{\text{out}}$ is an incorrect state for the target, $0 \leq b \leq \kappa/(v + 1)$ and $\kappa = (3/4)^2 \approx 1.7$.

Proof. After Bob has implemented all the circuits and communicates all the outputs to Alice, Alice holds the same classical state as Bob (equation (D3)). After Alice post-processes the outputs this state becomes (see Lemma D1
for notation)

\[
\rho_{\text{out}}(\mathcal{U}_1^{(1)}, \ldots, \mathcal{U}_m^{(v+1)}) = \sum_{\sigma_1^{(1)}, \ldots, \sigma_{v+1}^{(v+1)}} \frac{\text{prob}(\mathcal{P}_1^{(1)}, \ldots, \mathcal{P}^{(v+1)}_{m})}{2^{m(v+1)}}
\]

\[
\otimes \left( + |\psi^m \rangle \langle \psi^m | (\mathcal{CZ} P_j^{(k)} \mathcal{U}_m^{(v)} \sigma_j^{(v)} + (\mathcal{CZ} P_j^{(k)} \mathcal{U}_m^{(v)} \sigma_j^{(v)})) \otimes \mathcal{P}_m^{(k)}(\rho_m) \right) |+\rangle^\otimes \times (\otimes Z^j |+\rangle)_{j = 1}^{v+1}.
\] (D10)

Crucially, notice that the probability associated to each deviation \( \mathcal{P}_1^{(1)}, \ldots, \mathcal{P}^{(v+1)}_{m} \) does not depend on the choice of single-qubit gates made by Alice. Thus, summing over all choices of single-qubit gates, this state becomes

\[
\rho_{\text{out}} = \sum_{\sigma_1^{(1)}, \ldots, \sigma_{v+1}^{(v+1)}} \frac{\text{prob}(\mathcal{P}_1^{(1)}, \ldots, \mathcal{P}^{(v+1)}_{m})}{2^{m(v+1)}} \sum_{\mathcal{U}_1^{(1)}, \ldots, \mathcal{U}_m^{(v+1)}} \text{prob}(\mathcal{U}_1^{(1)}, \ldots, \mathcal{U}_m^{(v+1)})
\]

\[
\otimes \left( + |\psi^m \rangle \langle \psi^m | (\mathcal{CZ} P_j^{(k)} \mathcal{U}_m^{(v)} \sigma_j^{(v)} + (\mathcal{CZ} P_j^{(k)} \mathcal{U}_m^{(v)} \sigma_j^{(v)})) \otimes \mathcal{P}_m^{(k)}(\rho_m) \right) |+\rangle^\otimes \times (\otimes Z^j |+\rangle)_{j = 1}^{v+1}.
\] (D11)

The lemma can now be proven following the same steps as in the proof of theorem 1.

Using these three lemmas we can now calculate completeness and soundness for the mesothetic protocol and obtain theorem D1:

**Theorem D1.** Suppose that Alice’s device is noiseless. Then, for any number \( v \geq 3 \) of trap circuits, the mesothetic protocol is verifiable with

\[
\delta_\text{cr} = 1 \text{ and } \varepsilon_\text{cr} = \frac{\kappa}{v + 1},
\]

where \( \kappa = 3(3/4)^2 \approx 1.7 \).

**Proof.** Completeness can be proven trivially showing that for all circuits, the random Pauli gates used for the QOTP cancel between them, and that the effects of the QOTP in the last band are recovered by the classical post-processing made by Alice. Therefore, all the circuits implement the desired computation, and a correctly implemented protocol yields the correct output for the target and the state of the traps indicating acceptance.

To prove soundness we need to compute the trace distance in equation (D2). Using lemma D3 this trace distance becomes

\[
D(\mathcal{U}_1^{\text{tar}} \otimes |\text{acc} \rangle \langle \text{acc}| + (1 - b) I \mathcal{U}_1^{\text{tar}} \otimes |\text{acc} \rangle \langle \text{acc}| + (1 - l) \mathcal{U}_1^{\text{tar}} \otimes |\text{rej} \rangle \langle \text{rej}|, I \mathcal{U}_1^{\text{tar}} \otimes |\text{acc} \rangle \langle \text{acc}| + (1 - l) \mathcal{U}_1^{\text{tar}} \otimes |\text{rej} \rangle \langle \text{rej}|) \leq b
\] (D13)

and since \( b \leq \kappa/(v + 1) \) by lemma D3, we obtain soundness \( \varepsilon_\text{cr} = \kappa/(v + 1) \).

We can also compute completeness and soundness in the case where Alice’s device suffers bounded and potentially gate-dependent noise:

**Theorem D2.** Suppose that Alice’s device is affected by bounded noise, i.e. that for all circuits \( k \in \{1, \ldots, v + 1\} \) and bands \( j \in \{1, \ldots, m\} \) she applies \( E_j^{(k)} U_j^{(k)} \), where \( E_j^{(k)} = (1 - r_j^{(k)}) I + r_j^{(k)} \mathcal{E}_j^{(k)} \) for some arbitrary CPTP-map \( \mathcal{E}_j^{(k)} \) and number \( 0 \leq r_j^{(k)} < 1 \). Then, for any number \( v \geq 3 \) of trap computations, the mesothetic protocol is verifiable with

\[
\delta_\text{cr} = 1 \text{ and } \varepsilon_\text{cr} = g \frac{\kappa}{v + 1} + 1 - g,
\]

where \( \kappa = 3(3/4)^2 \approx 1.7 \), \( g = \prod_{k,j} 1 - r_j^{(k)} \), and \( r_j^{(k)} \) is the maximum error rate of the round of gates in band \( j \) of circuit \( k \), where this maximum is taken over all choices gates for this round.

**Proof.** The proof of completeness is the same as for theorem D1. To compute soundness, we denote as \( \rho_\text{out}' \) the state in Alice’s register at the end of a protocol run when Alice’s device is noisy, and as \( \rho_\text{out} \) the state in Alice’s register at the end of a protocol run when Alice’s device is noiseless (Lemma D3). Indicating as \( r_j^{(k)} \) the maximum error rate for gates in band \( j \), we rewrite this noisy map as \( \mathcal{E}_j^{(k)} = (1 - r_j^{(k)}) I + r_j^{(k)} \mathcal{E}_j^{(k)} \) for some other CPTP map \( \mathcal{E}_j^{(k)} \). This allows to rewrite the classical state in Alice’s register at the end of the protocol as \( g\rho_\text{out} + (1 - g)\rho_\text{out}' \) and to obtain the upper-bound the trace distance.

\[ \square \]
We conclude this appendix by showing how our protocol can be made blind. Blindness is a property exhibited by many cryptographic protocols [25] defined as follows:

**Definition 5 (Blindness).** A protocol \( \{E_{\text{ABC}}^{(p)}\} = \{E_{\text{AB}}^{(p)} \otimes E_{\text{BC}}^{(p)}\} \) on input \( \rho_{\text{in}} \) is blind if for any set of maps \( \{E_{\text{BC}}^{(p)}\} \) acting on Bob’s register and on the channel, the state in Bob’s register at the end of the protocol leaks at most a constant function of the input.

Typically blind cryptographic protocols leak an upper-bound on the size of the target circuit.

As introduced above, our mesothetic protocol is not blind. Indeed, Bob has access to non-trivial information about the target circuit, such as the position of \( \text{cZ} \) gates. This is not a problem for our scopes, as verifiability only relies on Bob’s ignorance of the number \( n_v \) which is kept secret by Alice with the QOTP. Nevertheless, blindness may be required for maintaining the privacy of the users in future scenarios of delegated computations, thus it is important to understand whether our protocol can be turned into a blind protocol.

To obtain blindness, Alice (endowed with an ideal device) must recompile the target circuit into a normal form, for instance by recompiling the target circuit into a circuit of the type illustrated in figure 6 (which inspired to Brickwork States [52]). Then, instead of giving as input to the mesothetic protocol a classical description of the target circuit, she only needs to input the desired size of the circuit in normal form \( (n \text{ qubits, depth } m) \).

Implementing target and traps on a circuit in normal form makes Alice’s instructions to Bob independent from the specific target circuit that Alice wishes to verify. This allows to prove blindness:

**Theorem D3 (Blindness with Circuit in Normal Form).** Suppose that Alice can apply noiseless single-qubit gates. If Alice rewrites the target circuit in normal form, the mesothetic protocol (with input the desired size of the circuit in normal form) only leaks an upper-bound on the size of the target circuit.

**Proof.** To prove blindness we notice that during the protocol run Bob cannot retrieve any information about the target circuit. Indeed, Bob’s tasks are the same for all circuits (prepare the same input state, execute the same entangling gates and measure in the same basis) and these tasks do not depend on the target circuit, since this target is implemented on a circuit in normal form. Moreover, the only type of information that Bob receives from Alice during the implementation of the circuits are the qubits at step 4.2.1, but the QOTP prevents Bob from retrieving useful information: at any \( k = 1, \ldots, v + 1 \) and \( j = 1, \ldots, m \), if Bob sends to Alice a state \( |j^{(k)}\rangle \), Alice returns to him the state

\[
U_j^{(k)} \rho_j^{(k)} U_j^{(k)\dagger} = \mathcal{Q}_p \mathcal{Z}_j \mathcal{X}_j |j^{(k)}\rangle \langle j^{(k)}| \mathcal{Z}^{-1}_j \mathcal{X}^{-1}_j ,
\]

where \( P_j \) is the Pauli operator that undoes previous QOTP. Summing over all possible \( \alpha_{ij}^{(k)} \) and \( \alpha_{ij}^{(k)\dagger} \) yields the completely mixed state.

\[ \square \]

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