Quantum Memristors with Superconducting Circuits

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Memristors are resistive elements retaining information of their past dynamics. They have garnered substantial interest due to their potential for representing a paradigm change in electronics, information processing and unconventional computing. Given the advent of quantum technologies, a design for a quantum memristor with superconducting circuits may be envisaged. Along these lines, we introduce such a quantum device whose memristive behavior arises from quasiparticle-induced tunneling when supercurrents are cancelled. For realistic parameters, we find that the relevant hysteretic behavior may be observed using current state-of-the-art measurements of the phase-driven tunneling current. Finally, we develop suitable methods to quantify memory retention in the system.

Circuit elements that intrinsically carry a recollection of their past evolution\textsuperscript{1–3} promise to bring forth novel architectural solutions in information processing and unconventional computing\textsuperscript{4} due to their passive storage capabilities. These history-dependent circuit elements can be both dissipative and non-dissipative, such as mem-capacitors and meminductors\textsuperscript{2,5}, or just dissipative, such as memristors. Classical memristors\textsuperscript{6–9} are elements whose operational definition relates the voltage $V$ and the current $I$, complemented with an update of one or more internal variables $x$ carrying information of the electrical history of the system. For a voltage-controlled memristor

$$I(t) = G[x(t), V(t), t] V(t),$$
$$\dot{x}(t) = f[x(t), V(t), t].$$

The memductance (memory conductance) $G$ depends on both the instantaneous input voltage $V$ and the state variable $x$, which tracks the past memristor configuration via the update function $f$. Such dynamics leads to the characteristic pinched hysteresis loops under periodic driving\textsuperscript{6,7,10,11}, a strictly non-linear conductive effect showcasing zero-energy information storage\textsuperscript{1}.

Even though both the quantization of superconducting circuits\textsuperscript{12} and applications of memristors are well established techniques, memristive operation in the realm of quantum dynamics is a largely unexplored area. From an intuitive point of view, the combination of powerful memristive concepts with quantum resources, such as superposition and entanglement, promises groundbreaking advances in information and communication sciences. With this motivation in mind, the idea of a quantum memristor was recently defined in ref. 13 by introducing the fundamental components for engineering memristive behavior in quantum systems. However, superconducting circuits naturally include memristive elements in Josephson junctions, a feature exploited in a recently proposed classical superconducting memristor design\textsuperscript{14}. While this conductance asymmetric superconducting quantum interference device (CA-SQUID) design was able to produce hysteretic behavior\textsuperscript{14}, it did not include the quantum features of the circuit, including the dissipative origins of the memory or its measurement and quantification. These features are of utmost importance, as the operation of the design is based on quasiparticle tunneling, whose control and measurement have recently seen significant strides forward\textsuperscript{15,16}. Indeed, to our knowledge, up to now no experimental work has studied the hysteretic IV-characteristics of such systems. In our opinion, the reasons for this are two-fold, namely, 1) the pinched hysteresis loops were only recently predicted to exist for such systems in ref. 14 with the use of the aforementioned CA-SQUID and a proper selection...
of parameters, and 2) the experimental apparatus required to control and measure quasiparticle excitations with high accuracy is just beginning to emerge (see refs 15 and 16).

In this Article, we show that a suitably designed superconducting quantum circuit element with an external phase bias serves as a prototypical quantum memristor via low-energy quasiparticle tunneling. To this end, we describe the device in a fully quantum-mechanical fashion. We apply an ensemble interpretation of the system input and output, while the average superconducting phase difference stores information of the past dynamics. We study the hysteretic signature in a regime achievable with recent quantum nondemolition projective measurements\(^\text{14}\), and construct a memory quantifier related to the accumulation of internal state change. Finally, we discuss the quantumness of our proposal, comparing it with ref. 14. Our proposal represents, to our knowledge, the first design of a superconducting quantum memristor from fundamental principles, exploiting quasiparticle tunneling in memristive quantum information processing.

The envisioned device has the rf SQUID design shown in Fig. 1(a). It consists of a superconducting loop with inductance \(L\), which is interrupted by a dc SQUID with negligible loop inductance acting as an effective flux-tunable Josephson junction. The dc SQUID junctions are made from different materials so that they have the same critical current but a different normal conductance\(^\text{14}\). In this way, the effective critical current of the dc SQUID can be completely suppressed by a bias flux of half a flux quantum, \(\Phi_0/2\), threading its loop\(^\text{14}\). Finally, we also apply a bias flux \(\Phi_d\) to the rf SQUID loop, resulting in the phase bias \(\varphi_d = 2\pi\Phi_d/\Phi_0\).

The total Hamiltonian of this device is the sum of the system Hamiltonian \(\hat{H}_S\) a term for the quasiparticle degree of freedom, and a total tunneling term. The latter includes quasiparticle contributions but, due to the vanishing effective critical current (note that these contributions would yield a renormalization of the qubit frequency in the low-energy regime considered in ref. 17), neither pair contributions, nor the Josephson counterterm\(^\text{17–19}\). Under these conditions, the system Hamiltonian takes the harmonic form

\[
\hat{H}_S = E_C \hat{n}^2 + E_L (\hat{\varphi} - \varphi_d)^2,
\]

where \(\hat{n}\) and \(\hat{\varphi}\) are the Cooper-pair counting and phase difference operators of the effective junction, respectively. We define the capacitive energy scale \(E_C = 2e^2/C_d\) with the intrinsic junction capacitance \(C_d\) and the inductive energy scale \(E_L = (1/L) (\Phi_0/(2\pi))^2\). Regarding the dc SQUID, we assume the limit of strong conductance asymmetry needed for the effective junction picture due to the inclusion of quasiparticle excitations (see Supplementary Information). In this limit, the dissipative flow is through the physical junction with a smaller superconducting gap while the junction with a larger gap functions as a shunt for the total Josephson current through the SQUID. Furthermore, we demand that the phase bias is changed adiabatically, i.e., sufficiently slowly to avoid the generation of quasiparticles. Finally, our device operates in the low-energy regime \(\hbar \omega_{\text{low}} \ll \Delta E \ll 2\Delta\), where \(\omega_{\text{low}} = \sqrt{2E_C E_L}/\hbar\) is the system transition frequency and \(\Delta E\) is the characteristic energy of the quasiparticles above the gap \(\Delta\). Even though the Hamiltonian does not warrant operation as a qubit due to the lack of sufficient anharmonicity, the system dynamics is confined to the two lowest eigenstates of Eq. (2) when the aforementioned assumptions are complemented with operation in the high-frequency regime \(\hbar \omega_{\text{high}} \gg \Delta E\). In this regime, there exist no quasiparticles with sufficiently high energy to excite the system. We emphasize that the slow biasing and high frequency assumptions utilized in this article are not contradictory. The former refers to suppressing unwanted generation of quasiparticles due to the biasing field\(^\text{20}\) while the latter refers to a condition on the quasiparticle bath.

The two-level master equation describing the quasiparticle-induced decay takes the Lindblad form\(^\text{19}\)

\[
\partial_t \hat{\rho} = -i \hbar [\hat{H}_S, \hat{\rho}] + \hat{D}(\hat{\rho}) \text{ for the system density } \hat{\rho}, \text{ with } \hat{D}(\hat{\rho}) \text{ the corresponding Lindbladian dissipator. Note that the master equation assumes adiabatic steering, and employs the Born-Markov and secular approximations.}
We omit the quasiparticle-induced average frequency shift and the pure dephasing channel. See Supplemental Material for the estimation of these effects. In the low-energy limit, the decay rate factorizes into

$$\Gamma_{1\rightarrow 0} = \left| \langle 0 | \sin \frac{\hat{\varphi}_t}{2} | 1 \rangle \right|^2 S_{qp}(\omega_{10}),$$

(3)
in the lowest order in $\omega_{10}/\Delta$. Here, $|0\rangle$, $|1\rangle$ are the lowest energy eigenstates of $\hat{H}$, and the quasiparticle spectral density $S_{qp}(\omega)$ now depends on the distribution function which may, in general, include both equilibrium and non-equilibrium contributions. Note that the decay rate in Eq. (3) stems from the $\sin \hat{\varphi}/2$ dependence of the quasiparticle–system coupling and is crucial to the memristive behavior detailed in the following section. By using the properties of displaced number states (see Supplementary Information), the squared inner products in Eq. (3) have a convenient cosine form valid for any pair of Fock states $|n\rangle$, $|m\rangle$,

$$\left| \langle m | \sin \frac{\hat{\varphi}_t}{2} | n \rangle \right|^2 = \frac{P(g_{0}, n, m) [1 + (\gamma_{+}^{n-m}) \cos \hat{\varphi}_t]/2, m \leq n}{P(g_{0}, n, m) [1 + (\gamma_{+}^{m-n}) \cos \hat{\varphi}_t]/2, m \geq n},$$

(4)

with

$$P(g_{0}, n, m) = \begin{cases} \exp(-g_{0}^{2} m!^{2(n-m)} \lambda_{m}^{2(n-m)} (g_{0}^{2})) \right)^{2}, & m \leq n \\ \exp(-g_{0}^{2} m!^{2(m-n)} \lambda_{m}^{2(m-n)} (g_{0}^{2})) \right)^{2}, & m \geq n. 
\end{cases}$$

(5)

Here, $g_{0} = [E_{c}/(32E_{p})]^{1/4}$ and $\lambda_{m}$ denotes an associated Laguerre polynomial. Notably, the sign of the cosine term in Eq. (4) depends on the parity difference between the states involved. While this potentially provides insight into interesting phenomena when multiple decay channels are involved, we concentrate on the two-level process and leave such considerations for future studies.

To understand how memristive behavior emerges from quasiparticle tunneling, we study the charge flow in the device. Let $\hat{a}$ be the annihilation operator for a harmonic excitation in the system. This allows us to write $\hat{\varphi} = 2g_{0}(\hat{a} + \hat{a}^{\dagger}) + \varphi_{d}(t)$ and $\hat{h} = i(\hat{a}^{\dagger} - \hat{a})(\hat{a}g_{0})$, and denote by $\varphi_{ind} = \varphi - \varphi_{d}$ the operator for the phase over the rf SQUID loop inductance. The directionnal convention for the superconducting phase differences and the different currents are presented in Fig. 1(b). The average charging current $\langle \hat{I}_{ch} \rangle$ and the inductive current $\langle \hat{I}_{ind} \rangle$ can be rigorously derived (see Supplementary Information) to obtain, by current conservation, the average quasiparticle current through the effective junction. The result is $\langle \hat{I}_{qp} \rangle = 2e \text{Tr} \{\hat{D}(\hat{\varphi}) \hat{h} \} = \Gamma_{1\rightarrow 0} (-e) \langle \hat{h} \rangle$, which corresponds to the dissipative current induced by the interaction with the quasiparticle bath represented by the dissipator $\hat{D}(\hat{\varphi})$. Using $\langle \hat{V} \rangle = -2e \langle \hat{h} \rangle / C_{p}$, the average quasiparticle current is determined by

$$\langle \hat{I}_{qp} \rangle = G_{qp} \langle \varphi \rangle, \langle \hat{V} \rangle, t \rangle \langle \hat{V} \rangle,$$

(6)

where we have preemptively written the effective conductance as a function of the selected memory variable $\langle \hat{\varphi} \rangle$, input $\langle \hat{V} \rangle$, and time $t$. Solving for the dynamics, we obtain

$$G_{qp} \langle \varphi \rangle, \langle \hat{V} \rangle, t \rangle = P(g_{0}, 1, 0) S_{qp}(\omega_{10}) C_{d} \frac{1}{2} + \cos \langle \hat{\varphi} \rangle - \langle \hat{\varphi}_{ind} \rangle,$$

(7)

where the average inductive phase difference only requires knowledge of the input via

$$\langle \hat{\varphi}_{ind} \rangle = \frac{2\pi}{\Phi_{0} \omega_{10}} \partial_{t} - \partial_{t} \ln \left( \frac{C_{d} \langle \hat{V} \rangle}{e \text{Im} \{\rho_{01}(0)e^{i\omega_{10}t} \}} \right) \langle \hat{V} \rangle,$$

(8)

and we denoted the initial system coherence in the energy eigenbasis by $\rho_{01}(0) = \langle 0 | \hat{\varphi}| 1 \rangle$. The memory variable update function in $\partial_{t} \langle \hat{\varphi} \rangle = f \{ \langle \hat{V} \rangle, t \}$ only depends on the input and time, and has the explicit form

$$f \{ \langle \hat{V} \rangle, t \} = \frac{2\pi}{\Phi_{0} \omega_{10}} \left[ \partial_{t} \ln \left( \frac{C_{d} \langle \hat{V} \rangle}{e \text{Im} \{\rho_{01}(0)e^{i\omega_{10}t} \}} \right) \right] \langle \hat{V} \rangle - \partial_{t} \ln \left( \frac{C_{d} \langle \hat{V} \rangle}{e \text{Im} \{\rho_{01}(0)e^{i\omega_{10}t} \}} \right) \langle \hat{V} \rangle - \frac{2e}{\hbar} \langle \hat{V} \rangle + \partial_{t} \varphi_{d}(t).$$

(9)

Equations (6)–(9) indicate that a simple superconducting device operates as a voltage-controlled quantum memristor when the average voltage over a tunneling element is interpreted as the system input, the average quasiparticle tunneling current as the output, and the average superconducting phase difference as the
The hysteresis curve starts from a point in the weak-damping trajectory due to \( \varphi(t) \big|_{t=0} = \varphi_0(0) \), and by assuming a resonant sinusoidal phase bias \( \varphi_0(t) = \varphi_0 + (2eV_0)/(|h\omega|)\sin(\omega_0 t) \). Weak voltage damping implies that \( \langle \dot{V} \rangle \approx e/(C_d g_0) \Im\{\rho(t)\} e^{\omega_0 t} \) = \( V_0 \cos(\omega_0 t) \). The update is given by the classical Josephson relation \( \partial_t \varphi(t) = 2eV/|h(\omega)| \). The solution embodies the two implicit assumptions for the classical memristor: (1) the rf SQUID loop has a negligible inductance, and (2) the internal dynamics is negligibly weakly coupled to the classical Josephson relation. It should be noted that physically speaking our device is considered a flux-controlled memristive device as it includes non-zero capacitive and inductive elements while having no external capacitive coupling. However, only considering the quasiparticle contribution to the current and studying the above-mentioned equations allows us to define the device as a voltage-controlled memristor from an operational point-of-view.

The operation of the constructed memristor is of ensemble nature, that is, the system input and output are quantum averages obtained from the measurement record of the corresponding observables. Experimental input consists of initialization and a slowly oscillating flux bias applied to the rf SQUID loop. In this way, one obtains independently generated records which, consequently, have a complex correlation exhibiting memory features via Eq. (6). In fact, the selected system input is not independent of the decay, but experiences a memory-dependent damping

\[
\langle \dot{V} \rangle = \frac{e}{C_d g_0} \exp \left( -\frac{1}{C_d} \int_0^t G_{qp}(\chi) \langle \dot{V} \rangle \right) \Im\{\rho(t)\} e^{\omega_0 t},
\]

which allows one to self-consistently solve the fundamental equations above. One such solution is identifiable as mimicking the operation of the classical superconducting memristor\(^1\), in which the memory is fully stored in the phase bias. It is obtained in the weak-damping limit by initializing the system with \( \langle \dot{V} \rangle \big|_{t=0} = V_0 \) and \( \varphi_0(t) = \varphi_0(0) \), and by assuming a resonant sinusoidal phase bias \( \varphi_0(t) = \varphi_0 + (2eV_0)/(|h\omega|)\sin(\omega_0 t) \). Weak voltage damping implies that \( \langle \dot{V} \rangle \approx e/(C_d g_0) \Im\{\rho(t)\} e^{\omega_0 t} \) = \( V_0 \cos(\omega_0 t) \). The solution embodies the two implicit assumptions for the classical memristor: (1) the rf SQUID loop has a negligible inductance, and (2) the internal dynamics is negligibly affected by the same dissipation that produces the output.

As a first step, we need to verify whether the above-described classical-limit solution is consistent with the semiclassical results of ref. 14. In Fig. 2, one clearly sees that we observe the hysteretic current-voltage characteristic curves as required for a memristive element. In other words, a proper choice of the sinusoidal drive allows one to self-consistently solve the fundamental equations above. One such solution is identifiable as mimicking the operation of the classical superconducting memristor\(^1\), in which the memory is fully stored in the phase bias. It is obtained in the weak-damping limit by initializing the system with \( \langle \dot{V} \rangle \big|_{t=0} = V_0 \) and \( \varphi_0(t) = \varphi_0(0) \), and by assuming a resonant sinusoidal phase bias \( \varphi_0(t) = \varphi_0 + (2eV_0)/(|h\omega|)\sin(\omega_0 t) \). Weak voltage damping implies that \( \langle \dot{V} \rangle \approx e/(C_d g_0) \Im\{\rho(t)\} e^{\omega_0 t} \) = \( V_0 \cos(\omega_0 t) \). The solution embodies the two implicit assumptions for the classical memristor: (1) the rf SQUID loop has a negligible inductance, and (2) the internal dynamics is negligibly affected by the same dissipation that produces the output.

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the absence of area correlates with purely time-local current response. In other words, a nonlinear conductance cannot produce a non-zero area since it depends only on the instantaneous value of the voltage. The memory quantifier for the $k$th traversed loop takes the form $N_k^m = \int_{t^k}^{t^{k+1}} dt F_m(t)$ (See Supplementary Information), where $t_k^k$ fulfills $\langle \hat{V} \rangle \big|_{t_k^k} = 0$ for each $k$. This quantifier stores the evolution information of $F_m(t) = F_0(t) + \langle \hat{V} \rangle^2 \partial_{\langle \hat{\phi} \rangle} \langle \hat{\phi}, \langle \hat{V} \rangle, t \rangle / 2$, where

$$F_0(t) = \frac{1}{2} \langle \hat{V} \rangle^2 \partial_{\langle \hat{\phi} \rangle} \langle \hat{\phi}, \langle \hat{V} \rangle, t \rangle$$



(11)

which corresponds to the response specific to the selected memory variable, and the second term to the explicit time dependence of the memductance not included in the internal memory variable. However, it is in principle always possible to redefine the memory variable to absorb the explicit time dependence in the memductance, so that $F_m(t) = F_0(t)$. The two expressions in Eq. (11) imply that the quantifier corresponds to a time-dependent weighted record of the change in the memory variable $\partial_{\langle \hat{\phi} \rangle}$ or the instantaneous distance from its initial value $\Delta \langle \hat{\phi} \rangle = \langle \hat{\phi} \rangle - \langle \hat{\phi} \rangle \big|_{t=0}$. If the conductance is a non-linear function of only the instantaneous input, $F_m(t)$ vanishes in integration due to input periodicity. See Supplemental Information for the decay of the quantifier as well as its response to different initializations.

Finally, our quantum memristor is formulated in the ideal case of zero leakage supercurrent. Adding a nonzero pair-tunnelling term, not only modifies the energy and state structure, but inflicts a Josephson tunneling current which may disrupt the operation. While there can be multiple factors contributing to the leakage supercurrent, such as magnetic flux noise, the primary experimental factor to tackle is possibly the critical current imbalance in the SQUID. The state-of-the-art critical current suppression factor based purely on fabrication techniques is $\sim 10^{-2}$ while the balanced SQUID promises a factor of $10^{-3}$ to $10^{-4}$, for a maximum critical current of 30 nA. In terms of the Hamiltonian, this implies that the imbalance term is $10^{-1}$ to $10^{-3}$ times the charging energy scale used here. In addition, our formulation assumes only the quasiparticle decay channel and omits other natural loss channels (dielectric, inductive, radiative). Recent experimental work has studied quasiparticle-limited relaxation and shown significant progress in suppressing the additional decay channels, modifying the quasiparticle population through different means, and discerning between the different decay mechanisms.

In conclusion, we have demonstrated a prototype design for a quantum memristor in a superconducting circuit relying on quasiparticle tunneling. The pinned hysteretic behavior of the average quasiparticle current is a clear signature of conductance beyond typical non-linearity, and modified by both the characteristics of the circuit and the quasiparticle bath. The measurement resolution can potentially be varied by tuning the non-equilibrium quasiparticle population, by just using the state-of-the-art injection and trapping methods during the lifetime of the quasiparticles. Our work paves the way for the engineering of on-demand quantum non-Markovianity using the superconducting quantum memristor as a building block. Furthermore, we may consider possible applications such as the codification of quantum machine learning protocols and neuromorphic quantum computing.
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Author Contributions

J.S., as the first author, has been responsible for the development of this work. M.S. supported J.S. with the mathematical demonstrations and calculations. M.S. and E.S. suggested the seminal ideas. F.D. and M.D. have helped to check the feasibility and improve the ideas and results shown in the paper. All authors have carefully proofread the manuscript. E.S. supervised the project throughout all stages.

Additional Information

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