Avoiding noise frequency interference with binary phase pulse driving and CDS for capacitive TSP controller

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Abstract: Noise frequency interference avoidance (NFIA) using binary phase pulse driving and correlated double sampling (CDS) circuit technique is applied to avoid the interference of noise signal in capacitive touch screen panel (TSP). The proposed analog front-end circuitry is composed of a charge transfer circuit and a two-stage cascaded CDS circuit. The purpose of the two-stage cascaded CDS circuit is to remove harmonic noise frequencies interfering with touched data in TSP. If the system detects that noise frequencies interfere with the touched data, the proposed NFIA technique can be applied. Our proposed methodology is implemented in a real TSP system using 0.13-μm CMOS process and the measured SNR is 46 dB with the scan rate of 120 Hz in 21.5 inch TSP.

Keywords: noise frequency interference avoidance (NFIA), binary phase pulse signaling, correlated double sampling (CDS), touch screen panel (TSP)

Classification: Integrated circuits

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1 Introduction

Recently, touch screen panel (TSP) using a finger or a stylus pen is popular in tablet PCs and smart phones. Touched finger is for selecting icons, scrolling text on web-browser, and drawing a picture on the display panel. In desktop PC users use input devices as keyboard and mouse. But, TSP quickly replaces keyboard and mouse as input devices in mobile devices. Input devices such as keyboard and mouse is not critical with 60 Hz noise and noise caused by a lamp or a charger. However, in TSP
various electrical noises interfere with the touched data [1]. Therefore a technique to avoid these noise interferences with normal touched data should be implemented.

In this paper we proposed a noise frequency interference avoidance (NFIA) methodology to protect the touched data in capacitive touch screen panels. The 60 Hz power line noise and the lamp noise can be easily removed by the proposed AFE circuitry. But, the charger noise exists widely as in-band and out-of-band noise. The in-band noise can be removed digital filtering method. But, it is difficult to remove out-of-band noise caused from the charger noise and other harmonics noises from touched data. Our proposed technique can remove the out-of-band noise and the harmonics noise. In this paper, first, we describe the proposed analog front-end circuitry. Secondly, explain the technique for reducing noise frequency interference effect. Finally, the measured results will be given.

2 Proposed architecture

The proposed analog front-end circuitry in the receiver (Rx) for capacitive touch screen panel is composed of a differentiator, a charge transfer (CT) circuit and two-stage cascaded correlated double sampling (CDS) circuit. Depending on noise conditions one CDS or two CDS circuits are used. The circuitry is shown in Fig. 1. The CT circuit is transferring the charge in TSP capacitor to the internal capacitor ($C_{st1}$). The 1st and 2nd stage CDS circuit are for reshaping the stored charge in $C_{st1}$ to suppress DC noise and harmonic frequency noise in TSP.

![Proposed analog front-end circuitry in the receiver](image)

In the CT circuit, using $S_{T1}$ or $S_{T2}$, $C_m$ is charged or discharged. If $S_{T1}$ is on, the current is flowing from $V_{IN}$ node to $R_1$ and $C_m$ is charged. Then $S_5$ is to sample the voltage signal and $C_{st1}$ integrates the sampled signal in the CT circuit. Correlated double sampling technique is to reduce DC noise and 1/f noise [2]. Also, CDS technique can be used to reduce even harmonics of the operating clock frequency in TSP. However, odd harmonics of the operating frequency cannot be removed. In order to reduce interference caused by harmonics of the operating frequency, it is widely used that the frequency hopping (FH) methodology in electronic sensing nodes. But, sometimes the hopping frequency to avoid noise interference causes another unwanted frequency interference to other blocks in system.
Our proposed noise frequency interference avoidance technique adopts a binary phase pulse driving in the transmitter (Tx) with CDS circuitry. The binary phase pulse driving in Tx reduces the frequency interference by modifying the driving signal phase without changing the signal frequency in the Tx. The proposed noise frequency interference avoidance algorithm takes either the normal RZ (Return Zero) pulse driving in Tx with a single stage CDS in Rx or the binary phase pulse driving in Tx with two-stage cascaded CDS in Rx depending on noise frequency interference conditions. Fig. 2 shows timing diagram operating two modes (mode-1: RZ pulse driving + 1st CDS operation and mode-2: binary phase pulse driving + 1st & 2nd CDS operation) of analog front-end circuitry in Rx. The n-time pulsing and integration is executed and then the final symbol value is averaged.

The normal RZ pulse driving in Tx with 1st CDS in Rx operation is as follows. First, Tx is driven by the RZ pulse signaling. CT circuit is transferring the charge in Cm to Cst1. And, 1st CDS circuit is sampling the output of CT circuit. The final output of 1st CDS circuit is provided to an ADC circuit for further digital signal processing for touched data. The mode-2 operation, the binary phase pulse driving with 1st & 2nd CDS is used, and the output of the 1st CDS is sampled by the following 2nd CDS with ΦS3. The frequency of ΦS3 is a half of ΦS2.

Frequency response of two modes is summarized in as below. Touched data and noise in mode-1 can be expressed as

\[ S_S(\@f_S) + N_S(2n - 1)(\@f_S) \rightarrow S_S + N_S \]  

(mode-1),

where \( S_S \) represents the touched signal, \( N_S \) represents noise signal, and \( f_S \) is the sampling frequency. The expression means touched data are obtained from sam-
pling ($\Phi_{S2}$) plus noise at the odd harmonics of the sampling frequency.

Touched data and noise in mode-2 is given as

$$S_S(@ f_{\Phi S2}) + N_S((2n - 1) \pm 0.5)(@ f_{\Phi S2}) \rightarrow S_S + N_S \quad \text{(mode-2)}$$

The expression means touched data are obtained from sampling ($\Phi_{S2}$) and noise present at $(2n - 1) \pm 0.5$ of the sampling frequency is added due to the sampling in the 2nd CDS with $0.5f_{\Phi S2}$.

The proposed algorithm can reduce the noise interfered data by selecting the operating mode properly after inspecting the touched data. Fig. 3 shows a flow chart of the proposed NFIA. Tx driving frequency is set as 200 KHz in our TSP and the sampling frequency of the CDS is set as 400 KHz. At the beginning, the system is operating in mode-1. If the noise frequency in the TSP is the same as the odd harmonics of the sampling frequency, the noise interferes with the normal touched data and the noise-interfered touched data is stored to a frame memory. After the system compares the noise-interfered touched data with the pre-determined valid touched data without any interference, the NFIA algorithm decides whether to stay in mode-1 or to switch into mode-2. For example, if the number of touched data point is more than the maximum valid touched data point (the maximum valid touched data point is set as 10), the system changed the operation from mode-1 to mode-2. During mode-2 operation, the same procedure is processed for deciding whether to stay in mode-2 or to switch back to mode-1 operation.

![Flow chart of the proposed noise frequency interference avoidance algorithm](image)

Fig. 3. Flow chart of the proposed noise frequency interference avoidance algorithm
3 Measurement results

Fig. 4 shows the fabricated microphotograph. The proposed scheme has been designed and fabricated with 0.13 µm CMOS technology and the total chip size of the capacitive touch controller is 4.1 mm × 4.1 mm. The proposed two-channel AFE occupies 370 µm × 160 µm. Thus the proposed single channel AFE occupies 29600 µm². The touch controller with the proposed NFIA is applied to 21.5-inch and 23-inch TSP module, respectively. The 21.5-inch TSP is composed of 40 channel transmitter (Tx) electrodes and 60 channel receiver (Rx) electrodes. Tx and Rx electrodes are connected to Tx driver and Rx sensor in the chip. In the 23-inch TSP module, 58 Tx electrodes and 102 Rx electrodes are implemented. Fig. 5 shows the measured results with X-axis of 52 channels in Tx driver, Y-axis of 52 points among 102 channels in Rx sensor, and the measured touch data in Z-axis. Fig. 5(a) shows the measured results of 2-point touched data in mode-1 operation with a 500 KHz noise injection. The SNR is 43.47 dB. At 600 KHz noise injection. The operating mode is switched to mode-2 by our NFIA. Fig. 5(b) shows the measured 2-point touched data in mode-2 operation with a 600 KHz noise injection. The SNR is 43.20 dB. Fig. 5(c) shows the measured 2-point touched data in mode-1 operation with a 700 KHz noise injection. The SNR is 43.68 dB. Our proposed scheme shows that noise signal is not interfered.

Measurement results and comparison with other works are summarized in Table I. The proposed NFIA technique is implemented in the touch screen controller chip, and the proposed analog front-end (AFE) block area is smaller than reference [1] and [3]. The measured results showed that the 23 inch TSP system (Rx 2ea + Tx 1ea + MCU 1ea) consumes 28.4 mW and the 21.5 inch TSP system (Rx + MCU + Tx 1ea) consumes 16.7 mW. The measured SNR is 46 dB when the scan rate is 120 Hz in the 21.5 inch TSP system.
4 Conclusion

The noise frequency interference avoidance technique for TSP controller is successfully implemented. The proposed noise frequency interference avoidance algorithm takes either RZ pulse driving with a single stage CDS or binary phase pulse driving with two cascaded CDS depending on noise frequency interference conditions. The measured results showed that the 21.5 inch TSP system consumes 16.7 mW and shows 46 dB SNR with the scan rate of 120 Hz.

Table I. Performance summary and comparison with other works

| ITEM          | This work   | [1]  | [3]  |
|---------------|-------------|-----|-----|
| Size          | 21.5 inch   | 23 inch | 10.1 inch | 32 inch |
| TSP           | Tx: 40      | Tx: 58 | Tx: 27 | Tx: 78 |
|               | Rx: 60      | Rx: 102 | Rx: 43 | Rx: 138 |
| Tx            | VCC: 3.3 V  | VCC: 3.3 V | VCC: 3.3 V | VCC: 3.3 V (Veff.: 52.7 V) |
| Rx            | VCC: 3.3 V  | VCC: 3.3 V | VCC: 3.3 V | VCC: 3.3 V |
| SNR           | 46 dB/finger | 42 dB/finger | 39 dB/finger | 50.8 dB/finger |
| Power Consumption | 16.7 mW*     | 28.4 mW** | 18.7 mW** | 214.7 mW*** |
| Scan Rate     | 120 Hz      | 120 Hz | 120 Hz | 120 Hz |
| Process       | 0.13 µm CMOS | 0.35 µm CMOS | 0.18 µm CMOS |
| AFE size/ch   | 29,600 µm²  | 124,000 µm² | 30,000 µm² |

*Power consumption of Rx sensor + Tx driver.
**Power consumption of Rx sensor + Tx driver + Micro-controller.
†Effective Tx voltage is 52.7 V (255-code division driving)

Fig. 5. Measurement results with different noise frequency injection case 1), 3) RZ pulse driving + 1st CDS,
case 2) binary phase pulse driving + 1st & 2nd CDS

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