Design and Analysis of a Frequency Reconfigurable Penta-Band Antenna for WLAN and 5G Applications

Rekha Shanmugam

Abstract

This paper presents a discussion on a low-profile, frequency-reconfigurable penta-band antenna. This antenna consists of two asymmetric L-shaped rectangular patches electrically connected by a single PIN diode. The proposed antenna operates at five frequency bands depending upon the switching states of the PIN diode. It operates at 2.4 GHz (WLAN) and 5.3 GHz (5G) during forward bias of the diode, 3.3 GHz (5G) and 5.9 GHz (WLAN) during reverse bias of the diode and 4 GHz during zero bias condition providing a wide bandwidth (3.6–4.8 GHz) at this state. The antenna has voltage standing wave ratio (VSWR) ranging from 1 to 2, gain value ranges from 2.6 to 5.0 dBi, and a maximum radiation efficiency of 85%. This antenna can be integrated with modern devices such as smart phones, laptops, and other handheld devices due to its simplistic geometry. A prototype of the model is fabricated, and the results are validated.

Key Words: Frequency Reconfigurability, PIN Diode, Rectangular Patch, WLAN, 5G Applications.
bias voltage and keeping the other parameters (such as geometry and feeding probe) constant. The bandwidth can also be changed by varying the PIN diode. In [12], a ground plane was embedded with a DGS structure having two symmetrical U-shaped slots (with open ends) and an I-shaped slot (with a short end). The ground plane incorporated three PIN diodes to achieve multiband resonances.

In [13], a frequency reconfigurable antenna with coplanar waveguide (CPW) feeding was designed to cover a wide frequency range (2–10 GHz). The antenna used four PIN diodes to achieve different resonant frequencies in the wide frequency band. Further, the type of feeding, structure of the radiator, and number of diodes employed were the deciding factors for the operating frequency ranges. Discussions about a reconfigurable antenna having proximity couple feeding for WLAN and sub-6 GHz applications were presented in [14]. The antenna consisted of two substrates (separated by air) with four PIN diodes. However, the dimensions of the antenna are large and the structure is complex with this type of feeding. Finally, by using a reconfigurable feed network, frequency and polarization diversity can be achieved [15]. From the literature, it can be understood that PIN diodes are more user friendly in terms of less switching complexity, higher speed and reliability.

A compact, low-profile, frequency-reconfigurable microstrip patch antenna built on a FR4 substrate is discussed in this paper. Compared to conventional reconfigurable antenna models, this antenna only contains one PIN diode and generates five resonant frequencies. During forward bias, the operating bands are 2.4 and 5.3 GHz, whereas they are 3.3 and 5.9 GHz in reverse bias. These operating frequencies cover the WLAN (2.4 and 5.9 GHz) and 5G (5.3 and 3.7 GHz) bands. During zero bias, the antenna operates between 3.6 and 4.8 GHz.

Section II presents a discussion about the antenna design, including the geometry, design equations, switching techniques, and current distribution. Section III focuses on the simulation and fabrication results, while Section IV provides a brief conclusion of the proposed work.

II. ANTENNA DESIGN

1. Geometry of the Proposed Antenna

The model consists of two asymmetric L-shaped rectangular patches, with one of the patches fed through a 50 Ω microstrip line. The antenna was printed on a lossy FR4 substrate material—relative permittivity (εr) of 4.4 and tangent loss (δ) of 0.02—with a thickness of 1.6 mm. The use of FR4 was preferred in the proposed model because of its affordability and availability. The radiating element was backed by a metallic truncated ground plane to provide optimum efficiency, gain, and directivity. A rectangular stub was placed on the ground to improve impedance matching at higher resonant frequencies. An electrical path was provided between the two rectangular patches through a PIN diode. A full wave electromagnetic (EM) simulator—Ansys High Frequency Structure Simulator (HFSS)—was used to carry simulation of the proposed design. The total length of the radiator was 33.5 mm, and the width was 4 mm, and the overall dimensions was 40 mm × 35 mm × 1.6 mm. The geometry of the antenna is shown in Fig. 1. Table 1 presents the dimensions of the proposed model.

2. Design Equations

The effective length and dielectric permittivity of a rectangular radiator element are given by Eqs. (1) and (2) from [16].

\[ L_{\text{eff}} = \frac{c}{4f_r \sqrt{\varepsilon_{\text{eff}}}} \]  
\[ \varepsilon_{\text{eff}} \approx \frac{\varepsilon_r + 1}{2} \]

![Fig. 1. Geometry of proposed model.](image)

| S. No. | Dimension | Value (mm) |
|-------|-----------|------------|
| 1     | L × W     | 40 × 35    |
| 2     | b         | 33.5       |
| 3     | c         | 4          |
| 4     | a         | 11         |
| 5     | b         | 6          |
| 6     | c         | 17         |
| 7     | d         | 9          |
| 8     | e         | 3          |
| 9     | f         | 9.5        |
Here, $c$ is the velocity of light in free space, $f_r$ is the resonant frequency, $\varepsilon_{\text{eff}}$ is the effective dielectric permittivity, $\varepsilon_r$ is the permittivity of the dielectric substrate, $w$ is the width of the substrate, and $h$ is the height of the substrate.

If the antenna is excited at an appropriate impedance point, the reflection coefficient should be $<-10$ dB and voltage standing wave ratio (VSWR) would range between 1 and 2. The reflection coefficient is related to the impedance matching of the system and is given by Eq. (3).

$$\text{Reflection coefficient } |\Gamma| = \frac{Z_a - Z_f}{Z_a + Z_f}$$

Here, $Z_a$ is the impedance of the antenna, $Z_f$ is the feedline’s characteristic impedance, and $\Gamma$ is the reflection coefficient. The VSWR is related to the reflection coefficient through Eq. (4).

$$\text{VSWR} = \frac{1+|\Gamma|}{1-|\Gamma|}$$

3. Switching Techniques

A PIN diode (NXP BAP64-03, 100 mA, 20 V) was employed for frequency reconfiguration in the proposed model. The equivalent circuits of the PIN diode in its ON and OFF states are shown in Fig. 2(a) and (b), respectively.

The three biasing states of a PIN diode are as follows: (i) zero DC, (ii) positive DC, and (iii) negative DC. In the simulation, the PIN diode was modelled as a lumped RLC equivalent. For zero DC bias, there was no connection between the two L-shaped rectangular patches (such that there was no electrical path between them). The lower L-shaped rectanglar patch was the only active radiator. During positive DC bias, the diode represented a series combination of 2.5 $\Omega$ resistance and 1.68 nH inductance (in the simulation). For zero DC bias, the current flow between the radiating strips. During reverse DC bias, the diode provided a parallel combination of 0.3 pF capacitance and 15 k$\Omega$ resistance in series with an inductance of 1.68 nH (in the simulation). The higher value of $R$ during reverse bias restricted the current flow.

The inductor value was kept constant, as it did not have any significant effect on the current distribution. The values of $R$, $L$, and $C$ were considered from the data sheet of the PIN diode model. These values were tuned in the given range, ensuring resonance was achieved in the desired frequency band. The assignment of a lumped RLC boundary in the simulation tool, Ansys HFSS, is shown in Fig 2(c). Bias voltages of 0, +3, and −3 V were applied to the circuit for the zero, forward, and reverse biases, respectively. Depending on the type of biasing, different resonant frequencies were obtained from the single antenna model.

4. Current Distribution

The proposed frequency reconfigurable antenna was simulated using Ansys HFSS. The far field radiation was mainly due to the current on the radiator. The simulated surface current densities at all five frequency bands are presented in Fig. 3. During forward bias, the antenna resonated at two frequencies. At the lower resonance, the surface current density was large on the lower L-shaped patch, as indicated in Fig. 3(a). The length of the lower L-shaped patch was 20 mm (i.e., $c = 17 + e$ mm), in which most of the current was distributed in the lower part (i.e., $c = 17$ mm). The approximate length of the quarter wave at 2.4 GHz was calculated as 17 mm using the design Eqs. (1) and (2). This is illustrated using the vector surface current distribution at 2.4 GHz (phase 0º) in Fig. 4.

As indicated in Fig. 3(b), the surface current was distributed on the upper and lower L-shaped strips. It can also be observed that the surface current was distributed strongly around the stub. The length of the stub was slightly less than the quarter wavelength at 5.3 GHz. This rectangular stub on the ground plane played a vital role in achieving impedance matching at higher frequencies.

To illustrate the influence of the stub for impedance matching, the simulated reflection coefficient is plotted in Fig. 5.

Table 2. Different configurations of PIN diodes with their simulated operating frequencies

| Case | DC bias (V) | Biasing state | Operating freq. (GHz) | Bandwidth (GHz) |
|------|-------------|---------------|-----------------------|-----------------|
| Case 1 | 0           | Zero bias     | 4                     | 3.6–4.8         |
| Case 2 | +3          | Forward bias  | 2.4                   | 2.1–2.5         |
| Case 3 | −3          | Reverse bias  | 3.3                   | 2.8–3.6         |

Fig. 2. Equivalent circuit of PIN diode in (a) ON, (b) OFF state, and (c) lumped RLC boundary in HFSS.
Fig. 3. Simulated surface current density of the proposed design at (a) 2.4 GHz, (b) 5.3 GHz, (c) 3.3 GHz, (d) 5.9 GHz, and (e) 4 GHz.

Fig. 4. Vector surface current distribution at 2.4 GHz (phase 0°).

Fig. 5. Effects of stub at higher frequencies: (a) forward bias and (b) reverse bias.
With the rectangular stub, the antenna realized improved matching at the higher frequencies: 5.3 GHz in forward bias and 5.9 GHz in reverse bias.

III. RESULTS AND DISCUSSION

To validate the results, the model was fabricated and then measured using an Agilent vector network analyzer. A photograph of the fabricated antenna is presented in Fig. 6, while Fig. 7 displays a comparison between the simulated and measured return losses. The simulated antenna (at zero bias) achieved a −10 dB impedance bandwidth of 1.2 GHz (3.6–4.8 GHz) with a center frequency at 4 GHz. During forward bias of the diode (+3 V), the antenna resonated at two frequencies: 2.4 GHz (2.1–2.5 GHz) and 5.3 GHz (5–5.6 GHz). During reverse bias of the diode (−3 V), the antenna resonated at 3.3 GHz (2.8–3.6 GHz) and 5.9 GHz (5.5–6.1 GHz).

The measured resonant frequency at zero bias was 4.1 GHz with a bandwidth of 3.7–4.9 GHz. During forward bias, the antenna resonated at 2.47 GHz (2.2–2.6 GHz) and 5.4 GHz (5.1–5.7 GHz). During reverse bias, the antenna resonated at 3.38 GHz (2.8–3.7 GHz) and 6 GHz (5.5–6.3 GHz). The small deviation between the measured and simulated results could be due to minor fabrication errors and soldering losses in the SMA connector.

The measured gain at all resonant frequencies is displayed in Fig. 8. The gain varied between 2.6 and 5 dBi for the operating frequencies, which is acceptable for the previously mentioned applications. The efficiencies of the proposed antenna at all biasing conditions are displayed in Fig. 9. Further, the maximum and minimum radiation efficiencies were 85% and 76%, respectively. The simulated and measured E- and H-plane gain patterns at all
five operating frequencies are presented in Fig. 10. It can be observed that the antenna radiated omni-directionally in the H-plane at 2.4, 6, and 4.1 GHz. All the E-plane patterns adopted a figure-of-8 shape, with a null lobe occurring at 90°. The simulated and measured patterns were in good agreement, with a minor difference of 0.5 dB.

Table 3 presents a detailed summary of the simulated and measured results. Table 4 presents a detailed comparison between the proposed antenna and other (similar) models in the literature.

IV. CONCLUSION

A simple and compact penta-band frequency-reconfigurable antenna is proposed in this paper. The presented patch antenna uses a single PIN diode connected between two rectangular strips. The various switching states of the PIN diode are responsible for achieving the desired operating frequency. It can operate at five frequency bands (2.4, 3.3, 5.3, 5.9, and 4 GHz), rendering it suitable for applications in WLAN and 5G. The antenna has acceptable ranges of gain from 2.6 to 5 dBi, and the maximum efficiency is 85%. This prototype was fabricated on easily available low-cost FR4 substrate, and the results were validated through measurements. The simulated results were in good agreement with the measured results.
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Rekha Shanmugam is currently working as an Assistant Professor in the Department of Electronics and Communication Engineering, Sreenidhi Institute of Science and Technology, Hyderabad, India. She received her PhD in the area of “Design and development of substrate integrated waveguide antennas” from Karunya Institute of Science and Technology, Coimbatore in 2019. She has 7 years of teaching and 5 years of Research experience. Her research areas include wireless sensor networks, antenna design (especially substrate integrated waveguide antennas), reconfigurable antennas, and MIMO antennas. She has more than 12 publications in international conferences and journals, including Microwave and Optical technology letters (Wiley), International Journal of RF and Microwave computer Aided engineering (Wiley). She is a lifetime member of the CSI and a member of IEEE (Antenna and Propagation Society).