Search Spaces for Neural Model Training

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Abstract

While larger neural models are pushing the boundaries of what deep learning can do, often more weights are needed to train models rather than to run inference for tasks. This paper seeks to understand this behavior using search spaces – adding weights creates extra degrees of freedom that form new paths for optimization (or wider search spaces) rendering neural model training more effective. We then show how we can augment search spaces to train sparse models attaining competitive scores across dozens of deep learning workloads. They are also tolerant of structures targeting current hardware, opening avenues for training and inference acceleration. Our work encourages research to explore beyond massive neural models being used today.

1 Introduction

In the area of deep learning, increasing the size of neural models has led to dramatic advances [1–3], motivating training of hundreds of billions of parameters [4, 5]. However, the growing size of models incur higher memory costs and runtimes, hindering training or inference tasks that can be tackled using current hardware [6]. Nevertheless, larger models have been shown to train faster [7] and better [2] which drives their adoption.

An area of research that seeks to compensate the increasing costs of model size is sparsity [8], which moves weights to zero so they can be discarded from storage or computations. Interestingly, neural models are capable of performing tasks on sizes smaller than they were trained for [9, 10]. While sparsity emerges as a promising option to reduce inference costs for overparameterized neural models [11, 12], training observes limited success [13–22]. This raises an interesting question – why do larger neural models train better – that remains to be understood. Being able to run inference but not training using fewer weights points to a problem with search, or how models navigate search spaces throughout training, rather than model capacity [23].

This paper explains why deep learning tasks improve with the size of search spaces being explored during training. We find that adding weights provides extra degrees of freedom that form new paths of optimization and facilitate the search for neural models. Using this understanding, we propose a series of steps to augment search spaces of sparse models during training to approximate the behavior of larger models. We then show this methodology achieves competitive results on dozens of deep learning workloads, even when satisfying constraints needed to accelerate training and inference using Sparse Tensor Cores [24] in NVIDIA GPUs.

The paper is organized as follows. In Section 2 we use search spaces to describe reasons more weights are needed for training. We then devise approximations to augment their size when training sparse models in Section 3. Section 4 shows sparse models constructed in this fashion perform competitively across a plethora of deep learning workloads and can target current hardware accelerators [24] for training and inference. In Section 5 we relate to prior research. Section 6 concludes with directions for future work.

1 We will make the code available in the near future.

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2 A primer on search spaces

We begin by understanding what role search spaces (or the set of all possible weight combinations that a model can take) have in neural model training. We show neural weights needed for inference are discovered throughout training with help from added weights (or weights that can be discarded after training). More specifically, we observe adding weights help neural models better explore search spaces to discover promising combinations for inference. While they do not learn meaningful representations, added weights form alternate paths for optimization that enable training to escape critical points. By combining these observations, we provide intuition that more weights are needed for training because they augment search spaces allowing neural models to train more effectively.

2.1 Discovering weights needed for inference

A popular technique to reduce inference costs is to discard weights of a neural model whose magnitudes fall below a certain threshold after training (typically denoted as sparsity), since they contribute the most to defining the behavior of a neural model [22]. Neural models constructed in this fashion (or sparse models) can achieve the same accuracy as larger models when trained from the same initialization [14] or with additional training steps [24]. Thus, we can assume neural weights retained after training are critical for inference, as opposed to removed weights which are not necessary.

The question then arises whether weights relevant for inference can be determined earlier in the training process. For this purpose, we examine how weights evolve throughout training, classifying them into two types of behavior. We define a set of weights as active at a particular point in time if their magnitudes remain above a chosen threshold from that point until the end of training, representing weights that are important for inference. Conversely, the set of weights whose magnitudes remain below the threshold from that point until the end of training are inactive, after which they play no role in inference tasks. There are also the remaining weights that constantly move above and below the threshold. While other combinations of weights that can contribute to inference may exist, this is one instance that is known to produce good accuracy [14].

Figure 1 (left) shows how sets evolve over the course of training when using a threshold that maintains a quarter of the weights for inference. In early training, most weights switch between either side of the threshold as neural models are still learning, eventually stabilizing into active or inactive sets. Neural weights relevant for inference (or form the active set) are not prebaked [17], but rather discovered as the model learns. As a result, we can presume all weights including ones that are eventually discarded (or added weights) play a crucial role in determining what will be used for inference.
2.2 Exploring search spaces with model size

An important aspect of training is how well can neural models navigate search spaces to discover better weight combinations for inference tasks. Given added weights help uncover weights relevant for inference, we posit they must play an important role in exploring search spaces. We characterize exploration with distances that neural models undertake throughout training, which are measured using cumulative increments $\sum_t \sum_w |w(t+1) - w(t)|$ over successive time steps $t$ across convolutions and linear layers, excluding other neural layers.

Figure 1 (middle) illustrates the distance traversed (or exploration of the search space) as a function of weights added to a model that can successfully perform inference tasks (assume a model one-fourth its original size) but requires more weights to train. We observe distance increases with the number of weights being added, therefore larger models can navigate search spaces more effectively.

Based on the above, we reason using more weights during training expands the size of search spaces, making it easier to discover combinations of weights that are needed for inference. More precisely, adding weights helps neural models explore new regions of the search space, escaping critical points that often lead to poor task accuracy in smaller models \cite{23}. However, the exact function of added weights (which can be later discarded for inference) is discussed below.

2.3 Roles of weights using similarities

To better understand the role of weights during training, we look at how similar are their values over time using correlations. We can interpret correlations as the degree with which weights in a neural model learn: weights that get reinforced (or learn) throughout training are correlated to previous values and exhibit temporal patterns, and conversely weights that have random changes (or don’t learn) are uncorrelated and have no dependence over time.

We measure the similarity between a pair of series $x$ and $y$ through the Pearson correlation coefficient \cite{26}: $\rho_{x,y} = E[(x - \mu_x)(y - \mu_y)]/\sigma_x \sigma_y$, where $\mu$ is the mean, $\sigma$ the standard deviation, and $E[\cdot]$ the expected value. Coefficients of value +1 denote two series have identical trends, 0 indicates the series are random, and −1 represents series with opposite behavior. We treat each individual weight as a time series $w \in \{w_1, w_2, \ldots, w_t\}$ and compute Pearson coefficients between windows $x \in \{w_1, w_2, \ldots, w_{t-\tau}\}$ and $y \in \{w_{\tau}, w_{\tau+1}, \ldots, w_t\}$, representing the similarity between the current weights and their values at some future time $\tau$. Since correlations naturally decay with $\tau$ (temporal patterns are less likely to persist over long durations), an important metric is the time $\Delta$ after which weights no longer correlate to their future values ($\rho = 0$).

Figure 1 (right) illustrates the fraction of total training after which weights become completely uncorrelated ($\Delta$) as a function of their magnitudes obtained after training. While weights belonging to the active set exhibit correlations that persist across a significant portion of training, weights forming the inactive set have short-term correlations and behave randomly over small periods. We conjecture long-term correlations of large weights are characteristic of learning, as they signify repeated reinforcement along the weight direction, whereas the near-random motion of small weights suggests they don’t learn useful representations and can be removed during inference.

Since weights that move into the inactive set are absent of correlations across long periods of training, we interpret their crucial role during training as follows: short-term interactions enable training to take different paths for optimization, which facilitates escape from bad saddle points \cite{27} or high error plateaus \cite{28} that can slow down learning. In later sections, we also discover their learned values can be periodically destroyed throughout training without affecting accuracy. Thus, we reasonably conclude that added weights removed during inference do not learn meaningful representations, but rather their short-term behavior is responsible for widening the search space.

2.4 An intuitive explanation about search spaces

Based on the data above, we construct the hypothesis that “weights removable at inference time represent extra degrees of freedom that are needed to augment search spaces for training neural models effectively.” This explains the prevailing observation that neural models with reduced sizes, which perform well at inference tasks, would underperform during training.
For a more precise formulation, assume neural models are parameterized by \( n \). In this scenario, training traverses through \( n \)-dimensional search spaces to minimize the loss function, leveraging all \( n \) weights in the process. Conversely, models trained with \( d n \) weights, where \( d \in (0, 1) \), navigate search spaces that are constrained to \( d n \) dimensions. Operating on reduced spaces makes training more susceptible to critical points, limiting weight combinations that neural models can explore during training. Since weights removable at inference observe short-term interactions, they do not learn representations but rather augment search spaces allowing training to be more effective.

Figure 2 illustrates an example. Let’s assume we are training neural models in a two-dimensional loss landscape of convex shape with a minimum at \( P \) and a vertical barrier \( B \). A one-parameter model that can move left or right during training will converge prematurely at \( A \) if initialized at \( I \) such that \( B \) obstructs its path to \( P \) (solid curve). Adding another weight augments the search space so training can take new possible paths along the loss landscape to escape \( B \) (dashed curve). After reaching \( C \), training can discard the added weight and converge on \( P \) (dotted curve). A similar generalization can be made for higher dimensions, where adding weights helps training escape saddle points. This behavior is believed to be a crucial aspect for neural model training with backpropagation.

Figure 2: Training trajectories for one- and two-parameter neural models on a 2D convex landscape with a minimum \( P \) and barrier \( B \).

\[ \text{I} \quad \text{P} \quad \text{B} \quad \text{A C} \quad \text{0.600} \quad \text{1.200} \quad \text{1.200} \quad \text{1.800} \quad \text{1.800} \quad \text{2.400} \quad \text{2.400} \quad \text{2.400} \quad \text{2.400} \quad \text{2.400} \quad \text{2.400} \quad \text{3.000} \quad \text{3.000} \quad \text{3.000} \quad \text{3.000} \quad \text{3.600} \quad \text{3.600} \quad \text{3.600} \quad \text{3.600} \]

3 Recommendations for sparse training

While search spaces are important for training, their sizes are often made smaller for sparse models. Building on understanding from previous sections, we uncover the following recommendations to approximate wider search spaces when training sparse models:

1. Rewiring weights that participate in training allows sparse models to explore different regions of the search space.
2. Gradient updates for weights that do not participate in training encourages alternate paths for optimization.
3. Inducing exploitation (stop rewiring after some amount of training steps, reset non-participating weights to zero or regularize them) reduces noise from gradient accumulations.

Appendix C summarizes possible methods for expanding the size of search spaces during neural model training using these three simple steps.

3.1 Rewiring of neural weights

Sparse models are commonly trained by rewiring (or sampling) a subset of weights from a neural model according to some criteria (magnitude, sign, gradients, etc), allowing them to learn which weights are needed for inference. Some works rewire weights every training iteration \([18, 22, 29-31]\), while others rewire every hundreds of training steps \([21]\) or after an entire pass through the data \([19, 20]\). Since there are no clear reasons behind these choices, an important question becomes how often must weights be rewired so neural models can learn.

We study the effects rewiring rates have on accuracy when training sparse models of various sizes \( d \), where \( d \) denotes the fraction of weights being used. Figure 3 (left) plots the task error (or accuracy difference between regular and sparse models) as a function of the number of training steps \( r \) taken between rewirings. We find rewiring frequency does not matter when sparsity is low or moderate \((d \sim 0.5)\), but errors increase with \( r \) as models get sparser \((d \ll 0.5)\). Therefore, weights should be rewired frequently (within a few training steps) to avoid losing accuracy.

We can use search spaces to explain why training improves with more frequent rewiring. A sparse model is made of weights that participate in training (are used in forward and backward propagations)
Figure 3: Investigations into various aspects of training sparse models using ResNet50. Left: Task error (or accuracy difference between regular and sparse models) as a function of rewiring steps \( r \). Middle: Task error as a function of scaling factor \( s \) applied to gradients of weights that do not participate in training. Right: Task error where non-participating weights are reset to zero every \( z \) training steps. We consider sparse models of different sizes \( d \) denoting the ratio of weights being used. Lines represent polynomial fits of sample points. Appendix \[B\] covers data for a broader span of neural models and tasks.

3.2 Updates for weights not participating in training

Previously, we found neural weights not needed for inference form short-term interactions that make training more effective. Since sparse models lack weights that have similar roles, we can approximate this behavior using gradient updates for non-participating weights. Because non-participating weights do not contribute to the loss, their gradients determine whether another optimization path is better suited to traverse the region being explored in the loss landscape. Repeating gradient updates registers the importance of these paths over some period of training and can trigger rewiring when weights exceed a threshold, which allows training to explore different regions of the search space while operating on (or forward and backward propagating) a smaller set of weights.

We evaluate the importance of gradients updates on non-participating weights by reducing their contribution with a scale factor \( s \). Figure 3 (middle) shows the task error as a function of the scale factor \( s \) using various sizes \( d \). We observe error increases as \( s \to 0 \) (no gradients contribute when \( s = 0 \)), which can be attributed to premature convergence when training lacks expressive power to explore different paths. Dampened gradient updates affect sparser models \( (d \ll 0.5) \), which are sensitive to reduced spaces, more than larger models \( (d \sim 0.5) \), which may still have sufficient search. As a result, non-participating weights should be updated regularly to retain accuracy.

Gradient updates for weights that do not participate in training could mean they also learn representations, such that more capacity rather than search improves accuracy. We verify this by resetting non-participating weights to zero after every \( z \) training steps, thus removing any representations they might have learned. Figure 3 (right) shows the task error as a function of \( z \). Errors are the highest when values are reset every iteration \( (z = 1) \), which is equivalent to training on reduced spaces (weights can still be rewired but we no longer encourage certain ones to participate in training). Conversely, error decreases as weights are reset less frequently and saturates after sufficient training steps \( (z \sim 1k) \). Interestingly, this also represents the time it takes for added weights to uncorrelate, as shown in previous sections. The ability to remove information from non-participating weights reinforces the idea that they augment search spaces rather than model capacity.
While most of literature trains sparse models using fewer weights and gradients [18–20], more recent success has been found by updates gradients for weights that do not participate in training [29, 31, 33, 34] as described above.

3.3 Balancing between exploration and exploitation

Deep learning like other optimization problems treads a delicate balance between exploration and exploitation. In early stages of training neural models explore search spaces using high learning rates, whereas late stages exploit specific regions using small learning rates. Training sparse neural models can also affect this balance, as fewer weights reduces the degrees of freedom and thus hinders exploration during training. On the other hand, gradient updates on non-participating weights introduces noise as any nonzero value will not reflect what is being used for training, which limits exploitation (training bounces around basins of attraction due to gradient noise).

Figure 4 shows task error degrades without proper exploration, by training smaller models with neural layers of reduced widths (No EXPLORE), or exploitation, by training sparse models delineated so far (No EXPLOIT). Therefore, we seek ways to induce exploitation when augmenting search spaces for more exploration. One course of action is to remove noise introduced by non-participating weights during late stages, so training can take steepest descents towards the minima. To achieve this we stop rewiring weights after sufficient training (FIX), such that non-participating weights can no longer contribute to the loss. Figure 4 shows this decreases error rates tremendously.

Another option is to draw analogies with added weights, which can be safely discarded after sufficient training since they do not learn representations over time. We reset non-participating weights to zero roughly every $1k$ training steps (or the time it takes for added weights to uncorrelate) in order to remove gradient noise that may trigger unnecessary rewiring, allowing training to exploit nearby regions. Figure 4 shows error decreases substantially when non-participating weights are either reset to zero (RESET) or regularized with a decay factor (REGULARIZE) as suggested in [31]. Sparsity literature introduces alternative courses for inducing exploitation [19–21].

4 Empirical data

Using recommendations from the previous section, we are in position to train sparse models approximating wider search spaces. This section presents empirical evidence that wider search spaces can help sparse models achieve better accuracy for inference tasks. First, we demonstrate that our strategy performs competitively against state-of-the-art sparsity research. Then, we explore the effects of sparsity on models that are trained to the limits of their capacity. Lastly, we discover sparse models are tolerant to sparsity structures that target hardware acceleration using Sparse Tensor Cores [24].

4.1 Comparisons to sparsity research

Experiments are conducted across a wide breadth of deep learning tasks and neural architectures trained on large data sets (Appendix F details the setup). We draw comparisons between full-sized models that have free search (REGULAR), smaller models consisting of neural layers with reduced widths (REDUCE), and sparse models that approximate wider search spaces (SEARCH). Appendix F shows where accuracy for the latter falls between the former two.

Table I lists accuracies for regular models and their differences for sparse models (adding the two numbers produces accuracies for sparse models) across various tasks as a function of $d$, where $d$
denotes the ratio of weights being used. We find using our recommendations most sparse models can be halved in size ($d = 0.5$) without sacrificing any task accuracy, whereas training with a quarter of the weights ($d = 0.25$) often reduces accuracy by less than 1%. Some exceptions include efficient convolutional models and sparser models ($d = 0.1$) which may be constrained by capacity.

We also draw comparisons to sparsity literature. While LOTTERY [14] determines at initialization which weights participate in sparse models [15][16], SET [19] and RIGL [19] rewire them throughout training (see Appendix D). However, we do not consider methods that move weights across layers [20][33] or adopt concepts similar to those explored in this paper [31][34]. Figure 5 shows the task error (or difference in accuracy between regular and sparse models) for these, which indicates our strategy outperforms competing approaches on all tasks and model sizes by a wide margin.

4.2 Effects of longer training

Since conventional models can achieve higher accuracy when trained on larger data sets or with longer training schedules [35], another interesting direction is to explore the effects of sparsity on

Table 1: Accuracies for regular models and their differences for sparse models using different sizes $d$.

| Model          | REGULAR | $d = 0.5$ | $d = 0.25$ | $d = 0.1$ | Model          | REGULAR | $d = 0.5$ | $d = 0.25$ | $d = 0.1$ |
|----------------|---------|-----------|-----------|-----------|----------------|---------|-----------|-----------|-----------|
| ResNet18       | 70.30   | +0.01     | −0.92     | −2.75     | SqueezeNet V1  | 60.77   | −0.88     | −5.20     | −          |
| ResNet34       | 73.87   | −0.20     | −0.65     | −2.27     | MobileNet V2   | 71.53   | −0.87     | −0.87     | −          |
| ResNet50       | 76.71   | −0.05     | −0.59     | −2.17     | Stacked UNet-64| 69.53   | −1.41     | −3.79     | −8.19     |
| ResNet101      | 77.50   | −0.25     | −0.61     | −1.52     | SSD-ResNet18   | 19.15   | −0.81     | −2.49     | −5.55     |
| ResNetX50      | 77.68   | +0.02     | −0.49     | −2.01     | SSD-ResNet50   | 24.93   | −0.52     | −2.06     | −5.31     |
| ResNetX101     | 79.27   | +0.20     | +0.19     | −1.06     | Faster R-CNN   | 37.71   | −0.25     | −1.61     | −5.51     |
| WideResNet50   | 78.13   | +0.13     | −0.34     | −1.06     | Mask R-CNN     | 38.26   | −0.34     | −1.42     | −4.80     |
| WideResNet101  | 78.63   | −0.12     | +0.04     | −1.00     | Mask R-CNN     | 35.03   | −0.88     | −2.65     | −7.44     |
| InceptionV3    | 77.10   | −0.08     | −0.88     | −3.25     | Mask R-CNN 3×  | 40.78   | −0.12     | −1.12     | −3.51     |
| Xception       | 79.28   | +0.04     | −0.28     | −1.26     | Mask R-CNN 3×  | 37.05   | −0.03     | −0.81     | −2.98     |
| DenseNet121    | 75.46   | −0.46     | −1.73     | −4.35     | RetinaNet      | 36.48   | −0.42     | −2.42     | −6.00     |
| DenseNet161    | 78.77   | +0.01     | −0.86     | −2.35     | RPN            | 57.61   | −0.16     | −0.90     | −2.56     |
| DenseNet169    | 76.97   | +0.04     | −0.96     | −3.23     | DETR           | 39.90   | +0.10     | −0.60     | −0.80     |
| VGG11-BN       | 70.70   | −0.33     | −0.79     | −2.24     | Prs2PixHD*     | 68.83   | −2.27     | +3.02     | +3.52     |
| VGG16-BN       | 74.00   | −0.25     | −0.46     | −1.75     | Few-Shot Vid2Vid*| 25.78   | −0.52     | −0.65     | −4.42     |
| VGG19-BN       | 74.88   | +0.09     | −0.48     | −1.52     | FAZE*          | 2.94    | +0.04     | −0.02     | +0.04     |
| DRN-C-26       | 75.22   | −0.30     | −0.74     | −2.35     | Vavvani Base   | 26.87   | −0.68     | −1.92     | −3.62     |
| DRN-C-42       | 76.78   | −0.30     | −0.62     | −1.98     | Vavvani Large  | 28.43   | −0.09     | −0.92     | −2.12     |
| DRN-A-50       | 78.30   | −0.25     | −0.74     | −2.28     | Levenshtein*   | 6.16    | +0.11     | +0.23     | +0.45     |
| DeiT Tiny       | 72.70   | −2.81     | −8.09     | −16.49    | GNMT           | 24.81   | −0.12     | −0.26     | −0.15     |
| DeiT Small      | 80.08   | −1.53     | −3.75     | −8.30     | XL Base*       | 22.88   | +0.49     | +2.04     | +5.41     |
| DeiT Base       | 81.95   | −0.75     | −1.44     | −         | XL Large*      | 17.90   | +0.16     | +1.01     | +2.65     |
| ShuffleNetV2    | 68.44   | −0.43     | −1.44     | −         | BERT Base      | 87.66   | −0.04     | −         | −         |
| MNASNet V1      | 71.80   | −1.09     | −3.36     | −         | BERT Large     | 90.92   | −0.02     | −         | −         |

* Lower means better (negative differences mean better)
We have shown earlier that by using search spaces we can reduce the size of neural models while (e.g., removing blocks [38], channels or filters [39, 40], layers [41]) often degrade accuracy. This task error between regular and sparse models trained for the same duration. Right: Training time it takes for sparse models to match accuracy of regular models. Appendix C covers data for a broader set of neural models and tasks.

models that are trained to the limits of their capacity. We train neural models longer by extending their learning rate schedules after warmup, e.g. a schedule 1/t becomes 2/t.

Figure 6 (left) plots accuracy differences as a function of training time t, which denotes the ratio of training steps to the original training schedule. The fact accuracy improves with more training suggests neural models are often not trained to capacity using conventional schedules. We observe sparse models achieve worse accuracy than regular models that are undertrained (t ~ 1), but can match accuracy when trained to capacity (t >> 1).

Figure 6 (middle) illustrates task errors between regular and sparse models that have been trained for the same number of steps. We find error scales inversely with t, since more training gives neural models better chances to explore search spaces. Particularly, errors can reach zero with sufficient training [21] for models that are not constrained by capacity (d ≥ 0.25). Figure 6 (right) shows the time it takes for sparse models to recover accuracy of regular models is relatively short when t ~ 1, but significantly longer as accuracy saturates (t >> 1). This time also increases for smaller model sizes, reminiscent of efforts that seek to undertrain massive neural models for efficiency [27].

4.3 Application on hardware accelerators

We have shown earlier that by using search spaces we can reduce the size of neural models while maintaining accuracy. However, such models cannot be accelerated on modern hardware with current matrix-math pipelines [36,37] without imposing particular structures (or positions of weights used during training and inference). On the other hand, neural structures targeting hardware acceleration (e.g., removing blocks [38], channels or filters [39,40], layers [41]) often degrade accuracy. This apparent tradeoff between accuracy and performance (or speed) has hindered their adoption.

Table 2: Accuracies for regular models and their differences for 2:4 sparse models for various t.

| Model            | t   | REGULAR | 2:4 1D | 2:4 2D | Model            | t   | REGULAR | 2:4 1D | 2:4 2D |
|------------------|-----|---------|--------|--------|------------------|-----|---------|--------|--------|
| ResNet18         | 8x  | 72.17   | +0.00  | −0.28  | VGG19           | 1x  | 74.88   | +0.04  | −0.21  |
| ResNet34         | 3x  | 75.14   | +0.06  | −0.27  | Xception        | 1x  | 79.28   | +0.04  | −0.11  |
| ResNet50         | 4x  | 77.67   | +0.05  | +0.09  | DETR            | 1x  | 39.90   | −0.30  | −0.40  |
| ResNet101        | 3x  | 78.94   | −0.09  | −0.31  | Pix2PixHD*      | 1x  | 68.83   | −1.34  | +0.68  |
| InceptionV3      | 3x  | 78.11   | −0.11  | −0.18  | Few-Shot Vid2Vid* | 1x | 26.06   | −0.49  | +0.04  |
| ResNet50         | 3x  | 78.36   | −0.21  | −0.19  | FAE*            | 1x  | 2.49    | −0.08  |        |
| ResNext101       | 1x  | 79.27   | +0.28  | +0.36  | GNMT            | 1x  | 24.81   | +0.15  | +0.09  |
| WideResNet50     | 1x  | 78.13   | −0.07  | −0.08  | Transformer Large | 1x | 28.43   | −0.10  | −0.39  |
| WideResNet101    | 1x  | 78.63   | +0.08  | −0.07  | BERT Large      | 1x  | 90.92   | +0.03  | −0.55  |
| DRN C 26         | 3x  | 77.66   | −0.05  | −0.11  |                 |     |         |        |        |

* Lower means better (negative differences mean better)
Therefore, we explore whether search spaces can make hardware-aware structures more amenable for deep learning. As a case study, we consider Sparse Tensor Cores introduced in NVIDIA Ampere GPU architecture [24], which have twice the math throughput of regular matrix operations. The hardware expects a 2:4 sparsity structure that takes at least two values to be zero for each group of four values. Appendix H illustrates examples of 2:4 sparsity for inference (1D) and for training (2D).

Table 2 lists accuracy differences between sparse and regular models (positive values mean sparse performs better) using various ratios \( t \) for training schedules. We find structured neural models can generally retain accuracy for tasks. Using 2:4 for training (2D) and inference (1D) roughly match accuracy of regular models, while neural models adopting coarser structures such as block sparsity [9, 38] fail to benefit from search spaces and perform no better than smaller models (see Appendix H). Therefore, we can conclude 2:4 sparsity structures are particularly effective at approximating wider search spaces due to their finer granularity. This suggests possible avenues towards accelerating training [31] as well as obtaining efficient models for inference [24, 31].

5 Related work

While this paper presents new perspectives on the benefits of using more weights, overparameterization has been extensively explored for neural model training, ranging from theoretical studies of how sufficiently large yet contrived models can converge to global minima [12, 42] to empirical investigations using more realistic examples of how training explores loss landscapes [43, 44], albeit from different angles. More recently, dramatic advances have been made by training larger neural models [4, 5]. It was further observed that accuracy scales as a function of model size (dubbed as scaling laws [1–3]) and larger models can converge to better accuracy in fewer training steps [7, 44].

Applying sparsity to reduce the size of neural models has been a topic of interest for the past three decades [22, 23, 45–48]. Typically, sparse models are constructed by training much larger models (that are easier to train) and removing some of their weights either after training [24, 48] or gradually alongside training [9, 49, 50]. However, the above are only useful to reduce costs for inference.

For training acceleration, early works [13, 15, 16] sought to remove weights before training with limited success [17, 50]. Surprisingly, it was shown sparse models can be trained when initialized the same way as a larger trained model [14]. After this breakthrough, many works [20, 22] tried to dynamically learn sparse models by rewiring their weights throughout training (though some works were published earlier [18, 19]), which improved accuracy but not enough to match that of larger models. More recent advances [29, 31, 33, 34, 51] introduced gradient updates for all weights in a neural model, including ones that do not participate in training, and observed better success.

Our explanations about what role adding more weights might have on training – they act as extra degrees of freedom which facilitate search for neural models – complement existing observations that larger models (which have more weights) train better and faster. We use this understanding to consolidate many ideas that have been circling around in sparsity literature but remain not clearly understood (how frequently must weights be rewired and why). Recent works converging in similar directions [31, 34] or seeking better understanding [52] are conducted in tandem with our research.

6 Conclusion

We describe a simple reason more weights are needed for training than inference – adding more weights gives neural models extra degrees of freedom to augment search spaces (with new paths for optimization) for training neural models effectively (or escaping critical points). Our experiments uncover recommendations to approximate the behavior of larger models (or wider search spaces) when training sparse models and demonstrate that they work across dozens of deep learning workloads.

We believe these results open many questions. On the practical side, it may be interesting to consider how could this strategy be adopted to accelerate real-world training and inference workloads today, reducing the environmental impact from training very large models. On the theoretical side, we would like to understand better ways to approximate wider search spaces in cases where accuracy still suffers as well as how to address potential biases induced by sparse models which may affect applications. We hope that our results will spur further research on these unconventional architectures, which challenge the default choice held by massive neural models today.
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A primer on search spaces

We expand our investigations on the role of search spaces in neural model training to cover more neural architectures and deep learning tasks.

Figure 7 shows the fraction of weights whose magnitudes remains above (active set) or below (inactive set) a threshold until the end of training, where the threshold is chosen such that one-fourth of the weights remain for inference tasks. Similar as observed in Section 2.1, we find neural weights that are needed for inference (or active set) are discovered throughout rather than early in training for other tasks as well.

Figure 8 illustrates cumulative distances (normalized by the maximum) that neural models traverse in search space as a function of the number of weights being added to the active set. Besides vision tasks, we find adding weights also help neural models better explore search spaces for language tasks.

Figure 9 shows the fraction of time ($\Delta$) it takes for weights to decorrelate as a function of magnitudes obtained after training. Since their correlations are short-termed, added weights (or weights that obtain small values) do not learn meaningful representations over time, rather they provide alternate paths for optimization during training to escape critical points.
Similar conclusions can be drawn as in Section 2, providing more evidence that the size of search spaces influences neural model training.

B Recommendations for training

We expand our investigations on how to approximate the behavior of larger neural models (or wider search spaces) when training sparse models, covering more neural architectures and deep learning tasks.

Figure 10 plots the task error as a function of rewiring steps $r$ for sparse models of different sizes $d$, where $d$ denotes the ratio of weights being used. We observe that error increases with less frequent rewiring ($r \to \infty$) for other vision tasks, since rewiring is related to how often the search space expands.

Figure 11 shows the task error as a function of the scale factor $s$ applied to gradient updates for non-participating weights. We observe the error increases with decreasing contributions of the gradients ($s \to 0$), which suggests updates to non-participating weights are also important during training for other tasks.
Figure 11: Same as Figure 5 (middle). From left to right: Mask R-CNN (Semantic Segmentation), Transformer (Machine Translation), Transformer-XL (Language Modeling).

Figure 12 demonstrates the task error as a function of the number of training steps \( z \) at which non-participating weights are reset to zero. Similar to results in Section 3.2, error rates saturate after sufficient training (\( z \sim 1k \)), which reinforces the idea that non-participating weights augment search spaces rather than model capacity.

Figure 13 compares various exploration and exploitation strategies for training sparse models, as described in Section 3.3. While task accuracy degrades with lack of proper exploration or exploitation, inducing exploitation by removing gradient noise from non-participating weights (Fix, Reset, Regularize) substantially decreases the error rates.

Figure 13: Same as Figure 4. From left to right: Transformer (Translation), Transformer-XL Base (Language Modeling), Transformer-XL Large (Language Modeling).
The above results across more deep learning workloads further validate recommendations put forth in Section 3 for training sparse models.

C Methodology

Algorithm 1 summarizes possible methods using recommendations described in the paper.

Algorithm 1  \( \text{TRAIN}(D, \gamma, d, v, z, \beta) \)

1: Initialize neural weights \( w \) at random
2: for each training iteration \( t \) do
3: Sample a mini batch of data \( D \)
4: \( p \leftarrow w_i \) if \( w_i \geq \tau \), where \( \tau \) is chosen so that \( |p| = d \) \( \triangleright \) Get participating weights
5: \( n \leftarrow w_i \) if \( w_i < \tau \) \( \triangleright \) Get non-participating weights
6: \( t \leftarrow L(p, D) \) \( \triangleright \) Forward pass
7: \( \frac{\partial L}{\partial w} \leftarrow \frac{\partial L(p, D)}{\partial w} \) \( \triangleright \) Backward pass
8: \( w \leftarrow w + \gamma \frac{\partial L}{\partial w} \) \( \triangleright \) Optimizer step
9: if \( t \geq v \) then \( n \leftarrow 0 \) \( \triangleright \) FIX
10: if \( t \mod z = 0 \) then \( n \leftarrow 0 \) \( \triangleright \) RESET
11: \( n \leftarrow n - \beta n \) \( \triangleright \) REGULARIZE

We rewire weights based on magnitude in order to preserve long-term correlations that represent learning. At each point in time, the top \( d \)-proportion of weights in each layer of a neural model participate in training, and the rest do not participate. We use the participating weights to compute loss and gradients, while optimizers perform gradient updates for both participating and non-participating weights (equivalent to a straight-through-estimator).

For example, the forward stage computes \( x_{\ell+1} = px_{\ell} \) recursively for an input \( x \) and participating weights \( p \), which is then fed into the loss function \( L \). The backward stage derives two sets of gradients: activation gradients from layer \( \ell \) are passed to downstream layer \( \ell - 1 \) using \( \frac{\partial L}{\partial x_{\ell-1}} = \frac{\partial L}{\partial x} \times p^T_{\ell} \), and weight gradients from layer \( \ell \) are computed as \( \frac{\partial L}{\partial w_{\ell}} = \frac{\partial L}{\partial x_{\ell}} \times x^T_{\ell-1} \), where \( w \) denotes all of the weights in a neural model.

Training observes no memory storage savings, since we perform gradient updates for all the weights. The equations above can also only accelerate forward stages, and parts of backward stages that compute activation gradients. For further acceleration, we can consider applying sparsity to parts of backward stages that compute weight gradients [30].

We also note the methods above makes a tradeoff between exploration and exploitation. While approximating wider search spaces enhances more exploration, restricting non-participating weights enables exploitation. \( v, z \), and \( \beta \) dictate how and when to switch between these two phases. These variables can either be automated or kept as default, and used either in isolation or combined.

D Implementation details

We design experiments on PyTorch [53] using custom autograd functions for convolutions and linear layers. Our functions emulate sparsity using a binary tensor (or mask) that we multiply elementwise with the weights of each layer during forward and backward propagation. We determine masks based on weight magnitudes, as described earlier, after the optimizer step and before the next training iteration.

SEARCH. We approximate wider search spaces as described in the previous appendix. We find the best tradeoff between exploration and exploitation using variables \( v, z \), and \( \beta \). Stopping rewiring after half of training \( (v = 0.5) \) often provides the best results. \( z \) should be large enough to keep short-term correlations (on the order of 1k training steps). \( \beta = 0.0002 \) chosen in [31] roughly matches our choice for \( z \). All methods perform equally well with their optimal variables, so we can take different choices across workloads. For most experiments we adopt FIX and use R in a few select cases.
**REDUCE.** For smaller models, we reduce the widths of neural layers by a factor of $1/d$ to match the number of weights used in sparse models. In some cases, we approximate this behavior by applying sparsity once at initialization, which represents an upper bound since tensors retain their dimensionality.

**LOTTERY.** We construct lottery tickets by training neural models to completion (e.g., $k$ steps) and computing masks based on their trained weights $w_t$. We initialize sparse models with the original initialization ($t = 0$) or after some amount of training ($t = \epsilon$), and train them for $k - t$ steps using the same hyperparameters. Following [54], we choose $\epsilon$ between $k/10$ and $k/100$ across various workloads. While larger values for $\epsilon$ can deliver better accuracy, this comes at the cost of training acceleration, also neural models are initialized closer to their solutions [55].

**SET AND RIGL.** We rewire participating weights over time by removing their weakest values and either adding new ones randomly [19] or based on their gradients [21]. We initialize weights to zero when they become participating. The fraction $f$ of weights to rewire decays linearly throughout training, which works as well as more complex schedules (such as cosine and inverse power). We choose the best initial value for $f$ and rewiring frequency across various workloads.

**E Experimental setup**

**E.1 Image classification**

We train popular convolutional models like ResNets [56], VGG [57], Stacked U-Nets [58], Dilated Residual Networks [59], Inception [60], MobileNet [61], as well as vision transformers like DeiT [62]. Training involves standard pipelines for image classification on ImageNet-2012 as described in literature and found in public code repositories. For most workloads, we adopt learning rates with linear warmups for the first 5 epochs, drop the learning rate by a factor of ten at epochs 30-60-80, and stop training after 90 epochs. A few select neural models (e.g., mobilenets and vision transformers), however, are trained for more epochs using linear or cosine schedules.

We measure model quality using top-1 classification accuracy. We apply sparsity to convolutions and linear layers with some exceptions: convolutions whose input channels are not divisible by 16 (e.g., first convolution layer, group and depthwise separable convolutions).

**E.2 Image segmentation and detection**

Image segmentation and detection tasks include both regression and classification components. Popular detectors and segmentors are typically trained in two phases: first a backbone is trained for image classification, followed by the addition of model components that are trained for detection or segmentation. Backbones are trained on ImageNet-2012, while downstream tasks are trained on COCO. We adapt training scripts and code from Detectron2 [63].

We train neural models such as regions with convolutional neural networks (R-CNN) variants [64,65], vanilla one-shot detectors [66], and with focal loss [67]. Convolution and linear layers encountered in pretrained backbones are sparse, like for classification tasks. Detection and segmentation heads are also targeted.

**E.3 Generative modeling**

Generative Adversarial Networks (GANs) contain two subnetworks: a generative model and a discriminative model which combine regression and discrimination tasks during training. For image and video tasks, the generator model regresses pixel colors. We explore conditional GANs for super image-to-image translation and video-to-video synthesis using Imaginaire [68]. We measure quality of generated outputs using the Frechet Inception Distance (FID). We experiment with generative neural models like Pix2PixHD [69], Vid2Vid [70], and FewShot-Vid2Vid [71], targeting convolution and linear layers.

**E.4 Machine Translation**

We explore transformer and recurrent neural models for language translation. All models are encoder-decoder style architectures trained for English to German (En-De) translation on WMT. We adapt...
model and training code from Fairseq [72] and NVIDIA Deep Learning Examples [73]. We measure model quality using BLEU scores.

We experiment with GNMT [74] and transformer-based architectures [75, 76]. All linear layers are sparse, except for embeddings and vocabulary projections.

E.5 Language modeling

We consider recent advances in word-level language modeling using transformer decoder (left-to-right) or encoder (bi-directional) architectures [77, 78]. We pretrain language models in an unsupervised fashion on WikiText-103 or Wikipedia corpus, and evaluate on downstream tasks that are zero shot or require additional finetuning. We train them using Megatron [79] and NVIDIA Deep Learning Examples [73]. Model quality is measured in terms of perplexity or F1 score.

We train language models such as Transformer-XL [80] and BERT [78]. We make all linear layers sparse, except for embeddings, vocabulary projections, and classification heads for downstream tasks.

F Search capacity

It is also interesting to understand how well can our sparse models approximate wider search spaces. To this end, we measure where does accuracy of sparse models (SEARCH) fall between accuracies of larger neural models with free search (REGULAR) and smaller ones that operate on reduced spaces (REDUCE). We designate a metric \( f = (\text{REGULAR} - \text{SEARCH}) / (\text{REGULAR} - \text{REDUCE}) \) to represents the capacity for search, where a value of one means accuracy matches that of free search, and zero implies accuracy is no better than without any additional search.

Figure 14 illustrates \( f \) as a function of size \( d \) for various tasks. For \( d \approx 0.5 \), sparse models are able to approximate wider search spaces found in larger models across all neural architectures and tasks. This helps explain why models with moderate amounts of sparsity are less sensitive to search constraints. While \( f \) decreases for sparser models (\( d \ll 0.5 \)), they are still much more efficient than trivially smaller models (obtained by reducing widths of neural layers). Even at \( d = 0.1 \), most models can capture three-quarters of the possible search capacity.

G Effects of longer training

We expand our investigations on the effects of longer training to cover more neural architectures and deep learning task. Figure 15 illustrates accuracy deltas as a function of training time \( t \). For vision tasks, we find sparse models of moderate sizes (\( d \geq 0.25 \)) can match accuracy of regular models after sufficient training (\( t \geq 3 \)). On the other hand, for language modeling, sparse models cannot match accuracy for any time \( t \) because regular models are already near capacity for the task at hand. Obviously, when using popular training schedules (\( t \sim 1 \)), sparse models can be trained a bit longer to recover the lost accuracy.
This appendix discusses various structures (or collection of weights in a sparse model) considered in the paper that are amenable for acceleration using modern matrix-math hardware.

### H.1 Block sparsity

We first look at block sparsity [9, 38], which removes blocks of contiguous elements (or weights) in a neural layer as shown in Figure 16. Block sparsity addresses common issues that are present for unstructured formats: indices for active blocks reduce storage overhead by a factor of the block size, blocks are stored contiguously in memory which reduces irregular memory accesses, and their computations can exploit faster matrix-math hardware, such as Tensor Cores in NVIDIA GPUs.

We construct block sparse structures by (1) partitioning a neural layer into a set of blocks, (2) aggregating elements in each block into a metric, and (3) removing blocks according to some criteria based on their metrics. While we remove blocks based on largest magnitude \( \max_{i \in (1, b^2)} |w_i| \), other choices such as the \( p \)-norm \( \left( \sum_{i=1}^{b^2} |w_i|^p \right)^{1/p} \) achieve similar results.

Because structures restrict the combination of weights that can be formed in a neural model, an important question is then for what block sizes \( b \) (if any) can sparse models approximate wider search spaces. Table 3 lists accuracy differences between sparse and regular models for \( d = 0.5 \) using various block sizes. We find block sparse models fail to maintain accuracy, performing no better than smaller models with neural layers of reduced widths. Notably, accuracy deteriorates for all tasks and block sizes, including smaller blocks \( b \leq 4 \) that are less amenable for hardware acceleration. In other words, block structures are too coarse for approximating wider search spaces. For example,
Table 3: Accuracies for regular models and their differences for block sparse models using different block sizes $b$.

| Model       | REGULAR | REDUCE | $b = 1$ | $b = 2$ | $b = 4$ | $b = 8$ | $b = 16$ | $b = 32$ |
|-------------|---------|--------|---------|---------|---------|---------|---------|---------|
| Transformer-XL | 22.88   | -2.48  | -0.82   | -2.03   | -2.35   | -2.24   | -2.36   | -2.41   |
| Transformer   | 28.43   | -0.77  | -0.21   | -0.75   | -1.10   | -1.47   | -1.98   | -1.94   |
| GNMT         | 24.81   | -2.75  | -0.15   | -0.15   | +0.02   | -0.08   | -0.15   | +0.30   |
| ResNet50     | 76.71   | -1.63  | -0.46   | -0.99   | -2.19   | -2.79   | -       | -       |
| Mask RCNN    | 35.03   | -0.88  | -0.28   | -1.17   | -2.43   | -3.74   | -4.41   | -5.00   |

different weights in a block may have different roles during training: a block that participates in training may contain weights that are not important, whereas a non-participating block may have weights that were crucial to keep. Both cases prevent sparse models from retaining weights over time that are relevant for search, and thus impacting accuracy.

H.2 2:4 sparsity

We next consider Sparse Tensor Cores [24] introduced in NVIDIA Ampere GPU architecture which exploit 2:4 sparsity and have twice the math throughput of regular matrix units. Figure 16 shows that 2:4 sparsity mandates each group of four values must have at least two values that are zero. Typically, 2:4 is applied on weights $w$ in the forward pass, $y = wx$. However, we can also apply 2:4 on weight transposes $w^T$ for the backward pass, $\partial L/\partial x = \partial L/\partial y \times w^T$. We denote these two options as 2:4 1D that accelerates forward pass for inference [24], and 2:4 2D that accelerates both forward and backward passes for training.

The 2:4 sparsity structure must always be imposed along the inner dimension of dot products. For linear layers, we apply 2:4 on a $n \times k$ weight tensor along $k$ or $n$ (for forward or backward pass, respectively). For convolutions, we apply 2:4 on a $k \times c \times r \times s$ weight tensor along input channels $c$ or $k \times r \times s$ (for forward or backward pass, respectively), where $k$ denotes output channels, $r$ and $s$ are kernel dimensions.

We can satisfy 2:4 1D constraints by removing weights with lowest magnitudes. Since 2:4 2D constraints have no trivial solution, we seek to minimize the cumulative magnitude of the removed weights. In other words, for each $4 \times 4$ block in the tensor, we construct all possible combinations of 2:4 2D patterns, compute their 1-norm, and choose the structure that has the largest norm.

I Comparisons to sparsity research

We expand our comparisons to literature covering more neural architectures and deep learning tasks. Figure 17 illustrates the task error (or difference in accuracy between regular and sparse models) across various methods, neural architectures, and tasks. We find our strategy outperforms competing approaches in most cases with a few exceptions: we do not outperform lottery tickets in segmentation tasks because detectors and segmentors are trained with small learning rates which limit exploration. For generation tasks, our models are slightly worse for $d \sim 0.5$ (though noisy scores make it difficult to draw conclusions), but the asymptotic behavior of error rates as $d \to 0$ clearly indicates that our approach is superior.
Figure 17: Same as Figure 5. Clockwise: InceptionV3 (Classification), DenseNet161 (Classification), VGG19 (Classification), GNMT (Translation), Pix2PixHD (Generation), Mask RCNN (Segmentation).