A common algebraic description for probabilistic and quantum computations

Martin Beaudry†
Université de Sherbrooke

José M. Fernandez
Université de Montréal

Markus Holzer‡
Technische Universität München

Abstract

We study the computational complexity of the problem SFT (Sum-free Formula partial Trace) : given a tensor
formula $F$ over a subsemiring of the complex field $(\mathbb{C}, +, \cdot)$ plus a positive integer $k$, under the restrictions that
all inputs are column vectors of $L_2$-norm 1 and norm-preserving square matrices, and that the output matrix is a
column vector, decide whether the $k^{\text{th}}$ partial trace of $FF^\dagger$ is superior to $1/2$. The $k^{\text{th}}$ partial trace of a matrix is
the sum of its lowermost $k$ diagonal elements. We also consider the promise version of this problem, where the
$1/2$ threshold is an isolated cutpoint. We show how to encode a quantum or reversible gate array into a tensor
formula which satisfies the above conditions, and vice-versa; we use this to show that the promise version of SFT
is complete for the class BPP for formulas over the semiring $(\mathbb{Q}^+, +, \cdot)$ of the positive rational numbers, for BQP
in the case of formulas defined over the field $(\mathbb{Q}, +, \cdot)$, and for P in the case of formulas defined over the Boolean
semiring, all under logspace-uniform reducibility. This suggests that the difference between probabilistic and
quantum polynomial-time computers may ultimately lie in the possibility, in the latter case, of having destructive
interference between computations occurring in parallel.

1 Introduction

The “algebraic approach” in the theory of computational complexity consists in characterizing complexity classes
within unified frameworks built around a computational model or problem involving an algebraic structure (usually
finite or finitely generated) as the main parameter. In this way, various complexity classes are seen to share the same
definition, up to the choice of the underlying algebra. Successful examples of this approach include the description of
$\text{NC}^1$ and its subclasses $\text{AC}^0$ and $\text{ACC}^0$ in terms of polynomial-size programs over finite monoids [4], and analogous
results for PSPACE, the polynomial hierarchy and the polytime mod-counting classes, through the use of polytime
leaf languages [4]. A more recent example is the complexity of problems whose input is a tensor formula, i.e.
a fully parenthesized expression where the inputs are matrices (given in full) over some finitely generated algebra

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†Corresponding author.
‡Part of the work was done while the author was at Département d'I.R.O., Université de Montréal.
Beaudry: Département de mathématiques et d’informatique, Université de Sherbrooke, 2500 boul. Université, Sherbrooke, Québec, J1K 2R1
Canada. email: beaudry@DMI.usherbrooke.ca
Fernandez: Département d'I.R.O. Université de Montréal, C.P. 6128, succ. Centre-Ville, Montréal, Québec, H3C 3J7 Canada. email:
fernandez@iro.umontreal.ca
Holzer: Institut für Informatik, Technische Universität München, Arcisstraße 21, D-80290 München, Germany. email:
holzer@informatik.tu-muenchen.de
and the allowed operations are matrix addition, multiplication, and tensor product (also known as outer, or direct, or Kronecker product). Depending on the semiring over which the formula is defined, the problem of deciding whether the output matrix contains at least one nonzero entry is complete for NP (Boolean semiring) and \( \text{MOD}_q - \text{P} \) (modulo semiring \( \mathbb{Z}_q \)) \cite{7}. Other common-sense computational problems on tensor formulas were analyzed in \cite{7, 5}.

Tensor formulas are a compact way of specifying very large matrices. As such, they immediately find a potential application in the description and the behavior of circuits, be they classical Boolean, arithmetic (tensor formulas over the appropriate semiring) or quantum (formulas over the complex field, or an adequately chosen subsemiring thereof). In this paper, we formalize and confirm this intuition, in that we define a meaningful computational problem over tensor formulas which enables us to capture the significant complexity classes \( \text{P}, \text{BPP}, \) and \( \text{BQP} \). Looking at variants of the problem enables us to capture further complexity classes; a table in the last section summarizes our results.

Apart from offering a first application of the algebraic approach to quantum computing, our paper reasserts the point made by Fortnow \cite{12}, that for the classes \( \text{BPP} \) and \( \text{BQP} \), the jump from classical to quantum polynomial-time computation consists in allowing negative matrix entries for the evolution operators, which means the possibility of having destructive interference between different computations done in parallel.

2 Background on circuits and complexity

We use standard notions and notations from computational complexity, see for example \cite{2, 20}. In particular we assume that the reader is familiar with the deterministic and probabilistic Turing machine models, with the usual notion of a Boolean circuit, and with logspace many-one reducibility: a set \( K \) is logspace time many-one reducible to a set \( L \) if there is a logspace computable mapping \( f \) such that for all \( x, x \in K \) iff \( f(x) \in L \).

To handle the three types of computation discussed in this paper (deterministic, probabilistic and quantum), we use gate arrays as a common setting. From now on, we reserve the word circuit to the traditional idea of an acyclic network with a unique output bit, and we use gate array to describe those computational networks which satisfy the following definition.

**Definition 2.1.** Let \( n, d \geq 1 \). A width \( n \), \( d \)-leveled gate array is a \( n \times d \) array where each line is called a wire and each column a level. The size of a gate array is the number \( nd \). A gate is a set of array entries from the same level (corresponding to the wires involved in the gate’s operation) together with a square matrix which describes its action. Gates on a given level act on disjoint sets of entries from this level. Let the levels be numbered 1 to \( d \) from left to right. Each wire carries a bit from a level to the next in the left-to-right direction; the value entering column 1 from the left is called an input the value exiting level \( d \) to the right is an output.

A gate of \( k \) binary inputs operates on the set of \( k \)-bit vectors by mapping each of the \( 2^k \) possible combinations of input values to a combination of output values. The extra constraint, that all gates act on neighboring wires, can be enforced on an arbitrary array at the cost of inserting a quadratic number of extra levels with “swap” gates, which interchange the values carried by two adjacent wires.

Gate arrays are used in particular to describe reversible classical computations. A computation is reversible iff knowledge of its output is sufficient to be able to deterministically reconstruct the input. It has been shown that for any polynomial-time deterministic algorithm there exists an equivalent polynomial-time reversible algorithm; in other words, from every polynomial-size Boolean circuit an equivalent reversible gate array \cite{13} can be constructed, by
• modifying the circuit so that the numbers of input and output bits are equal;
• replacing the usual one-output gates with reversible gates;
• making sure that an especially identified “decision” bit takes value 1 at the output level iff the original circuit’s output is 1.

From the description of the original circuit, its equivalent reversible gate array can be constructed in deterministic logspace; circuit size and depth are increased only by a polynomial factor; usually, a polynomial number of extra input bits initialized at 0, called ancillary bits, also has to be added in the process. It has been shown that this array can be built solely with the one- and two-bit reversible operations, plus either one of the “Toffoli” (Θ) or “Fredkin” (Φ) gates, where

$$\Theta = \begin{bmatrix} 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 \end{bmatrix}$$

and

$$\Phi = \begin{bmatrix} 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 \end{bmatrix} ;$$

here the top left position corresponds to bit values 000 and the bottom right to 111.

Standard techniques can therefore be used in sequence to transform the description of a polytime deterministic Turing machine and its input x into an instance of the Circuit Value Problem with constant inputs (where x is hardwired) \[16\], then to turn this circuit into a reversible gate array, in order to give the following definition for the class P. (Alternatively, one can start from the definition of P as the class of those languages decided by logspace-uniform families of polynomial-size Boolean circuits.)

**Definition 2.2.** P is the class of those languages \(L \subset \Sigma^*\) for which there exist a logspace-computable function which, given an input \(x \in \Sigma^*\), computes the encoding of a reversible gate array \(C(x)\) with constant inputs, whose decision bit takes value 1 at the output level iff \(x \in L\).

An encoding for \(C(x)\) is suitable for this definition if it consists of a reasonable description of the array’s inputs, wiring and gates; the latter can wlog be restricted to have constant fan-in/fan-out, so that the action of each gate can be specified with a constant-size Boolean matrix.

Complexity classes for polynomial-time probabilistic computation are usually defined in terms of a polytime Turing machine which picks a random bit at every step of its computation, and otherwise acts deterministically (see e.g. \[2\]).

An equivalent circuit is built from this Turing machine and its input, in which an appropriate number of random bits are fed in alongside the (constant) input bits; whether the input belongs to \(L\) is verified by counting those combinations of random bits for which the output bit takes value 1. All random bit combinations have equal length and are equally likely.

**Definition 2.3.** PP is the class of those languages \(L \subset \Sigma^*\) for which there exist a logspace-computable function which, given an input \(x \in \Sigma^*\), yields the encoding of a reversible gate array \(C(x)\) with a combination of constant and random inputs, such that \(x \in L\) iff \(f_C(x) > \frac{1}{2}\) and \(x \notin L\) iff \(f_C(x) < \frac{1}{2}\), where \(f_C(x)\) denotes the probability that
C(x)’s decision bit takes value 1 at the output level.

BPP is defined with the extra condition that there exists a parameter \( \varepsilon, 0 < \varepsilon < \frac{1}{2} \), such that \( x \in L \) iff \( f_C(x) > \frac{1}{2} + \varepsilon \) value 1 at the output level.

The class NP can be similarly defined, with the condition that \( x \in L \) iff \( f_C(x) > 0 \).

The definition of BPP includes the implicit constraint, that the proportion of accepting computations can never fall inside the interval \([\frac{1}{2} - \varepsilon, \frac{1}{2} + \varepsilon]\); in other words, \( \frac{1}{2} \) is an isolated cutpoint. Note that both PP and BPP can be redefined with a cutpoint other than \( \frac{1}{2} \).

Polynomial-time quantum computation was defined originally in terms of quantum Turing machines [8]: the data handled by this machine (qubits) are formally represented as a vector whose complex components give the distribution of amplitudes for the probability that the qubits be in a certain combination of values; each transition of the machine acts as a unitary transformation on this vector.

It was later shown [21] that a quantum Turing machine and its input can be encoded in deterministic polynomial time into an array of quantum gates, if one is allowed a small probability of error. Each wire in a quantum gate array represents a path of a single qubit (in time or space, forward from left to right), and is described by a state in a two dimensional Hilbert space with basis \(|0\rangle\) and \(|1\rangle\). Just as classical bit strings can represent the discrete states of arbitrary finite dimensionality, so a string of \( n \) qubits can be used to represent quantum states in any Hilbert space of dimensionality up to \( 2^n \). The action of a gate of \( k \) inputs is a unitary operation of the group \( \mathbb{U}(2^k) \), i.e., a generalized rotation in a Hilbert space of dimension \( 2^k \). It has been shown that a small set of one- and two-qubit gates suffices to build quantum arrays, in that any \( n \)-qubit gate can be simulated by a subarray consisting of two-qubit gates, and the number thereof is at most an exponential in \( n \) (see for example [3, 9, 18, 17]). As two-qubit gates it suffices to take the \textit{controlled-not} \( N \). Because of its usefulness we also mention the two-qubit “swap” gate \( T \).

\[
N = \begin{bmatrix}
1 & 0 & 0 & 0 \\
0 & 1 & 0 & 0 \\
0 & 0 & 0 & 1 \\
0 & 0 & 1 & 0 
\end{bmatrix}, \quad T = \begin{bmatrix}
1 & 0 & 0 & 0 \\
0 & 0 & 1 & 0 \\
0 & 1 & 0 & 0 \\
0 & 0 & 0 & 1 
\end{bmatrix}.
\]

The vector of qubits received as input by a quantum gate array can be regarded as a linear combination of \textit{pure states}. There is a \textit{measurement} done on the array’s output, which consists in projecting the output vector onto a subspace, usually defined by setting a chosen subset of the qubits to \(|1\rangle\) (“accepting subspace”). If the qubits are numbered 1 to \( n \), then a \( k \)-qubit accepting subset can be chosen to be qubits 1 to \( k \), at the cost of inserting a quadratic number of extra swap gates. For the sake of simplicity, we can assume that the final output state will be such that all qubits other than the decision qubit have value \(|0\rangle\). This is without loss of generality, as it will be possible to “uncompute” the circuit while keeping the value of the decision bit. Thus, the accepting subspace has dimension 1, and contains only one base vector, and similarly for the rejecting subspace.

**Definition 2.4.** BQP is the class of those languages \( L \subset \Sigma^* \) for which there exist a logspace-computable function which, given an input \( x \in \Sigma^* \), yields the encoding of a quantum gate array \( C(x) \) with constant inputs, and a parameter \( \varepsilon, 0 < \varepsilon < \frac{1}{2} \), such that \( x \in L \) iff \( f_C(x) > \frac{1}{2} + \varepsilon \) and \( x \notin L \) iff \( f_C(x) < \frac{1}{2} - \varepsilon \), where \( f_C(x) \) denotes the probability that the qubits of \( C(x) \) be projected onto the accepting subspace at the output level.
The same definition, with unitary operators and input vectors having rational entries and without the condition that $\frac{1}{2}$ be an isolated cutpoint, yields a “quantum” version of the (classical) class PP. However, this “new” class is in fact no different than PP itself, as can be shown by a simple counting complexity theory.

For any language $L$ in this class, there exists a quantum circuit that accepts it, for which we can define the non-negative functions $f(x)$ and $g(x)$, as the sum of all the positive and negative contributions, respectively, to the total amplitude for the accepting configuration on a given input $x$. The amplitude of this unique accepting configuration is $f(x) - g(x)$. Similarly, define $f'(x)$ and $g'(x)$ for the rejecting configuration, with the corresponding rejecting amplitude being $f'(x) - g'(x)$. It is easy to see that $f$, $g$, $f'$, and $g'$ are all #P functions. The difference between the probability of accepting and rejecting of this circuit is thus

$$\begin{align*}
(f - g)^2 - (f' - g')^2 &= f^2 + g^2 + 2tf'g' - (f'^2 + g'^2 + 2fg)
\end{align*}$$

which is a $\text{GapP}$ function, since #P is closed under (finite) sum and product. This function will be positive if and only if $x$ is in $L$, which is another way of characterizing languages in the class PP [11].

On the other hand, the languages defined with quantum gate arrays where unitary operators have rational entries and such $x \in L$ iff $f_C(x) > 0$ form the complexity class NQP, the quantum analogue to NP, which coincides with the (classical) class coC$_=\text{P}$ [10].

### 3 Tensor Algebra

A semiring is a tuple $(\mathbb{K}, +, \cdot)$ with $\{0, 1\} \subseteq \mathbb{K}$ and binary operations $+, \cdot : \mathbb{K} \times \mathbb{K} \to \mathbb{K}$ (sum and product), such that $(\mathbb{K}, +, 0)$ is a commutative monoid, $(\mathbb{K}, \cdot, 1)$ is a monoid, multiplication distributes over sum, and $0 \cdot \alpha = \alpha \cdot 0 = 0$ for every $\alpha$ in $\mathbb{K}$ (see, e.g., [15]). A semiring is a ring if and only if $(S, +, 0)$ is a group. In this paper we consider the following semirings: the Booleans $(\mathbb{B}, \lor, \land)$, the field of rational numbers $(\mathbb{Q}, +, \cdot)$, the semiring $(\mathbb{Q}^+, +, \cdot)$ of positive rational numbers, and the field of complex numbers $(\mathbb{C}, +, \cdot)$.

Let $M_{\mathbb{K}}^k$ denote the set of all matrices over $\mathbb{K}$, and define $M_{\mathbb{K}}^{k,\ell} \subseteq M_{\mathbb{K}}^k$ to be the set of all matrices of order $k \times \ell$. Let $[k]$ denote the set $\{1, 2, \ldots, k\}$; for a matrix $A$ in $M_{\mathbb{K}}^{k,\ell}$ and $(i, j) \in [k] \times [\ell]$, the $(i, j)$th entry of $A$ is denoted by $a_{i,j}$ or $(A)_{i,j}$. Addition and multiplication of matrices in $M_{\mathbb{K}}^k$ are defined in the usual way. Additionally we consider the tensor product $\otimes : M_{\mathbb{K}}^{k,\ell} \times M_{\mathbb{K}}^{m,n} \to M_{\mathbb{K}}^{km,\ell n}$ of matrices, also known as Kronecker product, outer product, or direct product, which is defined as follows: for $A \in M_{\mathbb{K}}^{k,\ell}$ and $B \in M_{\mathbb{K}}^{m,n}$ let $A \otimes B \in M_{\mathbb{K}}^{km,\ell n}$ be

$$A \otimes B := \begin{bmatrix}
a_{1,1} \cdot B & \ldots & a_{1,\ell} \cdot B \\
\vdots & \ddots & \vdots \\
a_{k,1} \cdot B & \ldots & a_{k,\ell} \cdot B
\end{bmatrix}.$$ 

Hence $(A \otimes B)_{i,j} = (A)_{q,r} \cdot (B)_{s,t}$ where $i = k \cdot (q - 1) + s$ and $j = \ell \cdot (r - 1) + t$. 

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The following notation is used: let $I_n$ be the order $n$ identity matrix, $e^n_i$ the $n \times 1$ column vector whose $i$th entry has value 1 and the others 0, and let $A \otimes^n$ stand for the $n$-fold iteration $A \otimes A \otimes \cdots \otimes A$.

**Stride permutations**, which play a crucial role in the implementation of efficient parallel programs for block recursive algorithms such as the fast Fourier transform (FFT) and Batcher’s bitonic sort (see [19]) will be useful in our proofs. The **mn-point stride $n$ permutation** $p_{mn}^n \in M_{K}^{mn,mn}$ is defined as

$$p_{mn}^n e^m_i \otimes e^n_j = e^n_j \otimes e^m_i,$$

where $e^m_i \in M_{K}^{m,1}$ and $e^n_j \in M_{K}^{n,1}$. In other words, the matrix $p_{mn}^n$ permutes the elements of a vector of length $mn$ with stride distance $n$. We will make use of the following identities on stride permutations.

**Proposition 3.1.** The following holds for all $\ell, m, n$:

1. $(p_{mn}^n)^{-1} = p_{mn}^n$;
2. $p_{mn}^n = p_{m}^{\ell mn} \cdot p_{n}^{\ell mn}$;
3. $p_{n}^{\ell mn} = (p_{n}^{\ell n} \otimes I_m) \cdot (I_\ell \otimes p_{n}^{mn})$. \hfill \qed

### 3.1 Tensor formulas

**Definition 3.2.** The **tensor formulas** over a semiring $K$ and their order are recursively defined as follows.

1. Every matrix $F$ from $M_{K\ell}^{k}$ with entries from $K$ is a (atomic) tensor formula of order $k \times \ell$.
2. If $F$ and $G$ are tensor formulas of order $k \times \ell$ and $m \times n$, respectively, then
   - $(F + G)$ is a tensor formula of order is $k \times \ell$ if $k = m$ and $\ell = n$;
   - $(F \cdot G)$ is a tensor formula of order $k \times n$ if $\ell = m$;
   - $(F \otimes G)$ is a tensor formula of order $km \times \ell n$.
3. Nothing else is a tensor formula.

We say that a tensor formula $F$ is sum-free whenever none of $F$ and its subformulas has the form $G + H$. Let $T_K$ denote the set of all tensor formulas over $K$, and define $T_K^{k,\ell} \subseteq T_K$ to be the set of all tensor formulas of order $k \times \ell$.

In this paper we only consider semiring elements whose value can be given with a standard encoding over some finite set $G$. Input matrices can therefore be string-encoded using list notation such as “[[001][101]].” Nonatomic tensor formula can be encoded over the alphabet $\Sigma = G \cup \{+,\cdot,\otimes\}$. Strings over $\Sigma$ which do not encode valid formula are deemed to represent the trivial tensor formula $0$ of order $1 \times 1$.

The size of a tensor formula $F$ is 1 if $F$ is atomic, otherwise $F = G \circ H$ for $\circ \in \{+,\cdot,\otimes\}$ and the size of $F$ is 1 plus the sizes of $G$ and $H$. The diameter of tensor formula $F$, denoted by $|F|$, is $\max\{k,\ell\}$ if $F$ is atomic of order $k \times \ell$; otherwise we have that $F = G \circ H$ is of order $k \times \ell$, and $|F| = \max\{k,\ell,|G|,|H|\}$.

It will sometimes be convenient to speak of a tensor formula in graph-theoretical terms: in this context, a tensor formula is a binary tree whose edges are directed toward the root (“output node”), whose leaves (“input nodes”) are labelled with atomic formulas and each of whose interior nodes is labelled with an operation from the set $\{+,\cdot,\otimes\}$. The depth of a tensor formula is the maximum root-leaf distance.

**Definition 3.3.** For each semiring $K$ and each $k$ and each $\ell$ we define $\text{val}_{K}^{k,\ell}: T_{K}^{k,\ell} \rightarrow M_{K}^{k,\ell}$, that is, we associate with node $f$ of order $k \times \ell$ of a tensor formula $F$ its $k \times \ell$ matrix “value,” which is defined as follows:
1. $\text{val}^k_{\mathbb{F}}(f) = F$ if $f$ is an input node labeled with $F$, 
2. $\text{val}^k_{\mathbb{F}}(f) = \text{val}^k_{\mathbb{F}}(g) + \text{val}^k_{\mathbb{F}}(h)$ if $f = (g + h)$, 
3. $\text{val}^k_{\mathbb{F}}(f) = \text{val}^k_{\mathbb{F}}(g) \cdot \text{val}^m_{\mathbb{F}}(h)$ if $f = (g \cdot h)$, and 
4. $\text{val}^k_{\mathbb{F}}(f) = \text{val}^{k/m}_{\mathbb{F}}(g) \otimes \text{val}^{m/n}_{\mathbb{F}}(h)$ if $f = (g \otimes h)$.
5. For completeness, recall that $\text{val}^k_{\mathbb{F}}(f) = 0$ whenever the formula is not valid.

The value $\text{val}^k_{\mathbb{F}}(F)$ of a tensor formula $F$ of order $k \times \ell$ is defined to the value of the unique output node.

### 3.2 The sum-free partial trace problem

A column vector $v$ with complex coefficients is a *unit* vector iff its $L_2$-norm is 1, that is, iff $v^\dagger v = 1$. In this paper, we work on probabilistic and quantum computations where the probability amplitudes are encoded in unit column vectors, and the foremost requirement on the computing model is that the inner product (hence also the $L_2$ norm) be preserved at each step of a computation. The action of each such step on the various combinations of values transported by the wires is described with a square matrix; our requirement is equivalent to asking that each matrix preserves the inner product (unitary matrices).

A square matrix $M$ over the complex numbers is *unitary* iff $M^\dagger = M^{-1}$. For a matrix $M$ over the real numbers, this translates into $M^T = M^{-1}$; which means that $M$ is *orthogonal*. It is an easily verified fact that an orthogonal matrix contains only nonnegative entries if, and only if, it is a permutation matrix (i.e., exactly one entry per line and column is 1 and all others are 0).

In the sequel, whenever we deal simultaneously with the cases where matrices with real or complex coefficients, we use the notations and vocabulary from the real case alone, in order to make the text easier to read.

The *trace* of a square matrix is the sum of its diagonal elements; for $k > 0$, its $k$th *partial trace* is the sum of its last $k$ diagonal elements, counting upwards from the lower right corner. For completeness, if $k$ exceeds the diameter of the matrix, then the $k$th partial trace coincides with the usual trace.

**Definition 3.4.** A *sum-free tensor formula* is OSL if and only if it satisfies the conditions:

- all inputs are orthogonal square matrices and/or unit column vectors;
- the output matrix is a column vector.

(We choose the term “orthogonal-system-like” because as we will show, such a formula can be reorganized as a product $M \cdot V$ of an orthogonal matrix with a column vector, i.e. as the specification of an orthogonal system of linear equations.)

**Definition 3.5.** Let $\mathbb{K}$ be a finitely generated semiring. An instance of problem $\text{SFT}(\mathbb{K})$ (“sum-free formula partial trace”) consists of an order $N \times 1$ OSL tensor formula $F$ over semiring $\mathbb{K}$ and a positive integer $k$; the problem consists in deciding whether the $k$th partial trace of $(\text{val}^{N,1}_{\mathbb{K}}(F)) \cdot (\text{val}^{N,1}_{\mathbb{K}}(F))^\top$ is greater than some predetermined constant $\alpha$, $1/2 \leq \alpha < 1$. In the “promise version” of $\text{SFT}(\mathbb{K})$, no instance can yield a $k$th partial trace which evaluates in the interval $[1 - \alpha, \alpha]$.

We also define a *nonzero version* to $\text{SFT}(\mathbb{K})$, as the problem which consists in deciding whether the $k$th partial trace of $(\text{val}^{N,1}_{\mathbb{K}}(F)) \cdot (\text{val}^{N,1}_{\mathbb{K}}(F))^\top$ is nonzero.
The following propositions show that basic questions on inputs for problem SFT($\mathbb{K}$) can be answered in polynomial time.

**Proposition 3.6.** [5] If $F$ is a tensor formula of depth $d$ which has input matrices of diameter at most $p$, then $|F| \leq p^{nd}$, and there exists a formula which outputs a matrix of exactly this diameter. (Proof omitted.)

**Proposition 3.7.** [5] Testing whether a string encodes a valid tensor formula and if so, computing its order, is feasible in deterministic polynomial time. (Proof omitted.)

## 4 From gate arrays to tensor formulas to gate arrays

In this section we show how to encode the description of a reversible or quantum gate array into a OSL tensor formula over the appropriate semiring, and conversely, how to compute from an OSL formula $F$ a gate array which will later used as a mean to solve an SFT instance built from $F$.

### 4.1 From arrays to formulas

**Lemma 4.1.** Let $C$ be a gate array operating on $n$ wires, whose gates can be described with orthogonal matrices over semiring $\mathbb{K}$. There is a logspace computable function which, given a suitable coding of $C$, computes a tensor formula $F(C)$ of logarithmic depth such that for each $x = (x_1, \ldots, x_n) \in \{0, 1\}^n$,

$$C(x) = \text{val}_{\mathbb{K}}^{n:1}(F(C) \cdot d_x),$$

where $d_x = \bigotimes_{i=1}^{n} x_i \chi_1 = e_1^1$ if $x_i = 0$, and $\chi_1 = e_2^2$ otherwise.

**Proof.** Let $C$ have $m$ levels and let $C_i$ denote the $i^{th}$ level, with $C_1$ the left-most and $C_m$ the right-most. We describe how to construct an equivalent tensor formula $M(C)$ from $C$ assuming that 0 and 1 are encoded by $e_1^2$ and $e_2^2$, respectively (for quantum arrays, that $|0\rangle$ and $|1\rangle$ are encoded by $e_1^1$ and $e_2^1$, respectively). We distinguish two cases.

(i) If each gate of $C_i$ acts on consecutive wires, that is, if $C_i$ contains $\ell \geq 1$ gates $H_1, \ldots, H_\ell$, acting on wires $j_1$ to $k_1, \ldots, j_\ell$ to $k_\ell$, with $j_1 \leq k_1 < j_2 \cdots k_{\ell-1} < j_\ell \leq k_\ell$, then

$$M(C_i) = \left( I_2^{\otimes j_1-1} \otimes H_1 \otimes I_2^{\otimes j_2-k_1-1} \otimes \cdots \otimes H_\ell \otimes I_2^{\otimes n-k_\ell} \right)$$

is the orthogonal matrix of order $2^n \times 2^n$ describing the action of the $i^{th}$ level of $C$.

(ii) If $C_i$ contains gates acting on nonadjacent wires, then choose a permutation $\sigma$ of the wires which brings next to each other those wires which are involved in the same gate. Denote by $D_i$ the $i^{th}$ level reorganized in this way; its action on the (permuted) wires is described with a formula $M(D_i)$ built as in case (i) above. The permutation is implemented by inserting between levels $i - 1$ and $i$ extra depth levels consisting of swap gates, which are collectively described by a formula $P_\sigma$; it is undone with other extra levels, inserted between $i$ and $i + 1$ and described by $P_{\sigma^{-1}}$. Any permutation can be expressed as a product of a polynomial number of cycles of the form $(j, j+1, \ldots, k-1, k)$.
Figure 1: Simulating an arbitrary controlled-not by a controlled-not acting on neighboring wires.

with \( j < k \); therefore it suffices to describe the formulas \( P_{j,k}(C) \) and \( \bar{P}_{j,k}(C) \) which implement this cycle and its inverse, respectively. \( \bar{P}_{j,k}(C) \) which implements its inverse. The reader can verify that

\[
P_{j,k}(C) = \left( I_2^{\otimes j-1} \otimes T_{j,k} \otimes I_2^{\otimes n-k} \right), \quad \text{where} \quad T_{j,k} = \prod_{i=1}^{k-j-1} \left( I_2^{\otimes k-j-i} \otimes T \otimes I_2^{\otimes i-1} \right),
\]

and

\[
\bar{P}_{j,k}(C) = \left( I_2^{\otimes j-1} \otimes \bar{T}_{j,k} \otimes I_2^{\otimes n-k} \right), \quad \text{where} \quad \bar{T}_{j,k} = \prod_{i=1}^{k-j-1} \left( I_2^{\otimes i-1} \otimes T \otimes I_2^{\otimes k-j-i} \right);
\]

with \( \sigma = (\cdots) \), this yields

\[
P_{\sigma}(C) = \bar{P}_{j_1,k_1}(C) \cdots \bar{P}_{j_\ell,k_\ell}(C) \quad \text{and} \quad P_{\sigma^{-1}}(C) = P_{j_\ell,k_\ell}(C) \cdots P_{j_1,k_1}(C),
\]

so that

\[M(C_i) = P_{\sigma^{-1}}(C) \cdot M(D_i) \cdot P_{\sigma}(C).\]

A sample construction for \( j = 1 \) and \( k = 4 \) is depicted in Figure 1. The complete tensor formula \( F(C) \) is given by

\[F(C) = \prod_{i=1}^{m} M(C_i),\]

which can be parenthesized in order to have logarithmic depth. It is readily verified that for each \( x \in \{0, 1\}^n \)

\[C(x) = \text{val}_{\mathbb{F}_2}^{-1}(F(C) \cdot d_x).\]

Formula \( F(C) \) is logspace constructible from \( C \): in particular, a permutation \( \sigma \) suitable for case (ii) can be built by choosing a reorganization \( D_i \) of level \( C_i \) in which the gates \( H_1, \ldots, H_\ell \), act on wires \( j_1 \) to \( k_1 \), \( j_\ell \) to \( k_\ell \), such that

\[1 = j_1, k_1 + 1 = j_2, k_{\ell-1} + 1 = j_\ell; \text{ then the cyclic decomposition of } \sigma \text{ has the form } (1, 2, 3, \ldots, h_1)(2, 3, \ldots, h_2) \cdots \text{ where for each } i \geq 2, \text{ the wires } 1, 2, \ldots, i - 1 \text{ are left untouched by the } i^{th} \text{ cycle.}
\]

\[\text{Note that according to the usual convention, the input-to-output direction in a gate array is left-to-right, while in its matrix representation, the array’s action on its input is given as a product of orthogonal matrices with a column vector, and is read right-to-left.}\]
4.2 From formulas to arrays

In the formula-to-array part, one must deal with the fact that an OSL formula may contain matrices of various sizes, and column vectors at atypical locations. The latter may be regarded a nonstandard or disorderly manner of specifying the array’s inputs. Matrices of nonstandard orders, however, cannot be readily interpreted in terms of Boolean or quantum computation: one may accept to work with many-valued bits and qubits, or the matrices may be padded in order to turn their orders into powers of 2, which is the option we choose in this paper.

**Lemma 4.2.** There exists a polynomial-time algorithm which turns an OSL tensor formula \( F \) over semiring \( \mathbb{K} \) into a formula \( \Pi(F) \) where all subformula sizes are powers of 2, and whose output is

\[
\begin{bmatrix}
\text{val}^{\otimes 2}(\pi(F)) \\
0
\end{bmatrix},
\]

where 0 denotes a (possibly empty) null block.

**Proof.** For an integer \( n \geq 0 \), let \( \pi(n) \) denote the smallest power of 2 greater than or equal to \( n \). We also define a unary operator \( \pi \) which acts as follows on a matrix \( A \):

- if \( A \) is a \( n \times n \) square matrix, then \( \pi(A) \) is a \( \pi(n) \times \pi(n) \) block-diagonal square matrix consisting in a copy of \( A \) at the top left position and a copy of the identity matrix \( I_{\pi(n)-n} \) at the bottom right;
- if \( A \) is a \( n \times 1 \) column vector, then \( \pi(A) \) is \( \pi(n) \times 1 \) with the entries of \( A \) at the first \( n \) positions, and value 0 in the \( \pi(n) - n \) others;
- if \( A \) is neither of the above, then \( \pi(A) \) is undefined.

Whenever \( A \cdot B, \pi(A) \) and \( \pi(B) \) are defined, we have \( \pi(A \cdot B) = \pi(A) \cdot \pi(B) \), so that in the simple case where \( F \) does not contain any occurrence of the Kronecker product, \( \Pi(F) \) is built by replacing each atomic subformula of \( F \) with its image by \( \pi \).

This does not work in general. Consider for example the formula \((A \otimes B) \cdot (V \otimes W)\) where \( A \) and \( B \) are \( 33 \times 33 \) and \( 35 \times 35 \), respectively, and \( V \) and \( W \) are \( 21 \times 1 \) and \( 55 \times 1 \), respectively: the orders of \((\pi(A) \otimes \pi(B))\) and \((\pi(V) \otimes \pi(W))\) do not match. There also exist cases where the orders match but the entries of \((A \otimes B) \cdot (V \otimes W)\) are not consecutive in the column vector \((\pi(A) \otimes \pi(B)) \cdot (\pi(V) \otimes \pi(W))\). Some subformulas may even yield matrices which are neither square nor column vectors.

Nevertheless, we claim that if matrices \( \Pi(A) \) and \( \Pi(B) \) are available, then there exists permutations \( Q \) and \( Q' \) and a block \( H \) such that

\[
Q \cdot (\Pi(A) \otimes \Pi(B)) \cdot Q' = \begin{bmatrix}
A \otimes B & 0 \\
0 & H
\end{bmatrix},
\]

where \( Q \) and \( Q' \) can be specified with polynomial-size sum-free tensor formulas. (Note that \( H \) is orthogonal whenever both \( A \) and \( B \) are.) In the special case where both \( A \) and \( B \) are column vectors, \( Q' = I_1 \) and the claim reads

\[
Q \cdot (\Pi(A) \otimes \Pi(B)) = \begin{bmatrix}
A \otimes B \\
0
\end{bmatrix}.
\]

We first show how to reorder the lines of \( \Pi(A) \otimes \Pi(B) \) where both \( A \) and \( B \) are column vectors. With \( A = [x_1 \cdots x_m]^T \) and \( B = [y_1 \cdots y_n]^T \), let \( \mu = 2^l \geq \pi(m) \), \( \sigma = \mu - m \), \( \nu = 2^k \geq \pi(n) \), and \( \tau = \nu - n \). We start
with
\[ \Pi(A) = [x_1 \cdots x_m \bar{x}_{m+1} \cdots \bar{x}_\mu]^T, \quad \Pi(B) = [y_1 \cdots y_n \bar{y}_{n+1} \cdots \bar{y}_\nu]^T \]
and
\[ \Pi(A) \otimes \Pi(B) = [x_1 y_1 x_1 y_2 \cdots x_1 \bar{y}_\nu x_2 y_1 x_2 y_2 \cdots x_\mu \bar{y}_\nu]^T; \]
the \( \bar{x}_i \)'s and \( \bar{y}_i \)'s are the elements added by padding. Multiplying to the left with the stride permutation \( P^{\mu\nu}_\nu \) gives
\[ P^{\mu\nu}_\nu \cdot (\Pi(A) \otimes \Pi(B)) = [x_1 y_1 x_2 y_1 \cdots x_\mu \bar{y}_\nu x_1 y_2 x_2 y_2 \cdots x_\mu \bar{y}_\nu]^T. \]

Next we multiply with the matrix
\[ R^{\mu\nu}_n = \begin{bmatrix} P^{\mu\nu}_\mu & 0 \\ 0 & (N^{\mu\tau})^k \end{bmatrix} \]
where \( N^{\mu\tau} = I_\tau \otimes P^{\mu}_2 \). The reader can verify that
\[ R^{\mu\nu}_n \cdot P^{\mu\nu}_\nu \cdot (\Pi(A) \otimes \Pi(B)) = [x_1 y_1 x_1 y_2 \cdots x_1 y_n \cdots x_m y_n H]^T = [(A \otimes B) H]^T \]
where \( H \) is a size \( \mu \nu - m n \) block whose first \( \nu \sigma \) entries are \( \bar{x}_{m+1} y_1, \ldots, \bar{x}_\mu y_n \) and the other positions contain a permutation of \( x_1 y_{n+1}, \ldots, x_1 y_\nu, \ldots, x_\mu y_\nu \).

There remains to show how to build matrices \( P^{\mu\nu}_\nu \) and \( R^{\mu\nu}_n \) with polynomial-size sum-free tensor formulas. By Proposition 3.1, it is readily verified that \( P^{\mu\nu}_\nu = (P^{\mu\nu}_2)^k \), and that for any \( \ell \geq 1 \), the induction formula
\[ P^{\mu\nu}_2 = (P^{\mu\nu}_2 \otimes I_2) \cdot (I_2 \otimes P^{\mu\nu}_2) \]
yields the matrix \( P^{\mu\nu}_2 \) a quadratic-size tensor formula with input nodes for \( I_2 \) and \( P^{\mu\nu}_2 \).

Meanwhile, \( R^{\mu\nu}_n = (S^{\mu\nu}_n)^k \), where
\[ S^{\mu\nu}_n = \begin{bmatrix} P^{\mu\nu}_\mu & 0 \\ 0 & N^{\mu\tau} \end{bmatrix}. \]

In order to build this matrix, let
\[ U = \begin{bmatrix} P^{2n}_2 & 0 \\ 0 & I_2 \tau \end{bmatrix} \]
and
\[ P^{n\mu}_2 = (P^{2n}_2 \otimes I_{2^{\mu-1}}) \cdot (I_n \otimes P^{\mu}_2) \]
by Proposition 3.1; observe that
\[ (U \otimes I_{2^{\mu-1}}) \cdot (I_\nu \otimes P^{\mu}_2) = \begin{bmatrix} P^{2n}_2 \otimes I_{2^{\mu-1}} & 0 \\ 0 & I_\nu \tau \mu \end{bmatrix} \cdot \begin{bmatrix} I_n \otimes P^{\mu}_2 & 0 \\ 0 & I_\tau \otimes P^{\mu}_2 \end{bmatrix} = \begin{bmatrix} P^{n\mu}_2 & 0 \\ 0 & I_\tau \otimes P^{\mu}_2 \end{bmatrix} = S^{\mu\nu}_n. \]

Expressed in this way, matrix \( R^{\mu\nu}_n \) can be built with a polynomial-size sum-free tensor formula, where matrix \( U \) is
either given explicitly by a made-to-purpose gate if \( n \) is the diameter of an input matrix, or built inductively in the case where \( n = \pi(p) \) for some \( p \), because in this case \( U = P^{2n}_2 \).

The same technique applies to reorder the lines for arbitrary matrices \( A \) and \( B \); in this case the \( x_i \)'s and \( y_i \)'s are lines and each \( x_i y_j \) in the above equations must be read as \( x_i \otimes y_j \). The claim for the existence of a matrix \( Q' \) which reorders the columns is proved in a dual manner.

Let \( F \) be an OSL formula; the following algorithm builds a formula \( \Pi(F) \) which satisfies the conditions of the Lemma, by recursively defining \( \Pi(G) \) for each subformula \( G \) of \( F \).

- For each atomic subformula \( G \), let \( \Pi(G) = \pi(G) \).
- Repeat recursively from the leaves toward the root of \( F \): for each subformula \( G = H \circ K \) for which \( \Pi(H) \) and \( \Pi(K) \) have already been computed and \( \circ \in \{\cdot, \otimes\} \):
Proof. If $\circ$ is “$\otimes$” then let $\Pi(G) = Q \cdot (\Pi(H) \otimes \Pi(K)) \cdot Q'$ and insert the appropriate subformulas for $Q$ and $Q'$ (note that $\Pi(Q) = Q$ and $\Pi(Q') = Q'$);
- otherwise $\circ$ is “$\cdot$”: if the orders of $\Pi(H)$ and $\Pi(K)$ match, then let $\Pi(G) = \Pi(H) \cdot \Pi(K)$; else they differ by a power of 2 and the smaller matrix must undergo some padding, that is, either $\Pi(G) = (I_2^{\otimes i} \otimes \Pi(H)) \cdot \Pi(K)$, or $\Pi(G) = \Pi(H) \cdot ((e_2^1)^{\otimes i} \otimes \Pi(K))$, for an appropriate $i$.\hfill $\square$

**Lemma 4.3.** There is a polytime computable function which, from a OSL tensor formula $F$ over semiring $\mathbb{K}$, computes a polynomial-size gate array $C(F)$ whose input is represented with a unit vector $V$, whose action over the inputs is given by an orthogonal matrix $M$, and such that matrices $MV$ and $\text{val}_{\mathbb{K}}^{2N,1}(F)$ satisfy

$$MV = \begin{bmatrix} \text{val}_{\mathbb{K}}^{2N,1}(F) \\ 0 \end{bmatrix},$$

where 0 denotes a (possibly empty) null block.

Proof. The formula $\Pi(F)$ is used as a specification for a gate array $C(F)$. For each atomic subformula $G$ of $F$, either $G$ is $m \times m$ for some $m \leq |F|$, where $|F|$ is the diameter of $F$, and $\Pi(G)$ is interpreted as the specification of a gate with $\log_2 \pi(m) = \lceil \log_2 m \rceil$ inputs, or $G$ is $m \times 1$ and $\Pi(G)$ specifies the probability amplitudes for all possible combinations of values of $\log_2 \pi(m) = \lceil \log_2 m \rceil$ input bits or qubits. In the former case, a polynomial-size array of elementary gates implements the operation specified by $\Pi(G)$; in the latter case, a size $m^{O(1)}$ array is built to take as input some constant unit vector (say $e_1^{\pi(m)}$) and yield as output the vector $\Pi(G)$. Next, working recursively from the leaves toward the root of $\Pi(F)$, the interior nodes are interpreted as specifications for combining the subarrays either in a sequential (nodes labelled “$\cdot$”) or parallel (nodes labelled “$\otimes$”) manner. The resulting gate array has polynomial size and satisfies the conditions of the lemma.\hfill $\square$

## 5 Complexity results

Over the Boolean semiring, a column vector is a unit vector as soon as it is nonzero, so that the standard, promise and nonzero versions of problem SFT coincide.

**Theorem 5.1.** Over the Boolean semiring, problem SFT is P-complete under logspace reducibility.

Proof. Given a size $n$ instance $(F, k)$ of SFT($\mathbb{B}$), we use Lemma 4.3 to build an equivalent reversible gate array $C(F)$ over $N = n^{O(1)}$ bits, and we compute the output value of each of these bits (i.e. we solve $N$ instances of the usual Boolean circuit value problem). This yields a combination of $N$ values which corresponds to a given position along the diagonal of

$$\left(\text{val}_{\mathbb{B}}^{2N,1}(F)\right) \cdot \left(\text{val}_{\mathbb{B}}^{2N,1}(F)\right)^T,$$

under the convention that combinations 00⋯0, ⋯, 11⋯1 correspond to lines (and columns) 1, ⋯, $2^N$, respectively. The hardness part consists in using Lemma 4.3 to reduce the P-complete circuit value problem [10] to an instance of SFT($\mathbb{B}$).\hfill $\square$
Theorem 5.3. The promise version of problem SFT(\mathbb{Q}) is complete for the class BQP, under logspace reducibility.

Proof. The hardness part is a generic reduction. Using Definition 2.3, we start with a \(m\)-leveled gate array \(C\) on \(n\) qubits numbered 1 to \(n\) whose accepting subspace is defined by setting qubit 1 to \(\lvert 1\rangle\), and whose gates are defined with unitary matrices over \(\mathbb{Q}\). Denote by \(f_C\) the probability that qubit 1 be projected to \(\lvert 1\rangle\) when the measurement takes place. We use Lemma 4.1 to build from \(C\) an equivalent tensor formula \(F(C) = \prod_{i=1}^{m} M(C_i)\). Meanwhile we define for the array’s input qubits a tensor product \(V\) of \(n\) unit vectors of size \(2 \times 1\). An easy induction on \(j\) shows that
\[
\text{val}_{\mathbb{Q}}^{2n-1} \left( \prod_{i=1}^{j} M(C_i) \cdot V \right)
\]
is exactly the vector of amplitudes after level \(j\) in \(C\). Thus the last \(2^n-1\) entries along the diagonal of
\[
\left( \text{val}_{\mathbb{Q}}^{2n-1}(F(C) \cdot V) \right) \cdot \left( \text{val}_{\mathbb{Q}}^{2n-1}(F(C) \cdot V) \right)^T
\]
add up to the value of \(f_C\), and the original array’s input is accepted iff this partial trace exceeds the threshold by which acceptance by \(C\) was defined. Scrutiny of the reduction shows that the constraint on \(f_C\) is transported intact from the description of \(C\) to the SFT(\mathbb{Q}) instance \(F(C) \cdot V\).

In the other direction, we use Lemma 4.2 to translate an instance \((F, k)\) for SFT(\mathbb{Q}) into the description of a quantum gate array over \(m\) qubits, \(m \geq \log_2 n\), and of its inputs; the \(k\)th partial trace of
\[
\left( \text{val}_{\mathbb{Q}}^{2m-1}(F) \right) \cdot \left( \text{val}_{\mathbb{Q}}^{2m-1}(F) \right)^T
\]
represents the probability that the output qubits of this array be projected onto the direct sum of the dimension-1 subspaces generated by \(\lvert 2^m - 1\rangle = \lvert 1 \cdots 11\rangle\), \(\lvert 2^m - 2\rangle = \lvert 1 \cdots 10\rangle\), \(\lvert 2^m - 3\rangle = \lvert 1 \cdots 01\rangle\), ..., and \(\lvert 2^m - k\rangle\). The promise on the partial trace is transported unmodified from the input tensor formula to the quantum gate array. \(\square\)

The argument described above can be used to prove that the “standard” (non-promise) version of problem SFT(\mathbb{Q}) is complete for PP, defined by removing the constraint from definition 2.4. Finally, when the proof is applied to the “nonzero” version of problem SFT(\mathbb{Q}), a completeness statement is obtained for the class NQP.

Finally, we consider problem SFT over the semiring of the nonnegative rational numbers. Note that, just as in the quantum case, the entries in the column vectors are regarded as probability amplitudes. All the gates do in a classical reversible array is permute the different vector components without ever mixing or combining them; no interference ever takes place and it does not matter in terms of the final result, whether the probabilities are represented as such or as amplitudes.

Theorem 5.3. Problem SFT(\mathbb{Q}^+) is PP-complete under logspace reducibility.

Proof. For a generic reduction, we start with a reversible gate array \(C\) whose input is a string of \(N = s(n) + t(n)\) bits, where the initial \(s(n)\) bits are the ancillary bits, all set to 0, and the other \(t(n)\) bits are random. By Lemma 4.1, \(C\) and its input can be encoded into \(F(C) \cdot V\), where the \(2^N \times 1\) unit vector \(V\) specifies the inputs, i.e. a bit string \(c_1 \cdots c_{s(n)} d_1 \cdots d_{t(n)}\) which satisfies the conditions
i. $c_i = 0$ for all $i \leq s(n)$, and

ii. all combinations of values for the random bits $d_1 \cdots d_{t(n)}$ are equally likely.

The corresponding $2^{t(n)}$ entries in the vector $\text{val}^{2N+1}(V)$ carry value $1/\sqrt{2^{t(n)}}$; all others contain 0. We demand wlog that $t(n)$ be even; dealing with the random bits pairwise enables us to ensure that no irrational values are necessary. Then

$$V = (e_1^\dagger)^{\otimes s(n)} \otimes \left[ \begin{array}{cccc} 1/\sqrt{2} & 1/\sqrt{2} \end{array} \right]^{\otimes t(n)} = (e_1^\dagger)^{\otimes s(n)} \otimes \left[ \begin{array}{cccc} 1/2 & 1/2 & 1/2 \end{array} \right].$$

Let the acceptance condition be that bit $c_1$ has value 1 at the output level. This corresponds to the first $2^{N-1}$ positions along the diagonal of $(\text{val}^{2N+1}(F(C) \cdot V)) \cdot (\text{val}^{2N+1}(F(C) \cdot V))^T$.

In the other direction, consider an instance $(F, k)$ for SFTP$(\mathbb{Q}^+)$. We have discussed in Section 4.2 how the column vectors and square matrices are interpreted as “inputs” and “gates” in the equivalent array, through the construction of a formula $\Pi(F)$ where all matrices have orders which are powers of 2. We add extra steps to the construction of $\Pi(F)$ in order to enforce the further condition, that all fractions have a power of 2 as denominator.

Consider an $n \times 1$ unit vector $v_i = [a_1^d \cdots a_n^d]^T$, where $a_1^2 + \cdots + a_n^2 = d^2$. Let $d$ not be a power of 2: $d < \pi(d)$. The reader can verify that there exist integers $b_1, \ldots, b_p$ such that $\pi(d)^2 = a_1^2 + \cdots + a_n^2 + b_1^2 + \cdots + b_p^2$ and $p \leq 3[\log_2 d]$. Let $q = \min(2^{2i} : 2^{2i} > n + 3[\log_2 d])$, and embed $v$ into the $q \times 1$ vector

$$\left[ \frac{a_1}{\pi(d)} \cdots \frac{a_n}{\pi(d)} 0 \cdots 0 \frac{b_1}{\pi(d)} \cdots \frac{b_p}{\pi(d)} \right]^T,$$

which can be interpreted as a distribution of probability amplitudes for $\log_2 q$ input bits. Denote by $\delta_i$ the fraction $d_i/\pi(d)$. Repeating this process on each input column vector yields an instance $(G, k)$ where the resulting partial trace is the same one obtained from $(F, k)$, times a factor $\Delta^2 = \prod_i \delta_i^2$. If we accept instance $(F, k)$ whenever the partial trace is above a threshold $\alpha$, then there exists a probabilistic polytime Turing machine $M$ which accepts $(G, k)$ with probability above $\frac{\alpha}{\Delta^2}$.

The algorithm of $M$ is divided into three phases; the first consists in building the new instance $(G, k)$ from the original $(F, k)$, the second in choosing nondeterministically a column vector to give as input to the equivalent array $C(G)$, and the third in deterministically simulating $C(G)$ on its input. In the second step $M$ nondeterministically selects values for the bits in the string $d_1 \cdots d_{t(n)}$; the preprocessing step has organized their probability distribution in order to ensure that this can be done with a sequence of nondeterministic binary choices, followed by a look-up into a table which is linear in size and is computed from the column vectors in $(F, k)$.

The reader can verify that this proof can be rewritten in terms of the promise problem SFTP$(\mathbb{Q}^+)$ and the complexity class BPP; in the second part of the proof the cutpoint and the size of the empty interval can be modified, however. Meanwhile, the complexity of the nonzero version is obtained with a straightforward application of the above argument.

\[ \square \]
Corollary 5.4. The promise and nonzero versions of problem $SFT(Q^+)$ are $BPP$-complete and $NP$-complete, respectively, under logspace reducibility.

6 Conclusion

Through the study of problem SFT, we have developed a common algebraic description for polynomial-time complexity classes, where the choice of the semiring determines the complexity class. For the inclusion chain $P \subseteq BPP \subseteq BQP$, in particular, the classical model of polytime probabilistic computation turns out to be a special case of polytime quantum computation where interference between computations is ruled out.

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