USES OF C-GRAPHS IN A PROTOTYPE FOR AUTOMATIC TRANSLATION.

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ABSTRACT

This paper presents a prototype, not completely operational, that is intended to use c-graphs in the translation of assemblers. Firstly, the formalization of the structure and its principal notions (substructures, classes of substructures, order, etc.) are presented. Next section describes the prototype which is based on a Transformational System as well as on a rewriting system of c-graphs which constitutes the nodes of the Transformational System. The following part discusses a set of operations on the structure. Finally, the implementation in its present state is shown.

1. INTRODUCTION.

In the past [10,11], several kinds of representation have been used (strings, labelled trees, trees with "decorations", graphs of strings and (semantic) networks). C-graphs had its origin as an alternative in the representation and in the treatment of ambiguities in Automatic Translation. In earlier papers [4,5] this structure is named E-graph but c-graph is better suited since it is a generalized "grado de cadenas" (graph of strings).

This structure combines some advantages of the Q-systems [7] and of the trees of ARIANE-78 [1,2,11], in particular, the use of only one structure for all the translation process (as in the former) and foreseeable decidability and parallelism (as in the latter). This paper presents a prototype, not completely operational, that uses c-graphs and is intended to translate assemblers to refine the adequacy of this kind of structure in the translation of natural languages.

2. DEFINITIONS

C-graph. A c-graph G is a cycle free, labelled graph {1,9} without isolated nodes and with exactly one entry node and one exit node. It is completely determined by a 7-tuple: G=(A,S,ρ,I,O,E,¢), where A is a set of arcs, S a set of nodes, ρ a mapping of A into SxS, I the input node, O the output node, E a set of labels (c-trees, c-graphs) and ¢ a mapping of A into E. For the sake of simplicity, arcs and labels will be merged in the representation of G (cf. Fig.1). Interesting c-graphs are sequential c-graphs (cf. Fig.2a) and bundles (cf. Fig.2b).

E-trees. A c-tree or a tree with decorations is an ordered tree, with nodes labelled by a label and a decoration that is itself a decorated tree, possibly empty.

Classes of c-graphs. There are three major classes: (1) recursive c-graphs (cf. Fig.3a) where each arc is labelled by a c-graph; (2) simple c-graphs (cf. Fig.1) where each arc is labelled by a c-tree and (3) regular c-graphs, a proper subclass of the second that is obtained by concatenation and alternation of simple arcs (cf. Fig.3b). By denoting concatenation by "\cdot" and alternation by "\oplus", we have an evident linear representation. For example, G=I.a\oplus j.(k+4). Note that not every c-graph may be obtained by these operations, e.g.G.

Substructures. For the sake of homogeneity, the only substructures allowed are those that are themselves c-graphs. They will be called sub-
c-graphs or seg's. For example, G1 and G2 are seg's of G.

\[
\begin{array}{c}
G1 \quad G1 \\
G3 = \quad G2
\end{array}
\]

a) A recursive c-graph.

\[
\begin{array}{c}
g \\
i \quad j \quad k
\end{array}
\]

b) A regular c-graph. G4 =

Fig. 3. Two classes of c-graphs.

Isolatability. It is a feature that determines, for each c-graph G, several classes of seg's. An isolated seg G' is intuitively a seg that has no arcs that "enter" or that "leave" G'. Depending on the relation that each isolated seg keeps with the rest of the c-graph, several classes of isolatability can be defined.

a) Weak isolatability. A seg G' of G is weakly isolatable (seg{l}) if and only if for every node x of G' (except I' and O'), all of the arcs that leave or enter x are in G'. E.g.: G5 = i is a seg{l} of G.

b) Normal isolatability. A seg G' of G is normally isolatable (seg{m}) if and only if it is a seg{l} and there is a path, not in G', such that it leaves I' and enters O'. Example: G6 = k is a seg{m} of G.

c) Strong isolatability. A seg G' of G is strongly isolatable (seg{f}) if and only if the only node that has entering arcs not in G' is I' and the only node that has leaving arcs not in G' is O'. When G' is not an arc and there is no seg{f} contained strictly in G', then G' is an "elementary seg{f}"; if G contains no seg{f}, then G is elementary. E.g. G4 is a seg{f} of G.

Order and roads. Two order relations are considered: (1) a "vertical" order or linear order of the arcs having the same initial node and (2) a "horizontal" order or partial order between two arcs on the same path. A road is a path from I to O. Vertical order induces a linear order on roads.

3. DEFINITION OF THE Prototype.

The prototype consists of a model and a data structure. The model is essentially a generalization of a Transformational System (TS) analogous to ROBRA [2] and whose grammars are rewriting systems of c-graphs (RSC) [4,5,6]. Regarding data structure, we use c-graphs.

3.1 A Transformational System.

This TS is a c-graph→c-graph transducer. It is a "control" graph whose nodes are RSC and the arcs are labelled by conditions.

A TS is a cycle free oriented graph, with only one input and such that,

1. Each node is labelled with a RSC or $\&$null.
2. $\&$null has no successor.
3. Each grammar of the RSC has a transition scheme S or $\varepsilon$ (empty scheme).
4. Arcs of the same initial node are ordered.

TS works heuristically. Given a c-graph $g_0$ as an input, it searches for the first path ending in $\&$null. This fact implies that all of the transition schemes on the path were satisfied. Any scheme not satisfied provokes a search of a new path. For example, if $S_1$ is satisfied, TS produces $G_1(g_0) = g_1$ and it proceeds to calculate $G_2(G_1(g_0)) = g_2$. If $S_4$ is satisfied the system stops and produces $g_4$. Otherwise, it backtracks to $G_1$ and tests $S_2$. If it is satisfied $g_2$ is produced. Otherwise, it tests $S_3$, etc.

Fig. 4. A Transformational System.

3.2 A Rewriting System.

Let us consider a simple example: let GR be the following grammar for syntactic analysis (without intending an example of linguistic value).

\[
R_1: (g_1 + a_1 + g_2)(g_3 + a_2 + g_4)^* / a_1=GN, a_2=GV / ==
\]
\[
(g_1 + g_2)(g_3 + a_2 + g_4) + a_1 = PHRA(a_1, a_2) /.
\]
\[
R_2: (g_1 + a_1 + g_2)(g_3 + a_2 + g_4)^+ / a_1 = VB, a_2 = GN / ==
\]
\[
(g_1 + g_2)(g_3 + c_2 + g_4) + a_1 = PRED(a_1, a_2) /.
\]
\[
R_3: a_1(g_1 + a_2 + g_2) / a_1 = NP, a_2 = AD / ==
\]
\[
a_1(g_1 + g_2) + a_1 = GN(a_1, a_2) /.
\]
\[
R_4: a_1(g_1 + a_2 + g_2) / a_1 = NP, a_2 = PRED / ==
\]
\[
g_1 + a_2 + g_1 = VG^2 + a_1 = PHRA(a_1, a_2) /.
\]
\[
R_5: (g_1 + a_1 + g_2)(g_3 + a_2 + g_4) / a_1 = PRON, a_2 = VB / ==
\]
\[
(g_1 + g_2)(g_3 + c_2 + g_4) + a_1 = GV(a_1, a_2) /.
\]
\[
R_6: (g_1 + a_1 + g_2)(g_3 + a_2 + g_4) / a_1 = ART, a_2 = NM / ==
\]
\[
(g_1 + g_2)(g_3 + a_2 + g_4) + a_1 = GN(a_1, a_2) /.
\]

As we can see, each rule has: a name (R1, R2, ...), a left side and a right side.

The left side defines the geometrical form...
and the condition that an actual seg must meet in order to be transformed. It is a c-graph scheme composed of two parts: the structural descriptor that defines the geometrical form and the condition (between slashes) that tests label information. The first part uses "*" as an "element of structural description" in the first rule. It denotes the fact that no seg must be right-concatenated to g3+~2+g4.

The right side defines the transformation to be done. It consists of a structural descriptor, similar to the one on the left side and a list of label assignments (also between slashes) where for each new label we precise the values it takes; and for each old one, its possible modifications. A point ends the rule. Note the properties of an empty g: if g' is any c-graph, then g.g'=g and g+g'=g'.

Let us analyze the phrase: "Ana lista la tira". The representation in our formalism is G7. Morphological analysis produces G8. Note that all ambiguities are kept in the same structure in the form of parallel arcs. The application of GR to G8 results in G9, where each arc will be labelled with a c-tree with a possible interpretation of G8 in grammar GR. The sequence of applications is R3, R6, R5, R1, R2, R4. The system stops when no more rules are applicable.

Operations are divided in two classes: (1) those where the structure is taken as a whole (global) and (2) those that transform substructures (local).

1. Global Operations.

Concatenation and alternation have been defined above. These operations produce sequential c-graphs and bundles respectively, as well as the polynomial writing of regular c-graphs.

Expansion. This operation produces a bundle exp(G) from all the roads of a c-graph G. For example, expansion of G10 produces exp(G10)=(b.f)+(c.d.f)+(c.e).

(factorization. There are two kinds and their results may differ. Consider G11=a.b+a.c+d.e+f+g.f+h.e. Left factorization produces G12=a.(b+c)+d.e+f+h.e, and right factorization G13=a.b+c.e+d.f.e+(d+g)+f.

Arborization. This operation constructs a c-tree from a c-graph. There may be several kinds of c-trees that can be constructed but we search for a tree that keeps vertical and horizontal orders, i.e. one that codes the structure of the c-graph. An "and-or" (y-o) tree is well suited for this purpose. The result of the operation will be a c-graph with one and only one arc labelled by the and-or tree. For example, arb(G)=G14 (cf. Fig. 7). Note that the non-regular seg has a as a root. Regular seg's have o.

2. Local Operations.

Replacement. Given two c-graphs G and G', this operation substitutes a seg G' in G for G', e.g. if G=G4, G'=m+n and G'=i, then the result will be
Addition. This operation inserts a c-graph \( G' \) into another, \( G \), by merging two distinct nodes \((x, y)\) of \( G \) with the input and output of \( G' \). Addition requires only that insertion does not produce cycles. Note that if \((1,0)\) are taken as a couple of nodes, we have alternation. Example, let \((2,3)\) be a couple of nodes of \( G_16 \) and take \( G'=G_17=s+u \). The resulting c-graph is \( G_{18} \).

![Fig. 8. Addition of a c-graph.](image)

5. Conclusion.

Once we have an operational version of the prototype, it is intended as a first approach to proceed to the translation of assemblers of the microprocessors available in our laboratory such as INTEL's 8085 or 8080 and MOTOROLA's 6800.

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