Using Averaged Modeling for Capacitors Voltages Observer in NPC Inverter

Bassem Omri, Kaisar Ammous, and Anis Ammous

Power Electronics Group, Université de Sfax, ENIS-Sfax, BP. 1173, 3038 Sfax, Tunisia

Correspondence should be addressed to Bassem Omri, omri_bessem@yahoo.fr

Received 17 May 2012; Accepted 12 November 2012

Copyright © 2012 Bassem Omri et al. This is an open access article distributed under the Creative Commons Attribution License, which permits unrestricted use, distribution, and reproduction in any medium, provided the original work is properly cited.

This paper developed an adaptive observer to estimate capacitors voltages of a three-level neutral-point-clamped (NPC) inverter. A robust estimated method using one parameter is proposed, which eliminates the voltages sensors. An averaged modeling of the inverter was used to develop the observer. This kind of modeling allows a good trade-off between simulation cost and precision. Circuit model of the inverter (implemented in Simpower Matlab simulator) associated to the observer algorithm was used to validate the proposed algorithm.

1. Introduction

Multilevel inverters can provide an effective alternative of high power applications, providing a high quality voltage, increasing efficiency and robustness, and reducing interference electromagnetics [1–4]. There are three main topologies of multilevel inverters: diode-clamped inverter [2, 5, 6], flying capacitor inverter [7, 8], and cascade multilevel inverter [6, 9]. Among the multilevel inverters, the most popular topology is the diode-clamped inverter which is called as a neutral-point-clamped (NPC) inverter in three levels. It is proposed by Nabae et al. [10]. This type of inverter avoids the complexity associated with the series connection of semiconductor switches or the bulky coupling transformer, produces low distortion harmonics, and has an average cost.

However, in NPC inverter, the DC-link voltage is divided by capacitors, and each capacitor is composed of series connection construction. Therefore, if the voltage unbalancing occurs between each capacitor, the line-to-line output voltage waveform has many harmonic components and the power devices in NPC inverter cannot guarantee the safe operation. So, one NPC inverter requirement is to produce a good control maintaining the optimal balance of capacitor voltages [11–13]. Thus, regulation of these capacitors voltages requires information about such voltages.

For that there are two possibilities either measuring or estimating such voltages.

Regarding the first possibility, it presents several disadvantages: besides the known difficulties when measuring a high-voltage level, if the number of levels increases the number of capacitors increases in NPC inverter. Then it will be necessary to use multiple-voltage sensors to measure the capacitor voltages, which reduces the reliability. Furthermore, the use of the voltages sensors presents many difficulties of establishment in front of the use of the current sensors. On the other hand, the second alternative uses software sensors, called observers, which replace the physical sensors (the voltage sensors). It is suitable to estimate the capacitors voltages through only the phase currents measurement. So, the observer is independent of the number of the level of NPC inverter. Unlike the traditional direct voltage sensing depends on the levels number.

An observer is a data-processing algorithm to reconstruct the variable state system from the input, mathematical model, and output measurement of the real system. It is an attractive solution because it benefits from a mathematical model of the system. It is less expensive and more reliable because it is applied by a digital computer. Actually, the calculation of the digital signal processors (DSP) is the execution of such sophisticated and complex algorithms
The proliferation of power electronic devices has led to a demand for more effective pulse width modulation strategies. The current waveform can be improved by increasing the frequency of the carrier wave; this approach reduces copper losses at the expense of increased switching losses. To overcome the limitations of existing switching strategies the technique known as space vector pulse width modulation (SVPWM) is becoming widely used in industry [18, 19].

SVPWM is a highly efficient method of generating the six-pulsed signals for the inverter stage of the motor drive. Conventional switching techniques treat each phase as a separately generated sinusoid that is displaced by 120 degrees. However, a change in the voltage of one-half bridge due to switching invariably influences the other two-phase voltages [20]. SVPWM evaluates the switching schema as a whole, which results in better use of the DC bus and generates significantly less harmonic distortion than the sine triangle method [20].

The SVPWM control of the 3-level NPC inverter consists to control it in the $(\alpha,\beta)$ coordinates which can generate 27 possible voltages vectors (Figure 2). They are divided in 6 large vectors (PON,OPN,NPO,NPP,NPN,PNP), 6 medium vectors (PON,OPN,NPO,NPP,NPN,PNP), 12 small vectors (with the redundancy: (PPO,OON), (OPO,NON), (OPP,NOO), (OOP,NOO), (ONO,POP), (POO,ONN)) and 3 zero vectors (OOO,NNN,PPP). When the power devices, $S_{k1}$ and $S_{k2}$, are connected to the positive DC-link rail the state is noted by “P”. In contrast, when the output is connected to the negative DC-rail by turning both $S_{k3}$ and $S_{k4}$ on, the state is called “N.” Moreover, the switching state is defined as “O” when $S_{k3}$ and $S_{k4}$ both power devices, are on.

The objective of SVPWM technique is to approximate a reference space vector $V_s$ somewhere within the hexagon of Figure 2 using a combination of the 27 switching vectors. Indeed, when the reference voltage is in one sector, several couples of vector are possible.

For example in Sector 1 (Figure 3), the $V_s$ vector can be divided on $V_{1a}, V_{12}$ or $V_{1b}, V_{12}$. We choose the second solution, since $V_{1b}$ is greater in amplitude than $V_{1a}$. At each commutation period of the inverter, the vector $V_s$, projected on two adjacent vectors, ensures the calculation of the dwelling time.

Let $T_{mod}$ be a modulation period, $T_{1b}$ dwelling time of the first vector $V_{1b}, T_{12}$ dwelling time of the second vector $V_{12}$, and $T_{null} = T_{mod} - T_{1b} - T_{12}$ dwelling time of zero vectors. We define the duty cycles $\rho_1$ and $\rho_2$, related to times $T_{1b}$ and $T_{12}$, respectively, of the vectors $V_{1b}$ and $V_{1a}$ by:

$$\rho_1 = \frac{T_{1b}}{T_{mod}}, \quad \rho_2 = \frac{T_{12}}{T_{mod}}. \quad (1)$$

For one modulation period $T_{mod}$, the output voltage of the inverter is given by:

$$T_{mod} \cdot V_s = (T_{1b}V_{1b} + T_{12}V_{12} + T_3 V_{null}). \quad (2)$$

![Figure 1: A 3-level NPC converter topologies.](image-url)
Advances in Power Electronics

Figure 2: Voltage space vectors in NPC inverter.

Figure 3: Sector 1.

And the dwelling time of each vector is given by

\[ T_{1b} = \frac{2\sqrt{3} T_{\text{mod}}}{E} \left( \frac{1}{2\sqrt{2}} V_{sa} - \frac{\sqrt{3}}{2\sqrt{2}} V_{sb} \right), \]

\[ T_{12} = \frac{2\sqrt{2} T_{\text{mod}}}{E} V_{sb}, \]

\[ T_{\text{null}} = T_{\text{mod}} - T_{12} - T_{1b}. \]  

For more details see [21].

3. The Nonideal Average Model

Based on the averaged representation of the PWM switch [22], Figure 4 shows the proposed averaged model of the one-leg three-level multilevel converters. \( I_{sa} \) is the average value of the load current over the switching period. This model applies a controlled voltage source \( (V_{a1} \text{ and } V_{a2}) \) and a controlled current source \( (I_{a1} \text{ and } I_{a2}) \). Each three-level converter leg has four controlled switches and six uncontrolled switches. The four controlled switches are directly governed by external control signals. The uncontrolled switches are indirectly governed by the state of the controlled switches and the circuit conditions.

The average values of the different voltage and current sources of the proposed averaged model of the bridge (A) are given by [22].

Consider that \( T_{\text{mod}} \) is the switching period of the active switch and \( \rho \) \( (\rho_1, \rho_2, \rho_3) \) the duty cycle, which is the ratio of the on-time of the active couple switches \( ((S_{i1}, S_{i2}), (S_{i2}, S_{i3}), \text{ or } (S_{i3}, S_{i4})) \) by the switching period \( T_{\text{mod}} \), respectively (Figure 5).

\[ V_d \text{ and } V_{ds} \text{ represent respectively, the voltage across the diode and the MOSFET in on-state. These values can be deduced from the devices static characteristics } I(V): \]

\[ \rho_i = \frac{T_{\text{on}i}}{T_{\text{pwm}}}, \quad \rho_{Mi} = \frac{T_{\text{off}i}}{T_{\text{pwm}}}, \]

\[ \rho_M = \rho_{M1} + \rho_{M2} + \rho_{M3}. \]  

When only output current behavior is considered, a simplified representation of the averaged model (Figure 6) can be used, where

\[ \langle I_{na} \rangle = (I_{na1} + I_{a2} - I_{a1}), \]

\[ \langle V_{a} \rangle = (V_{a1} + V_{a2}), \]

\[ \langle I_{a} \rangle = (I_{a2}). \]

The averaged values of the voltage source \( V_a \) and the current source \( I_a \) of the bridge (A) are given as follows.

If the current \( I_{na} \) is positive,

\[ \langle V_{a} \rangle = 2V_d \rho_1 + (E + 2V_d) \rho_3 \]

\[ + \left( \frac{E}{2} + V_d + V_{ds} \right) \rho_2 + (E + 2V_d) \rho_M, \]

\[ \langle I_{a} \rangle = -I_{na1} \rho_3, \]

\[ \langle I_{na} \rangle = I_{na2} \rho_2. \]
If the current $I_{sa}$ is negative,

$$
\langle V_a \rangle = -2Vdp_1 + (E - 2VDs)p_3 \\
+ \left( \frac{E}{2} - Vd - Vds \right) p_2 + (2Vd)p_M,
$$

(7)

$$
\langle V_a \rangle = -2Vdp_1 + (E - 2VDs)p_3 \\
+ \left( \frac{E}{2} - Vd - Vds \right) p_2 + (2Vd)p_M,
$$

(8)

$$
\langle I_a \rangle = I_{sa}p_3.
$$

(9)

In order to demonstrate the accuracy of the proposed averaged model, a circuit model of the inverter implemented in Simpower Matlab simulator, using the circuit represented in Figure 1 is used. For the simulation in this section and Section 5, we use 2 GHz Core2 Duo processor and 3 GB RAM.

The comparison between the current waveforms (Figure 7) obtained by the Simpower simulator and the current given by the proposed model shows the good accuracy of the averaged model behavior for converter electric analysis. The current error between the two types’ models is given in Figure 8.

Figure 9 shows the capacitors voltages ($C_1$ and $C_2$) waveforms obtained by the model Simpower simulator and the averaged model. The simulation results given by the proposed averaged model are in good agreement with Simpower simulations.

It is evident that multilevel converter analysis using fine models gives accurate results, but these simulations cost is unaffordable. When long time range simulation is needed, the proposed averaged model can be used, and the obtained results show the accuracy of the proposed model (Table 1).
Table 1: CPU running time (s) necessary to simulate a period (0.02 s).

| Type model                  | Running time (s) |
|-----------------------------|------------------|
| Nonideal averaged model     | 10               |
| Circuit model (Simpower Matlab) | 90              |

So, we used this model like a reference model in the observer design.

4. Observer Design of Capacitors Voltages

The DC side of the three-level NPC inverter (Figure 1) has a DC source \( E \) and two connections capacitors \( C_1 \) and \( C_2 \). The switching variable \( y_k \) represents the state of the multilevel inverter active switches, \( S_{kj} \), \( k \in \{1, 2, 3\} \), \( j \in \{1, 2, 3, 4\} \). The three states of the binary switching \( y_k \) can be defined as

\[
y_k = \begin{cases} 
1 & (S_{k1} = 1 \land S_{k2} = 1) \land (S_{k3} = 0 \land S_{k4} = 0) \\
0 & (S_{k3} = 0 \land S_{k2} = 1) \land (S_{k3} = 1 \land S_{k4} = 0) \\
-1 & (S_{k1} = 0 \land S_{k2} = 0) \land (S_{k3} = 1 \land S_{k4} = 1).
\end{cases}
\]

Applying the Kirchhoff laws to the three-level inverter circuit (Figure 1) and doing some mathematical manipulations, the dynamic equations of the AC currents, \( I_a, I_b, \) and \( I_c \), and the capacitor voltages, \( V_{C1} \) and \( V_{C2} \), are defined as functions of the circuit parameters and switching variables \( y_k \):

\[
\begin{bmatrix}
\frac{dI_a}{dt} \\
\frac{dI_b}{dt} \\
\frac{dI_c}{dt} \\
\frac{dV_{C1}}{dt} \\
\frac{dV_{C2}}{dt}
\end{bmatrix} = \begin{bmatrix}
0 & 0 & \psi_{11} & \psi_{12} \\
0 & 0 & \psi_{21} & \psi_{22} \\
0 & 0 & \psi_{31} & \psi_{32} \\
\frac{1}{C_1} & -\frac{1}{C_1} & -\frac{1}{C_1} & 0 \\
-\frac{1}{C_2} & -\frac{1}{C_2} & -\frac{1}{C_2} & 0
\end{bmatrix} \begin{bmatrix}
\frac{1}{L} & 0 & 0 & 0 \\
0 & -\frac{1}{L} & 0 & 0 \\
0 & 0 & -\frac{1}{L} & 0 \\
\frac{1}{C_1} & -\frac{1}{C_1} & -\frac{1}{C_1} & 0 \\
-\frac{1}{C_2} & -\frac{1}{C_2} & -\frac{1}{C_2} & 0
\end{bmatrix} \begin{bmatrix}
I_a \\
I_b \\
I_c \\
V_{C1} \\
V_{C2}
\end{bmatrix} + \begin{bmatrix}
0 \\
0 \\
0 \\
\frac{1}{C_1} \\
\frac{1}{C_2}
\end{bmatrix} \times idc,
\]

where \( T \) is the Concordia transformation:

\[
T = \begin{bmatrix}
\sqrt{2} \\
\sqrt{3} \\
\sqrt{3} \end{bmatrix} \begin{bmatrix}
1 & 0 \\
-\frac{1}{2} & \frac{\sqrt{3}}{2} \\
-\frac{1}{2} & -\frac{\sqrt{3}}{2}
\end{bmatrix}.
\]

Applying (11) to (8), the new model is represented in \((\alpha, \beta)\) coordinates by

\[
\begin{cases}
\frac{dI_\alpha}{dt} \\
\frac{dI_\beta}{dt} \\
\frac{dV_{C1}}{dt} \\
\frac{dV_{C2}}{dt}
\end{cases} = \begin{bmatrix}
\frac{1}{L} & 0 & 0 & 0 \\
0 & -\frac{1}{L} & 0 & 0 \\
0 & 0 & -\frac{1}{L} & 0 \\
\frac{1}{C_1} & -\frac{1}{C_1} & -\frac{1}{C_1} & 0 \\
-\frac{1}{C_2} & -\frac{1}{C_2} & -\frac{1}{C_2} & 0
\end{bmatrix} \begin{bmatrix}
\Gamma_{1\alpha} & \Gamma_{2\alpha} & 0 & 0 \\
0 & \Gamma_{1\beta} & \Gamma_{2\beta} & 0 \\
0 & 0 & \Gamma_{1\beta} & \Gamma_{2\beta} \\
\frac{1}{C_1} & -\frac{1}{C_1} & -\frac{1}{C_1} & 0 \\
0 & -\frac{1}{C_2} & -\frac{1}{C_2} & 0
\end{bmatrix} \begin{bmatrix}
I_\alpha \\
I_\beta \\
V_{C1} \\
V_{C2}
\end{bmatrix} + \begin{bmatrix}
0 \\
0 \\
\frac{1}{C_1} \\
\frac{1}{C_2}
\end{bmatrix} \times idc,
\]

where

\[
\Gamma_{1\alpha} = \sqrt{\frac{2}{3}} \left( \Gamma_{i1} - \frac{\Gamma_{i2}}{2} - \frac{\Gamma_{i3}}{2} \right), \quad \Gamma_{1\beta} = \frac{1}{\sqrt{2}} (\Gamma_{i2} - \Gamma_{i3}).
\]

This model can be written as

\[
\dot{X} = A(S)X + H(S, X),
\]

\[
y = CX,
\]

where \( X = (I_\alpha, I_\beta, V_{C1}, V_{C2})^T \), \( C = (1 \ 1 \ 0 \ 0) \), \( H(S, X) = \begin{bmatrix}
0 \\
0 \\
0 \\
1/C_1 \\
1/C_2
\end{bmatrix} \times idc \), and

\[
A(S) = \begin{bmatrix}
-\frac{R}{L} & 0 & 0 & 0 \\
0 & -\frac{R}{L} & 0 & 0 \\
0 & 0 & -\frac{R}{L} & 0 \\
\frac{1}{C_1} & -\frac{1}{C_1} & -\frac{1}{C_1} & 0 \\
\frac{1}{C_2} & -\frac{1}{C_2} & -\frac{1}{C_2} & 0
\end{bmatrix}.
\]

For different configurations, the capacitors are not always traversed by the load current. The current can pass by one or two capacitors as it cannot traverse any capacitor if the top
switchers of each leg are all closed or all open. Thus, some configurations give observability problem of the studied system. This can be proven by the test of rank, whenever, at least, a capacitor is traversed by the load current:

$$\Theta = \text{rang} \left( \begin{bmatrix} C & CA \\ CA & CA^2 \end{bmatrix} \right) = 3 \times 4. \quad (19)$$

It is clear that $\Theta$ is not of full rank; thus the system is not observable. So, to design the observer, a convenient representation of the model must be given.

To overcome this difficulty, the idea is to consider two interconnected affine models and to construct the new observer based on those models.

Now, consider that the system (15) can be represented as a set of the interconnected subsystems as follows:

$$\Sigma_1 : \begin{bmatrix} \frac{dI_a}{dt} \\ \frac{dI_b}{dt} \\ \frac{dV_{C1}}{dt} \end{bmatrix} = \begin{bmatrix} \frac{-R}{L} & 0 & \frac{\Gamma_{1a}}{L} \\ 0 & \frac{-R}{L} & \frac{\Gamma_{1b}}{L} \\ \frac{-\Gamma_{1a}}{C_1} & \frac{-\Gamma_{1b}}{C_1} & 0 \end{bmatrix} \begin{bmatrix} I_a \\ I_b \\ V_{C1} \end{bmatrix} + \begin{bmatrix} 0 \\ \frac{\Gamma_{2a}}{L} \\ \frac{\Gamma_{2b}}{L} \end{bmatrix} V_{C2} + \begin{bmatrix} 0 \\ 0 \\ \frac{C_1}{C_2} \end{bmatrix} \times idc, \quad (20)$$

$$\Sigma_2 : \begin{bmatrix} \frac{dI_a}{dt} \\ \frac{dI_b}{dt} \\ \frac{dV_{C2}}{dt} \end{bmatrix} = \begin{bmatrix} \frac{-R}{L} & 0 & \frac{\Gamma_{2a}}{L} \\ 0 & \frac{-R}{L} & \frac{\Gamma_{2b}}{L} \\ \frac{-\Gamma_{2a}}{C_2} & \frac{-\Gamma_{2b}}{C_2} & 0 \end{bmatrix} \begin{bmatrix} I_a \\ I_b \\ V_{C2} \end{bmatrix} + \begin{bmatrix} 0 \\ \frac{\Gamma_{1a}}{L} \\ \frac{\Gamma_{1b}}{L} \end{bmatrix} V_{C1} + \begin{bmatrix} 0 \\ 0 \\ \frac{C_2}{C_1} \end{bmatrix} \times idc. \quad (21)$$

Each subsystem can be written as

$$\Sigma_j \begin{cases} \dot{x}_j = A_j(S_k)x_j + \Gamma_j(S_k)x_j, \\ y = C_jx_j, \quad j = 1, 2 \quad k = 1, 2, 3 \quad i = 1, 2 \not= j, \end{cases} \quad (22)$$

where $x_j = (I_a, I_b, V_{Cj})^T$ represents the state of the $j$th subsystem, $S_k$ are the instantaneous inputs applied to the system, and $y$ is the measurable output, and

$$A_j(S_k) = \begin{bmatrix} \frac{-R}{L} & 0 & \frac{\Gamma_{ja}}{L} \\ 0 & \frac{-R}{L} & \frac{\Gamma_{jb}}{L} \\ -\frac{\Gamma_{ja}}{C_j} & \frac{-\Gamma_{jb}}{C_j} & 0 \end{bmatrix}, \quad \Gamma_j(S_k, x_j) = \begin{bmatrix} \frac{\Gamma_{ja}}{L} \\ \frac{\Gamma_{jb}}{L} \\ \frac{1}{C_j} \end{bmatrix} \times idc, \quad (23)$$

$$j = 1, 2, \quad k = 1, 2, 3, \quad i = 1, 2 \not= j,$$

and $C = (1 \quad 1 \quad 0)$. The function $\Gamma_j(S_k, x_j)$ is the interconnection function. It depends on the control $S_k$ and the state vector of each subsystem $\Sigma_j$.

From the preceding composition (22), the following system:

$$\phi_j \begin{cases} \dot{Z}_j = A_j(S_k)Z_j + \Gamma_j(S_k) - P_j^{-1}C_j(y - \hat{y}), \\ \dot{P}_j = -\theta_jP_j - \theta_j^T(S_k)P_j - P_jA_j(S_k) + C_jC_j^T, \end{cases} \quad (25)$$

is an observer for the system (16) for $j = 1, 2$, where $P_j^{-1}C_j$ is the observer gain which depends on the solution of the Riccati equation (25) for each subsystem $\Sigma_j$. The parameter $\theta_j$ must be a positive constant and sufficiently greater such that for any positive symmetric matrix $P_j(0)$, the following condition is verified [23]:

$$\forall \theta \geq \theta_0 \quad \exists \gamma > 0, \quad \delta > 0, \quad t_0 > 0 : \forall t \geq t_0, \quad \gamma \| \dot{P} \| \leq P(0) \leq \delta I. \quad (26)$$

The parameter $\theta_j$ determines the convergence rate of the observer. The authors [23] proposed guidelines for the tuning of the parameters of the adaptive observer.

Note that this observer is the deterministic version of the Kalman filter for state affine systems. It is clear that the system observability depends on the applied inputs. Then the convergence of this observer can be proved assuming that the inputs $S_i$ are regularly persistent (see Appendix A); it is a class of admissible inputs that allows to observe the system (for more details, see [19]). This guarantees the works of observer, and the observer gain is well defined, that is, the matrix $P_j$ is nonsingular.

Now, a further result based on regular persistence is introduced.

**Lemma 1.** Assume that the input $S_j$ is regularly persistent for system (22) and consider the following Lyapunov differential equation:

$$\dot{P}_j = -\theta_jP_j - A_j^T(S_k)P_j - P_jA_j(S_k) + C_jC_j^T. \quad (27)$$
With $P_j > 0$, then, $\exists \theta_{j0} > 0$ such that for any symmetric positive definite matrix $P_j(0)$, $\exists \theta_j > \theta_{j0}$, $\exists \alpha_j$, $\beta_j$, $t_0 > 0$: $\forall t > t_0$,
$$
\forall t > t_0, \quad \alpha_j I < P_j(t) < \beta_j I,
$$
where $I$ is the identity matrix.

The proof of this lemma follows the same steps of Theorem 3.2 in [24].

The system will be represented by a set of interconnected subsystems (Figure 10):

\[
\begin{align*}
\dot{\chi}_1 &= A_1(S_k)\chi_1 + \Gamma_1(S_k,\chi_1) + P_1^{-1}C_1^T(j - y) \\
\dot{\chi}_2 &= A_2(S_k)\chi_2 + \Gamma_2(S_k,\chi_2) + P_2^{-1}C_2^T(j - y) \\
\end{align*}
\]

(29)

The idea of this paper is to construct an observer for all the system $\Sigma$ from the separate observers of the subsystem $\Sigma_j$.

If $\phi_j$ is an exponential observer for $\Sigma_j$, then the following interconnected system:

\[
\begin{align*}
\dot{\chi}_1 &= A_1(S_k)\chi_1 + \Gamma_1(S_k,\chi_1) + P_1^{-1}C_1^T(j - y) \\
\dot{\chi}_2 &= A_2(S_k)\chi_2 + \Gamma_2(S_k,\chi_2) + P_2^{-1}C_2^T(j - y) \\
\end{align*}
\]

(30)

is an observer for the interconnected system $\Sigma$.

Each observer subsystem $\phi_j$ is defined by (25).

Remark 2. The proposed observer functions in the case if the inputs controls are regularly persistent (see Appendix A), which is equivalent to that all subsystem $\phi_j$ is observable.

Now, we give the sufficient conditions which ensure the convergence of the interconnected observer $\varphi$. For that, we introduce the following assumptions.

Assumption 1. Assume that the input $S_j$ is a regularly persistent input for subsystem $\Sigma_j$ and admits an exponential observer $\phi_j$ for $j = 1, 2$. If an input is regularly persistent, it excites sufficiently the system to obtain information necessary to constructing the not measured variables using the designed observer (see Appendix A). In this case, an observer of the form $\varphi$ can be designed and the estimation error will be bounded.

Assumption 2. The term $\Gamma_j(S_k,\chi_1)$ does not destroy the observability property of subsystem $\phi_j$ under the action of the regularly persistent input $S_j$. Moreover, $\Gamma_j(S_k,\chi_1)$ is Lipchitz with respect to $\chi_1$ and uniformly with respect to $S_j$ for $j = 1, 2$. This condition is verified because these functions are linear.

Then the following result can be established.

**Lemma 3.** Consider the interconnected system $\Sigma$ and the two Assumptions 1 and 2 are verified. Then the system $\varphi$ (30) is an observer for the system $\Sigma$ (29).

The proof of the lemma is given in Appendix B.

### 5. Simulation Results

In this section, a detailed simulation was carried out to validate the proposed interconnected observer. The general block diagram is shown in Figure 11. In this study the 3-level NPC inverter block was replaced by the averaged model of the inverter to develop the observer. The block of the interconnected observer is given by (30). In order to show the accuracy of the observer a fine model (circuit model) of the inverter (implemented in Matlab Simpower) was used to estimate the capacitors voltages. The estimated capacitors voltages of the two models are compared to the real capacitors voltages. We define the real capacitor voltage by the capacitor voltage given by the voltages sensors of the circuit model implemented in Simpower Matlab.

The different behavior of the interconnected observer was performed. The capacitors values are $C_1 = C_2 = 1000 \mu F$. The NPC Inverter is connected at three equilibrium phases where each phase composed by an inductor $L = 10 \text{ mH}$ and a resistor $R = 20 \Omega$. To estimate the NPC capacitor voltages according to (25) the observer parameter gain $\theta_j = 15000$ for $j = 1, 2$ is used. SVPWM was used to control the multilevel inverter described in Section 2. The switching frequency was fixed at 10 kHz. Simulations were carried out using matlab/simulink. Matlab simulator is an important tool where different system models (electrical, mechanical, thermal, etc.) can be developed. In fact, Matlab simulator enables averaged model representation.

Figures 12 and 13 show the comparison between the estimated capacitors voltages $C_1$ and $C_2$ (using nonideal average model and circuit model) and the real capacitor voltage.

Figures 14 and 15 show the estimated errors for the estimated capacitors voltages $V_{C1}$ and $V_{C2}$. The error for each model is calculated by $(V_{C1-\text{real}}) - (V_{C1-\text{estimated}})$.

In order to study the observer behavior, we changed the load of the first leg $(L = 10 \text{ mH}, R = 20 \Omega)$ to $(L = 10 \text{ mH}, R = 40 \Omega)$ at $t = 0.1 \text{ s}$ (Figures 16 and 17). In other terms,
we have not got equilibrium three phases. Then, Figures 18 and 19 show at $t = 0.1$ s the change of the load of the first leg ($L = 10 \text{ mH}, R = 20 \Omega$) to ($L = 10 \text{ mH}, R = 40 \Omega$) and at $t = 0.15$ s we return to the initial load.

Simulations results show the usefulness of this design. In any condition, the observer is able to follow the real capacitor voltages accurately. Moreover, the good agreement between estimated capacitors voltages obtained by the proposed averaged model and the circuit model from Simpower Matlab is illustrated. We notice that simulation time with fine model (circuit model implemented in Simpower Matlab) is eight times (Table 1) greater than the simulation with the averaged model. This is an important reason to use averaged modeling especially in complex converters architecture. Converter states observer and control becomes, more and more easy.
nonideal average model with disturbance load and its elimination.

Figure 18: Real and estimated capacitors voltages $V_{C_1}$ (V) using nonideal average model with disturbance load and its elimination.

Figure 19: Real and estimated capacitors voltages $V_{C_2}$ (V) using nonideal average model with disturbance load and its elimination.

6. Conclusion

The control and monitoring of the capacitors voltages in multilevel NPC converter are essential. Measuring voltages becomes expensive and impractical because of the high voltages and power levels handled in such applications. Thus the advantage of using an observation technique becomes evident.

This paper proposed an adaptive observer to estimate the capacitor voltages in NPC inverter. This observer can reconstruct the capacitor voltages accurately from the nonideal average model inputs and replace the needed capacitor voltage sensors. Simulation results show the good precision of the proposed average model. It can be used for the NPC converters analysis instead of fastidious simulations in circuit type simulators where simulations cost is unaffordable. The obtained results are acceptable, and let us conclude that the observer is well suited for purposes control of the capacitor voltages in multilevel NPC inverter.

Appendices

A. Observability Definitions

We introduce some definitions related with the inputs applied to the system. Consider a state-affine controlled system of the form

$$\begin{align*}
\Sigma & : \dot{x} = A(u)x + B(u), \\
y & = Cx,
\end{align*}$$

where $x \in \mathbb{R}^n$, $u \in \mathbb{R}^m$, $y \in \mathbb{R}^p$ with $A : \mathbb{R}^m \to M(n, m)$, $B : \mathbb{R}^m \to M(n, 1)$ continuous, and $C \in M(p, n)$, where $M(k, l)$ denotes the space of $k \times l$ matrices with coefficients in $\mathbb{R}$ (resp., $l$ are the number of rows (resp., columns). From now on, we will assume that $B(u) = 0$ without loss of generality.

Notation. Let $\varphi_u(r, t)$ denotes the transition matrix of

$$\frac{d}{dt} \varphi_u(r, t) = A(u(r))x + \varphi_u(r, t),$$

(A.2)

with the classical relation $\varphi_u(t_1, t_2)\varphi_u(t_2, t_3) = \varphi_u(t_1, t_3)$

We then define the following:

(i) The observability gramian

$$\Gamma(t, T, u) = \int_t^{t+T} \varphi_u^T(r, t)C^TC\varphi_u(r, t)dr.$$  

(A.3)

(ii) The universality index

$$\gamma(t, T, u) = \min_{i} (\lambda_i(\Gamma(t, T, u))),$$

(A.4)

where $\lambda_i(M)$ stands for the eigenvalues of a given matrix $M$.

The input functions are assumed to be measurable and such that $A(u)$ is bounded on the set of admissible inputs of $\mathbb{R}^m$.

Now, we give definitions of regular persistence of input $u$.

Definition 4 (regular Persistence). A measurable bounded input $u$ is said to be regularly persistent for the state-affine system $\Sigma$ if there exists $T > 0$, $\alpha > 0$, $t_0 > 0$ such that $\gamma(t, T, u) > 0$ for every $t \geq t_0$.

B. Regular Persistence

This proof is based on the demonstration in [25].

The dynamics of the estimation error $\epsilon_j = Z_j - \chi_j$ is given by $\dot{\epsilon}_j = (A(S_j) - P_j^T C^T C_j)\epsilon_j + \Delta \Gamma_j(S_j, \chi_j, Z_j)$, where $\Delta \Gamma_j(S_j, \chi_j, Z_j) = \Gamma_j(S_j, \chi_j) - \Gamma_j(S_j, \chi_j)$ for $j = 1, 2$.

Now, let $V = V_1 + V_2$ be a Lyapunov function for the interconnected system $\Sigma$, where $V_j(\epsilon_j) = \epsilon_j^T P_j \epsilon_j$ is a Lyapunov function for each subsystem $\Sigma_j$.

From the time derivative of $V_j(\epsilon_j)$, it follows that

$$V_j(\epsilon_j) \leq -\theta_j V_j(\epsilon_j) + \epsilon_j^T P_j \Delta \Gamma_j(S_j, \chi_j, Z_j)$$

(B.1)

Now, adding and subtracting the term $\Delta \Gamma_j(S_j, \chi_j, Z_j)^T P_j \Delta \Gamma_j(S_j, \chi_j, Z_j)$ we have

$$V_j(\epsilon_j) \leq -\theta_j V_j(\epsilon_j) + \epsilon_j^T P_j \Delta \Gamma_j(S_j, \chi_j, Z_j)$$

Next, regrouping the appropriate terms,

$$V_j(\epsilon_j) \leq -\theta_j V_j(\epsilon_j) + \frac{1}{2} \epsilon_j^T P_j \Delta \Gamma_j(S_j, \chi_j, Z_j) + \epsilon_j^T P_j \Delta \Gamma_j(S_j, \chi_j, Z_j)$$

(B.2)
From this inequality, we get $\dot{V}_j(\epsilon_j) \leq -(\theta_j - 1)\|\epsilon_j\|^2_{P_j} + \|\Delta \Gamma_j(S_j, \chi_j, Z_j)\|^2_{P_j}$.

Now, from Assumption 2, we have $\|\Delta \Gamma_j(S_j, \chi_j, Z_j)\|^2_{P_j} \leq (\lambda_l\|\epsilon_j\|^2_{S_j})_{\epsilon \in \{1, 2\} \neq j}$.

Then, we find $\dot{V}_j(\epsilon_j) \leq -(\theta_j - 1)\|\epsilon_j\|^2_{P_j} + (\lambda_l\|\epsilon_j\|^2_{S_j})_{\epsilon \in \{1, 2\} \neq j}$.

And replacing this expression in $V = V_1 + V_2$ it follows that:

$$\dot{V}(\epsilon) \leq \sum_{j=1}^{2} \left( -\left(\theta_j - 1\right)\|\epsilon_j\|^2_{P_j} + \left(\lambda_l\|\epsilon_j\|^2_{S_j}\right)_{\epsilon \in \{1, 2\} \neq j} \right).$$

(B.3)

Take into account that the inputs are regularly persistent, and then from Lemma 1 the matrix $P_j$ is bounded. Using the lemma on equivalence of norms, that is, there exists a positive constant $\mu_l$ such that

$$\|\epsilon_l\|^2_{P_j} \leq \mu_l\|\epsilon_j\|^2_{P_j}.$$  

(B.4)

Then, we get

$$\dot{V}(\epsilon) \leq \sum_{j=1}^{2} \left( -\left(\theta_j - 1\right)\|\epsilon_j\|^2_{P_j} + \left(\lambda_l\|\epsilon_j\|^2_{S_j}\right)_{\epsilon \in \{1, 2\} \neq j} \right).$$

or

$$\dot{V}_j(\epsilon_j) \leq -\sum_{j=1}^{2} \left( \left(\theta_j - 1\right) - \mu_l\lambda_l \right)\|\epsilon_j\|^2_{P_j}.$$  

(B.5)

Finally, we have

$$\dot{V}(\epsilon_j) \leq V(\epsilon(t))e^{-\gamma(t-t_0)}.$$  

(B.7)

For $\gamma = \min(y_1, y_2)$, where $y_j = (\theta_j - 1) - \mu_l\lambda_j$, taking $\epsilon = \text{col}(\epsilon_1, \epsilon_2)$, it is easy to see that

$$\|\epsilon(t)\| \leq K\|\epsilon(t_0)\|e^{-\gamma(t-t_0)}.$$  

(B.8)

References

[1] G. Baoming and F. Z. Peng, “Speed sensorless vector control induction motor drives fed by cascaded neutral point clamped inverter,” in Proceedings of the 24th Annual IEEE Applied Power Electronics Conference and Exposition (APEC ’09), pp. 1991–1997, February 2009.

[2] B. Hu, G. Xu, M. Zhang, J. Kang, and L. Xia, “Study on a novel clamped topology of multilevel converters,” in Proceedings of the IEEE International Electric Machines and Drives Conference (IEMDC ’09), pp. 379–384, May 2009.

[3] T. A. Lipo, M. D. Manjirekar, and P. Steimer, “Hybrid multilevel power conversion system: a competitive solution for high power applications,” in Proceedings of the 34th IEEE-IAS Annual Meeting, pp. 1520–1527, October 1999.

[4] O. Vodyakho and C. C. Mi, “Three-level inverter-based shunt active power filter in three-phase three-wire and four-wire systems,” IEEE Transactions on Power Electronics, vol. 24, no. 5, pp. 1350–1363, 2009.

[5] F. J. C. Padilha, W. I. Suemitsu, M. D. Bellar, and P. M. Lourenco, “Low-cost gate drive circuit for three-level neutral-point-clamped voltage-source inverter,” IEEE Transactions on Industrial Electronics, vol. 56, no. 4, pp. 1196–1204, 2009.

[6] T. Wanjekeche, D. V. Nicolae, and A. A. Jimoh, “A cascaded NPC/H-bridge inverter with simplified control strategy and minimum component count,” in Proceedings of the IEEE Africacon, vol. 4, pp. 769–777, September 2009.

[7] R. P. Aguilera and D. E. Quevedo, “Capacitor voltage estimation for predictive control algorithm of flying capacitor converters,” in Proceedings of the IEEE International Conference on Industrial Technology (ICIT ’09), pp. 1–6, February 2009.

[8] T. A. Meynard, H. Foch, P. Thomas, J. Courault, R. Jakob, and M. Nahrstaedt, “Multicell converters: basic concepts and industry applications,” IEEE Transactions on Industrial Electronics, vol. 49, no. 5, pp. 955–964, 2002.

[9] E. Babaei, “A cascade multilevel converter topology with reduced number of switches,” IEEE Transactions on Power Electronics, vol. 23, no. 6, pp. 2657–2664, 2008.

[10] A. Nabae, I. Takahashi, and H. Akagi, “A neutral-point clamped PWM inverter,” in Proceedings of the IEEE-IAS, pp. 761–766, October 1981.

[11] H. Zhang, S. J. Finney, A. Massoud, and B. W. Williams, “An SVM algorithm to balance the capacitor voltages of the three-level NPC active power filter,” IEEE Transactions on Power Electronics, vol. 23, no. 6, pp. 2694–2702, 2008.

[12] J. A. Ulrich and A. R. Bendre, “Floating capacitor voltage regulation in diode clamped hybrid multilevel converters,” in Proceedings of the IEEE Electric Ship Technologies Symposium (ESTS ’09), pp. 197–202, April 2009.

[13] Y. S. Lai, Y. K. Chou, and S. Y. Pai, “Simple PWM technique of capacitor voltage balance for three-level inverter with DC-link voltage sensor only,” in Proceedings of the 33rd Annual Conference of the IEEE Industrial Electronics Society (IECON ’07), pp. 1749–1754, November 2007.

[14] A. Birouche, Contribution sur la synthèse d’observateurs pour les systèmes dynamiques hybrides [Thèse de doctorat], Institut National de Polytechnique de Lorraine, Lorraine, France, 2006.

[15] S. Bonnabel and P. Rouchon, “Control and observer design for nonlinear finite and infinite dimensional systems,” in Lecture Notes in Control and Information Sciences, vol. 322, pp. 53–65, Springer, 2005.

[16] M. Morari, A. Bemporad, and G. Ferrari-Trecate, “Observability and controllability of piecewise affine and hybrid systems,” IEEE Transactions on Automatic Control, vol. 45, no. 10, pp. 1864–1876, 2000.

[17] H. Nademi, A. Das, and L. Norum, “Nonlinear observer based capacitor voltage estimation for sliding mode current controller in NPC multilevel converters,” in Proceedings of the IEEE Trondheim PowerTech, pp. 1–7, 2011.

[18] A. Gopinath, A. A. S. Mohamed, and M. R. Baiju, “Fractal based space vector PWM for multilevel inverters—a novel approach,” IEEE Transactions on Industrial Electronics, vol. 56, no. 4, pp. 1230–1237, 2009.

[19] M. A. Traelns, “Modélisation et Commande des Systèmes Physiques à Topologie Variable: Application au Convertisseur Multicellulaire,” L’Institut National des Sciences Appliquées de Lyon, 2009.

[20] J. Chen, R. Erickson, and D. Maksimović, “Averaged switch modeling of boundary conduction mode dc-to-dc converters,” in Proceedings of the 27th Annual Conference of the IEEE Industrial Electronics Society (IECON ’01), pp. 844–849, December 2001.

[21] B. Urmila and D. Subba Rayudu, “Optimum space vector PWM algorithm for three-level inverter,” ARPN Journal of Engineering and Applied Sciences, vol. 6, no. 9, pp. 24–36, 2011.

[22] K. Ammous, E. Haouas, and S. Abid, “Averaged modeling of multilevel converters,” The International Journal for
[23] V. Andrieu, L. Praly, and A. Astolfi, “High gain observers with updated gain and homogeneous correction terms,” Automatica, vol. 45, no. 2, pp. 422–428, 2009.

[24] H. Khalil, Nonlinear Systems, Prentice-Hall, 2nd edition, 1996.

[25] J. De León Morales, M. F. Escalante, and M. T. Mata-Jiménez, “Observer for DC voltages in a cascaded H-bridge multilevel STATCOM,” IET Electric Power Applications, vol. 1, no. 6, pp. 879–889, 2007.
