Research Article

Simultaneous Realization of Wavelength Conversion, 2R Regeneration, and All-Optical Multiple Logic Gates with OR, NOR, XOR, and XNOR Functions Based on Self-Polarization Rotation in a Single SOA: An Experimental Approach

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Received 31 October 2011; Revised 29 January 2012; Accepted 30 January 2012

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We highlight the feasibility of experimental implementation of both inverted and noninverted wavelength conversion, 2R regeneration, and all-optical logic functions, such as OR, NOR, XOR, and XNOR optical gates by exploiting the self-polarization rotation in a semiconductor optical amplifier (SOA) device without changing the setup configuration. Switching between each optical function is done by only adjusting the input optical power level. In order to allow optimum control and preserve the polarization state of the injected and collected signals, the polarimetric measures have been carried out in free space.

1. Introduction and State of the Art

Semiconductor optical amplifier (SOA) is a promising and fundamental component in today’s photonic networks and next-generation optical networks. It is characterized by high nonlinearities, compactness, multifunctionality, and high ability of integration. It has proven to be a versatile and multifunctional device to be used to achieve different functions in access, core, and metropolitan networks. Particularly, it has been envisioned for all-optical signal processing tasks at very high bit rates, that cannot be handled by electronics, such as wavelength conversion [1–4], signal regeneration [5, 6], optical switching [7] and, optical logic operations [8–10].

All-optical wavelength converters and optical regenerators can be achieved by exploiting SOA nonlinearities such as cross-gain modulation (XGM) [11], cross-phase modulation (XPM) [3, 12, 13], four-wave mixing (FWM) [14, 15], and cross-polarization modulation (XPolM) [6, 16, 17]. They have attracted a lot of interest thanks to their attractive features, such as small size, fast carrier dynamics, multifunctional aspect, power consumption, optical power efficiency, and high potential of integration. The main features of wavelength converters include their transparency to bit rate and signal format, operation at moderate optical power levels, low electrical power consumption, small frequency chirp, cascadability of multiple stages of converters, and signal reshaping.

All-optical wavelength converters at bit rates from 10 up to 100 Gbit/s were experimentally and theoretically investigated, by Leuthold et al., by using a fully integrated SOA-delayed interference configuration [1]. Furthermore, Randhawa et al. [3] have simulated wavelength converter for future broadcast networks at 40 Gbit/s using low-cost SOAs. Their performance analysis is carried out for an all-optical frequency converter based on XPM in two SOAs arranged in a Mach-Zehnder interferometer (MZI) configuration to evaluate the efficiency of conversion. Their results show that conversion is possible over a wavelength separation of 1 nm between the pump and the input wavelength. They have demonstrated that increasing the driving current can decrease the XPM effect and the XGM scheme shows extinction ratio degradation for conversion to longer wavelengths [3].
In addition, Spyropoulou et al. [4] have presented theoretical and experimental performance analysis of 40 Gbit/s non-return-to-zero (NRZ) all-optical wavelength conversion using a differentially biased SOA-MZI. Their theoretically obtained results are confirmed through experiments that demonstrate successful 40 Gbit/s wavelength conversion functionality for NRZ data signals only when a differentially biased SOA-MZI configuration is employed, whereas an error floor is obtained when 40 Gbit/s NRZ all-optical wavelength conversion with the standard single-control SOA-MZI scheme is attempted [4]. Moreover, Turkiewiez et al. [16] have reported all optical 1310 to 1550 nm wavelength conversion based on nonlinear polarization rotation in an SOA, at bit rate 10 Gbit/s, in between two transmission links by using two standard single-mode fiber-based spans.

Wavelength conversion based on FWM process in SOAs is an attractive technique, compared to XGM and XPM, since it is independent of modulation format, ultrafast, and capable of dispersion compensation. It offers strict transparency, including modulation-format and bit-rate transparency and is capable of multiwavelength conversions. However, it has low conversion efficiency and needs careful control of the input signal polarization. The main drawbacks of wavelength conversion based on FWM are polarization sensitivity and the frequency-shift dependent conversion efficiency. However, wavelength conversion based on XPolM is another promising approach. It uses the optically induced birefringence and dichroism in an SOA and has great potential to offer wavelength conversion with high extinction ratio.

Optical logic gates can be realized either by exploiting SOA nonlinearities, such as XGM [18–20], FWM [20, 21], and XPolM [10, 22, 23]. Berrettini et al. [20] have demonstrated an integrable scheme of reconfigurable and ultrafast photonic logic gate, based on a single SOA and able to process ultrafast signals. They have implemented XNOR function exploiting XGM and FWM in an SOA. They have showed that the same scheme can be easily reconfigured to obtain AND, NOR, and NOT logic gates [20].

Although the principle of all-optical gates, wavelength conversion, and 2R optical regeneration, which are based on nonlinear polarization rotation, has already been demonstrated by others authors [6, 10, 22–25], we propose and argue, in the next sections of this paper, a promising approach, which has not been reported yet according to our knowledge, of the implementation method of optical OR, NOR, XOR, and XNOR gates, wavelength converter, and 2R optical regenerator by exploiting the self-polarization rotation (SPR) in a SOA structure. The implementation of those functions was made by referring to the same setup configuration in free space that can allow optimum control and preservation of the polarization state of the injected and collected signals. Switching between each optical function is done by only adjusting the input optical power level.

2. Presentation of the Experimental Setup

For allowing optimum control and preservation of the polarization state of the injected and collected signals, the experimentation has been carried out in free space in the research laboratory in electronics, signal, optoelectronics and telecommunications (RESO), Brest National Engineering school (ENIB), France. We used a commercial, a bulk, and a tensile-strained SOA structure, having the reference: 1550 CRI/P-SN 2106, which is manufactured by OptoSpeed. It is based on InP/GaInAsP, having an active layer length \( L = 500 \mu m \), active zone width \( W = 2, 5 \mu m \), and an active layer height \( d = 0, 2 \mu m \). The experimental setup is shown in Figure 1. The SOA is placed in such a way that their TE and TM axes correspond, respectively, to the horizontal and vertical axes of the lab referential.

As the experiment was done in free space, the risk of errors is high. In order to reduce it, we adopted three calibration steps, which are:

(i) the optical beams alignment,

(ii) the optical elements alignment,

(iii) the calibration of the bench polarimeter at light running.

Light emitted from the SOA was collected and collimated with a microscope objective, then passed through the equivalent of a polarization controller, which is formed with a quarter-wave plate (QWP) and a half-wave plate (HWP). Subsequently, it was passed through a linear polarizer (LP) acting as an analyzer. Then, it was recollected with a fibred collimator (FC) that is connected to an optical spectrum analyzer (OSA), having a resolution of 0.07 nm in order to reject the amplified spontaneous emission (ASE) of the SOA. The passing axis of the linear polarizer, when set vertically, coincided with the TM axis in the sample and defined a reference direction from which the orientation \( \theta \) of the fast axis of the quarter-wave plate was estimated. This orientation could be modified, as the quarter-wave plate was mounted on a rotation stage whose movements were accurately determined by a computer-controlled step motor.

In order to inject a linear polarization while assuring an equality of both TE and TM powers, the linear input polarizer was fixed to an angle \( \theta = 135^\circ \) with regard to the horizontal axis. The linearization was made with the output polarization controller, which consists of the QWP and the HWP, whereas the signal blocking was made with the output polarizer (LP) around a power, known as the blocking power. We have varied both QWP and HWP in order to obtain the lowest possible output power.

After several tests, we have chosen a SOA blocking power having a value equal to \(-2\, \text{dBm}\), which is situated in the saturation regime interval of the device, because it allows to obtain a strong variation of the output power for a slight variation of the input power. This value seems to be the best compromise to optimize the static performances of the optical signal processing functions. Indeed, it allows a very good improvement of the extinction rate of the injected signal. The evolution of the transfer function of the SOA after blocking the output signal at an input power equal to \( P_{in} = -2\, \text{dBm} \) for a bias current equal to 150 mA and 200 mA is illustrated in Figure 2.
We notice that the SOA output power takes much more significant values when the injected current increases which corresponds to a low contribution of the ASE. We can also note that the curve of the measured static transfer function of the SOA can show three various regimes according to the injected optical power. The first regime corresponds to a “slow” increase of the output power by increasing the input power. The second regime makes reference to a fast diminution of the output power further to the blocking. In the third regime, the SOA output power becomes more and more important with the increase of the injected optical power.

3. Experimental Implementation of both Inverted and Noninverted Wavelength Conversion and 2R Optical Regeneration

All-optical wavelength conversion refers to the operation that consists of the transfer of the information carried in one wavelength channel to another wavelength channel in optical domain. It is a key requirement for optical networks, since it has basically to be used to extend the degree of freedom to the wavelength domain. Moreover, All-optical wavelength conversion is also indispensable in future optical packet switching networks to optimize the network performance metrics. It is very useful in the implementation of switches in WDM networks. In addition, it is crucial to lower the access blocking probability and therefore increase the utilization efficiency of the network resources in wavelength routed optical networks.

Referring to Figure 3, we can underline that by exploiting the nonlinear rotation of polarization in a single SOA, we can realize both inverted and noninverted wavelength conversion according to the choice of the average power value of the signal to be injected (pump). Indeed, if the value of this last one is lower than the blocking power, an inverted conversion wavelength is achieved. For the opposite case, a noninverted conversion is accomplished.

According to Figure 4, we can notice that the output extinction ratio is higher than the input extinction ratio \( (ER_{in} < ER_{out}) \). This result allows us to note that by exploiting the self-polarization rotation, it is possible to accomplish 2R optical regeneration of a signal. The improvement of the extinction ratio is about 11 dB if the input power is fixed to 0 dBm. To benefit from the extinction ratio improvement, the power corresponding to the low level of the signal to be regenerated must be slightly superior to the blocking power and the power referring to its high level must not be very high, in order to limit the SOA saturation phenomenon.
4. Experimental Implementation of All-Optical Multiple Logic Gates

4.1. Concept of All-Optical OR Logic Gate Implementation. Figure 5 exhibits the measured static transfer function of the OR gate that can be achieved and implemented using the same experimentation based on SPR.

The principle of operation for OR function is as follows: we consider that the pump signal is composed of signals: $E_1$ and $E_2$, which are simulated as logical entries for the logical gate. The output probe signal ($E_{out} = E_1 + E_2$) of the device serves as logical output. The three signals: $E_1$, $E_2$, and $E_{out}$ are simultaneously injected in the SOA. Then, the output stage, in the setup experimentation, is adjusted to block the signal when one among both pump signals is in its maximum power level, which corresponds to the high logic level (01, 10, 11). As a result, the output logic level is low (0). The other case corresponds to the high logic level (1). Consequently, the same experimentation serves to accomplish the optical OR logic gate implementation.

4.2. Concept of All-Optical NOR Gate Implementation. Referring to Figure 6, we can note that the optical NOR function can be achieved and implemented. Its functioning principle is the following: we assume that the pump signal is composed of signals: $E_1$ and $E_2$, which are considered as logical entries for the logical gate. The output probe signal ($E_{out} = E_1 + E_2$) of the device serves as logical output. The three signals: $E_1$, $E_2$, and $E_{out}$ are simultaneously injected in the SOA. Then, the output stage, in the setup experimentation, is adjusted to block the signal when one among both pump signals is in its maximum power level, which corresponds to the high logic level (01, 10, 11). As a result, the output logic level is low (0). Other cases correspond to the high logic level (1). So, the achievement of the optical OR logic gate is completed.

4.3. Concept of All-Optical XOR Gate Implementation. Figure 7 displays the measured static transfer function of the optical XOR gate that can be achieved and implemented using SPR. Its functioning principle is the following: the pump signal is assumed to be composed of signals: $E_1$ and $E_2$, which are considered as logical entries for the logical gate. The output probe signal ($E_{out} = E_1 \cdot E_2 + E_1 \cdot E_2$) of the device serves as logical output. The three signals: $E_1$, $E_2$, and $E_{out}$ are simultaneously injected in the SOA. Then, the output stage, in the setup experimentation, is adjusted to block the signal when both pump signals are in their maximum power level or when they are in their minimum power level, which corresponds, respectively, to the high logic levels (11) and (00). As a result, the output logic level is
These functions is done by only adjusting the input optical power level. Since each one of the proposed functions can be applied to various networking applications, they will play an important role in future high-capacity optical communication networks. This study can be extended to exploit the obtained static response of the SOA using the SPR and demonstrate its capabilities in the dynamic regime.

Acknowledgments

The authors would like to thank the team of RESO lab, EA 3380, ENIB, Brest, France: A. Sharaiha, M. Guégan, P. Morel, and F. Bentivegna for their interest, support, fruitful discussions, and cooperation and M. Tariaki for his help to complete the experiments.

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