Generalized and Extended Product Codes

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Abstract

Generalized Product (GPC) Codes, an unification of Product Codes and Integrated Interleaved (II) Codes, are presented. Applications for approaches requiring local and global parities are described. The more general problem of extending product codes by adding global parities is studied and an upper bound on the minimum distance of such codes is obtained. Codes with one, two and three global parities whose minimum distances meet the bound are presented. Tradeoffs between optimality and field size are discussed.

Keywords: Erasure-correcting codes, product codes, Reed-Solomon (RS) codes, generalized concatenated codes, integrated interleaving, MDS codes, PMDS codes, maximally recoverable codes, local and global parities, locally recoverable (LRC) codes.

1 Introduction

There has been considerable research lately on codes with local and global properties for erasure correction (see for instance [1][2][3][8][11][13][19][20][21][22][23][24][25][27] and references within). In general, data symbols are divided into sets and parity symbols (i.e., local parities) are added to each set (often, using an MDS code). This way, when a number of erasures not exceeding the number of parity symbols occurs in a set, such erasures are rapidly recovered. In addition to the local parities, a number of global parities are also added. Those global parities involve all of the data symbols and may include the local parity symbols as well. The global parities can correct situations in which the erasure-correcting power of the local parities has been exceeded.

The interest in erasure correcting codes with local and global properties arises mainly from two applications. One of them is the cloud. A cloud configuration may consist of many storage devices, of which some of them may even be in different geographical locations and the data is distributed across them. In the case that one or more of those devices fails, it is desirable to recover its contents “locally,” that is, using a few parity devices within a set
of limited size in order to affect performance as little as possible. However, the local parity may not be enough. In case the erasure-correcting capability of a local set is exceeded, extra protection is needed. In order to handle this situation, some devices containing global parities are incorporated, and when the local correction power is exceeded, the global parities are invoked and correction is attempted. If such a situation occurs, there will be an impact on performance, but data loss may be averted. It is expected that the cases in which the local parity is exceeded are relatively rare events, so the aforementioned impact on performance does not occur frequently. As an example of this type of application, we refer the reader to the description of the Azure system [12] or to the Xorbas code presented in [23].

A second application occurs in the context of Redundant Arrays of Independent Disks (RAID) architectures [6]. In this case, a RAID architecture protects against one or more storage device failures. For example, RAID 5 adds one extra parity device, allowing for the recovery of the contents of one failed device, while RAID 6 protects against up to two device failures. In particular, if those devices are Solid State Drives (SSDs), like flash memories, their reliability decays with time and with the number of writes and reads [16]. The information in SSDs is generally divided into pages, each page containing its own internal Error-Correction Code (ECC). It may happen that a particular page degrades and its ECC is exceeded. However, this situation may not be known to the user until the page is accessed (what is known as a silent failure). Assuming an SSD has failed in a RAID 5 scheme, if during reconstruction a silent page failure is encountered in one of the surviving SSDs, then data loss will occur. A method around this situation is using RAID 6. However, this method is costly, since it requires two whole SSDs as parity. It is more desirable to divide the information in a RAID type of architecture into \(m \times n\) stripes: \(m\) represents the size of a stripe, and \(n\) is the number of SSDs. The RAID architecture can be viewed as consisting of a large number of stripes, each stripe encoded and decoded independently. Certainly, codes like the ones used in cloud applications can be used as well for RAID applications. In practice, the choice of code depends on the statistics of errors and on the frequency of silent page failures. RAID systems, however, may behave differently than a cloud array of devices, in the sense that each column represents a whole storage device. When a device fails, then the whole column is lost, a correlation that may not occur in cloud applications. For that reason, RAID architectures may benefit from a special class of codes with local and global properties, the so-called Sector-Disk (SD) codes, which take into account such correlations [17][18].

From now on, we call symbols the entries of a code with local and global properties. Such symbols can be whole devices (for example, in the case of cloud applications) or pages (in the case of RAID applications for SSDs). Each symbol may be protected by one local group, but a natural extension is to consider multiple localities [22][25][29]. A special case of multiple localities is given by product codes [15]: any symbol is protected by either horizontal or vertical parities.

Product codes by themselves may also be used in RAID type of architectures: the horizontal parities protect a number of devices from failure. The vertical parities allow for rapid recovery of a page or sector within a device (a first responder type of approach). However,
if the number of silent failures exceeds the correcting capability of the vertical code, and the horizontal code is unusable due to device failure, data loss will occur. For that reason, it may be convenient to incorporate a number of global parities to the product code.

In effect, assume that we have a product code consisting of $m \times n$ arrays such that each column has $v$ parity symbols and each row has $h$ parity symbols. If in addition to the horizontal and vertical parities we have $g$ extra parities, we say that the code is an Extended Product (EPC) code and we denote it by $EP(m,v;n,h;g)$. Notice that if $g = 0$, we have a regular product code. Similarly, if $v = 0$, we have a Locally Recoverable (LRC) code.

Constructions of LRC codes involve different issues and tradeoffs, like the size of the field and optimality criteria. The same is true for EPC codes, of which, as we have seen above, LRC codes are a special case. In particular, one goal is to keep the size of the required finite field small, since operations over a small field have less complexity than over a larger field due to the smaller look-up tables involved. For example, Integrated Interleaved (II) codes [10][26] over $GF(q)$, where $q \geq \max\{m,n\}$, were proposed in [2] as LRC codes (II codes are closely related to Generalized Concatenated Codes [5][30]). Let us mention the construction in [14], which also reduces field size when failures are correlated. Similarly, we will propose a new family of codes that we call Generalized Product (GPC) codes, of which both product codes and II codes are special cases.

As LRC codes, EPC codes also have optimality issues. For example, LRC codes optimizing the minimum distance were presented in [25], and except for special cases, in general II codes are not optimal as LRC codes, but the codes in [25] require a field of size at least $mn$, so there is a tradeoff. The same happens with GPC codes: except for the special case of one global parity, they do not optimize the minimum distance. We examine some cases of EPC codes that do optimize the minimum distance for two and three global parities, but a larger field is required.

There are stronger criteria for optimization than the minimum distance in LRC codes. For example, PMDS codes [1][3][7][12] satisfy the Maximally Recoverable (MR) property [7][9]. The definition of the MR property is extended for EPC codes in [9], but it turns out that EPC codes with the MR property are difficult to obtain. For example, in [9] it was proven that an EPC code $EP(m,1;n,1;1)$ (i.e., one vertical and one horizontal parity per column and row and one global parity) with the MR property requires a field that is superlinear on the size of the array (and no explicit construction is given). We will not address EPC codes with the MR property here.

Although the constructions can be extended to finite fields of any characteristic, for simplicity, in what follows we assume that the finite fields have characteristic 2.

The paper is structured as follows: in Section 2 we present the definition of GPC codes and give their properties, like their erasure-correcting capability, their minimum distance and encoding and decoding algorithms. In Section 3 we present an upper bound on the minimum distance of EPC codes and we give constructions with one, two and three global parities attaining the bound. We end the paper by drawing some conclusions.
2 Generalized Product (GPC) Codes

We start by defining Generalized Product Codes, which unify product codes with II codes. These codes also consist of $m \times n$ arrays whose elements are in a finite field $GF(q)$ and it has similar characteristics to a $t$-level II code, except that the last $m - k$ rows are devoted to parity in such a way that each column in the code belongs in an $[m, m - k]$ MDS code. Explicitly,

**Definition 2.1** Take $t$ integers $1 \leq u_0 < u_1 < \ldots < u_{t-1} \leq n - 1$ and let $\underline{u}$ be the following vector of length $m = s_0 + s_1 + \cdots + s_{t-1}$, where $s_i \geq 1$ for $0 \leq i \leq t-1$:

$$\underline{u} = \begin{pmatrix} s_0 & u_0, u_0, \ldots, u_0, & s_1 & u_1, u_1, \ldots, u_1, \ldots, & s_{t-1} & u_{t-1}, u_{t-1}, \ldots, u_{t-1} \end{pmatrix}. \tag{1}$$

Consider a set $\{C_i\}$ of $t$ nested $[n, n - u_i, u_i + 1]$, $0 \leq i \leq t - 1$, Reed-Solomon [15] (RS) codes with elements in a finite field $GF(q)$, $q > \max\{m, n\}$, such that a parity-check matrix for $C_i$ is given by

$$H_i = \begin{pmatrix} 1 & 1 & 1 & \ldots & 1 \\ 1 & \alpha & \alpha^2 & \ldots & \alpha^{n-1} \\ 1 & \alpha^2 & \alpha^4 & \ldots & \alpha^{2(n-1)} \\ \vdots & \vdots & \vdots & \ddots & \vdots \\ 1 & \alpha^{u_i-1} & \alpha^{2(u_i-1)} & \ldots & \alpha^{(u_i-1)(n-1)} \end{pmatrix}, \tag{2}$$

where $\alpha$ is an element of order $O(\alpha) \geq n$ in $GF(q)$.

For $0 \leq m - k < s_{t-1}$, let $C(n; k, \underline{u})$ be the code consisting of $m \times n$ arrays over $GF(q)$ such that, for each array in the code with rows $\underline{c}_0, \underline{c}_1, \ldots, \underline{c}_{m-1}$, $\underline{c}_j \in C_0$ for $0 \leq j \leq m - 1$ and, if

$$\hat{s}_i = \sum_{j=1}^{t-1} s_j \text{ for } 0 \leq i \leq t - 1, \tag{3}$$

then

$$\bigoplus_{j=0}^{m-1} \alpha^{r_j} \underline{c}_j \in C_i \text{ for } 1 \leq i \leq t - 1 \text{ and } 0 \leq r \leq \hat{s}_i - 1 \tag{4}$$

$$\bigoplus_{j=0}^{m-1} \alpha^{r_j} \underline{c}_j = 0 \text{ for } 0 \leq r \leq m - k - 1. \tag{5}$$

Then we say that $C(n; k, \underline{u})$ is a $t$-level Generalized Product (GPC) code.

\[\square\]
In reality, it is not necessary that the codes $C_i$ in Definition 2.1 are RS with a parity-check matrix as given by (2), or not even MDS, but we make the assumption for simplicity. The codes may even be binary [28].

Before giving the properties of $t$-level GPC codes, we present some examples.

**Example 2.1** Assume that $k = m$ in Definition 2.1 then, there are no conditions (5) and $C(n; m, u)$ is a $t$-level Integrated Interleaved (II) [2][26] code.

So, $t$-level II codes can be viewed as a special case of $t$-level GPC codes. □

**Example 2.2** Assume that $t = 1$, then (1) gives $u = (u_0, u_0, \ldots, u_0)$ and, if $k < m$, $C(n; k, m, u_0, u_0, \ldots, u_0)$ is a regular product code [15] such that each row is in an $[n, n - u_0]$ code and each column in an $[m, k]$ code.

So, product codes can be viewed as a special case of $t$-level GPC codes. □

**Example 2.3** Assume that $t = 2$. Then, $C_1 \subset C_0$, $u = (u_0, u_0, \ldots, u_0, u_1, u_1, \ldots, u_1)$, $s_0 + s_1 = m$, and consider the 2-level GPC code $C(n; k, u_0)$ with $0 \leq m - k < s_1$. Let $\mathcal{C} = (C_0, C_1, \ldots, C_{m-1})$ be an $m \times n$ array in $C(n; k, u)$. Then, $C_j \in C_0$ for each $0 \leq j \leq m - 1$, and (4) and (5) give

\[
\bigoplus_{j=0}^{m} \alpha^{rj} C_j \in C_1 \text{ for } 0 \leq r \leq s_1 - 1
\]

and

\[
\bigoplus_{j=0}^{m-1} \alpha^{rj} C_j = 0 \text{ for } 0 \leq r \leq m - k - 1.
\]

The 2-level II codes presented in [10] correspond to $C(n; m, u)$ in this example, i.e., only equations (6) are taken into account since $k = m$.

As another special case, take $k = m - 1$ and $u = (1, 1, \ldots, 1, 2, 2)$. The rows $C_0, C_1, \ldots, C_{m-1}$ of $C(n; m - 1, (1, 1, \ldots, 1, 2, 2))$ constitute a 2-level II code. Each column is in an $[m, m - 1, 2]$ code, each row is in an $[n, n - 1, 2]$ code (single parity). The $C_0$ code is the $[n, n - 1, 2]$ code, and the $C_1$ code is an $[n, n - 2, 3]$ code given by the parity-check matrix

\[
H_2 = \begin{pmatrix}
1 & 1 & 1 & \cdots & 1 \\
1 & \alpha & \alpha^2 & \cdots & \alpha^{n-1}
\end{pmatrix}.
\]
Moreover, (6) and (7) give

\[
\bigoplus_{i=0}^{m-1} \alpha^i c_i \in C_1
\]

\[
\bigoplus_{i=0}^{m-1} c_i = 0.
\]

It is not hard to prove directly that this code can correct any 5 erasures, but this will be a consequence of Corollary 2.2 to be presented below. It consists of a product code (which has minimum distance 4) plus one extra (global) parity. This extra parity brings the minimum distance up from 4 to 6. For instance, if \( m = 4 \) and \( n = 5 \), erasure patterns like the following (vertices of a rectangle)

\[
\begin{array}{|c|c|}
\hline
E & E \\
\hline
E & E \\
\hline
\end{array}
\]

are uncorrectable by the product code but not by \( C(5; 3, (1, 2, 2)) \). An extra erasure in addition to the four depicted above can be corrected by either the horizontal or the vertical code.

\[\blacksquare\]

**Example 2.4** Assume that \( t = 3 \). Then, \( C_2 \subset C_1 \subset C_0 \),

\[u = \left( \begin{array}{cccc}
\begin{array}{c}
\vdots
\end{array}
& \begin{array}{c}
\vdots
\end{array}
& \begin{array}{c}
\vdots
\end{array}
& \begin{array}{c}
\vdots
\end{array}
\end{array}\right),
\]

\( s_0 + s_1 + s_2 = m \), and consider the 3-level GPC code \( C(n; k, u) \) with \( 0 \leq m - k \leq s_2 \). Let \( \underline{u} = (u_0, u_1, \ldots, u_{m-1}) \) be an \( m \times n \) array in \( C(n; k, u) \). Then, \( \underline{u}_j \in C_0 \) for each \( 0 \leq j \leq m - 1 \), and (4) and (5) give

\[
\bigoplus_{j=0}^{m-1} \alpha^{rj} \underline{u}_j \in C_2 \text{ for } 0 \leq r \leq s_2 - 1
\]

(8)

\[
\bigoplus_{j=0}^{m-1} \alpha^{rj} \underline{u}_j \in C_1 \text{ for } 0 \leq r \leq s_1 + s_2 - 1
\]

(9)

\[
\bigoplus_{j=0}^{m-1} \alpha^{rj} \underline{u}_j = 0 \text{ for } 0 \leq r \leq m - k - 1.
\]

(10)

\[\blacksquare\]
We are now ready to state the main result regarding GPC codes.

**Theorem 2.1** Consider an \( m \times n \) array corresponding to a \( C(n; k, u) \) \( t \)-level GPC code as given by Definition 2.1. Then, the code can correct up to \( u_0 \) erasures in any row, up to \( u_i \) erasures in any \( s_i \) rows, \( 1 \leq i \leq t - 1 \), and up to \( n \) erasures in any \( m - k \) rows.

**Proof:** We may assume that the rows with erasures contain more than \( u_0 \) erasures, since each row is in \( C_0 \), an \([n, n - u_0, u_0 + 1]\) code, hence, rows with up to \( u_0 \) erasures can be corrected. Assume that there are up to \( m - k \) erased rows and a number \( \ell \) of rows with more than \( u_0 \) erasures such that there are up to \( u_i \) erasures in any up to \( s_i \) rows, \( 1 \leq i \leq t - 1 \). We do induction on \( \ell \).

Assume first that \( \ell = 0 \), that is, we have up to \( m - k \) erased rows and the rest of the rows are erasure free. We can certainly correct such up to \( m - k \) erased rows by using (5) (which states that each column in the array is in an \([m, m - k, m - k + 1]\) MDS code).

So, assume that there are \( \ell \geq 1 \) rows with more than \( s_0 \) erasures each such that there are up to \( u_i \) erasures in any up to \( s_i \) rows, \( 1 \leq i \leq t - 1 \). By induction, up to \( \ell - 1 \) rows with this property are correctable.

Let \( i_0, i_1, \ldots, i_{m-1} \) be an ordering of the rows according to a non-increasing number of erasures such that:

1. Rows \( i_0, i_1, \ldots, i_{m-k-1} \) are erased.
2. Row \( i_{m-k+j} \) for \( 0 \leq j \leq \ell - 1 \) has \( v_j \) erasures, where \( u_{t-1} \geq v_0 \geq v_1 \geq \ldots \geq v_{\ell-1} > u_0 \).
3. Rows \( i_{m-k+\ell}, i_{m-k+\ell+1}, \ldots, i_{m-1} \) have no erasures.

It suffices to prove that the \( v_{\ell-1} \) erasures in row \( i_{m-k+\ell-1} \) can be corrected. Then we are left with \( \ell - 1 \) rows with more than \( s_0 \) erasures each such that there are up to \( u_i \) erasures in any up to \( s_i \) rows, \( 1 \leq i \leq t - 1 \), and the result follows by induction.

Choose a code \( C_s \) from the nested set of codes \( C_i \), \( 1 \leq i \leq t - 1 \), in Definition 2.1 such that \( C_s \) can correct \( v_{\ell-1} \) erasures. Rearranging the order of the elements of the sums in (4), and since \( C_{t-1} \subset C_{t-2} \subset \cdots \subset C_s \), from (4) we have

\[
\bigoplus_{j=0}^{m-1} \alpha^{r_j} \mathcal{C}_j \in C_s \text{ for } 0 \leq r \leq m - k + \ell - 1. \tag{11}
\]

Since the \((m - k + \ell) \times m\) matrix corresponding to the coefficients of the \( \mathcal{C}_j \)s in (11) is a Vandermonde matrix, it can be triangulated, giving

\[
\mathcal{C}_r \oplus \left( \bigoplus_{j=r+1}^{m-1} \gamma_{r,j} \mathcal{C}_j \right) \in C_s \text{ for } 0 \leq r \leq m - k + \ell - 1, \tag{12}
\]
where the coefficients $\gamma_{r,j}$ are a result of the triangulation. In particular, taking $r = m - k + \ell - 1$ in (12), we obtain

$$\mathcal{L}_{m-k+\ell-1} \oplus \left( \bigoplus_{j=m-k+\ell}^{m-1} \gamma_{m-k+\ell-1,j} \mathcal{L}_{i_j} \right) \in \mathcal{C}_s. \quad (13)$$

Since $\mathcal{L}_{m-k+\ell-1}$ has $v_{\ell-1}$ erasures and $\mathcal{L}_{i_j}$ has no erasures for $m - k + \ell \leq j \leq m - 1$, then $\mathcal{L}_{m-k+\ell-1} \oplus \left( \bigoplus_{j=m-k+\ell}^{m-1} \gamma_{m-k+\ell-1,j} \mathcal{L}_{i_j} \right)$ has $v_{\ell-1}$ erasures. Since the vector is in $\mathcal{C}_s$, the erasures can be corrected. Once $\mathcal{L}_{m-k+\ell-1} \oplus \left( \bigoplus_{j=m-k+\ell}^{m-1} \gamma_{m-k+\ell-1,j} \mathcal{L}_{i_j} \right)$ is corrected, $\mathcal{L}_{m-k+\ell-1}$ is obtained as

$$\mathcal{L}_{m-k+\ell-1} = \left( \mathcal{L}_{m-k+\ell-1} \oplus \left( \bigoplus_{j=m-k+\ell}^{m-1} \gamma_{m-k+\ell-1,j} \mathcal{L}_{i_j} \right) \right) \oplus \left( \bigoplus_{j=m-k+\ell}^{m-1} \gamma_{m-k+\ell-1,j} \mathcal{L}_{i_j} \right)$$

and the result follows by induction on $\ell$.

\[\square\]

Theorem 2.1 generalizes Theorem 1 in [2]. The proof of Theorem 2.1 is constructive in the sense that it provides a decoding algorithm. The following example illustrates Theorem 2.1 and the decoding algorithm.

**Example 2.5** Consider the 3-level GPC code $C(7; 4, (1, 1, 3, 4, 4, 4))$ according to Definition 2.1 and Example 2.4. We have three codes $C_2 \subset C_1 \subset C_0$, where $C_0$ is a $[7, 6, 2]$ code, $C_1$ is a $[7, 4, 4]$ code and $C_2$ is a $[7, 3, 5]$ code. In addition, each column is in a $[6, 4, 3]$ code. We may assume that the entries of these codes are in $GF(8)$ and that $\alpha$ is a primitive element in $GF(8)$.

Consider the following $6 \times 7$ array with erasures denoted by $E$:

$$
\begin{array}{ccccccc}
\mathcal{L}_0 & & & & & & E \\
\mathcal{L}_1 & E & E & E & E & E & E \\
\mathcal{L}_2 & & E & E & & E & \\
\mathcal{L}_3 & E & & & E & & E \\
\mathcal{L}_4 & E & E & E & E & E & E \\
\mathcal{L}_5 & & & & & & E \\
\end{array}
$$

The first step is correcting the single erasures in $\mathcal{L}_0$ and in $\mathcal{L}_5$. An ordering of the remaining rows in non-increasing number of erasures is $\{i_0, i_1, i_2, i_3\} = \{1, 4, 2, 3\}$. In particular, $\mathcal{L}_3$ has three erasures. Following the proof of Theorem 2.1, there are $m - k = 2$ erased rows (rows $\mathcal{L}_1$ and $\mathcal{L}_4$) and $\ell = 2$ rows with erasures, but not totally erased (rows $\mathcal{L}_0$ and $\mathcal{L}_3$). According to (8) and (9),
Notice that $C_1$ can correct three erasures (i.e., $s = 1$ in the proof of Theorem 2.1). Rearranging the $c_i$s above in non-increasing number of erasures, we obtain

$$
\begin{align*}
C_0 + C_1 + C_2 + C_3 + C_0 + C_5 & \in C_2 \\
C_0 + \alpha^3 C_1 + \alpha^2 C_2 + \alpha^3 C_3 + \alpha^4 C_4 + \alpha^5 C_5 & \in C_2 \\
C_0 + \alpha^2 C_1 + \alpha^4 C_2 + \alpha^6 C_3 + \alpha^8 C_4 + \alpha^{10} C_5 & \in C_2 \\
C_0 + \alpha^3 C_1 + \alpha^6 C_2 + \alpha^9 C_3 + \alpha^{12} C_4 + \alpha^{15} C_5 & \in C_1.
\end{align*}
$$

which corresponds to $\{11\}$ in the proof of Theorem 2.1 (notice, $C_2 \subset C_1$). The coefficients in the linear system above correspond to the following matrix:

$$
\begin{pmatrix}
1 & 1 & 1 & 1 & 1 \\
\alpha & \alpha^4 & \alpha^2 & \alpha^3 & 1 \\
\alpha^2 & \alpha^8 & \alpha^4 & \alpha^6 & 1 \\
\alpha^3 & \alpha^{12} & \alpha^6 & \alpha^9 & 1
\end{pmatrix}.
$$

Triangulating this matrix in $GF(8)$, where $1 \oplus \alpha \oplus \alpha^3 = 0$, gives

$$
\begin{pmatrix}
1 & 1 & 1 & 1 & 1 \\
0 & 1 & \alpha^2 & \alpha^5 & \alpha \\
0 & 0 & 1 & \alpha^3 & \alpha \\
0 & 0 & 0 & 1 & \alpha^3 \alpha^5
\end{pmatrix}.
$$

Applying this triangulation to the linear system, and since $C_2 \subset C_1$, we obtain the following triangulated system:

$$
\begin{align*}
C_1 + C_4 + C_2 + C_3 + C_0 + C_5 & \in C_2 \\
C_1 + \alpha^2 C_2 + \alpha^5 C_3 + \alpha^3 C_4 + \alpha^4 C_5 & \in C_2 \\
C_2 + \alpha C_3 + \alpha^3 C_4 + \alpha^5 C_5 & \in C_2 \\
C_3 + \alpha^3 C_0 + \alpha^5 C_5 & \in C_1.
\end{align*}
$$

Since $C_3$ has 3 erasures and $C_0$ and $C_5$ have no erasures, $C_3 \oplus \alpha^3 C_0 + \alpha^5 C_5$ has 3 erasures, which can be corrected in $C_1$. Then,

$$
C_3 = (C_3 \oplus \alpha^3 C_0 + \alpha^5 C_5) \oplus (\alpha^3 C_0 + \alpha^5 C_5).
$$

Similarly, $C_2 \oplus \alpha C_3 + \alpha^3 C_4 + \alpha C_5$ has 4 erasures, which can be corrected in $C_2$, and

$$
C_2 = (C_2 \oplus \alpha C_3 + \alpha^3 C_4 + \alpha C_5) \oplus (\alpha C_3 + \alpha^3 C_4 + \alpha C_5).
$$
Finally, $c_1$ and $c_4$ are obtained from (5). We can apply the triangulation, so we obtain

$$
c_4 = \alpha^2 c_2 + \alpha^5 c_3 + \alpha c_5 + \alpha^4 c_5,
\quad c_1 = c_4 \oplus c_2 \oplus c_3 \oplus c_0 \oplus c_5,
$$

completing the decoding.

Before discussing the dimension, the encoding and the minimum distance of the code, let us state and prove the following lemma.

**Lemma 2.1** Consider the $t$-level GPC code $C(n; k, u)$ as given by Definition 2.1. Then, if $\hat{s}_t = m - k$ and $\hat{s}_j$ is given by (3) for $0 \leq j \leq t - 1$, given $u_j + 1$ fixed locations in $\hat{s}_{j+1} + 1$ different rows, then there is an array in $C(n; k, u)$ that is non-zero in such $(\hat{s}_{j+1} + 1)$ ($u_j + 1$) locations and 0 elsewhere.

**Proof:** Given $j$ such that $0 \leq j \leq t - 1$ and $u_j + 1$ fixed locations in a vector of length $n$, since $C_j$ is an $[n, n - u_j, u_j + 1]$ MDS code, there is a codeword $\underline{w}$ in $C_j$ whose non-zero entries are in such $u_j + 1$ fixed locations. Assume that the $\hat{s}_{j+1} + 1$ rows selected are $i_0, i_1, \ldots, i_{\hat{s}_{j+1}}$, where

$$0 \leq i_0 < i_1 < \ldots < i_{\hat{s}_{j+1}} \leq m - 1.$$

Let $\underline{v} = (v_0, v_1, \ldots, v_{\hat{s}_{j+1}})$ be a codeword of weight $\hat{s}_{j+1} + 1$ in the (shortened) $[\hat{s}_{j+1} + 1, 1, 1, \hat{s}_{j+1} + 1]$ RS code whose parity-check matrix is given by

$$
\begin{pmatrix}
1 & 1 & 1 & \cdots & 1 \\
1 & \alpha^{i_0} & \alpha^{i_1} & \cdots & \alpha^{i_{\hat{s}_{j+1}}} \\
1 & \alpha^{2i_0} & \alpha^{2i_1} & \cdots & \alpha^{2i_{\hat{s}_{j+1}}} \\
\vdots & \vdots & \vdots & \ddots & \vdots \\
1 & \alpha^{(\hat{s}_{j+1} - 1)i_0} & \alpha^{(\hat{s}_{j+1} - 1)i_1} & \cdots & \alpha^{(\hat{s}_{j+1} - 1)i_{\hat{s}_{j+1}}} \\
\end{pmatrix}.
$$

In particular,

$$
\hat{s}_{j+1} \bigoplus_{s=0}^{\hat{s}_{j+1}} \alpha^{r_s} v_s = 0 \quad \text{for} \quad 0 \leq r \leq \hat{s}_{j+1} - 1. \quad (14)
$$

Consider the $m \times n$ array of weight $(\hat{s}_{j+1} + 1)$ ($u_j + 1$) such that row $i_s$ equals $v_s \underline{w}$ for $0 \leq s \leq \hat{s}_{j+1}$, and the remaining rows are zero. We will show that this array is in $C(n; k, u)$.\]
Since each row of the array is in $C_j$ by design, in particular, it is in $C_0$. Next, according to (4) and (5), we have to show that

$$\hat{s}_{j+1} \bigoplus_{s=0}^{\alpha^{r_i}} (v_s w) \in C_i \text{ for } 1 \leq i \leq t - 1 \text{ and } 0 \leq r \leq \hat{s}_i - 1$$

(15)

$$\hat{s}_{j+1} \bigoplus_{s=0}^{\alpha^{r_i}} (v_s w) = 0 \text{ for } 0 \leq r \leq m - k - 1.$$  

(16)

If $0 \leq j \leq i - 1$, then, $\hat{s}_{j+1} \geq \hat{s}_i$, and, for $0 \leq r \leq \hat{s}_i - 1$, by (14),

$$\hat{s}_{j+1} \bigoplus_{s=0}^{\alpha^{r_i}} (v_s w) = \left(\bigoplus_{s=0}^{\hat{s}_i} \alpha^{r_i} v_s \right) w = 0,$$

so (15) follows. Since $m - k \leq \hat{s}_{j+1}$, also (16) follows from (14).

If $i \leq j \leq t - 1$, then $C_j \subseteq C_i$ and $w \in C_i$, so (15) also follows in this case.

Example 2.6 Consider the 3-level GPC code $C(7; 4, (1, 1, 3, 4, 4))$ of Example 2.5. According to Lemma 2.1, the locations denoted by $E$ in the following arrays correspond to the non-zero entries of arrays in $C(7; 4, (1, 1, 3, 4, 4))$:

| E | E | E | E | E |
| E | E | E | E | E |
| E | E | E | E | E |
| E | E | E | E | E |
| E | E | E | E | E |

The arrays with erasures in locations $E$ above are uncorrectable, since, provided the zero array was stored, the decoding cannot decide between the zero array and the arrays with non-zero entries in the locations $E$.

Example 2.6

Before stating the dimension $K$ of a $t$-level GPC code $C(n; k, u)$, we give an auxiliary general lemma.
Lemma 2.2 Consider and $[n, k]$ code, and let $S = \{i_0, i_1, \ldots, i_{s-1}\}$, where $0 \leq i_0 < i_1 < \cdots < i_{s-1} \leq n-1$. Assume that, given a codeword with erasures in $S$, the code can correct such erasures, while, for any $i \not\in S$, erasures in $S \cup \{i\}$ are not correctable. Then,

$$n - k = s.$$  (17)

Proof: Since the erasures in $S$ are correctable, there are at least $s$ linearly independent parity equations, so

$$n - k \geq s.$$ 

Assume that $n - k > s$. Let $H$ be an $(n - k) \times n$ parity-check matrix of the code such that the first $s$ rows of $H$ are used to correct the $s$ erasures in $S$, thus, the $s \times s$ submatrix consisting of those first $s$ rows and columns $i_0, i_1, \ldots, i_{s-1}$ is invertible.

Consider next the matrix consisting of the first $s + 1$ rows in $H$. By row operations, we can make the entries $i_0, i_1, \ldots, i_{s-1}$ in the $(s + 1)$-th row equal to zero. Since the first $s + 1$ rows of $H$ have rank $s + 1$, then there is a non-zero location $i, i \not\in S$, in the $(s + 1)$-th row. Thus, columns $S \cup \{i\}$ in the first $s + 1$ rows of $H$ are linearly independent and hence erasures in $S \cup \{i\}$ are correctable, a contradiction, so (17) holds. 

Corollary 2.1 Consider the $t$-level GPC code $C(n; k, \underline{u})$ as given by Definition 2.1. Then, $C(n; k, \underline{u})$ is an $[N, K]$ code, where $N = mn$ and

$$K = kn - \left(\sum_{i=0}^{t-2} s_i u_i\right) - (s_{t-1} - m + k)u_{t-1}.$$  (18)

Proof: Let $\hat{s}_t = m - k$ and $\hat{s}_j$ be given by (3) for $0 \leq j \leq t - 1$. Assume that the zero array is stored, and a received array $W$ has erasures in the last $u_i$ entries of rows $m - \hat{s}_i$ to $m - \hat{s}_{i+1} - 1$ for $0 \leq i \leq t - 1$, and in all the entries of rows $k$ to $m - 1$. Thus, $W$ has a total of

$$n(m - k) + \left(\sum_{i=0}^{t-2} s_i u_i\right) + (s_{t-1} - m + k)u_{t-1}$$

erasures, and by Theorem 2.1 it will be correctly decoded as the zero codeword.

Consider an array $V$ which coincides with $W$, except in one location in which it has an extra erasure. We will show that any such $V$ is uncorrectable, so, by Lemma 2.2

$$N - K = n(m - k) + \left(\sum_{i=0}^{t-2} s_i u_i\right) + (s_{t-1} + m + k)u_{t-1},$$

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which is equivalent to (18).

For each \((u', v')\) such that \((u', v')\) is not in the set of erasures of \(W\), define \(i'\), \(0 \leq i' \leq t - 1\), such that \(m - \hat{s}_{i'} \leq u' \leq m - \hat{s}_{i'+1} - 1\), and let \(Y^{(u', v')} = \left(y_{a,b}^{(u', v')}\right)_{0 \leq a \leq m-1, 0 \leq b \leq n-1}\) be an array in \(\mathcal{C}(n; k, u)\) whose non-zero coordinates are in the intersection of rows \(u', u' + 1, \ldots, u' + \hat{s}_{i'+1}\) and columns \(v', n - u', n - u' + 1, \ldots, n - 1\). Such a non-zero array exists due to Lemma 2.1.

Assume that the extra erasure in \(V\) is in location \((u, v)\), and if \(u_t = n\), define \(j\), \(0 \leq j \leq t\), such that \(n - u_j \leq v \leq n - u_{j-1} - 1\). Consider the arrays \(Y^{(u', v)}\), where \(u \leq u' \leq m - \hat{s}_{j-1}\). For each \(u', u < u' \leq m - \hat{s}_{j-1}\), choose constants \(c_{u'}\) such that
\[
y_{(u', v)}^{u} \oplus \bigoplus_{z=u+1}^{u'} c_{z}y_{(z, v)}^{u} = 0.
\]

Then, defining
\[
Y = \bigoplus_{z=u}^{m-\hat{s}_j-1} Y^{(z, v)},
\]
we can see that \(Y\) has a non-zero entry in \((u, v)\), while the remaining non-zero entries are contained in the locations of the erasures of \(W\). So, array \(V\) is uncorrectable, since it can be decoded either as the zero array or as \(Y\). \(\square\)

Theorem II.1 in [26], which corresponds to Corollary 2 in [2], is a special case of Corollary 2.1.

The encoding is a special case of the decoding. For example, we may place the parities at the end of the array in increasing order of parities, as shown in Corollary 2.1. The parities are considered as erasures and may be obtained using the triangulation method described in Theorem 2.1. The fact that the locations of the erasures are known allows for a simplification of the decoding algorithm. For example, the triangulated matrix corresponding to the coefficients of [12] may be precomputed. We omit the implementation details.

**Example 2.7** We illustrate the proof of Corollary 2.1 with the 3-level GPC code \(\mathcal{C}(7; 4, (1, 1, 3, 4, 4, 4))\) of Examples 2.5 and 2.6. By Corollary 2.1, this code is a \([42, 19]\) code. Following the proof of Corollary 2.1, denote by \(E\) the erased locations in an array \(W\):

\[
W = \begin{array}{cccccc}
E & & & & & \\
E & & & & & \\
E & E & E & E & E & E \\
E & E & E & E & E & E \\
E & E & E & E & E & E \\
E & E & E & E & E & E \\
\end{array}
\]

If the non-erased locations of \(W\) are zero, by Theorem 2.1, the array will be decoded as the zero array. Now, consider the array \(V\) which has an extra erasure in location \((u, v) = (0, 1)\), rendering
Consider the following arrays $Y^{(u',1)}$, $0 \leq u' \leq 3$, defined as in Corollary 2.1 whose non-zero entries are denoted $y_{a,b}^{(u',1)}$ below:

\[
Y^{(0,1)} = \begin{bmatrix}
0 & y_{0,1}^{(0,1)} & 0 & 0 & 0 & 0 & y_{0,6}^{(0,1)} \\
0 & y_{1,1}^{(0,1)} & 0 & 0 & 0 & y_{1,6}^{(0,1)} & 0 \\
0 & y_{2,1}^{(0,1)} & 0 & 0 & 0 & y_{2,6}^{(0,1)} & 0 \\
0 & y_{3,1}^{(0,1)} & 0 & 0 & 0 & y_{3,6}^{(0,1)} & 0 \\
0 & y_{4,1}^{(0,1)} & 0 & 0 & 0 & y_{4,6}^{(0,1)} & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 0 \\
\end{bmatrix}
\]

\[
Y^{(1,1)} = \begin{bmatrix}
0 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & y_{1,1}^{(1,1)} & 0 & 0 & 0 & y_{1,6}^{(1,1)} & 0 \\
0 & y_{2,1}^{(1,1)} & 0 & 0 & 0 & y_{2,6}^{(1,1)} & 0 \\
0 & y_{3,1}^{(1,1)} & 0 & 0 & 0 & y_{3,6}^{(1,1)} & 0 \\
0 & y_{4,1}^{(1,1)} & 0 & 0 & 0 & y_{4,6}^{(1,1)} & 0 \\
0 & y_{5,1}^{(1,1)} & 0 & 0 & 0 & y_{5,6}^{(1,1)} & 0 \\
\end{bmatrix}
\]

\[
Y^{(2,1)} = \begin{bmatrix}
0 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & y_{2,1}^{(2,1)} & 0 & 0 & y_{2,4}^{(2,1)} & y_{2,5}^{(2,1)} & y_{2,6}^{(2,1)} \\
0 & y_{3,1}^{(2,1)} & 0 & 0 & y_{3,4}^{(2,1)} & y_{3,5}^{(2,1)} & y_{3,6}^{(2,1)} \\
0 & y_{4,1}^{(2,1)} & 0 & 0 & y_{4,4}^{(2,1)} & y_{4,5}^{(2,1)} & y_{4,6}^{(2,1)} \\
0 & y_{5,1}^{(2,1)} & 0 & 0 & y_{5,4}^{(2,1)} & y_{5,5}^{(2,1)} & y_{5,6}^{(2,1)} \\
\end{bmatrix}
\]

\[
Y^{(3,1)} = \begin{bmatrix}
0 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & y_{3,1}^{(3,1)} & 0 & y_{3,2}^{(3,1)} & y_{3,3}^{(3,1)} & y_{3,5}^{(3,1)} & y_{3,6}^{(3,1)} \\
0 & y_{4,1}^{(3,1)} & 0 & y_{4,2}^{(3,1)} & y_{4,3}^{(3,1)} & y_{4,5}^{(3,1)} & y_{4,6}^{(3,1)} \\
0 & y_{5,1}^{(3,1)} & 0 & y_{5,2}^{(3,1)} & y_{5,3}^{(3,1)} & y_{5,5}^{(3,1)} & y_{5,6}^{(3,1)} \\
\end{bmatrix}
\]
Such arrays with non-zero entries exist by Lemma 2.1 (see also Example 2.6). We choose $c_1$, $c_2$ and $c_3$ such that

\[
\begin{align*}
y_{1,1}^{(0,1)} \oplus c_1 y_{1,1}^{(1,1)} &= 0 \\
y_{2,1}^{(0,1)} \oplus c_1 y_{2,1}^{(1,1)} \oplus c_2 y_{2,1}^{(2,1)} &= 0 \\
y_{3,1}^{(0,1)} \oplus c_1 y_{3,1}^{(1,1)} \oplus c_2 y_{3,1}^{(2,1)} \oplus c_3 y_{3,1}^{(3,1)} &= 0
\end{align*}
\]

Then, defining $Y = Y^{(0,1)} \oplus c_1 Y^{(1,1)} \oplus c_2 Y^{(2,1)} \oplus c_3 Y^{(3,1)}$, we see that

\[
\begin{array}{|c|c|c|c|c|c|}
\hline
& y_{0,1}^{(0,1)} & 0 & 0 & 0 & X \\
\hline
0 & 0 & 0 & 0 & 0 & X \\
\hline
0 & 0 & 0 & X & X & X \\
\hline
0 & X & 0 & X & X & X \\
\hline
0 & X & 0 & X & X & X \\
\hline
\end{array}
\]

Array $Y$ is non-zero since $y_{0,1}^{(0,1)} \neq 0$ (entries denoted by $X$ may take any value). Array $V$ may be decoded either as the zero array or as $Y$, so it is uncorrectable. Since we can make the same argument for any entry $(u, v)$ not contained in the erasures of $W$, by Lemma 2.2 the number of parity symbols is exactly 23 and the dimension of the code is 19.

\[\blacksquare\]

The following corollary extends Theorem II.2 on $t$-level I codes as stated in [26] and proven as Corollary 3 in [2]. It also generalizes the well known result that the minimum distance of a product code is the product of the minimum distances of the two component codes.

**Corollary 2.2** Consider the $t$-level GPC code $\mathcal{C}(n; k, u)$ as given by Definition 2.1. Then, if $\hat{s}_t = m - k$ and $\hat{s}_i$ is given by (3) for $0 \leq i \leq t - 1$, the minimum distance of $\mathcal{C}(n; k, u)$ is

\[
d = \min \{(\hat{s}_{i+1} + 1) (u_i + 1) , 0 \leq i \leq t - 1 \}. \tag{19}
\]

**Proof:** For each $i$ such that $0 \leq i \leq t - 1$, consider an array in $\mathcal{C}(n; k, u)$ that has $\hat{s}_{i+1}$ rows with $u_i + 1$ erasures each, one row with $u_i$ erasures, and all the other entries are zero. By Theorem 2.1 such arrays will be corrected by the code $\mathcal{C}(n; k, u)$ as the zero codeword, thus

\[
d \leq \min \{(\hat{s}_{i+1} + 1) (u_i + 1) , 0 \leq i \leq t - 1 \}.
\]

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On the other hand, by Lemma 2.1 for each $0 \leq i \leq t - 1$, there is an array in $C(n; k, u)$ of weight $(\hat{s}_{i+1} + 1)(u_i + 1)$, so

$$d \geq \min\{(\hat{s}_{i+1} + 1)(u_i + 1), 0 \leq i \leq t - 1\}$$

and (19) follows.

Example 2.8 Consider the 3-level GPC code $C(7; 4, (1, 1, 3, 4, 4))$ of Example 2.5. According to Corollary 2.2, since $m = 6$, $k = 4$, $u_0 = 1$, $u_1 = 3$, $u_2 = 4$, $s_0 = 2$, $s_1 = 1$, $s_2 = 3$ (and hence, $\hat{s}_3 = m - k = 2$, $\hat{s}_2 = s_2 = 3$, $\hat{s}_1 = s_1 + s_2 = 4$), according to (19), the minimum distance of this code is

$$d = \min \{(5)(2); (4)(4); (3)(5)\} = 10.$$  

Consider next a product code, such that the vertical code is an $[m, k_0, m - k_0 + 1]$ code, and the horizontal code is an $[n, k_1, n - k_1 + 1]$ code. In the notation of GPC codes, we denote this 1-level GPC code as $C(n; k_0, \underbrace{n - k_1, n - k_1, \ldots, n - k_1}_m)$ (see Definition 2.1 and Example 2.2). We can look at it also from the perspective of columns, and then the code is a 1-level GPC code $C(m; k_1, \underbrace{m - k_0, m - k_0, \ldots, m - k_0}_n)$. The following theorem generalizes this argument for a $t$-level GPC code.

Theorem 2.2 Consider an $m \times n$ array corresponding to a $C(n; k, u)$ $t$-level GPC code as given by Definition 2.1. Then, viewed as an $n \times m$ array on columns, the code is a $C(m; n - u_0, u')$ $t$-level GPC code, where $\hat{s}_i = m - k$, $\hat{s}_i$ is given by (3) for $1 \leq i \leq t - 1$, $u_t = n$,

$$u' = \left(\begin{array}{cccc}
s'_0 \\
u'_0, u_0, \ldots, u'_0, u'_1, s'_1, u'_1, \ldots, u'_t, u'_{t-1}, \ldots, u'_{t-1}, s'_{t-1}\end{array}\right),$$

$$u'_{t-i} = \hat{s}_i \quad \text{for} \quad 1 \leq i \leq t, \quad s'_i = u_{t-i} - u_{t-i-1} \quad \text{for} \quad 0 \leq i \leq t - 2 \quad \text{and} \quad s'_{t-1} = u_1.$$
Proof: Denote by \( c_{i}^{(H)} \), \( 0 \leq i \leq m - 1 \), the rows of the array, and by \( c_{j}^{(V)} \), \( 0 \leq j \leq n - 1 \), the columns. Specifically, if the array consists of symbols \( (c_{i,j})_{0 \leq i \leq m - 1, 0 \leq j \leq n - 1} \), then

\[
\begin{align*}
c_{i}^{(H)} &= (c_{i,0}, c_{i,1}, \ldots, c_{i,n-1}) \quad \text{for} \quad 0 \leq i \leq m - 1
\end{align*}
\]

and

\[
\begin{align*}
c_{j}^{(V)} &= (c_{0,j}, c_{1,j}, \ldots, c_{m-1,j}) \quad \text{for} \quad 0 \leq j \leq n - 1.
\end{align*}
\]

Consider the \( t \) nested codes (on columns) \( C'_{t-1} \subset C'_{t-2} \subset \cdots \subset C'_{0} \), where \( C'_{i} \) is an \( [m, m - u'_{i}, u'_{i} + 1] \) code. A parity-check matrix of \( C'_{i} \) is

\[
H'_{i} = \begin{pmatrix}
1 & 1 & 1 & \ldots & 1 \\
1 & \alpha & \alpha^2 & \ldots & \alpha^{m-1} \\
1 & \alpha^2 & \alpha^4 & \ldots & \alpha^{2(m-1)} \\
\vdots & \vdots & \ddots & \ddots & \vdots \\
1 & \alpha^{u'_{i}-1} & \alpha^{2(u'_{i}-1)} & \ldots & \alpha^{(m-1)(u'_{i}-1)}
\end{pmatrix}
\]

(22)

In order to prove the theorem, according to Definition 2.1, we have to prove that each \( c_{j}^{(V)} \in C'_{0} \), \( 0 \leq j \leq n - 1 \), and

\[
\bigoplus_{j=0}^{n-1} \alpha^{rj} c_{j}^{(V)} \in C'_{i} \quad \text{for} \quad 1 \leq i \leq t - 1 \quad \text{and} \quad 0 \leq r \leq \hat{s}'_{i} - 1
\]

(23)

\[
\bigoplus_{j=0}^{n-1} \alpha^{ij} c_{j}^{(V)} = 0 \quad \text{for} \quad 0 \leq i \leq u_{0} - 1.
\]

(24)

\( C'_{0} \) is an \( [m, m - u'_{0}, u'_{0} + 1] \) code and by (21), \( u'_{0} = m - k \), so from (5), \( c_{j}^{(V)} \in C'_{0} \).

Notice also that since each \( c_{i}^{(H)} \in C_{0} \) for \( 0 \leq i \leq m - 1 \), (24) follows.

Next we have to prove (23). In effect, (23) holds if and only if, by (22),

\[
\bigoplus_{v=0}^{m-1} \alpha^{uv} \bigoplus_{j=0}^{n-1} \alpha^{rj} c_{v,j} = 0 \quad \text{for} \quad 1 \leq i \leq t - 1, 0 \leq u \leq u'_{i} - 1 \quad \text{and} \quad 0 \leq r \leq \hat{s}'_{i} - 1,
\]

if and only if, changing the summation order,

\[
\bigoplus_{j=0}^{n-1} \alpha^{rj} \bigoplus_{v=0}^{m-1} \alpha^{uv} c_{v,j} = 0 \quad \text{for} \quad 1 \leq i \leq t - 1, 0 \leq u \leq u'_{i} - 1 \quad \text{and} \quad 0 \leq r \leq \hat{s}'_{i} - 1,
\]

if and only if

\[
\bigoplus_{v=0}^{m-1} \alpha^{uv} c_{v}^{(H)} \in \hat{C}_{i} \quad \text{for} \quad 1 \leq i \leq t - 1, 0 \leq u \leq u'_{i} - 1,
\]

(25)
where a parity-check matrix for \( \hat{C}_i \) is given by

\[
\hat{H}_i = \begin{pmatrix}
1 & 1 & 1 & \cdots & 1 \\
1 & \alpha & \alpha^2 & \cdots & \alpha^{n-1} \\
1 & \alpha^2 & \alpha^4 & \cdots & \alpha^{2(n-1)} \\
\vdots & \vdots & \vdots & \ddots & \vdots \\
1 & \alpha^{s'_i - 1} & \alpha^{2(s'_i - 1)} & \cdots & \alpha^{(n-1)(s'_i - 1)}
\end{pmatrix}.
\] (26)

By (3) and (21),

\[
s'_i = \sum_{z=i}^{t-1} s'_z = \left( \sum_{z=i}^{t-2} u_{t-z} - u_{t-z-1} \right) + u_1 = u_{t-i},
\]

so, by (26) and (2), \( \hat{H}_i = H_{t-i} \) and hence \( \hat{C}_i = C_{t-i} \). By (21), \( u'_i = \hat{s}_{t-i} \), so (26) becomes

\[
\bigoplus_{v=0}^{m-1} \alpha^{uv} \mathcal{C}(H) \in C_{t-i} \text{ for } 1 \leq i \leq t-1 \text{ and } 0 \leq u \leq \hat{s}_{t-i} - 1,
\]

which is equivalent to (4) and thus (23) and (4) are equivalent, completing the proof.

Example 2.9 Consider the 3-level GPC code \( C(7; 5, (1, 1, 3, 3, 5, 5)) \). According to Theorem 2.2, this code is also a 3-level GPC code \( C(6; 6, (1, 1, 2, 2, 4, 4, 4)) \) consisting of \( 7 \times 6 \) arrays, so, according to Theorem 2.1, it can correct any column with one erasure, up to two columns with 2 erasures, up to 2 columns with 4 erasures and up to one erased column. This allows for correction of erasures that cannot be handled by the correction on rows. For example, consider the following array, where the erasures are denoted by \( E \):

\[
\begin{array}{ccccccc}
E & E & E & E & E & E & E \\
E & E & E & E & E & E & E \\
E & E & E & E & E & E & E \\
E & E & E & E & E & E & E \\
E & E & E & E & E & E & E \\
E & E & E & E & E & E & E \\
E & E & E & E & E & E & E
\end{array}
\]

The erasures cannot be decoded by the horizontal code \( C(7; 5, (1, 1, 3, 3, 5, 5)) \), but they can certainly be handled by the vertical code \( C(6; 6, (1, 1, 2, 2, 4, 4, 4)) \).
Example 2.9 suggests an expansion of the decoding algorithm as given in the proof by triangulation of Theorem 2.1: given a $t$-level GPC code $C(n; k, u)$, each time there are erasures we apply the decoding algorithm on rows as described in Theorem 2.1. If after this process there are still erasures remaining, we apply the decoding algorithm on columns for the $C(m; m - u_0, u')$ $t$-level GPC code as determined by Theorem 2.2. The method extends the decoding method of product codes, in which erasures are iteratively corrected by both codes, until they are either corrected or an uncorrectable pattern remains.

Let us point out that the decoding algorithm can be adapted to handle errors together with erasures, but we omit its description here.

3 Extended Product Codes and Optimality Issues

The $t$-level GPC codes $C(n; k, u)$ described in Section 2 are a special case of product codes with some extra (global) parities. Let us call an extended product (EPC) code such a code, and denote it by $EP(m; v; n, h; g)$, where $v$ is the number of vertical parities, $h$ the number of horizontal parities, and $g$ the number of global parities. For example, the 3-level GPC code $C(7; 4, (1, 1, 3, 4, 4, 4))$ of Examples 2.5, 2.6 and 2.7 is an $EP(6, 2; 7, 1; 5)$, while the 3-level GPC code $C(7; 5, (1, 1, 3, 3, 5, 5))$ of Example 2.9 is an $EP(6, 1; 7, 1; 8)$.

The next lemma gives an upper bound on the minimum distance of an $EP(m; v; n, h; g)$ code.

**Lemma 3.1** Let $d(m; v; n, h; g)$ be the minimum distance of an $EP(m; v; n, h; g)$ code. Then,

$$d(m; v; n, h; g) \leq \min\{d(v, h, g; a) : \lceil (g + 1)/(m - v) \rceil \leq a \leq \min\{g + 1, n - h\}\}, \quad (27)$$

where, if $b = \lfloor (g + 1)/a \rfloor$ and $r = g + 1 - ab$, then

$$d(v, h, g; a) = (v + b)(h + a) \quad \text{for} \quad r = 0 \quad (28)$$

and

$$d(v, h, g; a) = (v + b)(h + a) + h + r \quad \text{for} \quad r \neq 0. \quad (29)$$

**Proof:** Assume first that $r = 0$, the zero array is stored, and the received array has the locations $(i, j)$ erased, where, by (27),

$$0 \leq i \leq v + b - 1 \leq v + \frac{g + 1}{(g + 1)/(m - v)} - 1 = m - 1$$

and

$$0 \leq j \leq h + a - 1 \leq h + (n - h) - 1 = n - 1.$$
In particular, since \( ab = g + 1 \), there are \((v+b)(h+a) = vh + va + hb + g + 1\) erasures. We argue that such a received array is uncorrectable, which would prove (27) when \( d(v, h, g; a) \) satisfies (28). Notice that we have \( h(v+b) \) horizontal parities and \( v(h+a) \) vertical parities corresponding to the product code in order to correct the \((v+b)(h+a)\) erasures, but \( hv \) of such parities are dependent, so that leaves us with a total of \( h(v+b)+v(h+a) - hv = hv + hb + va \) parities corresponding to the product code. In addition, \( g \) global parities can be used, giving a total of \( hv + hb + va + g \) parities, insufficient to correct the \( hv + hb + va + g + 1 \) erasures.

Similarly, assume that \( r \neq 0 \), the zero array is stored, and the received array has the locations \((i, j)\) erased, where \( 0 \leq i \leq v+b-1, 0 \leq j \leq h+a-1 \leq n-1 \), and in addition, locations \((v+b, j)\) are also erased, where \( 0 \leq j \leq h + r - 1 \). Observe that all the erasures are within the array. In effect, since \( a \) does not divide \( g + 1 \),

\[
v + b = v + \left\lfloor \frac{g + 1}{a} \right\rfloor < v + \frac{g + 1}{a} \leq v + \frac{g + 1}{(g+1)/(m-v)} \leq v + (m-v) = m.
\]

In particular, since \( ab = g + 1 - r \), there are \((v+b)(h+a) + h + r = hv + va + hb + g + h + 1\) erasures. We will show that such a received array is uncorrectable, which would prove (27) when \( d(v, h, g; a) \) satisfies (28). Notice that we have \( h(v+b+1) \) horizontal parities and \( v(h+a) \) vertical parities corresponding to the product code in order to correct such patterns, but since, as before, \( hv \) of such parities are dependent, that gives a total of \( h(v+b+1) + v(h+a) - hv = hv + hb + va + h \) parities corresponding to the product code. In addition, \( g \) global parities can be used, giving a total of \( hv + hb + va + h + g \) parities, insufficient to correct \( hv + va + hb + g + h + 1 \) erasures.

\[\Box\]

**Example 3.1** Consider an \( EP(7, 2; 8, 3; 3) \) code and let \( d(7, 2; 8, 3; 3) \) be its minimum distance. According to (27),

\[
d(7, 2; 8, 3; 3) \leq \min\{d(2, 3, 3; a) : 1 \leq a \leq 4\},
\]

where, according to (28) and (29),

\[
\begin{align*}
d(2, 3, 3; 1) &= 24 \\
d(2, 3, 3; 2) &= 20 \\
d(2, 3, 3; 3) &= 22 \\
d(2, 3, 3; 4) &= 21,
\end{align*}
\]
so

\[ d(7, 2; 8, 3; 3) \leq 20. \]

Following the proof of Lemma 3.1, a pattern of 20 uncorrectable erasures is given by

\[
\begin{array}{ccccccc}
E & E & E & E & E \\
E & E & E & E & E \\
E & E & E & E & E \\
E & E & E & E & E \\
\end{array}
\]

We will say that an \( EP(m, v; n, h; g) \) code is optimal if it meets bound (27) with equality. We will devote the rest of this section to presenting some special cases of optimal \( EP(m, v; n, h; g) \) codes. We believe that there are optimal \( EP(m, v; n, h; g) \) codes for any choice of parameters, but the subject requires further research.

**Lemma 3.2** Consider the 2-level GPC code \( C(n; k_0, u) \) as given by Definition 2.1, where

\[ u = (k_1, k_1, \ldots, k_1, k_1 + 1, k_1 + 1, \ldots, k_1 + 1). \]

Then, \( C(n; k_0, u) \) is an optimal \( EP(m, m - k_0; n, n - k_1; 1) \) code.

**Proof:** It is clear that \( C(n; k_0, u) \) is an \( EP(m, m - k_0; n, n - k_1; 1) \) code. By Corollary 2.2 the minimum distance of this code is

\[ d = \min\{(m - k_0 + 1)(n - k_1 + 2), (n - k_1 + 1)(m - k_0 + 2)\}. \] (30)

But the right hand side of (30) coincides with the right hand side of bound (27), showing that when \( g = 1 \), the bound is tight.

Notice that, in particular, if \( m - k_0 = n - k_1 = 1 \) (single parity horizontal and vertical codes), then (30) gives \( d = 6 \), as claimed in Example 2.3.

Let us examine now the case of \( EP(m, 1; n, 1; 2) \) codes, where \( m, n \geq 3 \). In this case, bound (27) gives

\[ d(m, 1; n, 1; 2) \leq 8. \] (31)
Consider for example a 2-level GPC code $C(n; m - 1, (1,1,\ldots,1), 3, 3)$ or a 2-level GPC code $C(n; m - 1, (1,1,\ldots,1), 2, 2, 2)$. These are the only cases of GPC codes that are $EP(m, 1; n, 1; 2)$ codes. In both cases, according to Corollary 2.2 the minimum distance is 6, so bound (31) is not met.

However, bound (31) is tight, and to show this we present an $EP(m, 1; n, 1; 2)$ code with minimum distance 8. The construction is related to the PMDS constructions in [3], and we pay the price of requiring a larger finite field than for GPC codes.

Let $GF(2^b)$ be a finite field and $\alpha$ an element in $GF(2^b)$ such that $mn \leq O(\alpha)$, where $O(\alpha)$ denotes the order of $\alpha$. Consider the parity-check matrix $H_2$ given by

$$H_2 = \left( \begin{array}{c} I_m \otimes (1,1,\ldots,1) \\ (1,1,\ldots,1) \otimes I_n \\ 1 \alpha \alpha^2 \ldots \alpha^{mn-1} \\ 1 \alpha^{-1} \alpha^{-2} \ldots \alpha^{-(mn-1)} \end{array} \right),$$

where $I_m$ denotes the $m \times m$ identity matrix and $\otimes$ the Kronecker product [15] of two matrices. Notice that

$$\left( \begin{array}{c} I_m \otimes (1,1,\ldots,1) \\ (1,1,\ldots,1) \otimes I_n \end{array} \right)$$

corresponds to the parity-check matrix of the product code with single parity in rows and columns. We denote the matrix in (32) as $H_2$ to indicate that two extra parities are added to the product code.

The following lemma gives the minimum distance of the code whose parity-check matrix is $H_2$.

**Lemma 3.3** Consider the $EP(m, 1; n, 1; 2)$ code whose parity-check matrix $H_2$ is given by (32), $m, n \geq 3$ and $mn \leq O(\alpha)$. Then, the code has minimum distance 8.

**Proof:** We have to prove that any 7 erasures can be corrected.

First assume that there are six erasures in locations $(i_0, j_0)$, $(i_0, j_1)$, $(i_0, j_2)$, $(i_1, j_0)$, $(i_1, j_1)$ and $(i_1, j_2)$, where $0 \leq i_0 < i_1 \leq m - 1$ and $0 \leq j_0 < j_1 < j_2 \leq n - 1$ or $(i_0, j_0)$, $(i_0, j_1)$, $(i_1, j_0)$, $(i_1, j_1)$, $(i_2, j_0)$ and $(i_2, j_1)$, where $0 \leq i_0 < i_1 < i_2 \leq m - 1$ and $0 \leq j_0 < j_1 \leq n - 1$, and a seventh erasure in any other location. This seventh erasure can be corrected using either horizontal or vertical parities, thus, it is enough to prove that the two situations of six erasures described above are correctable. For example, using $5 \times 5$ arrays, these two situations are illustrated below:
Consider the first case, as illustrated by the array in the left. It suffices to prove, using the parity-check matrix as given by (32), that the $6 \times 6$ matrix

$$
\begin{pmatrix}
1 & 1 & 1 & 0 & 0 & 0 \\
0 & 0 & 0 & 1 & 1 & 1 \\
0 & 1 & 0 & 0 & 1 & 0 \\
0 & 0 & 1 & 0 & 0 & 1 \\
\alpha^{i_0n+j_0} & \alpha^{i_0n+j_1} & \alpha^{i_0n+j_2} & \alpha^{i_1n+j_0} & \alpha^{i_1n+j_1} & \alpha^{i_1n+j_2} \\
\alpha^{-i_0n-j_0} & \alpha^{-i_0n-j_1} & \alpha^{-i_0n-j_2} & \alpha^{-i_1n-j_0} & \alpha^{-i_1n-j_1} & \alpha^{-i_1n-j_2}
\end{pmatrix}
$$

is invertible. Redefining $i \leftarrow i_1 - i_0$, $j_1 \leftarrow j_1 - j_0$ and $j_2 \leftarrow j_2 - j_0$, where now $1 \leq i \leq m - 1$ and $1 \leq j_1 < j_2 \leq n - 1$, this matrix is invertible if and only if matrix

$$
\begin{pmatrix}
1 & 1 & 1 & 0 & 0 & 0 \\
0 & 0 & 0 & 1 & 1 & 1 \\
0 & 1 & 0 & 0 & 1 & 0 \\
0 & 0 & 1 & 0 & 0 & 1 \\
1 & \alpha^{j_1} & \alpha^{j_2} & \alpha^{i_0} & \alpha^{i_0+j_1} & \alpha^{i_0+j_2} \\
1 & \alpha^{-j_1} & \alpha^{-j_2} & \alpha^{-i_0} & \alpha^{-i_0-j_1} & \alpha^{-i_0-j_2}
\end{pmatrix}
$$

is invertible. This matrix is invertible if and only if the $5 \times 5$ matrix

$$
\begin{pmatrix}
1 & 0 & 0 & 1 & 0 \\
1 \oplus \alpha^{j_1} & 1 \oplus \alpha^{j_2} & \alpha^{i_0} & \alpha^{i_0+j_1} & \alpha^{i_0+j_2} \\
1 \oplus \alpha^{-j_1} & 1 \oplus \alpha^{-j_2} & \alpha^{-i_0} & \alpha^{-i_0-j_1} & \alpha^{-i_0-j_2} \\
0 & 1 & 0 & 0 & 1 \\
0 & 0 & 1 & 1 & 1
\end{pmatrix}
$$

is invertible, if and only if the $4 \times 4$ matrix

$$
\begin{pmatrix}
1 & 0 & 0 & 1 \\
1 \oplus \alpha^{j_2} & \alpha^{i_0} & 1 \oplus \alpha^{j_1} \oplus \alpha^{i_0+j_1} & \alpha^{i_0+j_2} \\
1 \oplus \alpha^{-j_2} & \alpha^{-i_0} & 1 \oplus \alpha^{-j_1} \oplus \alpha^{-i_0-j_1} & \alpha^{-i_0-j_2} \\
0 & 1 & 1 & 1
\end{pmatrix}
$$

is invertible, if and only if the $3 \times 3$ matrix

$$
\begin{pmatrix}
1 & 1 & 1 \\
\alpha^{i_0} & 1 \oplus \alpha^{j_1} \oplus \alpha^{i_0+j_1} & \alpha^{i_0+j_2} \\
\alpha^{-i_0} & 1 \oplus \alpha^{-j_1} \oplus \alpha^{-i_0-j_1} & \alpha^{-i_0-j_2}
\end{pmatrix}
$$

is invertible, if and only if the $3 \times 3$ matrix

$$
\begin{pmatrix}
1 & 1 & 1 \\
\alpha^{i_0} & 1 \oplus \alpha^{j_1} \oplus \alpha^{i_0+j_1} & \alpha^{i_0+j_2} \\
\alpha^{-i_0} & 1 \oplus \alpha^{-j_1} \oplus \alpha^{-i_0-j_1} & \alpha^{-i_0-j_2}
\end{pmatrix}
$$

is invertible.
is invertible, if and only if the 2 × 2 matrix
\[
\begin{pmatrix}
(1 \oplus \alpha^i)(1 \oplus \alpha^m) & (1 \oplus \alpha^j)(1 \oplus \alpha^m) \\
(1 \oplus \alpha^{-i})(1 \oplus \alpha^{-m}) & (1 \oplus \alpha^{-j})(1 \oplus \alpha^{-m})
\end{pmatrix}
\]
is invertible, if and only if, since \(1 \oplus \alpha^m \neq 0\),
\[
\begin{pmatrix}
1 \oplus \alpha^i & 1 \oplus \alpha^j \\
1 \oplus \alpha^{-i} & 1 \oplus \alpha^{-j}
\end{pmatrix}
= \begin{pmatrix}
1 \oplus \alpha^i & 1 \oplus \alpha^j \\
\alpha^{-i}(1 \oplus \alpha^i) & \alpha^{-j}(1 \oplus \alpha^j)
\end{pmatrix}
\]
is invertible, if and only if, since \(1 \oplus \alpha^i \neq 0\) and \(1 \oplus \alpha^j \neq 0\),
\[
\begin{pmatrix}
1 & 1 \\
\alpha^{-i} & \alpha^{-j}
\end{pmatrix}
\]
is invertible, if and only if \(\alpha^i \neq \alpha^j\), which is the case since \(1 \leq j_1 < j_2 < n - 1 < O(\alpha)\).

Consider now the second case, then, again by (32), we have to prove that the 6 × 6 matrix
\[
\begin{pmatrix}
1 & 1 & 0 & 0 & 0 & 0 \\
0 & 0 & 1 & 1 & 0 & 0 \\
0 & 0 & 0 & 0 & 1 & 1 \\
0 & 1 & 0 & 1 & 0 & 1 \\
\alpha^{i_0 n + j_0} & \alpha^{i_0 n + j_1} & \alpha^{i_1 n + j_0} & \alpha^{i_1 n + j_1} & \alpha^{i_2 n + j_0} & \alpha^{i_2 n + j_1} \\
\alpha^{-i_0 n - j_0} & \alpha^{-i_0 n - j_1} & \alpha^{-i_1 n - j_0} & \alpha^{-i_1 n - j_1} & \alpha^{-i_2 n - j_0} & \alpha^{-i_2 n - j_1}
\end{pmatrix}
\]
is invertible.

Redefining \(i_1 \leftarrow i_1 - i_0, i_2 \leftarrow i_2 - i_0\) and \(j \leftarrow j_1 - j_0\), where \(1 \leq i_1 < i_2 \leq m - 1\) and \(1 \leq j \leq n - 1\), the matrix above is invertible if and only if the matrix
\[
\begin{pmatrix}
1 & 1 & 0 & 0 & 0 & 0 \\
1 & \alpha^j & \alpha^{i_1 n} & \alpha^{i_1 n + j} & \alpha^{i_2 n} & \alpha^{i_2 n + j} \\
1 & \alpha^{-j} & \alpha^{-i_1 n} & \alpha^{-i_1 n - j} & \alpha^{-i_2 n} & \alpha^{-i_2 n - j} \\
0 & 0 & 1 & 1 & 0 & 0 \\
0 & 0 & 0 & 0 & 1 & 1 \\
0 & 1 & 0 & 1 & 0 & 1
\end{pmatrix}
\]
is invertible. Proceeding with Gaussian elimination like in the previous case, this matrix is invertible if and only if the 2 matrix
\[
\begin{pmatrix}
(1 \oplus \alpha^j)(1 \oplus \alpha^{i_1 n}) & (1 \oplus \alpha^j)(1 \oplus \alpha^{i_2 n}) \\
\alpha^{-j-i_1 n}(1 \oplus \alpha^j)(1 \oplus \alpha^{i_1 n}) & \alpha^{-j-i_2 n}(1 \oplus \alpha^j)(1 \oplus \alpha^{i_2 n})
\end{pmatrix}
\]
is invertible, if and only if \(\alpha^{i_1 n} \neq \alpha^{i_2 n}\), which is the case since
\[1 \leq i_1 n < i_2 n \leq (m - 1)n < O(\alpha).
\]
Next, assume that there are seven erasures, such that each row and column has at least two erasures. This can only happen if one row (column) has three erasures and two rows (columns) have two erasures. The situation is illustrated by the two cases below:

Let \( i_0 \) be the row with three erasures, and \( j_0 \) the column with three erasures, while \( j_1 < j_2 \) and \( i_1 \) is such that erasures are in \((i_1, j_0)\) and \((i_1, j_1)\) so the remaining two erasures are in \((i_2, j_0)\) and \((i_2, j_2)\). It suffices to prove, using the parity-check matrix as given by (32), that the 7 \( \times \) 7 matrix

\[
\begin{pmatrix}
1 & 1 & 1 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 1 & 1 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 1 & 1 \\
0 & 1 & 0 & 0 & 1 & 0 & 0 \\
0 & 1 & 0 & 0 & 0 & 0 & 1 \\
\alpha^{-i_0-j_0} & \alpha^{i_0-j_1} & \alpha^{i_0-j_2} & \alpha^{-i_1-j_0} & \alpha^{-i_1-j_1} & \alpha^{-i_1-j_2} & \alpha^{-i_2-j_0} & \alpha^{-i_2-j_1} & \alpha^{-i_2-j_2}
\end{pmatrix}
\]

is invertible.

Redefining \( i_1 \leftarrow i_1 - i_0, i_2 \leftarrow i_2 - i_0, j_1 \leftarrow j_1 - j_0 \) and \( j_2 \leftarrow j_2 - j_0 \), the matrix above is invertible if and only if the matrix

\[
\begin{pmatrix}
1 & 1 & 1 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 1 & 1 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 1 & 1 \\
0 & 1 & 0 & 0 & 1 & 0 & 0 \\
0 & 1 & 0 & 0 & 0 & 0 & 1 \\
\alpha^{j_1} & \alpha^{j_2} & \alpha^{i_1} & \alpha^{i_1+j_1} & \alpha^{i_2} & \alpha^{i_2+j_2} \\
1 & \alpha^{-j_1} & \alpha^{-j_2} & \alpha^{-i_1} & \alpha^{-i_1-j_1} & \alpha^{-i_2} & \alpha^{-i_2-j_2}
\end{pmatrix}
\]

is invertible, if and only if, doing Gaussian elimination like in the other two cases, the 2 \( \times \) 2 matrix

\[
\begin{pmatrix}
(1 \oplus \alpha^{j_1})(1 \oplus \alpha^{i_1}) & (1 \oplus \alpha^{i_2}) \\
(1 \oplus \alpha^{-j_1})(1 \oplus \alpha^{-i_1}) & (1 \oplus \alpha^{-i_2})
\end{pmatrix}
\]

is invertible, if and only if, since \( 1 \oplus \alpha^{j_1}, 1 \oplus \alpha^{i_1}, 1 \oplus \alpha^{j_2} \) and \( 1 \oplus \alpha^{i_2} \) are non-zero,
is invertible, if and only if, computing the determinant,

$$\alpha^{i_1 n + j_1} \neq \alpha^{i_2 n + j_2}$$

which is the case since \( mn \leq O(\alpha) \), thus

$$(i_2 - i_1)n + j_2 - j_1 \not\equiv 0 \pmod{O(\alpha)}.$$  

\[\square\]

Lemma 3.3 shows that the code given by parity-check matrix \( H_2 \) meets bound (31) with equality, something that could not be achieved by GPC codes with two global parities. Consider the 3-level GPC code \( C(n; m - 1, u) \), where

$$u = (1, 1, \ldots, 1), 2, 3, 3)$$

This is an \( EP(n, 1; m, 1; 3) \) code. According to Corollary 2.2 \( C(n; m - 1, u) \) has minimum distance 8, the same as the code given by parity-check matrix \( H_2 \), at the price of an extra parity. However, there is a tradeoff: the size of the field required by \( C(n; m - 1, u) \) is greater than \( \text{max}\{m; n\} \), while the field required by the code whose parity-check matrix is \( H_2 \) must have size greater than \( mn \). Also, by Theorem 2.1 \( C(n; m - 1, u) \) can correct 8 erasures involving two rows with 3 erasures and one row with two erasures, like for example

\[
\begin{array}{ccc}
E & E & E \\
E & E & \\
E & E & E \\
\end{array}
\]

The code generated by \( H_2 \) is unable to correct such pattern since it does not have enough parities, so even if both codes have the same minimum distance, \( C(n; m - 1, u) \) can correct more erasure patterns. These tradeoffs need to be evaluated when implementation is considered.

Let us finish this section with the case of \( EP(n, 1; m, 1; 3) \) codes. Bound (27) gives

$$d(m, 1; n, 1; 3) \leq 9.$$  \hspace{1cm} (33)

The next question is if bound (33) is tight. The answer is yes.

As in the case of two global parities, let \( GF(2^h) \) be a finite field and let \( \alpha \) be an element in \( GF(2^h) \) such that \( mn \leq O(\alpha) \). Consider the parity-check matrix \( H_3 \) given by
\[ H_3 = \begin{pmatrix}
I_m \otimes (1,1,\ldots,1) \\
\frac{(1,1,\ldots,1) \otimes I_n}{1 \alpha \alpha^2 \ldots \alpha^{mn-1}} \\
1 \alpha^{-1} \alpha^{-2} \ldots \alpha^{-(mn-1)} \\
1 \alpha^2 \alpha^4 \ldots \alpha^{2(mn-1)}
\end{pmatrix}. \tag{34}\]

Notice that \( H_2 \) as given by (32) consists of the first \( m + n + 2 \) rows of \( H_3 \). The following lemma gives the minimum distance of these codes under a certain condition.

**Lemma 3.4** The \( EP(m,1;n,1;3) \) code, where \( m, n \geq 3 \) and \( mn \leq O(\alpha) \) whose parity-check matrix \( H_3 \) is given by (34), has minimum distance 9 if and only if, for any \( i_1, i_2 \neq 0, 1 \leq i_1 \leq m - 1, 1 \leq |i_2| < m - 1 \) and \( j_1, j_2 \neq 0, 1 \leq j_1 \leq n - 1, 1 \leq |j_2| < n - 1 \),

\[ 1 \oplus \alpha^{-j_1} \oplus \alpha^{-i_2+j_2} \oplus \alpha^{-(i_2-i_1)n+j_2} \neq 0. \tag{35}\]

**Proof:** We have to prove that any 8 erasures are going to be corrected under condition (35).

Assume that there are 8 erasures, such that each row and column has at least two erasures. There are three situations under which this can happen:

1. Two rows have four erasures and four columns have two erasures.
2. Four rows have two erasures and two columns have four erasures.
3. Two rows (columns) have three erasures and one row (column) has two erasures.

The situation is illustrated by the four cases below. The first array illustrates case 1, the second array illustrates case 2, and the third and fourth arrays illustrate case 3.

Consider case 1 and assume that the erasures occurred in locations \((i_0, j_0), (i_0, j_1), (i_0, j_2), (i_0, j_3), (i_1, j_0), (i_1, j_1), (i_1, j_2)\) and \((i_1, j_3)\), where \(0 \leq i_0 < i_1 \leq m - 1\) and \(0 \leq j_0 < j_1 < j_2 < j_3 \leq n - 1\). We need to prove, using the parity-check matrix as given by (34), that the \( 8 \times 8 \) matrix
is invertible. Redefining \( i \leftarrow i_1 - i_0 \), \( j_1 \leftarrow j_1 - j_0 \), \( j_2 \leftarrow j_2 - j_0 \) and \( j_3 \leftarrow j_3 - j_0 \), where now \( 1 \leq i \leq m - 1 \) and \( 1 \leq j_1 < j_2 < j_3 \leq n - 1 \), this matrix is invertible if and only if matrix

\[
\begin{pmatrix}
1 & 1 & 1 & 1 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 1 & 1 & 1 & 1 \\
0 & 1 & 0 & 0 & 0 & 1 & 0 & 0 \\
0 & 0 & 1 & 0 & 0 & 0 & 1 & 0 \\
0 & 0 & 0 & 0 & 1 & 0 & 0 & 1 \\
1 & a^{i_1} & a^{i_2} & a^{i_3} & a^{i_{j_1}} & a^{i_{j_2}} & a^{i_{j_3}} & a^{i_{j_4}} \\
1 & a^{-i_1} & a^{-i_2} & a^{-i_3} & a^{-i_{j_1}} & a^{-i_{j_2}} & a^{-i_{j_3}} & a^{-i_{j_4}} \\
1 & a^{2i_1} & a^{2i_2} & a^{2i_3} & a^{2i_{j_1}} & a^{2i_{j_2}} & a^{2i_{j_3}} & a^{2i_{j_4}} \\
1 & a^{3i_1} & a^{3i_2} & a^{3i_3} & a^{3i_{j_1}} & a^{3i_{j_2}} & a^{3i_{j_3}} & a^{3i_{j_4}} \\
0 & a^{j_1} & a^{j_2} & a^{j_3} & a^{j_{j_1}} & a^{j_{j_2}} & a^{j_{j_3}} & a^{j_{j_4}} \\
0 & a^{-j_1} & a^{-j_2} & a^{-j_3} & a^{-j_{j_1}} & a^{-j_{j_2}} & a^{-j_{j_3}} & a^{-j_{j_4}} \\
0 & a^{2j_1} & a^{2j_2} & a^{2j_3} & a^{2j_{j_1}} & a^{2j_{j_2}} & a^{2j_{j_3}} & a^{2j_{j_4}} \\
0 & a^{3j_1} & a^{3j_2} & a^{3j_3} & a^{3j_{j_1}} & a^{3j_{j_2}} & a^{3j_{j_3}} & a^{3j_{j_4}} \\
\end{pmatrix}
\]

is invertible. This matrix is invertible if and only if the 7 × 7 matrix

\[
\begin{pmatrix}
1 & 0 & 0 & 0 & 0 & 1 & 0 \\
1 \oplus a^{j_1} & 1 \oplus a^{j_2} & 1 \oplus a^{j_3} & a^{in} & a^{in+j_1} & a^{in+j_2} & a^{in+j_3} \\
1 \oplus a^{-j_1} & 1 \oplus a^{-j_2} & 1 \oplus a^{-j_3} & a^{-in} & a^{-in-j_1} & a^{-in-j_2} & a^{-in-j_3} \\
1 \oplus a^{2j_1} & 1 \oplus a^{2j_2} & 1 \oplus a^{2j_3} & a^{2in} & a^{2in+j_1} & a^{2in+j_2} & a^{2in+j_3} \\
0 & 1 & 0 & 0 & 0 & 1 & 0 \\
0 & 0 & 1 & 0 & 0 & 0 & 1 \\
0 & 0 & 0 & 0 & 1 & 1 & 1 \\
\end{pmatrix}
\]

is invertible, if and only if the 6 × 6 matrix

\[
\begin{pmatrix}
1 & 0 & 0 & 0 & 1 & 0 \\
1 \oplus a^{j_1} & 1 \oplus a^{j_2} & 1 \oplus a^{j_3} & a^{in} & a^{in+j_1} & a^{in+j_2} \\
1 \oplus a^{-j_1} & 1 \oplus a^{-j_2} & 1 \oplus a^{-j_3} & a^{-in} & a^{-in-j_1} & a^{-in-j_2} \\
1 \oplus a^{2j_1} & 1 \oplus a^{2j_2} & 1 \oplus a^{2j_3} & a^{2in} & a^{2in+j_1} & a^{2in+j_2} \\
0 & 1 & 0 & 0 & 0 & 1 \\
0 & 0 & 1 & 0 & 0 & 1 \\
\end{pmatrix}
\]

is invertible, if and only if the 5 × 5 matrix

\[
\begin{pmatrix}
1 & 0 & 0 & 0 & 0 \\
1 \oplus a^{j_2} & 1 \oplus a^{j_3} & a^{in} & a^{in+j_1} & a^{in+j_2} \\
1 \oplus a^{-j_2} & 1 \oplus a^{-j_3} & a^{-in} & a^{-in-j_1} & a^{-in-j_2} \\
1 \oplus a^{2j_2} & 1 \oplus a^{2j_3} & a^{2in} & a^{2in+j_1} & a^{2in+j_2} \\
0 & 1 & 0 & 0 & 1 \\
0 & 0 & 1 & 0 & 1 \\
\end{pmatrix}
\]
is invertible, if and only if the $4 \times 4$ matrix

$$
\begin{pmatrix}
1 & 0 & 0 & 1
\alpha^2 & \alpha & 0 & 1
\alpha^{-1} & \alpha & 0 & 1
\alpha^{-2} & \alpha & 0 & 1
\end{pmatrix}
$$

is invertible, if and only if the $3 \times 3$ matrix

$$
\begin{pmatrix}
1 & 1 & 1
\alpha & \alpha^2 & \alpha^{-1}
\alpha^{-2} & \alpha^2 & \alpha^{-1}
\end{pmatrix}
$$

is invertible, if and only if the $3 \times 3$ matrix

$$
\begin{pmatrix}
1 & 1 & 1
\alpha & \alpha^2 & \alpha^{-1}
\alpha^{-2} & \alpha^2 & \alpha^{-1}
\end{pmatrix}
$$

is invertible, if and only if the $2 \times 2$ matrix

$$
\begin{pmatrix}
\alpha^{-1} & \alpha^{-j_2}
\alpha^{-j_2} & \alpha^{-j_3}
\end{pmatrix}
$$

is invertible, if and only if the $2 \times 2$ matrix

$$
\begin{pmatrix}
1 & 1
\alpha^{-j_1-j_2} & \alpha^{-j_1-j_3}
\end{pmatrix}
$$

is invertible, if and only if, computing the determinant of the matrix above,

$$
\alpha^{-j_1}(\alpha^{-j_2} + \alpha^{-j_3}) \neq 0,
$$

which is true since $1 \leq j_2 < j_3 \leq n-1 < O(\alpha)$. So all these cases of 8 erasures are correctable.

Consider next case 2 and assume that the erasures occurred in locations $(i_0, j_0), (i_0, j_1), (i_1, j_0), (i_1, j_1), (i_2, j_0), (i_2, j_1), (i_3, j_0)$ and $(i_3, j_1)$, where $0 \leq i_0 < i_1 < i_2 < i_3 \leq m-1$ and $0 \leq j_0 < j_1 \leq n-1$. We need to prove, using the parity-check matrix as given by (34), that the $8 \times 8$ matrix
is invertible. Redefining \(i_1 \leftarrow i_1 - i_0, i_2 \leftarrow i_2 - i_0, i_3 \leftarrow i_3 - i_0\) and \(j \leftarrow j_1 - j_0\), where now \(1 \leq i_1 < i_2 < i_3 \leq m - 1\) and \(1 \leq j \leq n - 1\), this matrix is invertible if and only if matrix

\[
\begin{pmatrix}
1 & 1 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & 1 & 1 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 1 & 1 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 1 & 1 \\
0 & 1 & 0 & 1 & 0 & 1 & 0 & 1 \\
1 & \alpha^j & \alpha^{i_1n} & \alpha^{i_1n+j} & \alpha^{i_2n} & \alpha^{i_2n+j} & \alpha^{i_3n} & \alpha^{i_3n+j} \\
1 & \alpha^{-j} & \alpha^{-i_1n} & \alpha^{-i_1n-j} & \alpha^{-i_2n} & \alpha^{-i_2n-j} & \alpha^{-i_3n} & \alpha^{-i_3n-j} \\
1 & \alpha^{2j} & \alpha^{2i_1n} & \alpha^{2i_1n+j} & \alpha^{2i_2n} & \alpha^{2i_2n+j} & \alpha^{2i_3n} & \alpha^{2i_3n+j}
\end{pmatrix}
\]

is invertible. This matrix is invertible if and only if the \(4 \times 4\) matrix

\[
\begin{pmatrix}
1 & 1 & 0 & 0 & 0 & 0 & 0 & 0 \\
1 & \alpha^j & \alpha^{i_1n} & \alpha^{i_1n+j} & \alpha^{i_2n} & \alpha^{i_2n+j} & \alpha^{i_3n} & \alpha^{i_3n+j} \\
1 & \alpha^{-j} & \alpha^{-i_1n} & \alpha^{-i_1n-j} & \alpha^{-i_2n} & \alpha^{-i_2n-j} & \alpha^{-i_3n} & \alpha^{-i_3n-j} \\
1 & \alpha^{2j} & \alpha^{2i_1n} & \alpha^{2i_1n+j} & \alpha^{2i_2n} & \alpha^{2i_2n+j} & \alpha^{2i_3n} & \alpha^{2i_3n+j}
\end{pmatrix}
\]

is invertible, if and only if the \(3 \times 3\) matrix

\[
\begin{pmatrix}
(1 \oplus \alpha^{i_1n})(1 \oplus \alpha^j) & (1 \oplus \alpha^{i_2n})(1 \oplus \alpha^j) & (1 \oplus \alpha^{i_3n})(1 \oplus \alpha^j) \\
(1 \oplus \alpha^{-i_1n})(1 \oplus \alpha^{-j}) & (1 \oplus \alpha^{-i_2n})(1 \oplus \alpha^{-j}) & (1 \oplus \alpha^{-i_3n})(1 \oplus \alpha^{-j}) \\
(1 \oplus \alpha^{2i_1n})(1 \oplus \alpha^{2j}) & (1 \oplus \alpha^{2i_2n})(1 \oplus \alpha^{2j}) & (1 \oplus \alpha^{2i_3n})(1 \oplus \alpha^{2j})
\end{pmatrix}
\]

is invertible, if and only if the \(3 \times 3\) matrix

\[
\begin{pmatrix}
1 & 1 & 1 \\
\alpha^{-i_1n} & \alpha^{-i_2n} & \alpha^{-i_3n} \\
1 \oplus \alpha^{i_1n} & 1 \oplus \alpha^{i_2n} & 1 \oplus \alpha^{i_3n}
\end{pmatrix}
\]

is invertible, if and only if the \(2 \times 2\) matrix

\[
\begin{pmatrix}
\alpha^{i_1n} \oplus \alpha^{i_2n} & \alpha^{i_1n} \oplus \alpha^{i_3n} \\
\alpha^{-i_1n} \oplus \alpha^{-i_2n} & \alpha^{-i_1n} \oplus \alpha^{-i_3n}
\end{pmatrix}
= (\alpha^{i_1n} \oplus \alpha^{i_2n})(\alpha^{i_1n} \oplus \alpha^{i_3n})
\begin{pmatrix}
1 & 1 \\
\alpha^{-(-i_1-i_2)n} & \alpha^{(-i_1-i_2)n}
\end{pmatrix}
\]

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is invertible, if and only if, computing the determinant of this last matrix,
\[ \alpha^{(-i_1-i_2)n} \oplus \alpha^{(-i_1-i_2)n} = \alpha^{-i_1n}(\alpha^{-i_2n} \oplus \alpha^{-i_2n}) \neq 0, \]
which is certainly the case since \(1 \leq i_2n < i_3n < mn < \mathcal{O}(\alpha).\)

Consider finally case 3. Let \(i_0 < i_1\) and \(j_0 < j_1\) be the rows and columns respectively with three erasures. It suffices to prove, using the parity-check matrix \(H_3\) given by (57), that the \(8 \times 8\) matrix

\[
\begin{pmatrix}
1 & 1 & 1 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 1 & 1 & 1 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 1 & 1 \\
0 & 1 & 0 & 0 & 1 & 0 & 0 & 1 \\
0 & 0 & 1 & 0 & 0 & 1 & 0 & 0 \\
\alpha^{i_0n+j_0} & \alpha^{2(i_0n+j_0)} & \alpha^{i_1n+j_1} & \alpha^{2(i_1n+j_1)} & \alpha^{i_2n+j_2} & \alpha^{2(i_2n+j_2)} & \alpha^{i_3n+j_3} & \alpha^{2(i_3n+j_3)}
\end{pmatrix}
\]

is invertible.

Redefining \(i_1 \leftarrow i_1 - i_0, i_2 \leftarrow i_2 - i_0, j_1 \leftarrow j_1 - j_0\) and \(j_2 \leftarrow j_2 - j_0\), the matrix above is invertible if and only if the matrix

\[
\begin{pmatrix}
1 & 1 & 1 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 1 & 1 & 1 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 1 & 1 \\
0 & 1 & 0 & 0 & 1 & 0 & 0 & 1 \\
0 & 0 & 1 & 0 & 0 & 1 & 0 & 0 \\
1 & \alpha^2 & \alpha^2 & \alpha^3 & \alpha^2 & \alpha^2 & \alpha^2 & \alpha^2
\end{pmatrix}
\]

is invertible, if and only if the matrix

\[
\begin{pmatrix}
1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
1 \oplus \alpha^2 & 1 \oplus \alpha^2 & \alpha^3 & \alpha^3 & \alpha^3 & \alpha^3 & \alpha^3 & \alpha^3 \\
1 \oplus \alpha^2 & 1 \oplus \alpha^2 & \alpha^3 & \alpha^3 & \alpha^3 & \alpha^3 & \alpha^3 & \alpha^3 \\
1 \oplus \alpha^2 & 1 \oplus \alpha^2 & \alpha^3 & \alpha^3 & \alpha^3 & \alpha^3 & \alpha^3 & \alpha^3 \\
0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 0 & 0
\end{pmatrix}
\]

is invertible, if and only if the matrix

\[
\begin{pmatrix}
1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
1 \oplus \alpha^2 & 1 \oplus \alpha^2 & \alpha^3 & \alpha^3 & \alpha^3 & \alpha^3 & \alpha^3 & \alpha^3 \\
1 \oplus \alpha^2 & 1 \oplus \alpha^2 & \alpha^3 & \alpha^3 & \alpha^3 & \alpha^3 & \alpha^3 & \alpha^3 \\
1 \oplus \alpha^2 & 1 \oplus \alpha^2 & \alpha^3 & \alpha^3 & \alpha^3 & \alpha^3 & \alpha^3 & \alpha^3 \\
0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 0 & 0
\end{pmatrix}
\]
is invertible, if and only if the matrix

\[
\begin{pmatrix}
1 & 0 & 0 & 1 & 1 \\
1 \oplus \alpha^{j_2} & \alpha^{i_2n} & 1 \oplus \alpha^{j_1} & \alpha^{i_2n+j_1} & 1 \\
1 \oplus \alpha^{-j_2} & \alpha^{-i_2n} & 1 \oplus \alpha^{-j_1} & \alpha^{-i_2n-j_1} & 1 \\
1 \oplus \alpha^{2j_2} & \alpha^{2i_2n} & 1 \oplus \alpha^{2j_1} & \alpha^{2(i_2n+j_1)} & 1 \\
0 & 1 & 1 & 1 & 1
\end{pmatrix}
\]

is invertible, if and only if the matrix

\[
\begin{pmatrix}
1 & 0 & 0 & 1 & 1 \\
1 \oplus \alpha^{i_1n} & 1 \oplus \alpha^{j_1} & 1 \oplus \alpha^{j_2} & 1 & 1 \\
1 \oplus \alpha^{-i_1n} & 1 \oplus \alpha^{-j_1} & 1 \oplus \alpha^{-j_2} & 1 & 1 \\
1 \oplus \alpha^{2i_1n} & 1 \oplus \alpha^{2j_1} & 1 \oplus \alpha^{2j_2} & 1 & 1 \\
0 & 0 & 0 & 1 & 1
\end{pmatrix}
\]

is invertible, if and only if the matrix

\[
\begin{pmatrix}
(1 \oplus \alpha^{i_1n})(1 \oplus \alpha^{j_1}) & (1 \oplus \alpha^{i_1n})(1 \oplus \alpha^{j_2}) & (1 \oplus \alpha^{i_2n})(1 \oplus \alpha^{j_1}) & (1 \oplus \alpha^{i_2n})(1 \oplus \alpha^{j_2}) \\
(1 \oplus \alpha^{-i_1n})(1 \oplus \alpha^{-j_1}) & (1 \oplus \alpha^{-i_1n})(1 \oplus \alpha^{-j_2}) & (1 \oplus \alpha^{-i_2n})(1 \oplus \alpha^{-j_1}) & (1 \oplus \alpha^{-i_2n})(1 \oplus \alpha^{-j_2}) \\
(1 \oplus \alpha^{2i_1n})(1 \oplus \alpha^{2j_1}) & (1 \oplus \alpha^{2i_1n})(1 \oplus \alpha^{2j_2}) & (1 \oplus \alpha^{2i_2n})(1 \oplus \alpha^{2j_1}) & (1 \oplus \alpha^{2i_2n})(1 \oplus \alpha^{2j_2}) \\
0 & 0 & 1 & 1
\end{pmatrix}
\]

is invertible, if and only if the matrix

\[
\begin{pmatrix}
1 & 1 & 1 & 1 \\
1 \oplus \alpha^{-i_1n-j_1} & 1 \oplus \alpha^{-i_1n-j_2} & 1 \oplus \alpha^{-i_2n-j_1} & 1 \oplus \alpha^{-i_2n-j_2} \\
1 \oplus \alpha^{i_1n-j_1} & 1 \oplus \alpha^{i_1n-j_2} & 1 \oplus \alpha^{i_2n-j_1} & 1 \oplus \alpha^{i_2n-j_2} \\
0 & 0 & 1 & 1
\end{pmatrix}
\]

is invertible, if and only if the matrix

\[
\begin{pmatrix}
\alpha^{-i_1n-j_1}(\alpha^{-j_1} \oplus \alpha^{-j_2}) & \alpha^{-j_1}(\alpha^{-i_1n} \oplus \alpha^{-i_2n}) \\
(1 \oplus \alpha^{i_1n})(\alpha^{j_1} \oplus \alpha^{j_2}) & (1 \oplus \alpha^{j_1})(\alpha^{i_1n} \oplus \alpha^{i_2n})
\end{pmatrix}
\]

is invertible, if and only if the matrix

\[
\begin{pmatrix}
\alpha^{-i_1n-2j_1-j_2} & \alpha^{-i_1n+2j_1n-j_1} \\
1 \oplus \alpha^{i_1n} & 1 \oplus \alpha^{j_1}
\end{pmatrix}
\]

is invertible, if and only if the matrix

\[
\begin{pmatrix}
\alpha^{-i_1n-2j_1-j_2} & \alpha^{-i_1n+2j_1n-j_1} \\
1 \oplus \alpha^{i_1n} & 1 \oplus \alpha^{j_1}
\end{pmatrix}
\]

is invertible, if and only if the matrix

\[
\begin{pmatrix}
\alpha^{-i_1n-2j_1-j_2} & \alpha^{-i_1n+2j_1n-j_1} \\
1 \oplus \alpha^{i_1n} & 1 \oplus \alpha^{j_1}
\end{pmatrix}
\]
which is invertible if and only if its determinant, which is given by the left hand side of (35) times a constant, is non-zero.

Notice that in Lemma 3.4, 8 erasures following the patterns of cases 1 and 2 will always be corrected, while case 3 will be corrected only when condition (35) is satisfied. So Lemma 3.4 by itself does not prove that there is an EP($m, 1; n, 1$) code with minimum distance 9, but we can find a code satisfying (35) using an argument similar to the one used to show an infinite family of PMDS codes in [11]. In effect, consider the field $GF(2^p)$, $p$ a prime number, such that $GF(2^p)$ is generated by the irreducible polynomial $M_p(x) = 1 + x + x^2 + \cdots + x^{p-1}$. The polynomial $M_p(x)$ may not be irreducible, for example, $M_5(x)$ is irreducible but $M_7(x) = (1 + x + x^3)(1 + x^2 + x^3)$, so not any prime number can be chosen. If we choose a prime number large enough, condition (35) will hold, as shown in the next corollary:

**Corollary 3.1** Consider the EP($m, 1; n, 1$) code whose parity-check matrix is given by (34) with $\alpha$ in (34) a zero of $M_p(x)$, $p$ a prime number, $M_p(x)$ irreducible and $mn < p$. Then the code has minimum distance 9.

**Proof:** We have to show that (35) is satisfied. Given an integer $z$, denote by $\langle z \rangle_p$ the unique integer $u$, $0 \leq u \leq p-1$, such that $u \equiv z \pmod{p}$. Let $M_p(\alpha) = 0$, then $O(\alpha) = p$. Hence,

$$1 \oplus \alpha^{-j_1} \oplus \alpha^{-i_{2n}+j_2} \oplus \alpha^{-(i_{2n}+j_2)+j_2} = 1 \oplus \alpha^{\langle -j_1 \rangle_p} \oplus \alpha^{\langle -i_{2n}+j_2 \rangle_p} \oplus \alpha^{\langle -(i_{2n}+j_2) \rangle_p}.$$ 

Take the first three elements in (35), i.e.,

$$1 \oplus \alpha^{-j_1} \oplus \alpha^{-i_{2n}+j_2} = 1 \oplus \alpha^{\langle -j_1 \rangle_p} \oplus \alpha^{\langle -i_{2n}+j_2 \rangle_p}.$$ 

Since $1 \leq j_1 \leq n - 1$, $1 \leq |j_2| \leq n - 1$, $1 \leq |i_2| \leq m - 1$ and $mn < p$, $\langle -j_1 \rangle_p = p - j_1 \neq 0$.

Assume that $\langle -i_{2n}+j_2 \rangle_p = 0$, then $-i_{2n}+j_2 = sp$ for some integer $s$. If $s = 0$, then $i_{2n} = j_2$, a contradiction since $1 \leq |j_2| \leq n - 1$ and $1 \leq |i_2| \leq m - 1$. So $s \neq 0$ and $j_2 = sp + i_{2n}$. If $s \geq 1$, since $1 \leq |i_2| \leq m - 1$ and $mn < p$, $j_2 = sp + i_{2n} \geq mn - (m-1)n = n$, a contradiction since $1 \leq |j_2| \leq n - 1$. If $s < 0$, $j_2 = sp + i_{2n} \leq -mn + (m-1)n = -n$, also a contradiction.

If $\langle -j_1 \rangle_p \neq \langle -i_{2n}+j_2 \rangle_p$, then the first three elements in (35) are distinct from each other, so they cannot be canceled by the 4th element and (35) holds. So, assume that $\langle -j_1 \rangle_p = \langle -i_{2n}+j_2 \rangle_p$. In particular,

$$-i_{2n} + j_2 = -j_1 + sp$$ for some integer $s$. (36)
Now, in order for the left hand side of (35) to be zero, in addition to (36), we need
\[ \langle - (i_2 - i_1)n + j_2 \rangle_p = 0, \]
giving
\[ - i_2n + j_2 = -i_1n + s'p \]
for some integer \( s' \).

Combining (36) and (37), we obtain

\[ i_1n - j_1 = s''p \]
for some integer \( s'' \).

Since \( 1 \leq i_1 \leq m - 1 \) and \( 1 \leq j_1 \leq n - 1 \),

\[ 1 \leq i_1n - j_1 < mn < p, \]
contradicting (38) and completing the proof.

\[ \square \]

Corollary 3.1 shows that bound (33) is indeed tight.

The construction in Corollary 3.1 depends on, for each \( m \times n \) array, finding a prime \( p \) such that \( mn < p \) and \( M_p(x) \) is irreducible (it is well known that \( M_p(x) \) is irreducible if and only if 2 is primitive in \( GF(p) \) \[15\]). Strictly speaking, it is not proven that the number of such primes is infinite, but it is believed it is, and from a practical point of view, it is always possible to find such a large enough prime number.

Let us point out that although the field of polynomials modulo \( M_p(x) \) has size \( 2^p \), no look-up tables are necessary in implementation, since most operations reduce to XORs and rotations \[4\]. We omit the details here.

4 Conclusions

We have studied extended product (EPC) codes, in which a few global parities are added to a traditional product code in order to enhance its distance properties. We presented a special case of extended product codes: generalized product (GPC) codes. We showed that GPC codes unify two types of codes: product codes and integrated interleaved (II) codes. We studied the distance properties of these type of codes. Although, except for the special case of one global parity, GPC codes do not optimize the minimum distance, they can be implemented with modest field size, and in addition they provide a large variety of possible parameters, making them an attractive alternative for implementation in practical cases. We showed some optimal constructions for two and three global parities requiring a larger field size.
References

[1] M. Blaum, J. L. Hafner and S. Hetzler, “Partial-MDS Codes and their Application to RAID Type of Architectures,” IEEE Trans. on Information Theory, vol. IT-59, pp. 4510-19, July 2013.

[2] M. Blaum and S. Hetzler, “Integrated Interleaved Codes as Locally Recoverable Codes,” Int. J. Information and Coding Theory, Vol. 3, No. 4, pp. 324–344, September 2016.

[3] M. Blaum, J. S. Plank, M. Schwartz and E. Yaakobi, “Partial MDS (PMDS) and Sector-Disk (SD) Codes that Tolerate the Erasure of Two Random Sectors,” IEEE Trans. on Information Theory, vol. IT-62, pp. 2673–81, May 2016.

[4] M. Blaum and R. M. Roth, “New Array Codes for Multiple Phased Burst Correction, IEEE Trans. on Information Theory, vol. IT-39,” pp. 66-77, January 1993.

[5] E. L. Blokh and V. V. Zyablov, “Coding of Generalized Concatenated Codes,” Problemy Peredachii Informatsii, Vol. 10(3), pp. 218–222, 1974.

[6] G. A. Gibson, “Redundant Disk Arrays,” MIT Press, 1992.

[7] P. Gopalan, C. Huang, B. Jenkins and S. Yekhanin, “Explicit Maximally Recoverable Codes with Locality,” IEEE Trans. on Information Theory, vol. IT-60, pp. 5245–56, September 2014.

[8] P. Gopalan, C. Huang, H. Simitci and S. Yekhanin, “On the Locality of Codeword Symbols,” IEEE Trans. on Information Theory, vol. IT-58, pp. 6925–34, November 2012.

[9] P. Gopalan, G. Hu, S. Saraf, C. Wang and S. Yekhanin, “Maximally Recoverable Codes for Grid-like Topologies,” arXiv 1605.05412v1, May 2016.

[10] M. Hassner, K. Abdel-Ghaffar, A. Patel, R. Koetter and B. Trager, “Integrated Interleaving – A Novel ECC Architecture,” IEEE Transactions on Magnetics, Vol. 37, No. 2, pp. 773–5, March 2001.

[11] C. Huang, M. Chen, and J. Li, “Pyramid Codes: Flexible Schemes to Trade Space for Access Efficiency in Reliable Data Storage Systems,” Proc. of IEEE NCA, Cambridge, Massachussetts, July 2007.

[12] C. Huang, H. Simitci, Y. Xu, A. Ogus, B. Calder, P. Gopalan, J. Li and S. Yekhanin, “Erasure Coding in Windows Azure Storage,” 2012 USENIX Annual Technical Conference, Boston, Massachusetts, June 2012.

[13] M. Kuijper and D. Napp, “Erasure Codes with Simplex Locality,” arXiv:1403.2779, March 2014.
[14] M. Li and P. C. Lee, “STAIR Codes: A General Family of Erasure Codes for Tolerating Device and Sector Failures in Practical Storage Systems,” 12th USENIX Conference on File and Storage Technologies (FAST 14), Santa Clara, CA, February 2014.

[15] F. J. MacWilliams and N. J. A. Sloane, “The Theory of Error-Correcting Codes,” North Holland, Amsterdam, 1977.

[16] Micron, “N-29-17: NAND Flash Design and Use Considerations Introduction,” http://download.micron.com/pdf/technotes/nand/tn2917.pdf.

[17] J. S. Plank and M. Blaum, “Sector-Disk (SD) Erasure Codes for Mixed Failure Modes in RAID Systems,” ACM Transactions on Storage, Vol. 10, No. 1, Article 4, January 2014.

[18] J. S. Plank, M. Blaum and J. L. Hafner, ”SD Codes: Erasure Codes Designed for how Storage Systems Really Fail,” 11th USENIX Conference on File and Storage Technologies (FAST 13), Santa Clara, CA, February 2013.

[19] D. S. Papailiopoulos and A. G. Dimakis, “Locally Repairable Codes,” IEEE Trans. on Information Theory, vol. IT-60, pp. 5843-55, October 2014.

[20] N. Prakash, G. M. Kamath, V. Lalitha and P. V. Kumar, “Optimal Linear Codes with a Local-Error-Correction Property,” ISIT 2012, IEEE International Symposium on Information Theory, pp.2776-80, July 2012.

[21] A. S. Rawat, O. O. Koyluoglu, N. Silberstein and S. Vishwanath, “Optimal Locally Repairable and Secure Codes for Distributed Storage Systems,” IEEE Trans. on Information Theory, vol. IT-60, pp. 212-36, January 2014.

[22] A. S. Rawat, D. S. Papailiopoulos, A. G. Dimakis and S. Vishwanath, “Locality and Availability in Distributed Storage,” arXiv:1402.2011, February 2014.

[23] M. Sathiamoorthy, M. Asteris, D. Papailiopoulos, A. G. Dimakis, A.G., R. Vadali, S. Chen and D. Borthakur, ”XORing Elephants: Novel Erasure Codes for Big Data,” Proceedings of VLDB, Vol. 6, No. 5, pp.325–36, 2013.

[24] W. Song, S. H. Dau, C. Yuen and T. J. Li, “Optimal Locally Repairable Linear Codes,” IEEE Journal on Selected Areas in Communications, Vol. 32 , pp. 1019–36, May 2014.

[25] I. Tamo and A. Barg, “A Family of Optimal Locally Recoverable Codes,” IEEE Trans. on Information Theory, vol. IT-60, pp.4661–76, August 2014.

[26] X. Tang and R. Koetter, “A Novel Method for Combining Algebraic Decoding and Iterative Processing,” ISIT 2006, IEEE International Symposium on Information Theory, pp. 474–78, July 2006.
[27] A. Wang and Z. Zhang, “Repair Locality with Multiple Erasure Tolerance,” IEEE Trans. on Information Theory, vol. IT-60, pp. 6979–87, November 2014.

[28] Y. Wu, “Generalized Integrated Interleaving BCH Codes,” ISIT 2016, IEEE International Symposium on Information Theory, pp. 1098–1102, July 2016.

[29] A. Zeh and E. Yaakobi, “Bounds and Constructions of Codes with Multiple Localities,” arXiv:1601.02763 January 2016.

[30] V. A. Zinoviev, “Generalized cascade codes,” Problemy Peredachii Informatsii, vol. 12, no. 1, pp. 5-15, 1976.