Compact Dual-Band Three-Way Metamaterial Power-Divider with a Hybrid CRLH Phase-Shift Line

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Abstract

A compact dual-band three-way metamaterial power divider is proposed that has three in-phase outputs. Fully printed composite right- and left-handed (CRLH) unequal and equal power dividers are first implemented for 900-MHz and 2.4-GHz bands with the power-division ratios of 2:1 and 1:1, respectively. An initial 1:1:1 power divider is then achieved by incorporating the input of the two-way equal block into an output of the unequal block, and trimming the interconnection parameters. The condition of an identical phase at the three outputs of the power divider is then met by devising a hybrid CRLH phase-shift line to compensate for the different phase errors at the two frequencies. This scheme is confirmed by predicting the performance of the power divider with circuit analysis and full-wave simulation and measuring the fabricated prototype. They results show agreement; the in-phase outputs as well as the desirable power-division are accomplished and outdo the conventional techniques.

Key Words: Delay Line, Dual-Band, Metamaterial, Phase-Shift Line, Power Divider, Size-Reduction.

1. INTRODUCTION

Every wireless communication system incorporates microwave power dividers, couplers, filters, and relevant parts [1–5]. In particular, since one path has to be branched (i.e., as found in an array antenna or comparator), the power divider is required at the junction between the route on one side and several ways on the other side. The rapid growth of the telecommunication industry has prompted microwave technologies to push the envelope to meet users’ demands, and power dividers have been developed to cope with the multiple bands for system integration, wide bands for broadband service, etc. [6, 7]. While achievements have been made in diversified and upgraded functions of the radio-frequency (RF) equipment, the constraints placed on the physical size have become the developers’ main concern. The response to these design priorities has been to focus a great amount of research on the production of size-reduced advanced power dividers, created by bending and folding, cascaded multi-stages, grid array, slot coupling between layers, R-L-C loads, etc., without loss of generality [8–11].

These conventional techniques, although prevalent, have the limitation of loss from multiple stages, restrictions in embedding a confined space, and higher cost due to stack-up fabrication. Alternative methods have been sought to iron out these problems, which have attracted substantial attention from the RF community [12–16]. For example, Caloz and Itoh [12] introduced composite right- and left-handed (CRLH) transmission lines; their attempts to realize the then-fancy phenomena of the backward wave, left-handed propagation, non-linear dispersion, and zeroth order resonance (ZOR) resulted in a long periodic geometry that comprised 24 cells of distributed elements [12]. They successfully demonstrated its metamaterial characteristics, but this geo-
metry was too large for incorporation into a device. Hence, many cells of lumped elements were cascaded in series to generate $+90^\circ$ or $-90^\circ$ and were attached to the arms of couplers and power dividers. Similarly, Marques et al. [13] formed a pair of slot rings under each of the arms of the Wilkinson power divider for the purpose of reducing the overall size. Inoue et al. [14] introduced an LTCC CRLH-type component similar to the lumped element-based multiple cells. A printed and small power divider with a metamaterial-inspired T-junction was shown by Saenz et al. [15], but it lacked a theoretical aspect, and its applicability to current challenges is doubted.

This paper proposes an improved method for fabrication of a smaller and more advanced component that will overcome the drawbacks of earlier metamaterial and ordinary approaches. A novel three-way dual-band CRLH power divider is designed to have a very small footprint and the equal power division and in-phase outputs. Differing from [16], a CRLH and short three-way dual-band power dividing structure is presented that provides a dual-band phase-error correction line. The solution process starts from the separate implementation of the 2:1 unequal and 1:1 equal power dividing blocks that are fully printed and metamaterials. This is followed by plugging the inputs of the equal block into an output of the unequal block for an initial 1:1:1 power divider, and slightly tuning the interconnection segment. This initial configuration and its possible modification inevitably give rise to phase errors between the outputs. Hence, we need a phase compensation line that works for the dual-band objective, but this is difficult to obtain only with the ordinary delay line for the single band or its harmonic relations. The dual-band phase compensation is accomplished by a hybrid CRLH phase-shift line as a mixture of the distributed and lumped elements, for the sake of efficiency and convenience.

The design method is validated by evaluating the proposed metamaterial structure by circuit analysis and full-wave simulation in the solution process. This novel 1:1:1 power divider is manufactured through a low-cost fabrication with an inexpensive cheap substrate and its performance is measured to confirm its desirable power division ratio and equalized phases at the output ports, especially considering that its physical placement is friendly to RF system layouts. Its size-reduction effect and technical applicability are also addressed.

II. THE PROPOSED CIRCUIT CONFIGURATION AND DESIGN OF ELEMENTARY BLOCKS

A novel, compact three-way power divider with CRLH characteristics is schematized in the following figure and compared to three examples that used other techniques. Fig. 1(a) shows the composition of the proposed structure. The unequal power divider block is sequentially combined with the equal block, and both blocks are realized as printed CRLH devices with small footprints. Fig. 1(b) is an extension of the Wilkinson power divider to three ways, but has a single band. Elongated segments are added to the first stage in Fig. 1(c), and four stages are placed in chain in Fig. 1(d). Fig. 1(b)–(d) heavily depend on the quarter-wave long arm for each stage, with extra delay lines, but fail to achieve effective size-reduction. However, since a dual-band CRLH phase-shift line will be substituted for the single-band and quarter-wave long arms of the traditional Wilkinson power divider, Fig. 1(a) will be appropriate for the present objective.

The equivalent circuit of the aforementioned phase-shift lines for the dual-band unequal and equal power divider
Fig. 2. The equivalent circuits of the composite right- and left-handed (CRLH) phase-shift line (a) and the unequal power divider (b).

blocks shown in Fig. 1(a) is set up as follows.

Fig. 2(a) is used to create phase $\Phi_1$ for a lower frequency $f_1$ and phase $\Phi_2$ for a higher frequency $f_2$, where the ratio of $f_2$ to $f_1$ is arbitrary due to the occurrence of the non-linear dispersion curve. These phases and frequencies yield simultaneous equations containing the circuit elements, and mathematical manipulation renders $L$'s and $C$'s as given in the equations \[16\].

\[
\left(\frac{\omega_1}{\omega_2}\right) - \frac{\omega_2}{\omega_1} = \frac{1}{Z_c}
\]

where $\omega_1 = 2\pi f_1$ and $\omega_2 = 2\pi f_2$, and $Z_c$ becomes $Z_{\text{RLH}}$ for one path and $Z_{\text{RH}}$ for another path in an unequal power divider. In the present case, applying $\Phi_1 = +90^\circ$ at $f_1 = 960$ MHz and $\Phi_2 = -90^\circ$ at $f_2 = 2.4$ GHz to Eq. (1) for Fig. 2(a) results in the CRLH phase-shift line of the following data.

Fig. 3(a) shows the phase as desired after applying $\Phi_1 = +90^\circ$ at $f_1 = 960$ MHz and $\Phi_2 = -90^\circ$ at $f_2 = 2.4$ GHz. This implies that this CRLH phase-shift line can be substituted for the quarter-wave long arms for the dual-band performance. Fig. 3(b) of the dispersion diagram presents the LH region including $f_1$, the ZOR, and the RH region including $f_2$ as a metamaterial property. This phase-shift line is input into the upper and lower paths of the dual-band unequal power divider as shown in Fig. 2(b). The characteristic impedance for each path is obtained with the following formulae, once we decide upon the power division ratio.

\[
Z_{k1} = Z_0\frac{1 + K^2}{K^2}, \quad Z_{k2} = K^2 Z_{k2}
\]

\[R = Z_0(\frac{1}{K} + 1)\]
Fig. 4. The circuit models on (a) the even-mode and (b) odd-mode.

Fig. 4(a) comes from Fig. 2, as ports 2 and 3 are excited with the in-phase signals and the bi-section line is treated as an open-circuited state. Fig. 2 becomes Fig. 4(b), in the assumption that the out-of-phase signals are fed into ports 2 and 3, and the axis of anti-symmetry is considered shunt-circuited. This applies to both the impedance lines. The frequency response of Fig. 2(b) will be given in the form of $S$-parameters, and the $Z$-parameters should be arranged for Fig. 2(a) and Fig. 4 in the first place. The series and shunt resonators in Fig. 2(a) are

$$Z_{se} = \left( j\omega L_R + \frac{1}{j\omega C'_{L_1}} \right), Z_{sh} = \left( j\omega C_R + \frac{1}{j\omega 2L_L} \right)^{-1}$$

With these, the diagonal and off-diagonal elements of the $Z$-parameter matrix for the CRLH phase-shift line can be written as

$$Z_{self} = \frac{Z_{se}^2 + Z_{se} Z_{sh}}{2Z_{se} + Z_{se}}, Z_{mutual} = \frac{Z_{se} Z_{sh}}{2Z_{se} + Z_{sh}}$$

The input impedance of the CRLH phase-shift line (the box in Fig. 4) is then obtained as

$$Z_{in}^{CRLH} = Z_{self} - \frac{Z_{mutual}}{Z_{load} + Z_{self}}$$

$Z_p^{K1}$ simply equals Eq. (5) in the even-mode, as in Fig. 4.

Simultaneously, in the odd-mode case, the input impedance into the CRLH phase-shift line through $R^{K1}$ from the output port becomes

$$Z_{p2}^{K1} = Z_{t}^{K1}|R^{K1}$$

where $R^{K1}$ means $R/(k^2+1)$.

For the equal block, $Z_{11} = Z_{dd} = Z_C$ becomes 70.7 $\Omega$.

As long as the impedance matrix is completed, the corresponding $S$-parameters can be expressed as follows.

$$S_{11} = \frac{2}{1 + K^2} \frac{Z_{p2}^{K1} - Z_{out}^{K1}}{Z_{p2}^{K1} + Z_{out}^{K1}}$$

$$S_{23} = \frac{K^2}{1 + K^2} \left( \frac{Z_{p2}^{K1} - Z_{out}^{K1}}{Z_{p2}^{K1} + Z_{out}^{K1}} \right)$$

$$S_{21} = \frac{1 - S_{11}^{2}}{\sqrt{K^2 + 1}}, S_{31} = \frac{1 - S_{11}^{2}}{2(K^2 + 1)}$$

All the necessary $S$-parameters are described mathematically on the basis of the primary $Z$-parameters from Fig. 4.
and the secondary ones of Eqs. (3)–(6). This process works for the unequal block, and also for the equal one. Therefore, the S-parameters are plotted as the frequency responses of the unequal and equal dual-band power dividers.

The predicted performance of the 2:1 unequal power divider is shown in Fig. 5(a), where $|S_{21}|$ is $-4.7$ dB and $-1.7$ dB at 960 MHz and 2.4 GHz, as desired. $|S_{21}|$ is equal to $|S_{31}|$, and the result of our code for the derived equations overlaps that of the commercial circuit analysis tool. Likewise, Fig. 5(b) shows a perfect agreement between the coding and the commercial program, and $|S_{21}| = -3$ dB and $|S_{31}| = -3$ dB as the 1:1 power division at the two target frequencies. The mathematical derivation has been validated and these elementary blocks are incorporated for the 1:1:1 power division.

III. Three-Way Dual-Band Power Divider by Combining the Two Blocks and Phase Correction by a Hybrid CRLH Line

In this section, the unequal and equal blocks are interconnected to form the three equal outputs, and realized and tested in a full-wave simulation. One output is different from the remaining outputs in terms of phase for each of the two frequencies; therefore, the phase correction will be attained in an efficient way.

The unequal and equal power-dividing circuits of Figs. 4 and 5 are physically implemented by following the steps of [16] to consider full-printing, and the input of the equal block is fed by the output of the 103-$\Omega$ path of the unequal dual-band power divider.

Fig. 6(a) and (b) illustrate how to connect the unequal dual-band power divider to the equal block. In accordance with this scenario, the initial shape of the fully printed CRLH three-way dual-band power divider is presented as in Fig. 6(c). The geometrical parameters of the two blocks remain almost unchanged from those of the structure of the 1:1 power-divider except for the addition of the connecting part: $W$, $L1_L$, $W_F$, $L_F$, $L2_L2$, $G_F2$, $L1_L3$, $W_F3$, $L_F3$, $L$, $L2_L$, $L2_L2$, $G_F$, $L1_L2$, $W_F2$, $L_F2$, $L2_L3$, and $G_F3$ in Fig. 6(c) are 30.6, 3.5, 0.4, 5, 5.3, 0.1, 3.1, 0.4, 5, 43.7, 7.5, 0.2, 3.5, 0.6, 5.6, 4.1, and 0.5 mm, respectively, with FR4 of $\varepsilon_r = 4.4$ as the substrate. The guided wavelength of 960 MHz called $\lambda_g$ requires the lengths of the CRLH phase-shift lines for the 51.5, 103, and 70.7 $\Omega$ paths to be $0.038 \lambda_g$, $0.031 \lambda_g$, and $0.034 \lambda_g$ in that order, which must be much less than 0.25 $\lambda_g$ of one Wilkinson’s stage. The electrical size of the total foot print of Fig. 6(c) is estimated as 0.173 $\lambda_g \times 0.247 \lambda_g$, and its implication is a significant size-reduction effect compared with the Wilkinson’s power divider for the single band and its multi-stage applications for the dual-band operation. The performance is presented with the following figures.

In Fig. 7(a) and (b), equal power division to the 3 output ports is achieved with nearly $-6$ dB of $|S_{21}|$, $|S_{31}|$, and $|S_{41}|$ at 960 MHz and 2.4 GHz, simultaneously in the electromagnetic (EM) simulation and measurement. Fig. 7(c) and (d) have $|S_{22}|$, $|S_{33}|$, and $|S_{44}|$ below $-12$ dB at $f_1$ and $f_2$, more or less. Nonetheless, while the phases of $S_{31}$ and $S_{41}$ are the same, they differ from the phase of $S_{21}$ as marked in Fig. 7(e). This causes the loss of a benefit of the in-phase outputs of the standard power divider. The location of port 2 in Fig. 6 also looks neither natural nor adaptable to RF cabling and harnessing. Therefore, the phase discre-
Fig. 7. The full-wave simulated and measured results of the proposed dual-band compact three-way power divider. (a) $S$-parameters from the electromagnetic (EM) simulation, (b) power-division performances, (c) inter-port isolation performances, (d) reflection coefficients, and (e) phases at the outputs.

...ency with different values at 960 MHz and 2.4 GHz should be fixed by considering a change in the position of port 2. The fact that port 2 is located on the opposite side of ports 3 and 4, and off the extension line from port 1 in Fig. 6(b) means that it can be made friendly to the system cabling and integration by moving it to the same side as port 4, as an optimal choice. Assuming this positioning plan for port 2, we need to assign a box and fill it with a device to compensate for the different phase errors at 960 MHz and 2.4 GHz. Before solving this problem, the phase errors at the
Fig. 8. The three-way dual-band power divider with a new location of port 2 and the insertion of the phase compensating hybrid composite right- and left-handed (CRLH) line for the in-phase outputs. (a) Moving port 2, (b) phase errors ($\Delta \phi_1$ and $\Delta \phi_2$) at $f_1$ and $f_2$, (c) $\Delta \phi_1$ and $\Delta \phi_2$ achieved by the phase correcting hybrid CRLH dual-band line, (d) equivalent circuit of in-phase power divider, (e) the phase correcting line added to the layout as the finalized three-way dual-band power divider, (f) photograph of the manufactured prototype, (g) in-phase outputs obtained, (h) power division, (i) isolation, and (j) return loss.
two frequencies with regard to a new location of port 2 should be checked. The changed layout differs from Fig. 7(e) and is given below. If a line shorter than 39 mm is chosen for the connection box, the phase errors will be known, given the layout of the modified three-way power divider, as follows.

The old port 2 in Fig. 7(e) is relocated, as shown in Fig. 8(a). A test line is inserted in the box to connect the output of the 103-Ω path of the unequal block to the new port 2, and the phase errors (ΔΦ1 and ΔΦ2) between S21 and S31 at f1 and f2 are found 14.5° and 168°, as observed in Fig. 8(b). A compact dual-band phase-shift line for ΔΦ1 and ΔΦ2 is made by a hybrid CRLH structure comprising a distributed element as the RH segment and lumped elements as the LH segment, as shown in the inset of Fig. 8(c). The RH segment is realized by a transmission line with L_R and W_L, while the two short-circuited chips L and the series chip C are responsible for the LH segment. Following the procedures to determine L and C as the unequal and equal block realization using Eq. (1), L_R, W_L, L, and C for Fig. 8(c) are calculated as 15 mm, 1.2 mm, 11 nH, and 1 pF, respectively, along with the following formulae regarding the RH segment.

\[
C = \frac{2.64(e_r + 1.41)}{\ln \left( \frac{5.98h}{0.8w + t} \right) 10^{-11}} \\
L = 0.00021 \left( \ln \left( \frac{0.0031l(w + h)}{0.0031l(w + h) + 0.5} + \frac{0.2235(w + h)}{l} \right) \right)
\]  

(8)

where w, t, and h mean the width and thickness of the metal signal trace, and the height of the microstrip-line substrate, in that order. This dual-band hybrid CRLH phase-shift line attains ΔΦ1 at f1 and ΔΦ2 at f2 as desired, and it is presented by the measured data in Fig. 8(c). Consequently, the phase errors of Fig. 8(d) and (e) are corrected, and the identical phases at the three output ports are accomplished as in Fig. 8(f). Its area is kept the same as 0.173 λ × 0.247 λ, which validates the miniaturization effect of the proposed scheme. The magnitudes of the S-parameters of this final geometry are also plotted in Fig. 8(h). The equal power division is achieved at the three output ports, although an insertion loss of around −0.5 dB from the ideal power of −4.7 dB occurs due to the accumulated dielectric loss of FR4 and the conduction loss of copper. The inter-port isolation is observed below −15 dB at f1 and f2. In addition, a good impedance match is apparent at the ports, with a return loss below −15 dB at the two target frequencies. Lastly, the restriction of the ordinary delay line to meet only either ΔΦ1 or ΔΦ2 is described.

The ordinary delay line is not able to achieve ΔΦ1 and ΔΦ2 at the target frequencies. The conventional delay line has the phase of a linear function; therefore, it works for a single band and its harmonics. In Fig. 9(a), the 169-mm long delay line can make 14.5° at f1, but fails to create ΔΦ2 at f2. In addition, a line of this length will also exceed the footprint of the proposed three-way power divider. The use of the 37.5-mm long delay line also only satisfies 168° at f2, but is forbidden at f1 for ΔΦ1. Therefore, our CRLH power divider outdoes the conventional components in that it results in a small size, but retains equal power division and in-phase outputs based on the non-linear dispersion properties.

IV. Conclusion

This paper proposes a small sized three-way power divider with a 1:1:1 power division and in-phase outputs to work for two frequencies. It consists of 2:1 unequal and equal dual-band power dividers that are fully printed CRLH building blocks and incorporated to make a compact device to divide one RF input power into three ways. In particular, the phase errors between port 2 and ports 3 or 4 at 960 MHz and 2.4 GHz are observed in the initial three-way power divider, and they have been compensated for to equalize the output phases by designing a hybrid CRLH dual-band phase-shift line. The suggested method is shown to be valid through circuit analysis based on the derived equations, a full-wave simulation, and the measurement of the fabricated prototype. The overall size is much less than that of Wilkinson and modified power dividers. This method is applicable to the designs of advanced power dividers.
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