Optical frequency locked loop for long-term stabilization of broad-line DFB laser frequency difference

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Abstract We present an experimental realization of the optical frequency locked loop applied to long-term frequency difference stabilization of broad-line DFB lasers along with a new independent method to characterize relative phase fluctuations of two lasers. The presented design is based on a fast photodiode matched with an integrated phase-frequency detector chip. The locking setup is digitally tunable in real time, insensitive to environmental perturbations and compatible with commercially available laser current control modules. We present a simple model and a quick method to optimize the loop for a given hardware relying exclusively on simple measurements in time domain. Step response of the system as well as phase characteristics closely agree with the theoretical model. Finally, frequency stabilization for offsets within 4–15 GHz working range achieving <0.1 Hz long-term stability of the beat note frequency for 500 s averaging time period is demonstrated. For these measurements we employ an I/Q mixer that allows us to precisely and independently measure the full phase trace of the beat note signal.

1 Introduction

Laser frequency difference stabilization is indispensable in multiple modern experimental schemes. Applications range from quantum optics, cold atomic physics and off-resonant light–atom interfaces [1–5], through frequency comb stabilization [6–9] to precision spectroscopy and sensing [10, 11]. In multiple applications, phase coherence of the two laser fields locked at a frequency offset is not required [2–11] and a mere frequency lock is a sufficient solution. Nevertheless, one of the most commonly used solutions is the optical phase locked loop (OPLL) [12–15]. In a generic OPLL the master laser (ML) and the slave laser (SL) are combined and the beat note is measured on a fast photodiode (PD), compared with a reference value and then the difference is fed through the loop filter and used to tune SL by employing a fast current modulator. However, phase difference has to be kept within tight margins for OPLLs to work, rendering them impractical for broad-line lasers.

If the OPLL is constructed to ensure only the frequency drift stabilization and not the phase coherence, it constitutes an optical frequency locked loop (OFLL). Such approach has been presented previously in Ref. [16]; however, it was applied to narrow-line external cavity diodes lasers (ECDL) and was based on a frequency voltage converter, allowing only up to 8 MHz offsets between SL and ML. OPLL setups involving phase-frequency detectors (PFD) have been presented in Refs. [17] or [18] but for frequency offsets only up to 7 GHz. Both works were concerned with phase coherence requiring complicated loop filters and either field programmable gate array (FPGA) implementation or two parallel proportional-integral (PI) controllers. Ivanov et. al. [18] apply their setup to DFB lasers albeit of narrower line (1 MHz) than in our case. They also present a detailed analysis of OPLL performance in the presence of frequency dividers.

Here we present an OFLL designed for the stabilization of the long-term (>100 μs) frequency drift. Our setup is based on an integrated PFD chip that compares the beat note signal of ML and SL with low-frequency reference. The PD and PFD are specifically matched to provide the simplicity of setup construction and usage.
The PFD delivers current pulses (phase error) which are averaged and converted to a voltage signal on a low-pass filter (LPF). After subtracting the programmable reference voltage (V_{ref} in Fig. 1), the signal (error signal) becomes proportional to the measured phase difference.

We design a single-stage loop filter as a proportional controller with only a small integral term to keep the phase difference in the detector range. The PFD chip may detect very large phase differences and thus can be applied to broad-line laser diodes. The error signal is fed back to a simple current controller with relatively slow response.

Several other methods had been developed for the purpose of frequency locking. These include feedback loops involving Mach–Zehnder interferometer with coaxial cable delay lines [19] or the application of electrical frequency filters [20, 21] performing frequency to amplitude conversion. These methods suffer from several significant limitations, including: less compact design, susceptibility to the environmental conditions as well as limited tunability of laser frequency difference.

We address these issues by employing a method that yields excellent phase stabilization [13] to the regime of long-term frequency stabilization of broad-line lasers, such as DFB laser diodes applicable in harsh environmental conditions [2, 9]. Additionally, a compact design is achieved using an integrated PFD chip. Long-term stability of our method is limited by the reference frequency generator, voltage source stability (DAC and inside LPF, see Fig. 1) as well as by PFD noise and hysteresis. The tuning of the setup can be performed in a real time by reprogramming the PFD chip and the generator.

Finally, we use an I/Q mixer to recover a full phase trace of the beat note signal. This method constitutes a new approach to laser phase-lock characterization and yields very informative data that allow us to recover phase variance and Allan deviation.

This paper is organized as follows: Sects. 2 and 3 discuss limitations of the OPLL operation and how these are addressed in our solution. Section 4 describes a simple theoretical model we have developed to simplify the setup and optimization of the OFLL using generic hardware. The process is then described in Sect. 5 in the form of a step-by-step procedure requiring merely simple time domain measurements. Section 6 describes how to modify the model to account for specific hardware characteristics. Section 7 shows the experimental performance of our OFLL realization and shows the results of I/Q mixer-based phase measurements. Section 8 concludes our work.

![Fig. 1 OFLL and a phase φ(t) measurement setup.](image)

**2 OPLL limitations**

In a typical OPLL the beat note of ML and SL is registered with a photodiode detector (PD) and then an electronic mixer is used for comparison of the phase with the reference local oscillator (LO) [22, 23]. Such construction imposes severe limitations on the maximal phase error |φ| < π/2 rad due to periodic response of the mixer. This limitation can be safely neglected in phase coherence-oriented applications where ⟨φ^2⟩ ≪ 1 and loop bandwidth exceeds laser linewidth. However, in the regime of slowly reacting broad-line lasers ⟨φ^2⟩ can reach thousands. This is caused by the intrinsic laser frequency drift within the closed-loop response time.

In feedback loop systems, unavoidable delays limit the reaction time of the loop. In the frequency domain this corresponds to the loop bandwidth f_u—the maximal frequency at which the loop gains G(f) is above unity. The loop bandwidth f_u is bound by 1/(2Δτ), where Δτ is the open-loop delay.

The phase fluctuations at frequencies above f_u cannot be compensated by the loop. Therefore, the open-loop delay is the main reason why the state-of-the-art, high-speed electronics is needed to maintain sufficiently high f_u and thus sufficiently low ⟨φ^2⟩ for a typical OPLL to function correctly.
3 Optical frequency locked loop

If one is concerned merely with a long-term frequency drift compensation, a slow loop is sufficient, provided it can process relatively large phase fluctuations $\langle \phi^2 \rangle$. This is enabled by replacing a simple mixer with a commercial PFD, integrated with programmable dividers in a single chip. If the beat note signal is divided by $N$, the maximum phase difference for linear PFD regime becomes $N\pi$. Allowing for larger phase detours $\langle \phi^2 \rangle$, the loop bandwidth $f_o$ is no longer required to exceed the laser linewidth. It results in larger acceptable open-loop delay $\Delta t$ enabling the usage of commercially available slow laser current controllers in the loop.

In the experiment we use two distributed feedback (DFB) lasers (Toptica DL100). The master laser (ML) is free running during all measurements. The slave laser (SL) and ML are controlled by two Toptica current controllers (DCC110). The PI controller is built using a single LM358N operational amplifier with several passive components. About 100 μW power from each laser is combined with a fiber coupler (Thorlabs FC780-50B) and the resulting beat note is monitored by a ~10 GHz photodiode (PD) (Finisar HFD6380-418) as depicted in Fig. 1.

The signal from complementary microwave outputs of the photodiode is guided by roughly 7-mm-long coplanar lines realized on standard FR4 laminate (0.15 mm clearance between 1 mm RF trace and surrounding ground, 1.6-mm-thick laminate, ground stitched to continuous plane on the bottom of a board with 0.3 mm vias spaced 0.6 mm apart) and directed to PFD chip (ADF41020, see [24]) and diagnostic SMA output. The PFD uses a charge pump to sink or source current pulses. Their width is proportional to the absolute value of the phase difference of the divided inputs, and the flow direction indicates its sign. Current pulses then pass through a low-pass filter (LPF) consisting of a parallel RC pair ($R = 390 \Omega$, $C = 330$ pF) connected to ground and a resistor ($R = 390 \Omega$) connected to stabilized voltage of 3 V. The current impulses are averaged and converted to a voltage signal. In the next step the Vref voltage from a PC-controlled digital analog converter (DAC) is subtracted from the signal by an operational amplifier. The resulting error signal is proportional to the phase difference. The photodiode saturates at about 500 μW total optical power, providing approximately −10 dBm RF power at 6 GHz. In this arrangement the PFD operates reliably between 4 and 15 GHz as depicted in Fig. 2 delivering unambiguous error signal in the entire range. Operation range is limited from above by the bandwidth of the photodiode and from below by the construction of input stage amplifiers in the PFD [24].

4 Simplified loop model

In this section we present a model of the OFLL used to select optimal loop parameters. We assume an OFLL as sketched in Fig. 3. The transfer function of the open loop $G(f)$ is the product of constituent transfer functions $G(f) = AD(f)L(f)/2\pi if$, where $A$ is the overall gain constant, accounting for the product of individual gains, $D(f)$ and $L(f)$ are unity gain transfer functions for laser and loop filter, respectively. The integration due to the frequency-phase conversion is explicitly included.

In optimization procedure we consider the closed loop which transfers function is given by $H(f) = 1/(1 + G(f))$. In turn the eigenfrequencies of the closed loop are given by the roots of the $H(f)$ denominator. Their location on the complex plane...
plane determines the time response as well as the stability of the loop. The necessary condition for non-oscillatory behavior is that all roots lay in the negative real half-plane. The denominator root of interest for optimization purposes is the one with the highest real part as that is approximately equal the inverse of the closed-loop response time.

To maintain generality and simplicity we assume laser response \(D(f)\) to be flat with dominant open-loop delay \(\Delta \tau\), \(D(f) = \exp(-i 2 \pi f \Delta \tau)\). We first consider a purely proportional loop filter, i.e., \(L(f) = 1\) with proportional gain constant included in \(A\). The optimal gain in this case is found to be \(A_p = 1/(e \Delta \tau)\). For the sake of further optimization considerations, we also note that when the gain is increased up to the stability margin, oscillations emerge at a frequency \(f_{p,u} = 1/(4 \Delta \tau)\) depending only on the open-loop delay \(\Delta \tau\). The dependence originates from the stability condition requiring the gain to be below unity at 180° phase shift. Upon the condition violation, the overall integration factor is responsible for the first 90° and the open-loop delay factor \(\exp(-i 2 \pi f \Delta \tau)\) for the second introducing the overall \(\frac{1}{4}\) factor. A purely proportional loop can stabilize at an arbitrary loop filter output voltage level. In particular, the level may gradually increase over several hours and reach the maximum rendering the loop partially unresponsive. An addition of the integral term to the loop filter ensures the loop stabilizes at the 0 V level. Thus, it compensates, otherwise steadily accumulating, total phase error, which in turn can be kept within the finite operating range of the phase detector. In this case the loop filter \(L(f)\), shown in Fig. 3, is characterized by a large DC gain \(\kappa\) and filter zero-frequency \(f_z\) having a unity proportional gain at high frequencies:

\[
L(f) = \kappa \frac{1 + i f f_z}{1 + i f/ f_z}. \tag{1}
\]

At \(f_z\) the gain contributions from the proportional and integral terms are equal.

The presented model can be used to predict open-loop time response and optimize \(f_z\) and \(A\) for a given \(\Delta \tau\) aiming at the quickest, non-oscillatory response. For large DC gains \((\kappa \gg 1)\) the optimal parameters do not depend on \(\kappa\). They are found to be very close to \(A = 1/(2 \Delta \tau)\) and \(f_z = 1/(10 \pi \Delta \tau)\).

The model definitions and optimization calculations can be found in a Mathematica worksheet in attached supplementary materials, allowing easy extension and adaptation to parameters measured in particular experimental scenario.

5 Setting up and optimization

An experimental procedure to quickly set up the OFLL relies solely on the measurements in the time domain thus eliminating the need for specialized devices such as network analyzers. The procedure aims at establishing the open-loop delay \(\Delta \tau\) and its total gain. During the measurement, a care should be taken to ensure a wide PFD phase detection range by setting a high \(N\) divisor value. This prevents loop oscillations originating from PFD overflow. Indications of these can be observed as discontinuities in the PFD output signal.

First, a proportional amplifier is used to close the loop with a gain \(P\) chosen to avoid oscillations. The proportional constant \(P\) should be subsequently increased until the loop develops oscillations. When these arise the total gain of the loop equals 1 and their frequency corresponds to the open-loop delay \(f_{p,u} = 1/(4 \Delta \tau)\). This allows us to immediately use the optimal PI parameters by merely correcting for the gain \(P\) used to arrive at oscillations. The derived model parameters correspond to the proportional gain \(P_{pl} = AP/((2 \pi f_{p,u})\) and integral gain \(I = 2 \pi P_{pl} f_z\). These equations can be combined with derived optimal parameter values to obtain a formula for the optimal PI gains in terms of used \(P\) and measured \(\Delta \tau: P_{pl} = P/\pi, I = P/(\pi \Delta \tau)\).

If \(N\) is to be later altered the resulting change in the total open-loop delay must be considered. Working at RF frequency \(f_{RF}\) the additional delay, due to the PFD response time, is \(2N/f_{RF}\). In the regime where \(\Delta N \ll f_{RF} \Delta \tau/2 + N\) the change \(\Delta N\) in \(N\) will merely affect proportional loop gain. Otherwise re-optimization with a new open-loop delay value may be required. It does not, however, require further measurements as the delay change may be calculated and accounted for.

6 Tailoring the model

The simplicity of the model presented in Sect. 4 provides its usability in a generic case. This, however, comes at the cost of omitting details in the loop element description. It is quite straightforward to adjust the model if the details of the involved transfer functions are known. As depicted in Fig. 4, we have compared the experimental data (RF freq. 6 GHz, \(N = 5120\)) with model predictions before and after accounting for the measured laser current controller response. In our case it comprised both \(\Delta \tau = 10 \mu s\) delay and a double pole at \(f = 12.8\ kHz\). The models are used with the open-loop delay \(\Delta \tau = 10 \mu s\) and PI parameters \((A, f_z)\) optimal according to Eq. 2 with \(\Delta \tau = 10 \mu s\). In Fig. 4b it is evident that in such conditions the simple model does not capture details of the loop time response. Here, according to the simple model, an increase in the loop gain relative to the situation depicted in Fig. 4a shall only improve the response time while the extended model properly predicts the appearance of the unwanted oscillatory behavior. The comparison is done under the assumption that the real open-loop delay \(\Delta \tau\) is known and used in both models. If this is not the case the procedure described in Sect. 5 can be used. However,
Additional phase shifts are introduced, in our setup, due to $D(f)$ double pole. Accounting for these we find the optimal gain and PI zero parameter values reduced by a factor of 4 compared to the predictions of the simple model:

$$A = 1/(8\Delta \tau), \quad f_z = 1/(40\pi \Delta \tau).$$

Therefore, we advise to use reduced gain $A$ and zero-frequency $f_z$. In terms of $P_{\text{PI}}$ and $I$ the corrected values read $P_{\text{PI}} = P_P/(4\pi), I = P_P/(80\pi \Delta \tau)$.

Similar methods of optimization based on time series analysis exist. Basic Ziegler–Nichols (ZN) approach [25] and Refined Ziegler–Nichols [26] constrain their analysis to processes without integration while our method accounts for integration by the PFD. Other methods such as [27] ignore open-loop delay and require trial-and-error parameter tuning. In contrast our method is fully systematic and accounts for the open-loop delay. The well-acknowledged ZN method provides the following PI parameters $I_{\text{ZN}} = 0.6\pi/\Delta \tau$, $P_{\text{PZN}} = P_P/2.2$. Our method requires exactly the same information as ZN; however, it provides $I$ that depends on $P_P$. Thus, it is more specific than Ziegler–Nichols method, while remaining equally simple yet less universal.

### 7 Performance

Here we discuss the system performance after application of the optimization procedures. In our configuration, with $\Delta \tau = 10\, \mu s$, the closed-loop time response was found to be around $100\, \mu s$ as depicted in Fig. 4a.

Performance and short-term stability is well characterized by the phase evolution of the system. As depicted in Fig. 1a the phase $\phi(t)$ has been measured using the apparatus consisting of an in-phase quadrature mixer (I/Q mixer) ADL5380 fed with a reference signal (LO') from HMC833 high-frequency generator. The mixer measures the in-phase $I_{\text{iq}}$ and 90° shifted $Q_{\text{iq}}$ components of the RF signal enabling the retrieval of the phase $\phi(t) = \arctan(Q_{\text{iq}}/I_{\text{iq}})$ relative to the LO'. We collected many $1\,$ms-long ($0 \leq \Delta t \leq 1\,$ms) records $\phi(\Delta t - t_0)$ of $\phi(t)$, each starting at a different $t = t_0$. After application of the phase unwrapping algorithm, we subtracted $\phi(t_0)$ from each record and obtained a record of many $\phi(\Delta t - t_0) - \phi(t_0)$. The records were subsequently used to obtain a probability density map $p(\phi)$ (histogram) of phase difference $\phi(\Delta t - t_0) - \phi(t_0)$ for each delay time $\Delta t$ depicted in Fig. 5. The histogram confirms that the phase distributions relative to $t_0$ are symmetric and virtually constant for times over $100\, \mu s$.

Since the average $\langle \phi(t_0) \rangle = 0$ the phase variance $\langle (\phi(\Delta t - t_0) - \langle \phi(t_0) \rangle)^2 \rangle = \langle \phi(\Delta t - t_0)^2 \rangle$. As depicted in Fig. 5b it reaches a constant value of $75 \times 10^3 \, \text{rad}^2$ after the settling time of $100\, \mu s$ confirming the correct functioning of the OFLL. The $N$ divider was set to be $N = 12,800$. 

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Fig. 4 Experimental and theoretical closed-loop time response for unit step perturbation. The signal was measured at the current controller (Ictrl) input. Optimal response (a) occurs for parameters $f_z = 800\, \text{Hz}, A = 12.5\, \text{kHz}$. Upon increasing $A$ damping period is prolonged (b). If the $A$ is lowered too much to suppress oscillatory behavior one may obtain slow sub-optimal response (c).
This corresponds to the PFD range of approx. $4 \times 10^4$ rad. Thus, the PFD range was kept far above the average phase deviation, allowing simple adjustments of the loop gain by altering $N$ values.

Finally, we characterize the long-term stability of the system. A convenient tool is the overlapping Allan variance, which is calculated using three separate phase measurements—two for locked laser and one for unlocked laser. To obtain the result for short averaging times, we acquired a single 1.4-s-long record of phase using the I/Q mixer (Fig. 1a). Since this measurement requires high sampling rate, the total time is limited by oscilloscope memory. For longer averaging times we use another technique and continuously measure average PFD output signal (error signal in Fig. 1) for 1800 s and calculate the corresponding phase. Allan variances inferred from these two measurements are consistent in the overlapping region.

Figure 6b depicts the result for the overlapping Allan deviation for locked and unlocked (free-running) laser. For the unlocked laser only data obtained with mixer-based measurement is presented. For the case of locked laser, long averaging times region is dominated by the phase white noise as inferred from the observed $\tau^{-1}$ power-law dependence. In this region, starting from $\tau \approx 100 \mu s$ we clearly see the effect of the OFLL. In particular, the long-term stability of our system is guaranteed by an intrinsically environmentally insensitive frequency detection, resulting in $<0.1$ Hz frequency deviation after 500 s averaging time. For much longer times we expect that electronic instabilities such as drift of voltage reference will cause the trend to collapse. At small averaging times the $\tau^{-1}$ trend discontinues due to internal frequency characteristics of the laser, such as white or flicker frequency noise [13, 18, 28]. This kind of noise dominates in the case of unlocked laser.

8 Conclusions

In conclusion, we have presented a laser difference stabilization technique extending the optical phase locked loop methods to the regime of broad-line lasers. We have discussed the OFLL operation, constructing a simple model which enabled us to present a simple method of OFLL optimization, relying merely on a straightforward time domain measurement. Furthermore, the loop diagnostic methods have been presented.

Our setup provides an excellent long-term frequency stability, yet providing a broad lock set point frequency range (4–15 GHz) with unambiguous error signal in the entire range, promising a variety of applications in quantum optics and cold atomic physics. If smaller offset frequencies are required, similar PFD chip suitable for smaller frequencies can be used. Furthermore, a phase variance measurement on the PFD output provides a simple method to eliminate an unwanted locking at zero-frequency offset thus extending the effective frequency range of unambiguous error signal to nearly 30 GHz.
Exploiting an integrated phase-frequency detector ADF41020 on an ordinary PCB with merely one microwave track, our design remains simple and readily compatible with generic laser current controllers. Digitally controlled ADF41020 frequency divisors and lock setpoint (e.g., by DDS AD9959) allow for easy regulation. The ADF41020 can independently output the divided measured frequency, which enables construction of fully automated, self-diagnosing systems with standard counters.

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References

1. S.H. Yim, S.B. Lee, T.Y. Kwon, S.E. Park, Appl. Phys. B 115(4), 491 (2014)
2. T. Léveque, L. Antoni-Micollier, B. Faure, J. Berthon, Appl. Phys. B 116(4), 997 (2014)
3. M. Dąbrowski, R. Chrapkiewicz, W. Wasilewski, Opt. Express 22(21), 26076 (2014)
4. M. Parniak, A. Leszczyński, W. Wasilewski, Phys. Rev. A 93(5), 053821 (2016)
5. T. Kawalcz, D. Bartoszek-Bober, Opt. Eng. 52(12), 126105 (2013)
6. S.T. Cundiff, J. Ye, Rev. Mod. Phys. 75(1), 325 (2003)
7. A.M. Jayich, X. Long, W.C. Campbell, Phys. Rev. X 6, 041004 (2016)
8. W. Gunton, M. Semczuk, K.W. Madison, Opt. Lett. 40(18), 4372 (2015)
9. M. Lezuius, T. Wilken, C. Deutsch, M. Giunta, O. Mandel, A. Thaller, V. Schkolnik, M. Schiemangk, A. Dinkelaker, A. Kohfeldt, A. Wicht, M. Krutzik, A. Peters, O. Hellmig, H. Duncker, K. Sengstock, P. Windpassinger, K. Lampmann, T. Hülsing, T.W. Hänsch, R. Holzwarth, Optica 3(12), 1381 (2016)
10. M. Lyon, S.D. Bergeson, Appl. Opt. 53(23), 5163 (2014)
11. R. Matthey, S. Schilt, D. Werner, C. Affolderbach, L. Thevenaz, G. Mileti, Appl. Phys. B 85(2–3), 477 (2006)
12. M. Prevedelli, T. Freegarde, T.W. Hänsch, Appl. Phys. B 60, S241 (1995)
13. J. Appel, A. MacRae, A.I. Lvovsky, Meas. Sci. Technol. 20(5), 055302 (2009)
14. F. Friederich, G. Schuricht, A. Deninger, F. Lison, G. Spickermann, P. Haring Bolivar, H.G. Roskos, Opt. Express 18(8), 8621 (2010)
15. C. Wei, S. Yan, A. Jia, Y. Luo, Q. Hu, Z. Li, Chin. Opt. Lett. 14(5), 051403 (2016)
16. T. Stace, A.N. Luiten, R.P. Kovacich, Meas. Sci. Technol. 9(9), 1635 (1998)
17. X.L. Wang, T.J. Tao, B. Cheng, B. Wu, Y.F. Xu, Z.Y. Wang, Q. Lin, Chin. Phys. Lett. 28(8), 084214 (2011)
18. E.N. Ivanov, F.X. Esnault, E.A. Donley, Rev. Sci. Instrum. 82(8), 083110 (2011)
19. U. Schunemann, H. Engler, R. Grimm, M. Weidemuller, M. Zielonkowski, Rev. Sci. Instrum. 70(1), 242 (1999)
20. S. Schilt, R. Matthey, D. Kauflmann-Werner, C. Affolderbach, G. Mileti, L. Thevenaz, Opt. Lett. 27(4), 4336 (2008)
21. N. Strauß, I. Ernsting, S. Schiller, A. Wicht, P. Huke, R.H. Rinkl, eff, Appl. Phys. B 88(1), 21 (2007)
22. L.H. Enloe, J.L. Rodda, Proc. IEEE 53(2), 165 (1965)
23. R.C. Steele, Electron. Lett. 19(2), 69 (1983)
24. Analog Devices, 18 GHz Microwave PLL Synthesizer, ADF41020, Datasheet. Rev. C (2014)
25. J.G. Ziegler, N.B. Nichols, J. Dyn. Syst. Meas. Control 115(2B), 220 (1993)
26. C. Hang, K. Åström, W. Ho, IEEE Proc. D Control Theory Appl. 138(2), 111 (1991)
27. J. Basilio, S. Matos, IEEE Trans. Educ. 45(4), 364 (2002)
28. S. Kunze, S. Wolf, G. Rempe, Opt. Commun. 128(4), 269 (1996)