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Abstract

Fabrication of smooth noble metal ultrathin films is crucial to many optical and electronic devices. However, the metals’ adatom-adatom cohesive force is usually stronger than the adatom-substrate adhesive force leading to a Vollmer-Weber, island-like, growth mode. This phenomenon imposes limitations on the surface smoothness and minimum (percolation) thickness necessary to obtain a smooth film. Here, we demonstrate a facile method to fabricate ultra-smooth ultrathin silver (Ag) films on silicon via physical vapor deposition. By removing the oxide layer on silicon substrates using a hydrofluoric acid treatment, Ag atoms bind strongly to the energetically favorable silicon atoms leading to smooth Ag films. We compare the results for Ag deposited on HF treated and untreated Si substrates for different Ag thicknesses. Our results show that HF acid treatment and annealing lead to a significant reduction in the surface roughness (∼0.5 nm), narrower peak-to-valley height distribution, and higher Kurtosis. Continuous Ag films were obtained down to Ag thickness of 5 nm. We expect our results to play a crucial role in minimizing electronic and optical losses for optoelectronic, plasmonic and optical metamaterial devices.

1. Introduction

The use of metallic films with thicknesses of a few to a few tens of nanometers is central to many research fields and technologies. Ultrathin noble metallic films, e.g., silver (Ag), gold (Au), and aluminum (Al) are used as building blocks for plasmonic devices, metamaterials [1] and metasurfaces [2], and nanoelectronic devices [3]. This is due to their high electrical conductivity and low optical losses, mainly in the visible-NIR wavelength range. The applications and devices that rely on ultrathin noble metallic films are versatile and numerous, including bio-sensing [4], Water Sanitation/desalination [5], enhanced spontaneous emission rate [6, 7], and enhanced photovoltaic emission [8].

However, thin metallic films deposited on oxides tend to exhibit a Vollmer-Weber (VW) growth mode, i.e., the metallic atoms tend to de-wet the surface and form islands [9]. This phenomenon leads to an increase in the surface roughness of the deposited films, which degrades the efficiency of thin-film devices. In addition, depositing a continuous ultrathin film (thickness < 10 nm) is challenging, i.e., there exists a percolation thickness below which the deposited film is not continuous. High surface roughness and lack of continuity increase the resistivity of the deposited film, increase the optical losses of the film, and decrease the surface plasmon polariton propagation length, i.e., degrades its electronic and optical properties [3, 10].

In particular, minimizing the optical losses in plasmonic devices has been the center of extensive research which has been realized by adding gain material to the plasmonic system to compensate for plasmonic losses [11], operating at long (IR) wavelengths where optical losses are low, minimizing the field confinement inside..
As purchased p doped Si substrates have a native or grown oxide layer. The metal [2], or by creating smoother plasmonic films to decrease electron-defect scattering [10]. Realization of smooth plasmonic films deposited via physical vapor deposition was demonstrated using various methods, e.g., deposition on special substrates [12], optimizing the deposition process [13] and using a seed wetting layer.

To obtain a smooth film, the surface adhesive force must be higher than the adatom cohesive force leading to a Frank-van der Merwe (FM) growth mode [9]. To do so, previous works showed that depositing Germanium (Ge) [3, 14] or a copper (Cu) [15] wetting layers significantly decreases the surface root mean square roughness (Rrms) of Ag films down to ~0.7 nm and 0.5 nm, respectively [3, 14, 15]. This is because the bond dissociation energy between Ag atoms (HAg-Ag = 162 KJ mol⁻¹) is lower than that of Ag and Ge and Ag and Cu atoms which are HAg-Ge = 174.5 KJ mol⁻¹ and HAg-Cu = 171.5 KJ mol⁻¹, respectively [16]. The Ag–Ag bond causes deposited Ag atoms to cluster. Consequently, creating a stronger bond between Ag atoms and a seed layer of Ge or Cu results in smoother films. Interestingly, the Ag–Si bond disassociation energy is HAg-Si = 185.1 KJ mol⁻¹ [16]. This suggests that in lieu of depositing a seed layer on an SiO₂/Si substrate [3], a possible alternative is to directly deposit Ag on Si by removing the SiO₂ layer. We note here that smooth thin film can be directly obtained using atomic layer deposition [17] and DC magnetron sputtering [18].

In this work, we create smooth Ag films deposited via thermal physical vapor deposition by removing the SiO₂ layer from Si surface with hydrofluoric (HF) acid. We show that removing the SiO₂ layer prior to depositing the Ag film and annealing the Ag film substantially increase the surface smoothness with surface roughness ~0.5 nm. Continuous Ag films down to 5 nm in thickness are obtained. The deposited Ag films enjoy significantly lower root-mean-square surface roughness, narrower grain-size distribution, and higher Kurtosis.

2. Experimental section

As purchased p doped Si substrates have a native or grown oxide layer (figure 1(a)). Removing the native oxide layer of Si using HF acid treatment (figure 1(b)) was demonstrated previously. We added 5 ml of HF with a concentration ≥40% to 110 ml DI water. The Si samples were dipped inside the solution for 2 min. After washing with the DI water, the samples were dried with N₂ and placed inside the glove box to avoid re-oxidation of the Si. Ag silver pellets (99.999% purity from Sigma-Aldrich) are then deposited via thermal vapor deposition. Thermal evaporator (Beijing Technol Co., Ltd.) was used to deposit the thin film. Ag thin films of 5 nm, 10 nm and 20 nm thickness were deposited via thermal vapor deposition method on Si substrate. The film thickness was determined using a calibrated quartz crystal monitor.

Three types of Si substrates were studied; untreated Si substrates, Si substrate treated with HF acid (HF/Si), and HF treated Si substrate annealed at 150 °C for 3 min on a hot plate (annealed/HF/Si). Vacuum annealing of the HF treated Si substrate assists in volatilizing any metal complexes remaining on the Si substrate [19]. These metal complexes can act as nucleation sites that may limit the formation of homogenous and smooth Ag films. We note that annealing the treated Si substrate increases the surface roughness of the HF treated Si substrate prior to Ag deposition. Minimum surface roughness was observed at annealing temperature of 150 °C which was used in all the samples. The thin films were deposited at 3 × 10⁻⁷ torr pressure and at 100-Watt and the deposition rate was maintained at 6 Å s⁻¹. We also note that we annealed the Si substrate prior to Ag film deposition which differs from the rapid post deposition annealing process demonstrated previously [14].

Figure 1. Fabrication of smooth Ag films: (a) Silicon substrates have a native or grown Silicon oxide layer. (b) We remove the native oxide on Si substrates using HF acid treatment. (c) Annealing the Si substrate evaporates metal complexes remaining on the Si substrate. (d) Depositing Ag atoms via physical vapor deposition on a bare Si substrate increases its wettability and lead to smooth Ag films.
The surface roughness of the prepared samples was measured using Bruker Multimode-8 Atomic force microscope and surface morphology was studied by using FE-Scanning electron microscope HITACHI, S-4800. The root mean square roughness (R\text{rms}) is the square root of the all the surface heights and is sensitive to large deviations from the mean height.

Kurtosis is a dimensionless measure of the combined weight of a distribution’s tails relative to the center of the distribution. We use it here to characterize the height distribution. The Kurtosis ‘κ’ is given by

$$\kappa = \frac{1}{R_{\text{rms}}^4} \frac{1}{N} \sum_{j=1}^{N} Z_j^4$$

where N is the sum of all counts, and Z is the measured height.

3. Results and discussion

Figures 2(a)–(c) show atomic force microscopy (AFM) surface topography images over a 1 \( \mu m \times \) 1 \( \mu m \) area of a 20 nm Ag film deposited on an Si, HF/Si and annealed/HF/Si substrates, respectively. Clearly, after HF treatment the surface roughness dramatically decreases. The measured R\text{rms} is 1.63 nm, 1.13 nm, and 0.72 nm for Si, HF/Si and anneled/HF/Si substrates, respectively. Figures 2(d)–(f) show the scanning electron microscope (SEM) images of the Si, HF/Si and annealed/HF/Si substrates, respectively. For the untreated Si sample (figure 2(c)), the Ag film consists of metallic islands with large distribution of grains, irregular shapes, and multiple cracks and voids, i.e., symptoms of the VW growth mode. On the other hand, Ag deposited on HF/Si and annealed/HF/Si substrates show uniform Ag film growth with similar grain size distribution and limited voids and cracks, i.e., the film growth mode is no longer a VW growth mode.

To test the efficacy of our approach for ultrathin Ag films we deposited 5 nm thick Ag films on three substrates. Figures 2(a)–(c) show atomic force microscopy (AFM) surface topography images over a 1 \( \mu m \times \) 1 \( \mu m \) area of a 5 nm Ag film deposited on an Si, HF/Si and annealed/HF/Si substrates, respectively, which also shows a noticeable decrease in the surface roughness after HF treatment. The measured surface R\text{rms} is 1.2 nm, 0.85 nm, and 0.95 nm for Si, HF/Si and annealed/HF/Si substrates, respectively. We note that the R\text{rms} of the film deposited on the annealed/HF/Si substrate slightly increased compared to the film on HF/Si substrate. As we mentioned earlier annealing is commonly utilized to evaporate metal complexes remaining on the Si substrate [19]. On the other hand, annealing increases the R\text{rms} of the as treated Si substrate. Consequently, for thicker films the evaporation of the metal complexes facilitates the Ag adatom adhesion to the Si substrate leading to smoother Ag films. However, for thinner Ag films, e.g., with 5 nm thickness, the overall R\text{rms} can may be strongly affected by the substrate’s initial roughness. The advantage of annealing is evident, however, when considering other surface parameters, e.g., peak-to-valley size distribution and Kurtosis, as we will detail later. Figures 3(d)–(f) show the scanning electron microscope (SEM) images of the Si, HF/Si and annealed/HF/Si substrate.
substrates, respectively. For the untreated Si sample (figure 3(c)), the Ag film consists of nanoparticles, i.e., the 5 nm Ag film is below the percolation thickness. Consequently, a 5 nm Ag film is not continuous and suffers from low electronic and optical quality as observed in previous reports [3, 15]. Following HF treatment, we see interconnected grains which can allow for a decent electron transport (figure 3(e)). Depositing the 5 nm film on the annealed/HF/Si substrate leads to a continuous film with voids (figure 3(f)). These results show that our approach enables the creation of continuous, ultrathin plasmonic films.

Figure 3. Surface characterization of 5 nm Ag film. (a)–(c) AFM images of a 5 nm Ag film deposited on (a) untreated Si substrate, (b) HF/Si substrate, and (c) annealed/HF/Si substrate. A significant decrease in surface roughness is clear following HF treatment. (d)–(f) SEM images of a 5 nm Ag film deposited on (d) untreated Si substrate, (e) HF/Si substrate, and (f) annealed/HF/Si substrate. The SEM images show that prior to HF treatment the Ag film is below its percolation thickness. After HF treatment and annealing, a continuous Ag film with voids is obtained.

Figure 4. Histograms of the 2D surface-height values obtained from AFM image analysis for an Ag film with thickness (a) 5 nm, (b) 10 nm, and (c) 20 nm. The histograms are obtained for Ag films deposited on untreated Si substrates (black), HF/Si substrates (red) and annealed/HF/Si substrates (blue). The results show that the narrowest peak-to-valley height values (annotated in the figures) are obtained for Ag films deposited on annealed/HF/Si substrates.

Table 1 summarizes the main results of all the samples in terms of $R_{\text{rms}}$, peak-to-valley distribution $R_{\text{PV}}$, and Kurtosis. The surface roughness of a 10 nm Ag film is 1.73 nm (Si), 0.87 nm (HF/Si), and 0.48 nm (annealed/
Table 1. Summary of surface morphology parameters for 5 nm, 10 nm, and 20 nm Ag films deposited on Si, HF/Si, and annealed/HF/Si substrates.

| Sample Type | Peak-to-valley roughness- $R_{PV}$ (nm) | RMS roughness- $R_{rms}$ (nm) | Kurtosis $\kappa$ |
|-------------|----------------------------------------|-------------------------------|-----------------|
| 5 nm Ag- Si | 11                                     | 1.2                           | 0.4213          |
| 5 nm Ag- HF/Si | 9                                       | 0.85                          | 1.922           |
| 5 nm Ag- annealed HF/Si | 8                                       | 0.95                          | 0.9159          |
| 10 nm Ag- Si | 12                                     | 1.73                          | 0.05            |
| 10 nm Ag- HF/Si | 8                                       | 0.87                          | 3.27            |
| 10 nm Ag- annealed HF/Si | 3.8                                     | 0.49                          | 2.95            |
| 20 nm Ag- Si | 11                                     | 1.63                          | 0.0005          |
| 20 nm Ag- HF/Si | 7.3                                     | 1.13                          | 0.946           |
| 20 nm Ag- annealed HF/Si | 6.5                                     | 0.72                          | 0.32            |
| Si          | 8.5                                    | 1.49                          | 12.9            |
| HF/Si       | 2.7                                    | 0.39                          | 1.005           |
| Annealed HF/Si | 2.6                                     | 1.6                           | 2.15            |

HF/Si which is lower than what was reported previously using Ge seed layer [3] and comparable to the $R_{rms}$ of Cu seed layer [15]. We note that using an Si seed layer differs from using an Si substrate. Formica et al [15] used an Si seed layer which lead to a rougher Ag film compared to a Cu seed layer. Their results indicated that the Si atoms acted as a nucleation dot for the growth of Ag islands. On the other hand, the growth of Ag films on an Si substrate provides homogenous nucleation sites that creates smoother surfaces. Table 1 also show the Kurtosis results for all the samples. Higher Kurtosis can indicate a sharper peak which reflects the homogeneity of the grain height across the film. From table 1 and figures 4(a)–(c), we can see that annealed/HF/Si samples have considerably higher Kurtosis and sharper peaks compared to untreated Si substrates. This reflects the predominance of a single grain height for the treated samples compared to untreated samples. We note that HF/Si samples have the highest Kurtosis even when compared to annealed/HF/Si samples. This is likely due to the increase in the surface roughness of the annealed/HF/Si substrates due to annealing which can increase the relative value of the distribution’s tail relative to its peak. The surface morphology parameters for the Si, HF/Si and annealed HF/Si substrate shows that there is no direct correlation between the substrate’s morphology and the morphology of the deposited films. Finally, although HF treatment of Si can lead to the formation of Si–F and Si–H bonds, it is unlikely that this would deteriorate the Ag adhesion to the substrate since the bond dissociation energy of the Ag–F and Ag–H bonds is $H_{Ag–F} = 357$ KJ/mol and $H_{Ag–H} = 202$ KJ/mol, respectively, which is higher than that of the Ag–Ag bond $H_{Ag–Ag} = 162$ KJ/mol [16].

4. Conclusion

We showed that by removing the oxide layer existing on an Si substrate via HF acid treatment, we can significantly improve the smoothness of deposited Ag films in terms of decreasing the surface rms roughness, narrowing the peak-to-valley roughness distribution, and increasing the distribution Kurtosis. Our method compliments other existing methods to increase the quality of plasmonic and metallic films deposited on Silicon substrates. Indeed, we provide a more economic method as we do not require using any seed layer. Moreover, our method is CMOS compatible and avoid introducing additional materials, e.g., Ge, which may degrade the optical properties of the deposited film. While we do not expect this method to be used for multilayer structures as in the case of using Ge seed layer [14], we expect that it can at least decrease the overall roughness of the multilayer structure without jeopardizing its optical properties. From a practical standpoint, we recommend the annealing and HF treatment in general. However, for applications where minimizing the surface roughness is crucial, avoiding the annealing process for 5 nm films is recommended. Using other acids that remove the oxide layer is also possible. Our method can certainly be extended to other plasmonic metals. For instance, we expect that our method would increase the smoothness of gold (Au) films. The bond dissociation energy of Au–Au atoms is $H_{Au–Au} = 226.2$ KJ/mol while for Au–Si atoms it is $H_{Au–Si} = 304.6$ KJ/mol [16].

Acknowledgments

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