Advanced control scheme of a unified power flow controller using sliding mode control

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ABSTRACT
This paper presents an advanced control scheme based on sliding mode control of a unified power flow controller (UPFC). This controller can generate a number of benefits in terms of static and dynamic operation of the power system such as the control law is synthesized with two kinds of controllers: sliding mode controller (SMC), and proportional integral (PI). Their respective performances are compared in terms of reference monitoring, sensitivity to disturbances and robustness. We have to study the problem of controlling power in electric system by UPFC. The simulation results show the effectiveness of the proposed strategy especially in chattering-free behavior, response to sudden load variations and robustness. All the simulations for the above work have been carried out using MATLAB/Simulink. Various simulations have given very satisfactory results and we have successfully improved the active and reactive power flows on a line of transmission, as well as to control voltage at the bus where it is connected, the studies and illustrate the effectiveness and capability of UPFC in improving power.

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1. INTRODUCTION
In recent years, the electrical power distribution system are suffering as of significant power flow quality (PQ) problems, which are characterized using low power factor, poor voltage quality, voltage stability, supply interruptions, and load unbalancing. These power profile issues have attracted attention to the researchers in the academic and industrial sectors. This difficulty and augment in power supply needs providing the system with recent control systems which contribute to the improvement of the electrical grids performances [1]. The use of flexible AC transmission system (FACTS) controllers in power system has been of worldwide interest for enhancing power system controllability and stability and increasing the power transfer capability due to their flexibility and speed. In addition, converter-based FACTS controllers are capable of independently controlling the active and reactive power flow in the power system [2].

UPFC is the member of FACTS device. It is the mainly adaptable and powerful FACTS device [3]. The fundamental theory of UPFC is that, the magnitude of voltage affects flow of reactive power and the phase angle affects flow of real power [4, 5]. The UPFC consists of two other FACTS devices: the Static Synchronous Compensator (STATCOM) and the Static Synchronous Series Compensator (SSSC), the SSSC injects a almost sinusoidal voltage, of variable magnitude in series using the system voltage which
provides the majority cost effective solution to mitigate voltage sags through improving power quality level that is required with a customer. While, the STATCOM is joined or connected with a common DC link capacitor. It can, at the same time, to UPFC bus voltage/shunt reactive power control, in addition perform the function of transmission line real/reactive power flow control [6].

Though UPFC implies many advantages, but its controller design still being a matter of challenge since it is a multi-variable controller. In literature, many works have been presented by different control schemes of UPFC for various power system applications. Recently, the sliding mode control method has been extensively employed for control of nonlinear systems. Several papers have been published based on SMC of UPFC [7-9]. The classical SMC is super in terms of parameters variation, response time, external disturbances. In addition, it ensures convergence in finite time and it is very simple to be implemented in any low cost material [10].

This article discusses the competence of UPFC on controlling independently active and reactive powers into the power transmission line, the improvement of the transient and dynamic stability of the power system using the UPFC. Using two kind of controllers PI and SMC, active and reactive powers are controlled. Their performances are analysed and compared in terms of reference tracking, sensitivity to robustness plus perturbations.

2. STRUCTURE OF THE UPFC

For many years, UPFC is considered as the most versatile of the FACTS device one that can be used to enhance steady state stability, dynamic stability and transient stability, which combines the good features of STATCOM and SSSC. Figure 1 shows the basic structure of UPFC which is consists of two voltage sourced converters (VSC) and employed to give galvanic isolation and regulate the voltage levels in the supply system. It is consisted of two PWM (Pulse Width Modulation) inverters, which are operated from a DC link provided by a DC storage capacitor. The first inverter is coupled in parallel while the second one is placed in series with the transmission line [11]. The detailed structure and the functionality of the UPFC can be found in [12].

![Figure 1. Basic structure of the UPFC](image)

3. THE UPFC MODEL

Figure 2 represents the simplified model circuit of the UPFC. The UPFC mathematical model can be divided into three sets of equations, namely: the equations of the parallel branch, the equations of the series branch and those of the continuous one.

![Figure 2. Equivalent circuit of the UPFC](image)
By applying Kirchhoff's law on the circuit of Figure 2, we can write the following system of equations:

\[
\frac{d}{dt} \begin{bmatrix} I_{sb} \\ I_{sc} \end{bmatrix} = \begin{bmatrix} -\frac{r}{L} & 0 & 0 \\ 0 & -\frac{r}{L} & 0 \\ 0 & 0 & -\frac{r}{L} \end{bmatrix} \begin{bmatrix} I_{sb} \\ I_{sc} \end{bmatrix} + \begin{bmatrix} 1 \\ 0 \\ 0 \end{bmatrix} \begin{bmatrix} V_{sa} - V_{ca} - V_{ra} \\ V_{sb} - V_{cb} - V_{rb} \\ V_{sc} - V_{cc} - V_{rc} \end{bmatrix} \] (1)

By applying Park's transformation to the system (1), we can write:

\[
\frac{d}{dt} \begin{bmatrix} I_{sd} \\ I_{sq} \end{bmatrix} = \begin{bmatrix} -\frac{r}{L} + \omega & 0 \\ 0 & -\frac{r}{L} \end{bmatrix} \begin{bmatrix} I_{sd} \\ I_{sq} \end{bmatrix} + \begin{bmatrix} 1/L & 0 \\ 0 & 1/L \end{bmatrix} \begin{bmatrix} V_{sd} - V_{cd} - V_{rd} \\ V_{sq} - V_{cq} - V_{rq} \end{bmatrix} \] (2)

3.1. Modeling of the UPFC shunt branch
The complete mathematical model of the UPFC shunt can be established similarly using the following matrix:

\[
\frac{d}{dt} \begin{bmatrix} I_{pa} \\ I_{pb} \\ I_{pc} \end{bmatrix} = \begin{bmatrix} -\frac{r_p}{L_p} & 0 & 0 \\ 0 & -\frac{r_p}{L_p} & 0 \\ 0 & 0 & -\frac{r_p}{L_p} \end{bmatrix} \begin{bmatrix} I_{pa} \\ I_{pb} \\ I_{pc} \end{bmatrix} + \begin{bmatrix} 1/L_p & 0 & 0 \\ 0 & 1/L_p & 0 \\ 0 & 0 & 1/L_p \end{bmatrix} \begin{bmatrix} V_{pa} - V_{ca} - V_{ra} \\ V_{pb} - V_{cb} - V_{rb} \\ V_{pc} - V_{cc} - V_{rc} \end{bmatrix} \] (3)

Since the system is assumed to be a balanced one, it can be transformed into a synchronous d-q-o frame by applying Park’s transformation. The matrix form (3) is given as follows:

\[
\frac{d}{dt} \begin{bmatrix} I_{pd} \\ I_{pq} \end{bmatrix} = \begin{bmatrix} -\frac{r_p}{L_p} + \omega & 0 \\ 0 & -\frac{r_p}{L_p} \end{bmatrix} \begin{bmatrix} I_{pd} \\ I_{pq} \end{bmatrix} + \begin{bmatrix} 1/L_p & 0 \\ 0 & 1/L_p \end{bmatrix} \begin{bmatrix} V_{pd} - V_{cd} - V_{rd} \\ V_{pq} - V_{cq} - V_{rq} \end{bmatrix} \] (4)

3.2. The modeling of the UPFC continues branch
For the DC-side circuit, based on the power balance equation in the output and input of UPFC, the net real power exchanged by both the converters through DC side should be zero to keep the capacitor voltage constant [13]. The DC voltage \( V_{dc} \) dynamics across the capacitor is given by the following equation:

\[
\frac{dv_{dc}}{dt} = \frac{1}{c_{dc}} (P_e - P_{ep}) \] (5)

With:

\[
P_e = v_{ca}i_{sa} + v_{cb}i_{sb} + v_{cc}i_{sc}
\]

\[
P_{ep} = v_{pa}i_{pa} + v_{pb}i_{pb} + v_{pc}i_{pc}
\]

With:

\( P_e \): active power absorbed of the AC system
\( P_{ep} \): active power injected with the shunt inverter

AC system.

By performing Park transformation, the DC voltage \( V_{dc} \) dynamics across the capacitor can be described by the following equations.

\[
\frac{dv_{dc}}{dt} = \frac{2}{2c_{dc}} (v_{pd}i_{pd} + v_{pq}i_{pq} - v_{cd}i_{rd} - v_{cq}i_{rq}) \] (6)

4. CONTROL STRATEGY OF THE UPFC
4.1. Control of the parallel converter
The ordinary working principle of the parallel compensation of STATCOM is described as follows: active power control which means stabilizing the capacitor voltage of the DC side; reactive power control which means stabilizing the terminal voltage [14]. According to the system of (2) the control strategy of parallel compensation (STATCOM) is decoupling of the two current loops control, to decrease the coupling between the active and reactive powers. Block diagram of the control system is shown by Figure 3.
4.2. Control of the series converter

The SSSC regulate the active and reactive power flow on the transmission line where the UPFC is installed by injection voltage of which the amplitude and the phase both can be adjusted. The control strategy of the series compensator is decoupling of the two current loops control. The diagram of control circuits of SSSC is given in the Figure 4. In this part, we have decided to evaluate the performances of the UPFC by two diverse controllers: PI and SMC.

4.3. PI controller

The PI controller is easy to develop. Figure 5 illustrates the two loops of our system (current and voltage), controlled by this type of regulator. In this figure, \( k_p \) and \( k_i \) successively represent proportional and integral gains of the PI controllers. These two gains are calculated using the pole compensation method where the system response time has been chosen equal to \( \tau = 5ms \). The calculated terms are given in Table 1.
Table 1. Optimal parameters of the proposed controllers

| Controller series part | Controller shunt part | Controller continues branch |
|-------------------------|------------------------|----------------------------|
| $K_p$ | $\frac{1}{s}$ | $\frac{1}{s} \frac{R_1}{C_1}$ | $\frac{1}{s} \frac{C_2}{\omega^2}$ |
| $K_i$ | $\frac{R_1}{(s)} K_p$ | $\frac{1}{s} \frac{C_2}{\omega}$ | $\frac{1}{s} \frac{C_1}{\omega} \xi_\omega$ |

It should be noted that the pole compensation method is not the only method existing in the literature for calculating the gains of the PI regulator. But it was used because of its simplicity.

4.4. Sliding mode controller (SMC)

SMC is one of the effective non linear robust control approaches since it provides system dynamics by an invariance property to uncertainties once the system dynamics are controlled in the sliding mode [15-17]. The main feature of SMC is that it just requires driving the error to a switching surface it consists of three parts Figure 6.

![Figure 6. Phase portrait of SMC](image)

a. The switching surface choice

In order to allow the application of the command by sliding mode, it is mandatory that the controlled system must be written in the following canonical form [18].

\[ \dot{x} = f(x, t) + B(x, t)V(x, t) \]
\[ x \in \mathbb{R}^n, V \in \mathbb{R}^m, \text{ran}(B(x, t)) = m \]

Where: $f(x, t), B(x, t)$ are two continuous and uncertain non-linear functions, supposed limited.

We take the general equation to determine the sliding surface, proposed by J.J. Slotine [19-20], given by

\[ S(X) = \left( \frac{d}{dt} + \lambda \right)^{n-1} e; e = x^* - x \]

Where: $e$ is the error on the signal to be adjusted; $\lambda$ a positive coefficient; $n$ system order; $x^*$: desired signal; $x$: state variable of the control signal.

b. Convergence condition

The convergence condition is defined by the Lyapunov equation [13]; it makes the surface attractive and invariant

\[ S \cdot \dot{S} < 0 \]

(9)

c. Control Calculation

The control algorithm is defined by the relation [15]

\[ V^{\text{com}} = V^{eq} + V^n \]

(10)

Here $V^n$ is the correction factor, $V^{eq}$ is the equivalent control vector, $V^{\text{com}}$ is the control vector which must be calculated in order that the stability conditions for the selected control are satisfied.

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\[ V^n = K \text{sat}(S(X)/\delta) \] (11)

\[
\text{sat}(S(X)/\delta) = \begin{cases} 
\text{sign}(S) \text{ if } |S| > \delta \\
S/\delta \text{ if } |S| < \delta 
\end{cases}
\] (12)

Here, \( \text{sat}(S(X)/\delta) \) is the proposed saturation function, \( \delta \) is the boundary layer thickness.

In our study, the errors between the references and measured \( I_d \) and \( I_q \) currents have been chosen as sliding mode surfaces, so we can write the following relation:

\[
\begin{align*}
\dot{s}_d &= I_{sd,ref} - I_{sd} \\
\dot{s}_q &= I_{sq,ref} - I_{sq}
\end{align*}
\] (13)

The first order derivative of (8), one obtains

\[
\begin{align*}
\dot{s}_d &= I_{sd,ref} - \omega I_{sq} + \frac{L}{L_s} I_{sd} - \frac{1}{L} (v_{sd} - v_{cd} - v_{rd}) \\
\dot{s}_q &= I_{sq,ref} + \omega I_{sd} + \frac{L}{L_s} I_{sq} - \frac{1}{L} (v_{sq} - v_{cq} - v_{rq})
\end{align*}
\] (15)

Replacing the expression of \( v_{cd} \) and \( v_{cq} \) in (10) by their expressions given in (15), one obtains

\[
\begin{align*}
\dot{s}_d &= I_{sd,ref} - \omega I_{sq} + \frac{L}{L_s} I_{sd} - \frac{1}{L} (v_{sd} - (v^n_{cd} + v^e_{cd}) - v_{rd}) \\
\dot{s}_q &= I_{sq,ref} + \omega I_{sd} + \frac{L}{L_s} I_{sq} - \frac{1}{L} (v_{sq} - (v^n_{cq} + v^e_{cq}) - v_{rq})
\end{align*}
\] (16)

\( I_{iq} \) will be the component of the control vector used to constraint the system to converge to \( S=0 \).

The control vector \( \mathbf{v}^{eq} \) is obtain with imposing \( \dot{S} = 0 \) so the equivalent control components are given with the following expression

\[
\begin{align*}
v_{cd}^{eq} &= -L_i I_{sd,ref} + L_i I_{sq} - r I_{sd} + v_{sd} - v_{rd} \\
v_{cq}^{eq} &= -L_i I_{sq,ref} - L_i I_{sd} - r I_{sq} + v_{sq} - v_{rq}
\end{align*}
\] (17)

Using the same procedures as for part shunt we get the following expression:

\[
\begin{align*}
v_{pq}^{eq} &= -L_p I_{pd,ref} + L_p I_{pd} - r_p I_{pd} + v_{pd} - v_{rd} \\
v_{cq}^{eq} &= -L_p I_{pq,ref} - L_p I_{pd} - r_p I_{pd} + v_{pq} - v_{rq}
\end{align*}
\] (18)

To obtain high-quality performances, dynamic an commutation around the surface, the control vector is imposed as follows [17]

\[ v = \mathbf{v}^{eq} + K \cdot \text{sign}(S) \] (19)

The sliding mode will exist only if the following condition is met:

\[ S \cdot \dot{S} < 0 \] (20)

5. SIMULATION RESULTS

This session is devoted to the numerical simulation using MATLAB/SIMULINK of a UPFC coupled to a 220V/50Hz grid. The parameters of the system used in the simulation are given in Table 2.

Three simulation tests were performed in this session to examine the performance of the controllers used.
5.1. Reference tracking test

The objective of this test has for goal the study of the two controllers (PI and SMC) behavior in reference tracking. Figure 7 shows the obtained results. As shown by this figure, we can notice that the powers follow almost perfectly their references for the two types of controllers with a very reduced response time for the SMC controller compared to the PI regulator.

5.2. Robustness

We tested the robustness of the used controllers for a variation of the reactance $X_L$. The results presented in Figure 8 show that this variation presents a clear effect on the active and reactive powers for the system with PI controller contrary to the system with SMC where this variation have almost no effect on active and reactive powers. Thus, it can be concluded that this last is robust against this parameter variation.

5.3. Sensitivity to a sub-voltage perturbation

The purpose of this test is the evaluation of the controllers used againsts a sub-voltage effect (+50%), applied between the instants $t = 0.6s$ and $t = 0.62s$. The obtained simulation results are exposed in Figure 9. Through this figure, we notice that the applied disturbance (sub-voltage) caused an almost negligible effect on the power curves for the two types of controllers used. Ce résultat est intéressant pour le système UPFC qui peut être soumis à des effets pareils.

| Table 2. UPFC parameters used for simulation |
|---------------------------------------------|
| Parameter name   | Symbol | Value | Unit  |
| Network voltage  | $V_r$   | 220   | V     |
| Voltage of the receiver | $V_s$   | 220   | V     |
| DC voltage       | $V_{dc}$ | 280   | V     |
| Network frequency| $f$     | 50    | Hz    |
| The capacity of the | $C$     | 2     | mF    |
| common circuit DC|         |       |       |
| Inductance 1     | $L$     | 1.125 | mH    |
| Resistance 1     | $r$     | 100   | Ω     |
| Inductance 2     | $L_p$   | 1.125 | mH    |
| Resistance 2     | $r_p$   | 100   | Ω     |

Figure 7. Reference tracking test
6. CONCLUSION

This article was dedicated to the application of a robust control method based on the sliding mode for the control of a UPFC system. A comparative study by numerical simulation has clearly shown the superiority of the control used (SMC) compared to the conventional control based on PI regulators especially in the robustness test and the decoupling between active and reactive powers. Basing on the obtained results, the UPFC device based on SMC, can adjust the distribution of the system power flow between the transmission lines quickly and smoothly, and does not have a significant impact on the other operating parameters of the system.

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