SUPERVISORY FAULT TOLERANT CONTROL WITH INTEGRATED FAULT DETECTION AND ISOLATION: A SWITCHED SYSTEM APPROACH

HAO YANG *, BIN JIANG *, VINCENT COCQUEMPOT **, LINGLI LU *

* College of Automation Engineering
Nanjing University of Aeronautics and Astronautics, 29 YuDao Street, Nanjing, China
e-mail: [haoyang,binjiang]@nuaa.edu.cn, linglanzhishui@163.com

** LAGIS Laboratory, UMR CNRS 8219
Lille 1 University: Sciences and Technologies, 59655 Villeneuve d’Ascq, France
e-mail: vincent.cocquempot@univ-lille1.fr

This paper focuses on supervisory fault tolerant control design for a class of systems with faults ranging over a finite cover. The proposed framework is based on a switched system approach, and relies on a supervisory switching within a family of pre-computed candidate controllers without individual fault detection and isolation schemes. Each fault set can be accommodated either by one candidate controller or by a set of controllers under an appropriate switching law. Two aircraft examples are included to illustrate the efficiency of the proposed method.

Keywords: fault tolerant control, fault detection and isolation, switching control, switched systems.

1. Introduction

Fault Detection and Isolation (FDI) and Fault Tolerant Control (FTC) are aimed at guaranteeing the primary system goal to be achieved in spite of faults (Patton et al., 2000; Blanke et al., 2006; Zhang and Jiang, 2008; Yang et al., 2010). The potential faults in a complex system often range over a very large region. A single controller (even an adaptive one) is often hard to design to stabilize all faulty situations effectively. General supervisory FTC approaches assume that the plant model belongs to a pre-specified set of models, including the nominal situation and all possible faulty situations, and that there exists a finite family of candidate controllers such that the faulty system is stabilized when controlled by at least one of those candidate controllers (Staroswiecki and Gehin, 2001; Parisini and Sacone, 2001).

The classical supervisory FTC approach, as shown by Fig. 1, follows three steps: (1) detect the occurrence of a fault; (2) identify the current fault situation; (3) switch to the related controller. There are three limitations behind such a framework:

L1. An individual fault detection scheme is required, which often relies on a set of residuals. It is well known that an unappropriate residual may lead to a false alarm or a missed detection (Patton et al., 2000). This also introduces a detection delay during which the faulty system is controlled by the original controller, the stability may be violated, or some unexpected behaviors may appear.

L2. A bank of filters/models has to be designed and work in parallel with the plant to identify the current fault (Zhang et al., 2008). This makes the FTC system complicated. An identification delay exists during which the faulty system is still controlled by the original controller. Stability may also be violated. Moreover, designing these filters often requires some structure conditions on the plant. The actual fault may be ill-isolated, possibly leading to fatal consequences.

L3. Each possible fault set can be accommodated by at least one of the candidate controllers. However, some complex faults are often difficult to be accommodated by only one controller.

In this work, we propose a new supervisory FTC scheme as shown in Fig. 2, where FDI and FTC are integrated via a switching algorithm. Controllers are sequentially switched until the appropriate one is found, and the
fault isolation boils down to finding the correct controller, which can be directly applied once selected. The switching delay in setting the correct controller still exists, but there is no individual detection and isolation algorithm, which can be directly applied once selected. The switching delay in setting the correct controller still exists, but there is no individual detection and isolation algorithm, which makes the scheme simpler and more easily verifiable. Moreover, the switching delay can be controlled according to the design parameters, while the state remains bounded during this delay as will be shown.

The proposed approach relaxes L1–L3 and has two good features:

1. FDI and FTC are integrated via a control switching algorithm. Individual detection and isolation schemes are not needed. Thus, the delay of detection and isolation is avoided. A switching delay exists during which the system remains stable.

2. Each faulty system is allowed not to be stabilized by one individual controller but can be stabilized by a set of controllers under an appropriate switching law.

Fig. 1. Classical supervisory FTC framework.

The key condition of realizing such an integrated FDI/FTC scheme is to guarantee the stability of the system in the pre-fault period, the FDI/FTC period and the post-FTC period by controller switching. In fact, a system under switching within a family of pre-computed candidate controllers can be described naturally by a switched system, since each mode of the switched system could represent one of the control configurations, while a switching from one configuration to another one is described using a switching function. Consequently, the system stability during the switching period of controllers is equivalent to the stability of the switched system. Our proposed supervisory FTC framework is based on stability criteria of the switched system with unstable modes.

The remainder of the paper is organized as follows. Section 2 gives some preliminaries. Section 3 discusses supervisory FTC with relaxation of L1–L2, while Section 4 focuses on the relaxation of L1–L3, followed by some concluding remarks in Section 5.

2. Preliminaries

In the following, let $\mathbb{R}$ denote the field of real numbers, $\mathbb{R}^r$ the $r$-dimensional real vector space and $| \cdot |$ the Euclidean norm. Class $\mathcal{K}$ is a class of strictly increasing and continuous functions $[0, \infty) \rightarrow [0, \infty)$ which are zero at zero. Class $\mathcal{K}_{\infty}$ is the subset of $\mathcal{K}$ consisting of all those functions that are unbounded. Furthermore, $\beta : [0, \infty) \times [0, \infty) \rightarrow [0, \infty)$ belongs to the class $\mathcal{KL}$ if $\beta(s, t)$ is of class $\mathcal{K}$ for each fixed $t \geq 0$ and $\beta(s, t)$ decreases to 0 as $t \rightarrow \infty$ for each fixed $s \geq 0$. Moreover, $t^-$ denotes the left limit time instant of $t$. Finally, $(\cdot)^\top$ is the transposition.

The system considered takes the general nonlinear form

$$\dot{x}(t) = G\left(x(t), u(t), f(u(t), x(t))\right)$$

(1)

with measurable states $x \in \mathbb{R}^n$ and inputs $u \in \mathbb{R}^p$. $G$ is smooth. Process and/or actuator faults are represented by the function $f : \mathbb{R}^p \times \mathbb{R}^n \rightarrow \mathcal{F}$, where $\mathcal{F} \subset \bigcup_{i \in N = \{1, \ldots, N\}} \mathcal{F}_i \subset \mathbb{R}^p$ and $\mathcal{F}_i$ is the $i$-th set of fault vectors. $N$ is a finite number, the fault free operation is $\mathcal{F}_N = \{0\}$.

The property that we wish to be invariant under the faults in $\mathcal{F}$ is that the system (1) remains stable whatever the fault $i \in \mathcal{N}$ and whenever it occurs, i.e., for any $\epsilon > 0$, there exists a $\delta > 0$ such that $|x(t)| \leq \epsilon, t \geq 0$, whenever $|x(0)| \leq \delta$.

Suppose that there are $\omega$ pre-computed candidate controllers for the supervision purpose and $\omega > 0$ is a finite number. Define $\Omega \triangleq \{1, 2, \ldots, \omega\}$. Write $u_i(t)$ for the signal the $i$-th controller, $i \in \Omega$.

The system (1) under controller switching among $u_i(t), i \in \Omega$, can be rewritten as the following switched system:

$$\dot{x}(t) = G\left(x(t), u_{\sigma(t)}(t), f(u_{\sigma(t)}(t), x(t))\right).$$

where $\sigma(t) : [0, \infty) \rightarrow \Omega$ denotes the switching function, which is assumed to be piecewise constant and continuous.

Fig. 2. New supervisory FTC framework.
from the right. It is clear that the original system under one of the controllers can be regarded as one of the modes of the switched system.

3. Supervisory FTC: Relaxation of L1–L2

For the sake of simplicity, it is assumed in this section that \( \Omega = N \), i.e., each control law \( u_i \) is associated with a fault \( i, i \in N \).

Assumption 1. For any \( i, j \in N \), there exists a family of continuous non-negative functions \( V_i(x) : \mathbb{R}^n \to \mathbb{R}_0^+ \), and functions \( \alpha_1, \alpha_2 \in \mathbb{K}_\infty, \lambda_0, \lambda_1 > 0, \mu_0 \geq 1 \) such that

\[
\alpha_1(|x|) \leq V_i(x) \leq \alpha_2(|x|),
\]

\[
u = u_i, f \in \mathcal{F}_i \implies \dot{V}_i(x) \leq -\lambda_0 V_i(x),
\]

\[
u = u_i, f \in \mathcal{F}_j, j \neq i \implies \dot{V}_i(x) \leq \lambda_1 V_i(x),
\]

\[
V_i(x) \leq \mu_0 V_j(x).
\]

Assumption 1 implies that, for faults \( f \in \mathcal{F}_i \), the controller \( u_i(t) \) stabilizes the plant as in (3). For faults \( f \notin \mathcal{F}_i \), \( V_i \) may increase, which implies that \( x \) may escape to a large region or infinity as in (4). Section 4 will consider the case that no individual controller satisfies the specification (3) for the faulty plant.

The inequalities (2)–(5) distinguish the system’s behavior under different controllers, which will play the key role in supervisory FDI/FTC. Other forms of \( V_i \) than (2)–(4) can also be defined, e.g., the dissipative form (Jiang et al., 2010) or the \( \mathbb{K}_\infty \) function form (Yang et al., 2009).

3.1. Fault detection. The initial system is always regarded as a fault-free system, i.e., \( f, f \in \mathcal{F}_N = \{0\} \), the applied controller being always \( u_N \) at \( t = 0 \). The system under the controller \( u_N \) satisfies (3). In the presence of full state measurements, the inequality (3) can be naturally used as a time-varying residual to detect the fault as follows:

\[
V_N(x(t)) > e^{\lambda_0 t} V_N(x(0)) \implies \text{Fault occurs.}
\]

Fault detection can be achieved by the controller itself without requiring any individual fault detection scheme. The faults that do not violate (3) with \( i = N \) are not necessary to be detected since they do not destroy stability. Denote \( t_{fd} \) as the first time at which the inequality (3) is violated. Note that \( x(t_{fd}) \) is still bounded.

Such a fault detection scheme is available even when the fault occurs from the beginning. In that case, (3) may be violated at the beginning and the fault is detected.

3.2. Fault isolation and supervisory FTC. For a switched system, to avoid arbitrary fast switchings, a “dwell-time” \( \tau > 0 \) is often required such that the period between any two switching instants is no less than \( \tau \). This implies that there is a finite number of switchings on any finite time interval. Such a “dwell-time” is also involved among controller switchings.

A performance based controller switching law is designed as follows.

Algorithm 1.

1. Set \( t_0 = t_{fd} \). Let \( s = 0 \). Define \( \Omega^* = \Omega - \{\sigma(t_f)\} \). Set \( \sigma(t_0) = i^* \), where

\[
i^* = \arg\max_{i \in \Omega^*} J_i(x(t_0), t_0).
\]

2. Choose \( t_{1+s} = t_s + \tau \). If

\[
\dot{V}_{\sigma(t_s)}(x(t_{1+s})) \leq -\lambda_0 V_{\sigma(t_s)}(x(t_s))
\]

then apply the controller \( u_{\sigma(t_s)}(t) \), \( \forall t \geq t_{1+s} \). Stop the switching.

Otherwise, go to Step 3.

3. Let \( \Omega^* = \Omega^* \setminus \{\sigma(t_s)\} \). Set \( \sigma(t_{1+s}) = i^* \), where

\[
i^* = \arg\max_{i \in \Omega^*} J_i(x(t_{1+s}), t_{1+s}).
\]

Apply the controller \( u_{\sigma(t_{1+s})}(t) \) at \( t = t_{1+s} \).

Let \( s = s + 1 \). Go to Step 2.

The main idea behind Algorithm 1 is illustrated in Fig. 3. At each switching instant, we select the next controller that optimizes the given cost function from the set \( \Omega^* \). If this is the correct controller, then we apply it and then stop the switching (Step 2). Otherwise, we remove this destabilizing controller from \( \Omega^* \) (Step 3). The inequalities (2) and (4) are essentially “filters” for the overall FDI/FTC design (see Step 2) rather than FDI only.

The transient behavior during the switching delay obviously depends on \( J_i(x(t), t), i \in M^* \). A few examples of relevant costs are given:

1. \( J_i(x(t), t) \) is the probability that fault \( i \) occurs in state \( x(t) \) at time \( t \). The switching policy selects the most likely fault mode.

2. \( J_i(x(t), t) \) is a control cost that we wish to minimize if fault mode \( i \) occurs. If the state escapes far away from its nominal trajectory, the control cost to bring back to its reference trajectory may be very large. The switching policy assumes that the worst fault mode has occurred, and selects first the associated control. The sooner the worst situation is recognized, the smaller the risk of an excessive control cost. On the contrary, the “optimistic” switching policy \( i^* = \arg\min_{i \in M^*} J_i(x(t), t) \) is based on the occurrence of the best fault mode.

3. \( i^* = \text{Random} \{i \in M^*(t)\} \) is always a possible option if no cost function can be elaborated.
Theorem 1. Consider the system \( \dot{x} = f(x, u) \) and a family of controllers satisfying Assumption 1. Suppose that a fault \( f \in \mathcal{F}_i, i \in \mathbb{N} \) occurs at \( t = t_f \). The fault detection law (6) and Algorithm 1 guarantee the stability of the origin.

Proof. Under Algorithm 1, at most \( N-1 \) switchings occur before the controller \( u_i(t) \) related to \( f \in \mathcal{F}_i \) is applied. We consider the worst case, i.e., \( \sigma(t_{N-2}) = i \). The results for other cases are easily obtained.

For two time instants \( t \) and \( t_0 \), from (3) and (4) that it follows

\[
V_i(x(t)) \leq e^{-\lambda_0(t-t_0)}V_i(x(t_0)), \quad t > t_0 \tag{7}
\]

\[
u = u_i, f \in \mathcal{F}_i \implies V_i(x(t)) \leq e^{\lambda_1(t-t_0)}V_i(x(t_0)). \tag{8}
\]

Consider \( t \in [t_{N-2}, \infty) \). Based on (5), (7) and (8), we further have

\[
V_i(x(t)) \\
\leq \mu_0 e^{-\lambda_0(t-t_{N-2})}V_{\sigma(t_{N-2})}(x(t_{N-2})) \\
\leq \mu_0^2 e^{-\lambda_0(t-t_{N-2})}e^{\lambda_1 \tau}V_{\sigma(t_{N-3})}(x(t_{N-3})) \\
\vdots \\
\leq \mu_0^{N-1} e^{-\lambda_0(t-t_{N-2})}e^{(N-2)\lambda_1 \tau}V_{\sigma(t_0)}(x(t_0)). \tag{9}
\]

Since \( N \) and \( \tau \) are bounded, so is \( x(t_0) = x(t_{fd}) \). From (9) it follows that \( |x(t)|, t \geq t_{fd} \) is always bounded and \( \lim_{t \to \infty} x(t) = 0 \). This completes the proof.

The switching process resulting from Algorithm 1 is equivalent to a switched system where some unstable modes are activated one by one and finally a stable mode is activated. Therefore, the proof of Theorem 1 essentially relies on the stability analysis of the equivalent switched system.

As for a switched system with unstable modes, the overall stability can be guaranteed if the activating period of stable modes is long enough compared with that of unstable modes (Yang et al., 2009). Correspondingly, the stability under Algorithm 1 can be guaranteed if the activating period of destabilizing controllers is short enough (each destabilizing controller is activated for a minimal period \( \tau \)), and the stabilizing controller is activated long enough (being always activated after it is selected) as shown in Fig. 4. The switching algorithms related to the dissipative form and \( K_\infty \) function forms of \( V_i \) can be found respectively in the works of Jiang et al. (2010) and Yang et al. (2009).

| Case 1 | \( k_1 \in [1, 1.5], k_2 = k_3 = 1 \) |
| Case 2 | \( k_2 \in (1, 1.5], k_1 = k_3 = 1 \) |
| Case 3 | \( k_3 \in [0.8, 1), k_1 = k_2 = 1 \) |
| Case 4 | \( k_1 = k_2 = k_3 = 1 \) |

### 3.3. Aircraft example

Fast and accurate flight control reconfiguration is of paramount importance for increasing aircraft survivability. The aircraft longitudinal differential equations under a small attack angle are expressed as (Mu et al., 2008)

\[
\begin{align*}
\dot{\theta} &= \omega, \\
\dot{\omega} &= k_1 \eta^0 \omega + k_2 \eta^0 \cos(\theta - \alpha) + k_3 \eta \nu,
\end{align*}
\]

where the states \( x = [\theta, \omega, \eta, \dot{\eta}, \dot{\omega}, \dot{\theta}]^T \) denote the pitch angle and the pitch rate, respectively. Here \( \alpha \) denotes the small attack angle. The input \( u \) is the elevator deflection angle. \( \eta^0 \) and \( \eta \) are longitudinal dynamics parameters chosen as \( \eta^0 = 20 \) (1/s), \( \eta = -5 \) (1/s²), \( \eta = -50 \) (1/s³). Finally, \( k_1, k_2 \) and \( k_3 \) are fault coefficients. In the healthy situation, \( k_1 = k_2 = k_3 = 1 \).

Tables 1 and 2 describe system situations and their corresponding controllers.

Cases 1 and 2 deal with process faults, Case 3 is related to the actuator one, and Case 4 is a healthy si-
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Table 2. Candidate controllers.

\[
\begin{align*}
  u_1 &= \frac{1}{\eta}[-1.5\eta^5\omega - \eta^8 \cos(\vartheta - \alpha) - 5\omega - 5\theta]
  
  u_2 &= \frac{1}{\eta}[-\eta^5\omega - \sgn(\omega)\eta^8 \cos(\vartheta - \alpha) - 5\omega - 5\theta]
  
  u_3 &= \frac{1.25}{\eta}[-\eta^5\omega - \sgn(\omega)\eta^8 \cos(\vartheta - \alpha) - 5\omega - 5\theta]
  
  u_4 &= \frac{1}{\eta}[-\eta^5\omega - \eta^8 \cos(\vartheta - \alpha) - 5\omega - 5\theta]
\end{align*}
\]

ulation. Consequently, we divide \(\mathcal{F}\) into four parts as \(\mathcal{F} \subset \bigcup_{i \in \mathcal{M} = \{1, 2, \ldots, 4\}} \mathcal{F}_i\), where \(\mathcal{F}_i\) is related to the fault values in Case \(i\). \(\mathcal{F}_4\) denotes the fault-free situation.

In the simulation, suppose that Case 1 occurs at \(t = 1.5\) s. Set \(k_1 = 1.5\). Define \(V(x) = x^TPx\), with

\[
P = \begin{bmatrix} 11 & 1 \\ 1 & 1.2 \end{bmatrix}.
\]

We further have

\[
\dot{V}(x) \leq -10V(x), \quad \forall f \in \mathcal{F}_1, u = u_1,
\]

\[
\dot{V}(x) \leq 17.6205V(x), \quad \forall f \in \mathcal{F}_1, u = \{u_2, u_3, u_4\},
\]

which satisfy Assumption 1. The system under the faulty Case 1 is stabilized only by the controller \(u_1(x)\). Suppose that the initial states are \([0.1 \text{ (rad)} \ 0.2 \text{ (rad/s)}]^{\top}\). Figure 5 shows that the fault is detected rapidly at \(t = 1.5\) s using the threshold (6).

![State trajectories](image)

Fig. 5. Fault detection.

Now we apply Algorithm 1 to achieve the FTC objective. Given the cost function \(J_i(x(t)) = \int_0^t x^2(s) + 0.1u_i^2(s) ds\), the optimal switching sequence obtained is \(u_2 \rightarrow u_3 \rightarrow u_1\). Choose \(\tau = 0.5\) s. Then \(u_2(x)\) is applied at \(t = 1.5\) s, and switches to \(u_3(x)\) at \(t = 2\) s. Then \(u_1(x)\) is selected and applied at \(t = 2.5\) s. The fault is identified to be Case 1. The correct controller \(u_1(x)\) is then applied for \(t \geq 2.5\) s. Figure 6 shows the state and input trajectories. It can be seen that the FTC goal is achieved, the states are always bounded, and the control magnitude is not large.

![State trajectory](image)

Fig. 6. State and input trajectories.

4. Supervisory FTC: Relaxation of L1–L3

The supervisory FTC scheme developed in Section 3 relaxes L1–L3, while L3 is still assumed, i.e., each possible fault set can be accommodated by at least one of candidate controllers. In this section, we further relax L3 and consider the case when some faults cannot be accommodated by any individual candidate controller, but can be accommodated by a set of controllers under an appropriate switching among them.

The main supervisory FTC idea is similar to that in Section 3. However, since there is no individual controller to accommodate the fault, the switching periodically works among a set of candidate controllers related to the current fault and never stops. The controller switching process is equivalent to a switched system with all unstable modes.

For clarity, we first discuss the switched system in Section 4.1, and then apply the result to supervisory FTC in Section 4.2.

4.1. Stabilization of switched systems with all unstable modes. Let us consider a switched system where all modes may be unstable. The main idea is to divide states into several parts. Consequently, the original switched system is regarded as a set of interconnected sub-switched systems. Under some conditions, each sub-switched system is input-to-state stable with respect to states of the others. This, together with small gain conditions, leads to the asymptotical stability of the overall system.

The switched system takes the form

\[
\dot{x} = f_\sigma(x, u_\sigma),
\]

where \(x \in \mathbb{R}^n\) are the states. Define \(\mathcal{M} = \{1, 2, \ldots, m\}\), where \(m\) is the number of modes. \(\sigma(t) : [0, \infty) \rightarrow \mathcal{M}\) denotes the switching signal, which is assumed to be a piecewise constant function continuous from the right. For any \(i \in \mathcal{N}\), \(u_i \in \mathbb{R}^p\) are the inputs and \(f_i\) is a smooth function with \(f_i(0, 0) = 0\). Denote by \(\Delta t_i\) the activating period of mode \(i\). The “dwell-time” \(\tau\) is still involved.
It is desirable to design $u_σ$ such that each mode of (10) is individually stable, which, however, is often hard for complex nonlinear structures. A natural question is whether the switched system can be stabilized by the appropriate design of $σ$ and $u_σ$. The answer is positive, as shown below.

Define a vector $z = [z_1^T, z_2^T, \ldots, z_m^T]^T$, $m \leq n$, satisfying
\[
\begin{align*}
  z_1 &\in \mathbb{R}^{n_1} = [x_1, \ldots, x_{n_1}]^T \\
  z_2 &\in \mathbb{R}^{n_2} = [x_{n_1+1}, \ldots, x_{n_1+n_2}]^T \\
  \vdots \\
  z_m &\in \mathbb{R}^{n_m} = [x_1, \ldots, x_{\sum_{i=1}^m (n_i-1)} \ldots, x_{\sum_{i=1}^m n_i}]^T,
\end{align*}
\]
where $\sum_{i=1}^m n_i = n$. It is clear that the original states $x$ are divided into $m$ parts by $z$.

**Assumption 2.** For each mode $i \in \mathcal{M}$ of the system (10), we can design a controller $u_i$ under which there exists a continuous non-negative function
\[
V(i): \mathbb{R}^n \to \mathbb{R}_0^+ = V_1(i)(z_1) + \cdots + V_m(i)(z_m),
\]
where $V_k(i)(z_k) \in \mathcal{C}^1: \mathbb{R}^{n_k} \to \mathbb{R}_0^+, k \in \mathcal{M}$, and there exist $\alpha_1$, $\alpha_2 \in \mathcal{K}_\infty$, and $\gamma_{ab} \in \mathcal{K}_\infty$ for $a, b \in \mathcal{M}$, $\lambda_0, \lambda_1 > 0$, $\mu \geq 1$ such that for $z_k \in \mathcal{M}$, $\forall k \in \mathcal{M}$,
\[
\begin{align*}
  \alpha_1(|z_k|) &\leq V_k(i)(z_k) \leq \alpha_2(|z_k|), \forall k \in \mathcal{M}, \\
  \dot{V}_i(z_i) &\leq -\lambda_0 V_i(z_i) + \max_{p \in \mathcal{M} - \{i\}} \gamma_{ip}(V_p(i)),
\end{align*}
\]
with the inequalities (13) and (14) imply that, for mode $i$, $z_i$ is Input-to-State Stable (ISS) (Sontag and Wang, 1996) with respect to other states of $z$, $\gamma_{ab}$ is the gain from $V_k(i)$ to $V_j(i)$, while all other states of $z$ may not be stable. Here $z_i$ is called a potentially stable state in mode $i$.

Although each mode $i$ cannot be stabilized, Assumption 2 guarantees that each mode has some potentially stable states under appropriate $u_i$, and all these potentially stable states in different modes form the whole state space.

Under Assumption 2, the switched system (10) can be regarded as $m$ interconnected switched systems as shown in Fig. 7. We call each switched system a $z_i$ switched system.

**Definition 1.** A periodical switching signal for the system (10) is given by
\[
\sigma(t) = l, \quad \forall t \in [kT + \sum_{\rho=0}^{l-1} \Delta t_\rho, kT + \sum_{\rho=0}^l \Delta t_\rho),
\]
for $k = 0, 1, \ldots$.
\[ a, b \geq \tau \text{ define} \]
\[
\begin{align*}
\phi_1(a) & \triangleq \frac{\lambda_0 a - (m - 1) \ln \mu}{\lambda_1}, \\
\phi_2(b) & \triangleq \frac{\lambda_1 b + (m - 1) \ln \mu}{\lambda_0}.
\end{align*}
\]

(19)

The condition (17) is equivalent to \( \Delta t_{wa} < \phi_1(\Delta t_i) \)
and \( \Delta t_i > \phi_2(\Delta t_{wa}) \). The following theorem gives
the sufficient stability conditions of the overall switched system.

Theorem 2. Under Assumption 2, if

1. there exists \( \Delta t \geq \tau \), such that
\[ \phi_2(\Delta t + \phi_1(\Delta t)) \geq \tau \]
and
\[ \phi_1(\Delta t) > (m - 1) \phi_2(\Delta t + \phi_1(\Delta t)); \]  
(20)

2. there exists \( \rho \in \mathcal{K}_{\infty} \), such that
\[ (\gamma_{1i_1} + \rho) \circ (\gamma_{i_2i_3} + \rho) \circ \cdots \circ (\gamma_{i_{m-1}i_1} + \rho)(s) \leq s \]  
(21)

for all \( s \geq 0 \), and for all \( 1 \leq i_j \leq m \), \( i_j \neq i_{j'} \)
whenever \( j \neq j' \),

then the switched system (10) is periodically stabilized by
the following switching law:
\[ T = \Delta t + (m - 1) \phi_2(\Delta t + \phi_1(\Delta t)). \]

Algorithm 2.

1. Let \( k = 0 \).

2. Activate Mode 1 at \( t = kT \), until \( t = kT + \Delta t \).
   Set \( i = 2 \), and go to Step 3.

3. Activate Mode \( i \) at
\[ t = kT + \Delta t + (i - 2) \phi_2(\Delta t + \phi_1(\Delta t)), \]
until
\[ t = kT + \Delta t + (i - 1) \phi_2(\Delta t + \phi_1(\Delta t)). \]

Go to Step 4.

4. Set \( i = i + 1 \). If \( i = m + 1 \), then \( k = k + 1 \).
   Go to Step 2. Otherwise, go to Step 3.

Proof. It can be easily obtained from Lemma 1 that,
der under the first condition, Algorithm 2 guarantees that all
\( z_i \) switched systems are ISS with respect to other states
of \( z \), i.e., \( \forall i \in \mathcal{M} \), the inequality (18) holds at \( T \).
On the other hand, the second condition means that the composition
of the gain function along every closed cycle
among interconnected \( z_i \) systems is less than the identity function.
Also note that all potentially stable states in different modes form
the whole state space. It follows from the works of Jiang et al. (1994)
as well as Jiang and Wang (2008) that the origin of the switched system (10)
is asymptotically stable at \( T \), i.e., there exists \( \beta^* \in \mathcal{K}_{\ell} \) such
that \( |x(T)| \leq \beta^*(|x(0)|, T) \). Proceeding in a similar way, we obtain
\[ |x((k + 1)T)| \leq \beta^*(|x(kT)|, T), \quad k = 0, 1, 2, \ldots \]

Therefore, the origin of the switched system (10) is
asymptotically stable under Algorithm 2.

The main idea of Theorem 2 is to guarantee that each
\( z_i \) switched system has enough time to activate modes
that have potentially stable states, as illustrated in Fig. 9,
which, together with the second condition of Theorem 2
(the small gain condition), leads to the asymptotical
stability of the overall switched system at the origin.

4.2. Supervisory FTC. This section applies the above
results to a supervisory FTC design problem. Recall the
system (1). Divide \( x \) into \( m \) parts by \( z \) as in (11). It is
still supposed that there are \( \omega \) pre-computed candidate
controllers for the supervision purpose. Recall that
\( \Omega = \{1, 2, \ldots, \omega\} \).

Assumption 3. There exist \( m \) candidate controllers
\( (m \leq \omega) \), denoted by \( u_i, i \in \mathcal{M} \), such that when the system (1)
experiences the fault \( f \in \mathcal{F}, i \in \mathcal{N} \), and \( u = u_i \),
there exists a continuous non-negative function \( V_j^{(i)} \) that
satisfies (12–16). Moreover, if \( u = u_s, \quad s \in \Omega \backslash \mathcal{M}, \)
then \( \forall j \in \mathcal{M} \).

\[ V_j^{(i)}(z_j) \leq \lambda_i V_j^{(i)}(z_j) + \max_{q \in \mathcal{M} \backslash \{j\}} \left\{ \gamma_{ij}(V_q^{(j)}) \right\}, \]

where \( \lambda_i \) and \( \gamma_{ab} \) are defined as in Assumption 2.
Assumption 3 implies that, for $f \in F_i$, each controller $u_i, i \in M$, may potentially stabilize some states, and all potentially stable states under these $m$ controllers compose the whole state space. However, when any other candidate controller $u_i, i \in \Omega \setminus M$, is applied, the system has no potentially stable state.

It should be pointed out that Assumption 3 covers the case when the healthy system (if $i = M$) can neither be stabilized by any individual candidate controller. This is often true in some kinds of systems, e.g., underactuated ones, while the switching control scheme can achieve the stability objective.

The fault detection law can be designed in much the same way as in Section 3. Once $u_i$ is applied, (14) can be used as a time-varying residual, and a fault detection law is given by

$$V_i(t) = e^{-\lambda(t-t_{ik})}V_i(t_{ik}) + \int_{t_{ik}}^t e^{-\lambda(t-\nu)} \max_{p \in M - \{i\}} \left\{ \gamma_{ip}(V_p(\nu)) \right\} d\nu \quad \Rightarrow \quad \text{Fault occurs,}$$

where $t_{ik}$ denotes the $k$-th time at which the controller $u_i(t)$ is applied. Suppose that the fault is detected at $t = t_{fd}$. For simplification, we only focus on one fault set $F_i$, for $i \in \mathcal{N}$. The results can be easily extended to the of multiple faults considered.

In the following, a $z_i$ switched system is considered with the dynamics of $z_i$ under different controllers. To avoid arbitrarily fast switchings, the “dwell-time” $\tau$ is also involved among controller switchings.

Define $T = \Delta t + (m-1)\phi_2(\Delta t + \phi_1(\Delta t))$ with $\Delta t \geq \tau$ to be designed, $\phi_1$ and $\phi_2$ being defined in (19). A performance based controller switching law is designed as follows.

**Algorithm 3.**

1. Define $\Omega^* \triangleq \Omega$. Let $s = 0, k = 0, v = 1$.
   Set $\sigma(t_{fd}) = i^*$, where
   $$i^* = \arg \min_{i \in \Omega^*} J_i(x(t_{fd}), t_{fd}).$$

2. Apply $u_i$ until $t = t_{fd} + (s+1)\tau$.
   If there is a $j \in M$ such that $V_j^{<i^*>}$ satisfies (14), then go to Step 4. Otherwise, go to 3.

3. Let $\Omega^* = \Omega^* \setminus \{i^*\}$. Set $\sigma(t_{fd} + (s+1)\tau) = i^*$ where
   $$i^* = \arg \min_{i \in \Omega^*} J_i(x(t_{fd} + (s+1)\tau), t_{fd} + (s+1)\tau).$$
   Let $s = s + 1$. Go to Step 2.

4. Let $\Omega^* = M$. Apply $u_i$ until
   $$t = t_{fd} + kT + s\tau + \Delta t.$$
period \( \tau \). At \( t = t_{fd} + (\omega - m)\tau \), one of the controllers \( u_i, \ i \in \mathcal{M} \), denoted by \( u_1 \), is selected and applied. It follows from Algorithm 3 that the activating period of \( u_1 \) is \( \Delta t \). All other consequent \( m - 1 \) controllers have the activating period \((m - 1)\phi_2 (\Delta t + \phi_1 (\Delta t))\).

At \( t = T^* = t_{fd} + \Delta t + (m - 1)\phi_2 (\Delta t + \phi_1 (\Delta t)) + (\omega - m)\tau \), all controllers have been applied for one time. Based on Assumption 3 and (23), we can obtain, following the same reasoning as in the proof of Theorem 2, that all \( z_i \) switched systems are ISS with respect to other states of \( z \) at \( T^* \), which, together with the second condition in Theorem 1, leads to the asymptotical stability of the origin at \( T^* \). Note that (23) implies (20), the rest of the proof being the same as that of Theorem 2. For the case when \( u_1 \) is selected at \( t = t_{fd} + (\omega - m)\tau \), with \( \omega < \omega \), the result can be obtained following the above procedure.

### 4.3. Aircraft team example

In a team of multiple aircrafts, one leading aircraft often determines the flying behavior of the whole team. The others have no behavior information by themselves. The flying performance of the whole team is achieved by communications among aircrafts (Giulietti et al., 2000).

Specifically, in the “climbing” process, it is required that all aircrafts in the team have the same pitch rates. We consider a team consisting of three aircrafts as shown in Fig. 11. Aircraft 1 is the unique leader that knows the prescribed pitch rate. Two other aircrafts follow Aircraft 1 by receiving the state information from Aircraft 1.

![Aircraft team](image)

Fig. 11. Aircraft team.

The longitudinal differential equation of each aircraft is the same as in the example in Section 3.3. For the purpose of cooperation, we divide the original controller \( u \) into the self controller \( u^c \) and the cooperative controller \( u^a \), i.e., \( u = u^c + u^a \). Suppose that the dynamic equations of pitch rates under their self controllers are as follows:

- **Aircraft 1**: \( \dot{\omega}_1 = -a_1 (\omega_1 - \omega^a) \), \( i = 1, 2, 3 \). Differentiating \( W_i \) along (24 - 26), we further have

\[
\begin{aligned}
\dot{W}_1 &= -2a_1 W_1, \\
\dot{W}_2 &\leq (2a_2 - b_2) W_2 + b_2 W_1, \\
\dot{W}_3 &\leq (2a_3 - b_3) W_3 + b_1 W_1.
\end{aligned}
\]

This implies that all \( \omega_i \) will approach \( \omega^a \).

Poor link quality is an inherit drawback of wireless communication, which often leads to great transmission power and a large number of retransmissions of sensors, and consequently, a drastically increasing communication cost (Akyildiz et al., 2002). Now we consider a communication fault case, i.e., that transmitter of Aircraft 1 is faulty, such that it does not have enough power to transmit information in a region as large as the healthy one. Thus, Aircrafts 2 and 3 cannot receive the information from Aircraft 1 simultaneously. Communication between Aircrafts 2 and 3 is also supposed to be unavailable. In such a faulty case, Aircraft 1 can send information to one aircraft only. Any fixed connection topology cannot achieve the team flight. However, a switching topology can do it.

**Under Topology 1**: Connecting Aircraft 2 with Aircraft 1 yields

\[
\begin{aligned}
\dot{W}_1 &= -2a_1 W_1, \\
\dot{W}_2 &\leq (2a_2 - b_2) W_2 + b_2 W_1, \\
\dot{W}_3 &\leq 2a_3 W_3.
\end{aligned}
\]

In this situation, \( \omega_1 \) and \( \omega_2 \) will tend to \( \omega^a \), but \( \omega_3 \) may run far away.

**Under Topology 2**: Connecting Aircraft 3 with Aircraft 1 yields

\[
\begin{aligned}
\dot{W}_1 &= -2a_1 W_1, \\
\dot{W}_2 &\leq 2a_2 W_2, \\
\dot{W}_3 &\leq (2a_3 - b_3) W_3 + b_1 W_1.
\end{aligned}
\]

In this situation, \( \omega_1 \) and \( \omega_3 \) reaches \( \omega^a \), while \( \omega_2 \) may escape.
Note that (28) and (29) inherently satisfy (13–16) in Assumption 2. In the simulation, suppose that $a_1 = 5$, $a_2 = a_3 = 2$, $b_2 = b_3 = 10$. We further have

\[
\begin{align*}
\text{Topology 1:} & \quad \begin{cases} 
\dot{W}_1 = -10W_1, \\
\dot{W}_2 = -6W_2 + 10W_1, \\
\dot{W}_3 = 4W_3.
\end{cases} \\
\text{Topology 2:} & \quad \begin{cases} 
\dot{W}_1 = -10W_1, \\
\dot{W}_2 = 4W_2, \\
\dot{W}_3 = -6W_3 + 10W_1.
\end{cases}
\end{align*}
\]

which satisfy (20) and (21) in Theorem 2.

Choose $\omega^* = 0.1$ (rad/s). The initial states are $\omega_1(0) = 0.3$ (rad/s), $\omega_2(0) = 0$ (rad/s), $\omega_3(0) = -0.2$ (rad/s). Suppose that the fault occurs at $t = 0.5$ s, at which both connections are broken. The fault can be detected rapidly at $t = 0.5$ s using (27).

Since there are only two topologies to be selected and applied, Algorithm 3 can be simplified and skips to Step 4. The cost functions are $J_1 = \omega_2^2 + \omega_3^2$, $J_2 = \omega_2^2 + \omega_3^2$. Topology 2 is firstly selected and applied at $t = 0.5$ s since $J_2(2) < J_1(2)$. Both dwell periods of Topologies 1 and 2 are chosen as 0.5 s. The periodical switching is as follows:

\[
\begin{align*}
\text{Topology 2:} & \quad \forall t \in [0.5 + k(s), 1 + k(s)) \\
\text{Topology 1:} & \quad \forall t \in [1 + k(s), 1.5 + k(s))
\end{align*}
\]

for $k = 0, 1, 2, \ldots$.

Figure 12 shows the switching signal (where ‘1’ represents Topology 1, and ‘2’ denotes Topology 2) and pitch rate trajectories under the proposed supervisory FTC law, from which we can see that all pitch rates approach $\omega^*$ in spite of the communication fault.

![Switching signal and pitch rate trajectories.](image)

Fig. 12. Switching signal and pitch rate trajectories.

5. Conclusion

This paper provides a new supervisory FTC framework without individual fault detection and isolation schemes. The proposed framework only relies on a controller switching scheme which is based on switched system theories.

Future work will be conducted along the following lines:

1. In this work, full state measurements are available, which facilitates FDI/FTC design. In the absence of measurable states, an output feedback controller would be potentially applied, or observers would be embedded into the proposed framework. Switching law design would be challenging.

2. In Section 4, exponential decay form of $V_i$ is considered such that $\phi_1$ and $\phi_2$ are independent of the states. The state-dependent $\phi_1$ and $\phi_2$ would be considered. In this case, the stability of the system should be checked on-line, and switching law design is much more complicated.

3. The state may oscillate during the switching period of controllers due to a large switching number and switching frequency. The trade-off between the simplicity of the switching algorithm and its effects on the transient performance will be investigated.

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Bin Jiang obtained a Ph.D. in automatic control from Northeastern University, Shenyang, China, in 1995. Currently he is a full professor and a vice dean of the College of Automation Engineering at the Nanjing University of Aeronautics and Astronautics. He serves as an associate editor for *IEEE Transactions on Control Systems Technology, International Journal of System Science, International Journal of Control, Automation and Systems, International Journal of Applied Mathematics and Computer Science*, and others. His research interests include fault diagnosis and fault tolerant control and their applications.

Vincent Cocquempot received the Ph.D. degree in automatic control from the Lille University of Sciences and Technologies, in 1993. He is currently a professor of automatic control and computer science at the University Technological Institute. He is the head of research in the LAGIS Laboratory, UMR CNRS 8219, Lille 1 University: Sciences and Technologies. His research interests include robust on-line fault diagnosis for uncertain dynamical nonlinear systems, fault detection and isolation, fault tolerant control for hybrid dynamical systems.

Lingli Lu is currently an M.Sc. student at the College of Automation Engineering, Nanjing University of Aeronautics and Astronautics, China. Her research interests include switching based fault diagnosis and fault tolerant control with application to unmanned aerial vehicles.

Hao Yang received Ph.D. degrees in automatic control from the Nanjing University of Aeronautics and Astronautics (NUAA) as well as Lille 1 University: Sciences and Technologies, France, both in 2009. In 2009, he joined the College of Automation Engineering of the NUAA, where he is currently an associate professor. He serves as an associate editor for *Nonlinear Analysis: Hybrid Systems*. His research interest includes stability and fault tolerant control of switched and hybrid systems, multi-agent systems, supervisory control and applications.

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