Multi-user Joint Maximum-Likelihood Detection in Uplink NOMA-IoT Networks: Removing the Error Floor

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Abstract—The Internet of Things (IoT) framework requires a massive number of connection thus demanding spectral efficient solutions such as Non-Orthogonal Multiple Access (NOMA). However, the main drawback of NOMA with successive interference canceler (SIC)-based detectors is the error floor in the uplink. In this paper, a reliable multi-user detection in uplink IoT NOMA is guaranteed by a Joint Maximum-Likelihood (JML) detector (i.e., optimum detection algorithm). We derive a closed-form upper bound of bit error rate (BER) of JML over Rayleigh fading channels for arbitrary number of IoT devices and an adaptive M-ary phase shift keying (M-PSK). Based on the extensive simulations, the derived expressions are validated and it is revealed that the JML improves the error performance in uplink NOMA and removes the error floor. Furthermore, regardless of the number of the IoT devices and modulation order, a full diversity order (i.e., number of receiving antennas) is guaranteed for each device.

Index Terms—IoT, uplink NOMA, multi-user detection, bit error rate (BER), joint maximum-likelihood.

I. INTRODUCTION

Non-Orthogonal Multiple Access (NOMA) scheme allows several users sharing entirely the same available time/frequency resources. Accordingly, with the aim to support massive machine-type communication (MMTC) in Internet of Things (IoT) framework, NOMA has emerged as an appropriate multiple access scheme [1], [2]. In NOMA, the successive interference cancellation (SIC) technique is consistently considered to eliminate inter-user interference (IUI). The performances of NOMA systems for SIC detector are studied in-depth in terms of information-theoretic perspectives (e.g., outage probability and capacity [3], [4]), which are based on theoretical Shannon limit. However, most of the existing works consider perfect SIC [4] which is not reasonable for practical scenarios. In [5], the authors relax this assumption and propose a joint decoding to remove the error floor of outage probability in the uplink NOMA due to SIC detectors. Nevertheless, this SINR-based analysis can reveal the performance for finite alphabets (i.e., short-packet communication) and it is proved that the SIC is not optimal in uplink NOMA for infinite alphabets [5], [6]. It noticeably suffers from the error floor and has a severe performance (e.g., bit error rate (BER)). In fact, this drawback gets worsen when the number of users is increased (e.g., more than two users), and none of the symbols becomes detectable. To resolve the error performance problem with infinite alphabets in the uplink, the joint maximum likelihood-detector (JML) has recently attracted attention [7]– [10]. These studies have shown the predominance of JML over SIC in the uplink. Besides, the JML is still the optimal detection for non-fully overlapped waveforms (e.g., semi-orthogonal) [11]. However, these studies are mostly based on simulations whereas an upper bound analysis of JML detector is investigated only in [7], [10]. Nevertheless, these studies are devoted to special cases by considering only two users which again do not meet the requirements of IoT networks. Thus, to enable the MMTC with a reliable performance, the JML in the multi-user uplink NOMA is still to be explored.

Motivated by the above discussions, in this letter, we study an uplink NOMA for an IoT network with a JML detector to guarantee a reliable multi-user detection. To the best of our knowledge, this work is the first to derive an upper bound BER expression for an arbitrary number of IoT devices and with an adaptive M-PSK modulation (according to link quality [12]). The analytical expressions are validated via computer simulations and we reveal that the JML eliminates the error floor completely and a full diversity order (i.e., receiving antenna) is achieved regardless of number of IoT devices or modulation order.

The rest of the paper is organized as follows. In Section II, the system model is introduced. In Section III, we perform an error performance analysis and an upper bound of BER is derived. Later, in Section IV, we validate the analytical results via simulations. Finally, Section V concludes the paper.

II. SYSTEM MODEL

We consider an uplink NOMA scheme, where $K$ active IoT devices belonging to the same cluster are sending their data towards a single base station (BS). We assume that the BS is equipped with $L$ antennas and the IoT devices are equipped with a single antenna (SIMO-NOMA). The $K$ IoT devices are allowed with equal priority to simultaneously access the same frequency block using their own powers. Hence, the received signal at the BS is given by

$$ y = \sum_{k=1}^{K} c_k \sqrt{P_k} x_k + w, \quad (1) $$

where $y \in \mathbb{C}^{L \times 1}$, $P_k$ is the transmitted power for the $k^{th}$ device ($D_k$) and $c_k \in \mathbb{C}^{L \times 1}$ denotes the flat fading channel vector (counting effects of path loss and small-scale fading) between the $k^{th}$ device and the BS. The components of the vector $c_k$ are independent and identically distributed (i.i.d.) and follow $c_k \sim \mathcal{CN}(0, \sigma^2 I_L)$, where $I_L$ denotes the $L \times L$ identity matrix. The additive white Gaussian noise (AWGN)
vector \( \mathbf{w} \in \mathbb{C}^{L \times 1} \) is given by \( \mathbf{w} \sim \mathcal{CN}(0, \frac{N_0}{2} \mathbf{I}_L) \). At the BS a channel state information (CSI) for each device is supposed to be available, and the received signal powers are presumed to be correctly measured using the pilot signals, such that \( \| \mathbf{h}_1 \| > \| \mathbf{h}_2 \| > \cdots > \| \mathbf{h}_k \| \), where \( \mathbf{h}_k = \sqrt{P_k} \mathbf{c}_k \), \( k = 1, \cdots, K \), and the symbol \( \| \cdot \| \) denotes the Frobenius norm.

The information bitstream is modulated as complex symbol \( x_k \in \mathbb{C}, k = 1, \cdots, K \), where it takes its value from the alphabet \( \chi_k = \{ s_{k1}, s_{k2}, \cdots, s_{kM_k} \} \), and the symbol \( s_{kn_k} \) is the \( n_k^{th} \) constellation point in \( M_k \)-ary modulation order of \( D_k \).

\[ |s_{kn_k}| = \sqrt{\epsilon_k} = \sqrt{\frac{2 \log_2 M_k}{\epsilon}} \text{ for } D_k, \] where \( \epsilon_k \) is the symbol energy and \( \epsilon \) is the bit energy. We consider an adaptive modulation that uses \( M_k \)-PSK with Gray coded mapping.

1) MRC-SIC detector: In SIC detector, IoT devices’ symbols are detected serially. Initially, the symbols of the IoT device with strongest gain channel is detected. Then, the contribution of these detected symbols to received signal is removed, producing a second signal to detect the IoT device’s symbols with second-best condition. This process is repeated until the symbols of the last IoT device with weakest channel condition are detected. Consequently, the procedure is summarized as

\[ \hat{x}_k = \arg \min_{s_{kn_k}} \| y_{SIC,k} - \mathbf{h}_k s_{kn_k} \|^2, \]

where \( k = 2, \cdots, K \), \( n_k = 1, \cdots, M_k \), and

\[ y_{SIC,k} = x_{SIC,k-1} - \mathbf{h}_{k-1} \hat{x}_{k-1}, \] where \( y_{SIC,1} = y \), and \( \hat{x}_1 = \arg \min_{s_{1n_1}} \| y - \frac{1}{2} s_{1n_1} \|^2 \).

Although the procedure seems simple to implement, unfortunately, it suffers from the error floor. To recover each signal, the detector treats the remaining low power signals as interference, thus an error floor occurs. Increasing the number of antennas at the receiver may improve the performance of SIC in low SNR, but can not remove the error floor.\(^1\)

2) MRC-JML detector: To avoid the drawback of the SIC detector, we suggest to jointly recover (i.e., optimal detection) the transmitted signals from IoT devices. The JML detector accomplishes an exhaustive search to simultaneously decode all signals \( x_1, x_2, \cdots, x_k \) as follows

\[ \hat{x}_1, \hat{x}_2, \cdots, \hat{x}_k = \arg \min_{s_{kn_k}} \| y - \sum_{k=1}^{K} \mathbf{h}_k s_{kn_k} \|^2. \]

Regardless of the order of selection, the JML traits the signals the same by realizing a full joint search for all possible combinations \( \{ \mathbf{h}_1 s_{1n_1}, \mathbf{h}_2 s_{2n_2}, \cdots, \mathbf{h}_k s_{kn_k} \} \), which avoids an error floor.

3) Computational complexity: The computational complexity comparison for MRC-JML and MRC-SIC is given in Table I. The complexity of MRC-JML increases exponentially with the increase of \( K \) and \( M_k \) whereas the complexity of MRC-SIC increases linearly with the same factors. However, this complexity reflects the required computational capacity rather than computing time. One can easily see from (4) that the search in JML is independent so that it can be computed in parallel and be compared in the final stage, whereas the SIC detection (2)-(3) should be implemented sequentially. Therefore, as long as we have enough computational capacity, which is quite possible since it is an uplink communication and the JML is implemented at the BS, the JML does not cause a latency due to computational time. Besides, this computational burden can be considered as acceptable, especially if we take into account the performance improvement by MRC-JML.

### III. Error Performance Analysis

In this section, we analyse the error performance of IoT uplink-NOMA system with JML. We derive an analytic expression of the upper bound for the BER for fading channels. For the purpose of a comprehensive study, we assume that the IoT device of order \( k \) sends its data according to an appropriate order modulation \( M_k \), which depends on the channel quality.

For the representation simplicity, let us start by considering a system composed of three IoT devices. The Fig. 1 shows the superposed symbols, where the \( D_1 \) chooses its symbols from 8-PSK constellation \( (M_1 = 8) \) and both the others devices: \( D_2 \) and \( D_3 \) from a quadrature phase constellation \( (M_2 = 4 \) and \( M_3 = 4) \). It is worth mentioning that these constellation orders depend to the channel quality. The first three bits belong to \( D_1 \), the following two bits belong to \( D_2 \) and the last two bits belong to \( D_3 \). Thus, the binary bit representations of the three symbols are given in the form of \( \{b_{11}b_{12}b_{13}b_{14}b_{21}b_{22}b_{31}b_{32}\} \), where the first sub-index represents the device and the second represents the bit order within the symbol.

For convenience, we assume that the superimposed sequence \( \{000, 00, 00\} \) that matches the symbols \( x_1 = s_{11} = \exp(j \frac{\pi}{4}) \) for \( D_1 \), \( x_2 = s_{21} = \exp(j \frac{\pi}{4}) \) for \( D_2 \) and \( x_3 = s_{31} = \exp(j \frac{\pi}{4}) \) for \( D_3 \) is received at the BS. Then, we proceed to analyze the BER of the first bit \( b_{k1} = 0 \) for each of the three devices. In view of symmetry of M-PSK modulation, it is evident that the BER is unchanged when \( b_{k1} = 1 \), therefore, the study is restricted on the case of \( b_{k1} = 0 \). Since the BS relies on ML detector to detect the symbols, it is clear that an error happens if the ML selects the device symbol with first bit \( b_{k1} = 1 \). Fig. 1a shows the location of the erroneous symbols for \( D_1 \) (shaded area) when the JML selects one of the superimposed symbols with the first bit equal to one, i.e., the lower half-circle of an 8-PSK constellation with Gray coded bit mapping (see Fig. 4). It is simple to notice that the number of erroneous symbols after this wrong decision is equal to the all possible symbols of the devices \( D_2 \) and \( D_3 \) superimposed with the four symbols of \( D_1 \) \((2^2 \times 4 \times 4) \) symbols. Thus, if we consider a K IoT devices with \( M_k \)-ary order for each \( D_k \), the number of erroneous symbols is clearly equal to \( \frac{1}{2} \prod_{i=1}^{K} M_i \).

\( ^1 \) For an easy-follow illustration, we have adopted an octal representation of the symbols for \( D_1 \), and quaternary representation for \( D_2 \) and \( D_3 \).

### TABLE I: Computational cost in terms of real operations

| Multiplier | Adder | Comparator |
|------------|-------|------------|
| \( (6L + 2L) \sum_{i=1}^{K} M_i \) | \( (6L - 1) \sum_{i=1}^{K} M_i + 4L(K - 1) \) | \( \sum_{i=1}^{K} (M_i - 1) \) |

| SIC | JML |
|-----|-----|
| \( 8L \) | \( 4L + 2L \) | \( 4L + 2L \) | \( 4L \) |
Fig. 1: Signal space diagram for superposed symbols of three IoT devices (8-PSK with two 4-PSK) with Gray coded bit mapping.

Fig. 1a for illustration. By examining the error patterns of the Fig. 1b and Fig. 1c, we observe the same number of errors which equal to $8\times\frac{1}{2}\times 4$ and $8\times 4\times \frac{1}{2}$ for $D_2$ and $D_3$, respectively. The difference between errors of distinct devices is due to error pattern that depends to the arrangement of the components of signals (power) within the overlapped symbols at receiver.

Based on the result above, we designate the distances between the first symbol $s_{k1}$ of each IoT $D_k$, $k=1,\ldots, K$, and the $\frac{M_k}{2}$ erroneous symbols $s_{kn_k}$, $n_k=\frac{M_k}{2}+1,\ldots, M_k$, by the vector $E_{k1} = \sqrt{e_k}^{-1}\left(s_{k1} - s_{\frac{M_k}{2}+1},\ldots, s_{k1} - s_{kM_k}\right)$, and the distances between $s_{k1}$ with the symbols of the alphabet $\chi_k$ by the vector $E_k = \sqrt{e_k}^{-1}(s_{k1}\otimes 1_{M_k} - \chi_k)$, where $\otimes$ denotes the Kronecker product, and $1_{M_k}$ is the all ones vector of dimension $M_k \times 1$. Correspondingly, the total $\frac{1}{2}\prod_{i=1}^{K}M_i$ pairwise error probabilities (PEP) following the erroneous decision between the symbol $s_{k1}$ with the symbols $s_{kn_k}$, $n_k=\frac{M_k}{2}+1,\ldots, M_k$, conditioned on the vectors $h_k$ of each IoT $D_k$, $k=1,\ldots, K$, are given by

$$Pr\left(s_{k1} \to s_{kn_k} \mid h_1,\ldots, h_K\right) = Q\left(\frac{\left\|\sum_{i=1}^{K}h_i\sqrt{e_i}d_i^{D_k}(m)\right\|}{\sqrt{2N_0}}\right),$$

where $m=1,\ldots, \frac{1}{2}\prod_{i=1}^{K}M_i$, and $d_i^{D_k} \in \mathcal{C}(\frac{1}{2}\prod_{i=1}^{K}M_i) \times 1$, $i=1,\ldots, K$, denotes the distances between the symbol under test $s_{k1}$ and the superimposed symbols with the erroneous symbol (first bit $b_{k1}=1$). Table 3 summarizes the elements of $d_{i}^{D_k}$.

It is noteworthy that the distances $\left\|\sum_{i=1}^{K}h_i\sqrt{e_i}d_i^{D_k}(m)\right\|$ are the distances between $M_k$-PSK symbols in a new space rotated by $h_k$, $k=1,\ldots, K$, and as long as the symbols have experienced the same rotation, in consequence, the symmetry and error patterns are retained. As a result of these properties and the circularity of PSK modulation, if we replace the term $s_{k1}$ by any other terms $s_{kn_k}$, where $n_k=2,\ldots, M_k$, the elements of $h_k$ are the same without dealing with order, then the components of $d_{i}^{D_k}$ for $i \neq k$, which contribute to the conditional PEP turns into the same. This means that the conditional PEP for different IoT devices, differs from each other in virtue of vectors $E_{kn}$.

At this juncture, we have study the error patterns for symbol $s_{k1}$, it is easy to show that the number of errors is constantly the same for the remaining of symbols $s_{kn_k}$, $n_k=2,\ldots, M_k$. From the symmetry of the $M_k$-PSK constellation (see Fig. 2a), we can notice that the distances between the points which are located in upper half-circle and those situated in lower half-circle are equal in pairs with respect to the right and left semicircle, consequently, there are a total of $\frac{M_k}{2}$ different vectors $d_i^{D_k}$ which depend to $E_{kn} = \sqrt{e_k}^{-1}\left(s_{kn} - s_{\frac{M_k}{2}+1},\ldots, s_{kn} - s_{kM_k}\right)$, $n=1,\ldots, M_k$. Thus, we consider only the contribution of $\frac{M_k}{2}$ symbols located in the right quadrant to calculate the upper bound. Additionally, by examining the remaining of bits $b_{klog_2 M_k}$ from symbols $s_{kn}$, $n=1,\ldots, M_k$, and taking into consideration the approximated formula of the probability of error [13] Eq. (10) which ties to the right quadrant, i.e., utilizing only the $\frac{M_k}{2}$ symbols and by supposing equally likely symbols in the new space diagram, we get

$$Pr\left(e \mid h_1,\ldots, h_K\right) \approx \frac{M_k}{2\log_2 M_k}P_{rq},$$

where

$$P_{rq} = \frac{1}{4M_k}\sum_{n=1}^{M_k} Q\left(\frac{\left\|h_i\sqrt{e_i}d_i^{D_n}(m) + \sum_{k=1}^{K}h_k\sqrt{e_i}d_i^{D_k}(m)\right\|}{\sqrt{2N_0}}\right),$$

The analysis for $M_k = 2$ is identical to $M_k = 4$. 

Fig. 2: The effective distances for $D_1$. 

(a) Error pattern for Device1 ($D_1$)  
(b) Error pattern for Device2 ($D_2$)  
(c) Error pattern for Device3 ($D_3$)
TABLE II: Distances to erroneous symbols

| Distances | $d_{1k}^{D_k}$ | $d_{2k}^{D_k}$ | ... | $d_{k}^{D_k}$ | ... | $d_{K}^{D_k}$ |
|-----------|----------------|----------------|-----|----------------|-----|----------------|
| $D_1$     | $E_{11} \otimes 1_{\Pi^{k-1}_{i=1} M_i}$ | $1_{\Pi^{k-1}_{i=1} M_i} \otimes d_2 \otimes 1_{\Pi^{k+1}_{i=1} M_i}$ | ... | $1_{\Pi^{K-1}_{i=1} M_i} \otimes d_k \otimes 1_{\Pi^{k+1}_{i=1} M_i}$ | ... | $1_{\Pi^{K-1}_{i=1} M_i} \otimes d_K$ |
| $D_2$     | $1_{\Pi^{k}_{i=1} M_i}$ | $1_{\Pi^{k}_{i=1} M_i} \otimes E_{21} \otimes 1_{\Pi^{k}_{i=1} M_i}$ | ... | $1_{\Pi^{K-1}_{i=1} M_i} \otimes d_k \otimes 1_{\Pi^{k}_{i=1} M_i}$ | ... | $1_{\Pi^{K-1}_{i=1} M_i} \otimes d_K$ |
| $D_k$     | $1_{\Pi^{k}_{i=1} M_i}$ | $1_{\Pi^{k}_{i=1} M_i} \otimes d_k \otimes 1_{\Pi^{k}_{i=1} M_i}$ | ... | $1_{\Pi^{K-1}_{i=1} M_i} \otimes E_{k1} \otimes 1_{\Pi^{k}_{i=1} M_i}$ | ... | $1_{\Pi^{K-1}_{i=1} M_i} \otimes d_K$ |
| $D_K$     | $d_1 \otimes 1_{\Pi^{k}_{i=1} M_i}$ | $1_{\Pi^{k}_{i=1} M_i} \otimes d_2 \otimes 1_{\Pi^{k}_{i=1} M_i}$ | ... | $1_{\Pi^{K-1}_{i=1} M_i} \otimes d_k \otimes 1_{\Pi^{k}_{i=1} M_i}$ | ... | $1_{\Pi^{K-1}_{i=1} M_i} \otimes E_{k1}$ |

where $d_{nk}^{D_k} = 1_{\Pi_{i=1}^{k-1} M_i} \otimes E_{nk} \otimes 1_{\Pi_{i=k+1}^{K} M_i}$. Incorporating (7) into (6), yields

$$Pr(e \mid h_1, \ldots, h_K) \leq \frac{2}{\log_2 M_k} \sum_{n=1}^{M_k} \prod_{i=1}^{K} M_i \left( \frac{\|d_{nk}^{D_k}\|}{\sqrt{2N_0}} \cdot \frac{\|\delta_{nk}^{D_k}\|}{\sqrt{2N_0}} \right).$$

Due to the characteristic of the vectors $h_1, \ldots, h_K$, we have

$$\delta_{nk}^{D_k} \sim \mathcal{CN} \left( 0, P_k \sigma_d^2 |d_{nk}^{D_k}(m)|^2 + \sum_{i \neq k} P_i \sigma_d^2 |d_{nk}^{D_k}(m)|^2 \right).$$

Like (7), if we denote by $\Gamma_{nk} = \frac{1}{\sqrt{2N_0}}$, and considering $L$ branches of uncorrelated signals $y \in \mathbb{C}^{L \times 1}$ received at BS, then $\Gamma_{nk}$ follows Erlang distribution with parameter $\gamma_{nk} = \frac{1}{\Gamma_{nk}}$. We can evaluate the BER comparisons for MRC-JML and MRC-SIC with respect to SNR for $L = 4$. In Fig. 33 we consider a fixed modulation where six ($K = 6$) IoT devices emitting their data using 4-PSK (i.e., $M_k = 4, \forall k$) constellation. In Fig. 33 each device sends data by using its own constellation order in a system composed of four ($K = 4$) IoT devices. By exploiting the CSI provided by the BS via the broadcast signaling, commonly, to provide a considerable spectrum efficiency in adaptive modulation, a low order (stronger modulation) is chosen for the worst channel conditions, while high order (weaker modulation) is adopted for a high channel condition. Hence, the orders are chosen as follows: $M_1 = 32, M_2 = 16, M_3 = 8, \text{and } M_4 = 4$. In both scenarios, we can easily observe that the MRC-JML detector outperforms the MRC-SIC detector significantly. It is evident that the MRC-JML can remove completely the error floor in the high SNR regime. Differently, the MRC-SIC detector suffers greatly from the error floor and presents a poorer performance. Indeed, none of the symbols can be detected (i.e., $P_k(e) \approx 0.5$) by using MRC-SIC. This clearly shows that the JML enables the MMTC by supporting higher number of devices (i.e., $K = 4, 6$) in the same resource block. Besides, it also allows using higher modulation orders, which is quite promising considering that the SIC detector fails to detect BPSK symbols even in two-user scenario [5], [6]. Furthermore, the analytical results derived from expressions (12)-(13) match well with the simulation results regardless of the number of users ($K$) or modulation orders ($M_k$) which validates our analysis.

**IV. Numerical Results**

In this section, we validate the derived expressions by using computer simulations [3]. In Fig. 33 and Fig. 35 we present BER comparisons for MRC-JML and MRC-SIC with respect to SNR for $L = 4$. In Fig. 33 we consider a fixed modulation where six ($K = 6$) IoT devices emitting their data using 4-PSK (i.e., $M_k = 4, \forall k$) constellation. In Fig. 35 each device sends data by using its own constellation order in a system composed of four ($K = 4$) IoT devices. By exploiting the CSI provided by the BS via the broadcast signaling, commonly, to provide a considerable spectrum efficiency in adaptive modulation, a low order (stronger modulation) is chosen for the worst channel conditions, while high order (weaker modulation) is adopted for a high channel condition. Hence, the orders are chosen as follows: $M_1 = 32, M_2 = 16, M_3 = 8, \text{and } M_4 = 4$. In both scenarios, we can easily observe that the MRC-JML detector outperforms the MRC-SIC detector significantly. It is evident that the MRC-JML can remove completely the error floor in the high SNR regime. Differently, the MRC-SIC detector suffers greatly from the error floor and presents a poorer performance. Indeed, none of the symbols can be detected (i.e., $P_k(e) \approx 0.5$) by using MRC-SIC. This clearly shows that the JML enables the MMTC by supporting higher number of devices (i.e., $K = 4, 6$) in the same resource block. Besides, it also allows using higher modulation orders, which is quite promising considering that the SIC detector fails to detect BPSK symbols even in two-user scenario [5], [6]. Furthermore, the analytical results derived from expressions (12)-(13) match well with the simulation results regardless of the number of users ($K$) or modulation orders ($M_k$) which validates our analysis. The upper bounds are very tight in the high SNR regime and it is also frequently for all upper bounds in communication systems. On the other hand, the upper bound is not tight in low SNR, and this is due to the fact that in this regime, the upper bound is a sum of PEPs with very large and close values. Moreover, once the performances of MRC-JML are compared according to the receiving SNR, the performances of devices with stronger gain channel surpass those of severe channels in the scenario of fixed modulation. Since the error patterns are the same (i.e., the same value of distances $E_{nk}$ and $\delta_k$ which contribute to compute the PEP for each device $k$), this superiority is

We assume that $\epsilon_k = 1, \forall k$, and $P_1 = \cdots = P_K$, which is the worst case in terms of interference. The channel conditions are varied according to the dominant gain channel by fixing variances as $\sigma_d^2 = 2\sigma_{k+1}^2$ with $\sigma_d^2 = 0$ dB.
explained by the quality of signal-to-interference-plus-noise ratio SINR, for each device \( k \). Whereas, when decreasing the modulation order for devices with a bad channel, it is noteworthy that the improvement of their performance is due to the compensation for the effect of the worst channel. This is caused by the enlarging of the minimum distance between symbols in the low order constellation.

Finally, to emphasize the benefit of diversity order provided by the JML detector, in Fig. 3 we present the BER performance of combining multiple signals at the BS for three \( (K = 3) \) active IoT devices with different antenna \( (L = 1, 2, 4, 8) \) situations. As illustrated in Fig. 3, the performance improves each time the number of receiving antennas is increased owing to the improvement of quality of the link. We can also see that the theoretical results correspond well with the results of the simulation, this matching improves even at low SNR because of the enhancement in the power of the link due to the technique of combining at BS. Likewise, one can easily see that the MRC-JML reaches the full diversity order which corresponds to the number of antennas \( L \). This order is noticed by checking the slope of BER curves in all figures.

V. CONCLUSION

This paper presents a reliable multi-user uplink IoT scheme through SIMO-NOMA. By applying MRC-JML (i.e., optimum detector) instead of MRC-SIC (i.e., iterative detector commonly used in the literature) to detect data from several IoT devices, the proposed scheme has proven its effectiveness to discard the error floor. In addition, the JML attains the full diversity order and this confirms the superiority of the JML comparatively to SIC. A comprehensive analysis of BER based on union bound approach is studied. A tight upper bound of BER for multi-user is derived by assuming adaptive M-PSK modulation and arbitrary \( K \) active IoT devices.

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