Room-temperature ferroelectricity in CuInP$_2$S$_6$ ultrathin flakes

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Two-dimensional (2D) materials have emerged as promising candidates for various optoelectronic applications based on their diverse electronic properties, ranging from insulating to superconducting. However, cooperative phenomena such as ferroelectricity in the 2D limit have not been well explored. Here, we report room-temperature ferroelectricity in 2D CuInP$_2$S$_6$ (CIPS) with a transition temperature of $\sim 320$ K. Switchable polarization is observed in thin CIPS of $\sim 4$ nm. To demonstrate the potential of this 2D ferroelectric material, we prepare a van der Waals (vdW) ferroelectric diode formed by CIPS/Si heterostructure, which shows good memory behaviour with on/off ratio of $\sim 100$. The addition of ferroelectricity to the 2D family opens up possibilities for numerous novel applications, including sensors, actuators, non-volatile memory devices, and various vdW heterostructures based on 2D ferroelectricity.
Ferroelectricity is a collective property of certain materials in which macroscopic polarization arises from spontaneous ordering of electric dipoles and can be switched by external electric field. Most technologically important ferroelectrics are perovskite oxides with strong covalent/ionic bonds, such as PbTiO$_3$ and BaTiO$_3$, which have been widely applied in electronic and optoelectronic devices$^{1-3}$. Due to the three-dimensional nature of the ferroelectric oxide lattices, epitaxial growth of high-quality films requires the careful selection of substrates with small lattice mismatch$^4$. This severely limits the possible materials that can be utilized in ferroelectric heterostructure devices. In addition, prevalent dangling bonds and defects at the interface drastically deteriorate the electronic coupling between ferroelectric and graphene like two-dimensional (2D) materials$^5$, due to the complex interface reconstruction and defect chemistry$^6$. Studying weakly bonded non-oxide ferroelectric compounds is thus both fundamentally and practically rewarding. Meanwhile, the groundbreaking work on graphene has triggered an intense search both fundamentally and practically rewarding. Meanwhile, the weakly bonded non-oxide ferroelectric compounds is thus far from the ultrathin limit.

Here, we report the experimental observation of switchable polarization in CuInP$_2$S$_6$ (CIPS) films down to 4 nm at room temperature. Second-harmonic generation (SHG) measurements show the transition from ferroelectric to paraelectric accompanies the structural change from inversion asymmetric to symmetric. Finally, we demonstrate a non-volatile memory device with on/off ratio of ~100 in a CIPS/Si ferroelectric diode.

**Results**

**Characterization of CuInP$_2$S$_6$.** CIPS is one of the few layered compounds which exhibits room-temperature ferroelectricity$^{11}$. The atomic structure of CIPS contains of a sulfur framework with the octahedral voids filled by the Cu, In and P–P triangular patterns. Bulk crystals are composed of vertically stacked, weakly interacting layers packed by van der Waals interactions (Fig. 1a,b)$^{12}$. Owing to the site exchange between Cu and P–P pair from one layer to another, a complete unit cell consists of two adjacent monolayers to fully describe the material’s symmetry. It is a collinear two-sublattice ferrielectric system with $T_c$ of about 315 K (ref. 11). When the temperature drops below $T_c$, due to the off-centre ordering in the Cu sublattice and the displacement of cations from the centrosymmetric positions in the In sublattice (symmetry changes from $C_{2h}$ to $C_s$), spontaneous polarization emerges in the ferrielectric phase with polar axis normal to the layer plane$^{13}$. For simplicity, CIPS will be referred as ferroelectric because ferrielectric materials exhibit the same macroscopic properties as ferroelectrics: namely, a spontaneous and switchable net electric polarization. To verify the ferroelectricity of the bulk sample, polarization versus out-of-plane electric field curve was measured on a 4-µm-thick CIPS flake using a commercial ferroelectric analyzer, where the clear hysteresis loop is direct evidence of ferroelectricity$^{13}$. Details of the ferroelectric and dielectric measurements as well as the temperature dependence are shown in Supplementary Note 1 and Supplementary Figs 1 and 2.

The weak interlayer vdW interaction in CIPS allows us to exfoliate ultrathin flakes from a single crystal and study the ferroelectric properties with reduced dimension. Flakes with different thicknesses were mechanically exfoliated on heavily doped Si substrate with or without SiO$_2$, depending on the purpose of the measurement. They were identified by optical contrast in a microscope and the thickness was subsequently measured using an atomic force microscopy (AFM). Figure 1c shows a typical AFM image of CIPS flakes with different thicknesses on a Si substrate covered with 285 nm SiO$_2$. In the height profile (Fig. 1d) along the line in Fig. 1c, flakes from two to five layers thick and clear monolayer steps are observed. The atomic structure and high quality of the CIPS crystal is also confirmed by high-resolution scanning transmission electron microscopy (STEM) imaging (Supplementary Fig. 3) and Raman spectroscopy (Supplementary Fig. 4).

**Piezoresponse force microscopy measurement.** To verify the thin-film ferroelectricity, CIPS flakes with various thicknesses were investigated using piezoresponse force microscopy (PFM) under both resonant and non-resonant modes (see Methods). The PFM amplitude reflects the absolute magnitude of the local piezoelectric response, while the phase indicates the polarization direction in each individual domain$^{14}$. Figure 2a–c shows the

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**Figure 1 | Crystal structure and characterization of CIPS.** (a,b) The side view (a) and side view (b) for the crystal structure of CIPS with vdW gap between the layers. Within a layer, the Cu, In and P–P form separate triangular networks. The polarization direction is indicated by the arrow. (b) The ferroelectric hysteresis loop of a 4-µm-thick CIPS flake. (c) AFM image of the CIPS flakes with different thicknesses. Scale bar, 2 µm. (d) The height profile along the line shown in c. Clear step height of 0.7 nm corresponding to single layer thickness of CIPS can be observed. L, Layers.
typical domain evolution of CIPS flakes with thickness ranging from 100 nm down to sub-10 nm. With reduced flake thickness (Fig. 2a), the PFM amplitude signal also decreases (Fig. 2b). Similar behaviour is commonly observed in conventional ferroelectric films due to the enhanced depolarization effect in thinner films\(^\text{15}\) as well as the nonuniform electric field of the AFM tip\(^\text{16}\). However, the amplitude signal above the background persists down to the lowest thickness (7 nm). The PFM phase image (Fig. 2c) is characterized by two-colour tones with a contrast of 180°, corresponding to the two opposite polarization directions perpendicular to the layer surface. To confirm the polarization is confined in vertical direction, thickness-dependent vector (vertical and lateral) PFM is conducted on flakes with thickness ranging from sub-100 nm to 100 nm (Supplementary Figs 5 and 6 and Supplementary Note 2). Persistent noise-level in-plane piezoresponse signal suggests negligible effect of possible residual strain on the polarization orientation\(^\text{17}\), thanks to the quasi-free-standing nature of the specimen. The domain patterns evolve from fractal in thinner flakes towards dendrite-like in thicker ones, together with an increase in domain size. The piezoresponse amplitude reduces with the layer thickness, consistent with a finite depolarization effect commonly found in ferroelectric thin films\(^\text{18,19}\). These observations imply that CIPS ultrathin flakes remain ferroelectric down to a few nanometre thick. Piezoelectric response was also obtained from a bilayer (\(~1.6\) nm) CIPS flake (Fig. 2d–g), benefiting from the absence of surface/interface recontructions in 2D materials. To further examine the ferroelectric polarization in ultrathin CIPS, we calculated the structure and polarization of CIPS by the density functional theory (DFT) calculation, and found that the ferroelectric phase can be stabilized in bilayer CIPS (see Methods and Supplementary Note 3 and Supplementary Fig. 7 for details).

**Ferroelectric switching.** By definition, a ferroelectric material should possess spontaneous polarization that is switchable. Although hysteresis loops have been obtained in thick CIPS flakes, it does not warrant switchable polarization in thinner samples, as conventional ultrathin ferroelectric films are notorious for their deteriorated ferroelectric performance owing to the depolarization effect\(^\text{18}\), interface/surface polarization pinning\(^\text{20}\), or, more generally, the ‘dead layer’ effect\(^\text{21}\). Hence, we carried out local switching tests by applying a bias between the conductive PFM tip and the heavily doped Si substrate. Figure 3a–c displays the PFM phase images of 400, 30 and 4 nm thick CIPS flakes after writing box-in-box patterns with reversed DC bias in the centre (see Supplementary Figs 8–12 and Supplementary Note 4 for detailed results). Clear reversal of phase contrast confirms the switching of polarization in CIPS down to \(~4\) nm. Meanwhile, there are no obvious changes in the corresponding surface morphologies as previously reported\(^\text{10}\). Furthermore, the CIPS flakes exhibit superior ferroelectric retention despite the relatively low ferroelectric Curie temperature (Supplementary Fig. 13). The written domain patterns are still discernible after several weeks in ambient condition. These observations clearly rule out the possible contribution of PFM signal from the electrochemical phenomena\(^\text{22–24}\). The corresponding switching spectroscopic loops were also recorded under resonance-enhanced PFM mode by applying an AC electric field superimposing on a DC triangle saw-tooth waveform (Supplementary Fig. 14). The well-defined butterfly loops of the PFM amplitude signals and the distinct 180° switching of the phase signals further corroborate the robust ferroelectric polarization in CIPS ultrathin flakes (Fig. 3d–f). Furthermore, the longitudinal piezoelectric coefficient of the CIPS flakes were quantitatively determined using off-resonant PFM (Supplementary Note 5 and Supplementary Figs 15 and 16).

**Second-harmonic generation measurement.** To probe the structural phase change accompanying the ferroelectric to paraelectric transition, we utilized SHG microscopy, which is a sensitive probe of broken inversion symmetry and an excellent tool for the investigation of ferroelectric order\(^\text{25,26}\). We first explored the structural symmetry through polarization-resolved SHG at 300 K under normal incidence excitation (see Methods and Supplementary Information for details of the measurement). Figure 4a illustrates the SHG intensity dependence on excitation polarization for fixed detection along the horizontal (H) or vertical (V) direction (laboratory coordinates), which are fit well (solid lines) using the allowed in-plane second-order susceptibility elements for point group m (ref. 27; see Supplementary Note 6 and Supplementary Fig. 17). The nonzero SHG directly reveals the broken inversion symmetry
that generates the ferroelectricity. This is clearly seen in the approximate sixfold rotational symmetry of the co-linearly polarized SHG (Supplementary Fig. 5), which reflects hexagonal ordering of the displaced Cu and In sublattices in the ferroelectric phase. We further investigated the SHG intensity as a function of temperature for CIPS flakes with thicknesses from 100 to 1,000 nm.

**Figure 3 | Ferroelectric polarization switching by PFM for CIPS flakes with different thicknesses.** (a–c) The PFM phase images for 400 nm (a) 30 nm (b) and 4 nm (c) thick CIPS flakes with written box-in-box patterns with reverse DC bias. Scale bar, 1 μm. (d–f) The corresponding PFM amplitude (black) and phase (blue) hysteresis loops during the switching process for 400 nm (d) 30 nm (e) and 4 nm (f) thick CIPS flakes.

**Figure 4 | Thicknesses-dependent second-harmonic generation (SHG).** (a) Polar plots of the SHG intensity in H and V directions (laboratory coordinates) as a function of the excitation laser linear polarization for a 100 nm thick CIPS flake. (b) Temperature dependence of the SHG intensity for CIPS flakes with thickness of 100, 50, 30 and 10 nm, respectively. The SHG intensity of each thickness is normalized to its intensity at 300 K.
Ferroelectric diode based on CIPS/Si heterostructure. All the above evidence unambiguously establishes the existence of ferroelectric order in 2D layers of CIPS, making it a promising non-volatile element in vdW heterostructures. We test its applications in a prototype ferroelectric diode (inset of Fig. 5a). The vdW heterojunction was fabricated by exfoliating the CIPS flakes (30 nm) on to a Si substrate and followed by patterning the top electrodes (see Methods). Electrical contact to the top electrode was made using an AFM conductive tip. Figure 5a shows the current change by sweeping the bias from 2.5 V to −2.5 V, and back to 2.5 V. Figure 5b shows the resistance calculated at a bias of −1.3 V while sweeping the switching pulse. A clear large hysteresis and resistive switching are observed. The ON and OFF states, which correspond to the low- and high-resistance states, respectively, can be assigned. The resistive switching and resulting memory effect is due to the polarization switching of the ferroelectric CIPS layer, as evidenced from the piezoelectric switching measurement of the same device (Fig. 5c). The coercive voltage coincides minima in the amplitude loop as well as the switching bias in the phase signal coincides with the bias, at which resistive switching takes place. This strongly suggests that the ferroelectric polarization reversal is the origin of the resistive switching in the vdW diode. The on/off ratio of about 100 is comparable to that observed in tunnel junctions of the resistive switching in the vdw diode. The on/off ratio of as well as the switching bias in the phase signal coincides with the bias, at which resistive switching takes place. This strongly suggests that the ferroelectric polarization reversal is the origin of the resistive switching in the vdW diode. The on/off ratio of about 100 is comparable to that observed in tunnel junctions of the resistive switching in the vdw diode.

Discussion
In summary, we have unambiguously established room-temperature ferroelectricity in ultrathin CIPS flakes of ~10 nm thick as well as piezoelectric response in bilayer CIPS. A simple vdW CIPS/Si ferroelectric diode exhibits non-volatile memory behaviour with on/off ratio of ~100, exhibiting the capability of integration with well-established Si-based platforms. Our discovery greatly enriches the functionalities of the 2D material family and opens new possibilities for novel devices based on ferroelectric 2D materials.

Methods
Sample preparation and characterization. High-quality single crystals of CIPS were synthesized by solid state reaction as previously reported. The thin flakes were obtained by mechanical exfoliation from synthetic bulk crystals onto heavily doped silicon substrates with or without a 285 nm SiO2 layer on top. The thickness of the flakes was identified from their optical contrast and AFM. Raman spectrum was carried out using a confocal Raman system (WITec) with the 532 nm laser excitation. SHG measurements were performed in reflection geometry with 100 fs pulses at 786 nm and a repetition rate of 76 MHz, which were focused to a spot size of ~1 μm by a 40 × 0.6 NA objective lens (Olympus). TEM sample was prepared by dropcasting the solution, which contains exfoliated thin flakes after sonication of the thick CIPS crystal, onto a lacy carbon TEM grid. Z-contrast STEM imaging was performed on a Nion UltraSTEM-100, equipped with a fifth order aberration corrector, operated at 60 kV. The convergence angle is set to be ~30 mrad. All Z-contrast STEM images were acquired from the ~86–200 mrad range.

PFM and ferroelectric polarization measurement. PFM measurement was carried out on a commercial atomic force microscope (Asylum Research MFP-3D) under both resonance-enhanced and off-resonance modes. In resonance-enhanced mode, a soft tip with a spring constant of ~2 N m−1 was driven with an ac voltage (Vac = 0.5–1 V) under the tip-sample contact resonant frequency (~300 kHz). In off-resonance mode, a stiff tip with a spring constant of ~40 N m−1 was driven at 10 kHz. The inverse optical lever sensitivity (InOLS, nm/V) was calibrated beforehand to obtain quantitative piezoelectric displacement data. Vector PFM was performed by imaging both the out-of-plane and in-plane PFM at different azimuth angles between the sample and AFM cantilever. Ferroelectric polarization measurements were carried out using a commercial ferroelectric

Figure 5 | Electric characterization of the vdW CIPS/Si diode. (a) The I–V curves from the typical vdW CIPS/Si diode with 30 nm thick CIPS, by sweeping the bias from 2.5 to −2.5 V, and then back to 2.5 V. Inset is the schematic of the device. (b) Resistance-switching voltage hysteresis loop of the diode measured at a bias voltage of −1.3 V. The schematic representations of the ON and OFF states with respect to the polarization direction are shown in the bottom-left and top-right insets, respectively. (c) Out-of-plane PFM amplitude (black) and phase (blue) measurements on the same diode device shown in a.
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Data availability. The data that support the findings of this study are available from the corresponding author upon request.

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Acknowledgements
This work was financially supported by the Singapore National Research Foundation under NRF RF Award No. NRF-RF2013-08, the start-up funding from Nanyang Technological University (M4081137.070). J.W. acknowledges the support from the Ministry of Education Singapore under grant No. MOE2013-T2-1-052, MOE2014-T2-1-099 and RG126/14. K.S. and X.X. acknowledge the support from U.S. Department of Energy Office of Basic Energy Sciences (DOE BES, DE-SC0008145 and SC0012509). F.L. and S.T.P. acknowledge the support from U.S. Department of Energy, Office of Science, Basic Energy Science, Materials Sciences and Engineering Division, and through a user project at ORNL’s Centre for Nanophase Materials Sciences, which is a DOE Office of Science User Facility. P.S. was funded by NSF CMMI grant 1463205.

Author contributions
F.L., L.Y., J.W. and Z.L. conceived and designed the research. L.Y. and F.L. prepared the samples and device, performed the PFM measurement and electric characterization. K.S. and X.X. conducted the SHG measurement. X.L. and P.S. performed the theoretical calculation. P.Y., X.W., J.Z. and H.W. carried out the characterization of single crystal sample. L.L., H.H., S.T.P. and W.Z. performed the TEM characterization. X.X., P.M.A., J.W. and Z.L. conducted the data analysis. F.L. and L.Y. co-wrote the manuscript with input from all authors.

Additional information
Supplementary Information accompanies this paper at http://www.nature.com/naturecommunications
Competing financial interests: The authors declare no competing financial interests.
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How to cite this article: Liu, F. et al. Room-temperature ferroelectricity in CuInP2S6 ultrathin films. Nat. Commun. 7:12357 doi: 10.1038/ncomms12357 (2016).

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