High dynamic range CdTe mixed-mode pixel array detector (MM-PAD) for kilohertz imaging of hard x-rays

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Abstract: A hard x-ray, high-speed, high dynamic range scientific x-ray imager is described. The imager is based on the mixed-mode pixel array detector (MM-PAD) readout chip coupled to a 750\,µm thick cadmium telluride (CdTe) sensor. The full imager is a 2 × 3 tiled array of MM-PAD sensor/readout chip hybrids. CdTe improves detection for high energy x-rays as compared to silicon sensors, enabling efficient x-ray imaging to extend to >100 keV. The detector is capable of 1 kHz imaging and in-pixel circuitry has been designed to allow for well depths of greater than \(4 \times 10^6\) 80 keV x-rays. A charge integrating front-end allows for quantitative measurement of high flux x-ray images beyond the capabilities of photon counting detectors. Detector performance is summarized and experimental measurements are presented.

Keywords: Hybrid detectors; Pixelated detectors and associated VLSI electronics; Solid state detectors; X-ray detectors

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1 Introduction

The mixed-mode pixel array detector (MM-PAD) is a wide dynamic range hybrid scientific x-ray imaging device [1] that, when coupled to a silicon sensor, has demonstrated utility for scientific investigations at synchrotron sources [2–6]. The same silicon sensor hybrid device is also the basis of the electron microscope pixel array detector (EM-PAD) [7] that has recently acquired record breaking microscopy resolution [8]. The MM-PAD readout chip is distinguished by in-pixel signal processing electronics that combine a charge integrating front-end with threshold triggered charge removal circuitry and a digital counter to extend the single-image well-depth while retaining low noise performance. This pixel architecture can record accurate flux measurements at high intensities that are beyond the capabilities of photon-counting detectors while maintaining single-photon signal resolution. In addition, the charge integrating architecture used here is not susceptible to systematic errors which can arise in photon-counting detectors when events have charge-splitting between pixels, such as under- or over-counting. This paper reports the specific capabilities of the Cornell-produced CdTe MM-PAD detectors that are in use at both the Advanced Photon Source (APS) and Cornell High Energy Synchrotron Source (CHESS).

Silicon has served as a high quality x-ray and electron detection layer for the MM-PAD and many other hybrid devices. The material properties of silicon are well suited to provide high fidelity direct detection of x-rays for energies below 20 keV. At 20 keV, where silicon has an absorption depth approaching 1 mm, the practical usefulness of silicon is greatly diminished (a typical silicon sensor thickness is 500 µm). Cadmium telluride (CdTe), when compared to silicon, has the huge advantage of offering high-efficiency direct detection of high energy x-rays with an absorption depth of approximately 1 mm at 100 keV. A plot of efficiencies for selected materials and sensor thicknesses is shown in figure 1. This extended x-ray energy range is important for studies in material science where properties of physically large samples and/or high atomic number materials are investigated [9, 10]. CdTe has been extensively studied for x-ray and gamma-ray detection. The main advantage, high energy detection efficiency, is a direct result of high atomic numbers of cadmium (Z = 48) and tellurium (Z = 52). Additionally, a relatively large bandgap of 1.44 eV
Figure 1. Efficiency curves for a 500 µm thick silicon sensor compared to a 750 µm thick CdTe sensor. These are the standard sensor thicknesses used with the MM-PAD chip. Efficiency for a 500 µm thick Ge sensor is also plotted for reference. At this point, no germanium sensor is available.

leads to low dark current compared to silicon (bandgap of 1.12 eV) and makes it suited for charge integrating front-end electronics. At the same time, CdTe suffers from charge trapping in the bulk material which leads to material polarization. Charge trapping and material non-uniformity/crystal defects lead to variable lateral fields that make charge collection a function of x-ray interaction depth. Additionally, CdTe fluoresces with Kα energies of 27.5 keV for Te and 23.2 keV for Cd. The mean free paths of these fluorescent x-rays are approximately 62.6 µm and 118.8 µm respectively in CdTe, meaning that for pixel sizes on the order of 100 µm there is an expectation of non-ideal imaging effects caused by fluorescent x-ray photons exiting pixels after the initial x-ray interaction with the sensor. These material effects have been extensively explored and summarized in the literature [11–14]. Despite the drawbacks of CdTe, it remains one of the best options for high-energy direct detection of x-rays and, as described in this paper, can be used to make a scientifically productive x-ray imager when combined with a high dynamic range readout chip.

A summary of the MM-PAD capabilities are shown in table 1, where figures for both the silicon and CdTe sensor equipped detectors are offered for comparison.

2 Detector description

The application specific integrated circuit (ASIC) is the same used in the silicon version of the MM-PAD [1] and the EM-PAD [7]. The ASIC is fabricated in the Taiwan Semiconductor (TSMC) 0.25 µm mixed-mode process with metal-insulator-metal (MiM) capacitors. The pixel pitch is
Table 1. Specifications MM-PAD with Si and CdTe sensors.

| Specification                        | CdTe MM-PAD | Si MM-PAD   |
|--------------------------------------|-------------|-------------|
| Pixel Size                           | both 150 μm × 150 μm |            |
| Format (2 × 3 tiling)                | both 256 pixels × 384 pixels |        |
| Read Noise                           | 1.6 keV equivalent | 1.3 keV equivalent |
| Full Well (absorbed x-ray energy)    | 4.6 × 10⁸ keV | 3.8 × 10⁸ keV |
| Maximum Sustained flux               | 3.9 × 10⁹ keV/s | 3.2 × 10⁹ keV/s |
| Sensor Thickness                     | 750 μm      | 500 μm      |

150 μm on a regular grid with each individual ASIC having 128 × 128 pixels arranged in 8 banks of 16 × 128 pixels. A high-level diagram of the pixel is shown in figure 2. A charge integrating amplifier accumulates the signal generated in the sensor diode onto a small (50 fF) capacitor. The small feedback capacitance is chosen to provide single photon sensitivity at low intensity levels. The output of the amplifier is monitored by a comparator during integration. When a preset threshold is exceeded during exposure, logic increments a counter and initiates an event which removes a fixed amount of charge, Δq, from the input node. This keeps the charge integrator within its operating range, greatly increasing the dynamic range of the pixel. Integration from the sensor continues during each charge removal event in a dead-timeless process. Response continues to be linear up to a maximum input rate of 3.9 × 10⁹ keV/pixel/s. Pixel output at the end of a frame is mixed mode, consisting of an analog value that is sampled from the front-end integrator and the 18-bit digital word that tracks the number of charge removal events [15]. The analog value is digitized with a 12-bit analog-to-digital converter (ADC) located external to the chip, running at 2.5 MHz. The x-ray equivalent noise of the digitized analog signal is 1.6 keV when using a CdTe sensor. The 12-bit value from the analog residual and the 18-bit in-pixel counter are scaled and combined to yield a 30-bit intensity value per pixel per frame. Full calibration of the detector requires scaling the two parts of the 30-bit digital word together to yield a continuous, linear response. The analog

![Figure 2](image-url)  
**Figure 2.** High-level schematic of the MM-PAD pixel. A charge integrating amplifier is monitored by a voltage comparator which, when triggered, will increment a digital counter and initiate a charge removal event (~ 1.6 MeV equivalent charge) as the integration continues. At the end of each frame, each pixel yields 18 bits of digital data and an analog residual, digitized off-chip to 12 bits. These two numbers are scaled and combined to give a 30-bit intensity/pixel.
Figure 3. A 2×3 tiling of MM-PAD CdTe modules in a vacuum housing with front face removed. Each module has a dedicated printed circuit board mounted to a secondary ADC board at right angles which in turn mates to a backplane board. A bias contact to the front of each sensor is made with a length of 38 gauge magnet wire. This wire is attached with a small amount of silver paint in an area of the sensor that extends beyond the imaging area. The magnet wire is attached to the associated circuit board with epoxy for mechanical stability of the assembly. The wire extends off the circuit board to a bias distribution point to provide the ability to bias the sensors up to 1000 V. The CdTe hybridized modules, (A), are in thermal contact with an aluminum heatsink (B). The temperature of the heat sink is monitored and actively controlled with a thermoelectric cooler (C). Water is circulated through a portion of the detector housing to provide heat transfer.

The readout uses a row-select combined with a per-bank analog multiplexer to cycle through columns. The full ASIC is read in 860 microseconds, during which time the ASIC cannot acquire additional charge and the front-end of the detector is held in reset. The ASIC readout and the wire bonds that supply all power and necessary control signals to the chip are restricted to one edge of the ASIC, allowing the chip to be abutted on 3 sides when tiled in a larger array, minimizing inactive area.

The fully tiled array, designed and used by the Cornell detector group, consists of 6 ASICs tiled in a 2×3 array with each ASIC having a dedicated pixellated CdTe Schottky diode matching the 128×128 pixels of the ASIC. The tiled detector has 256×384 active pixels. In practice, the tiling of the diodes, associated guard rings, and assembly requires a small inactive gap of 5 to 6 pixels between sensor modules. The CdTe-indium Schottky hole-collecting diode is manufactured by Acrorad (Okinawa, Japan) and the bump-bonding of the CdTe to the ASIC is performed by Oy Ajat (now Direct Conversion, Espoo, Finland). Each ASIC in the tiled array has a dedicated aluminum heat sink and printed circuit board. The ASIC-CdTe hybrid is attached to the heat sink using a pattern of thermally conductive silicone (Nusil R-2930). The hybridized ASIC, associated
Figure 4. Histogram of detector output for a sparse data set over 40,000 frames. The x-ray energy is 80 keV and was collected at APS 1-ID-E. Fluorescent and escape peaks, corresponding to 80 keV minus the fluorescence energy, are labeled. Fluorescence for Cd and Te occur at 23.2 keV and 27.5 keV respectively.

A printed circuit board, and the heat sink comprise the detector module which is complete after the ASIC is wire bonded and a high voltage bias line is provided for sensor biasing. Six modules are mounted on a larger aluminum heatsink block. For the CdTe system, the temperature is typically maintained at 0°C with a thermoelectric cooler. The modules and the entire inside of the detector housing is kept at a vacuum to prevent the condensation of water and to thermally isolate the detector modules. Thin layers of Apiezon N vacuum compatible grease is used between heatsinks to improve thermal contact.

The modules are mated to six secondary circuit boards with 8 ADC channels/board. These in turn mate to a large backplane board that serves to route all electrical signals to and from the modules and also provides a vacuum feed through by being sandwiched between two sections of housing with o-rings providing the vacuum seal.

The full detector data stream is assembled and organized by a dedicated FPGA that rearranges bits and translates the pseudo-random in-pixel pixel counter outputs to integer values. Once the analog and digital information is properly arranged, the data is conveyed to a PC via a CameraLink connection. The maximum frame rate is 1.1 kHz as dictated by the 860 µs readout.

An assembled MM-PAD ASIC 2 × 3 tiling with CdTe sensor is shown in figure 3.

3 Performance

This detector has been used in several high-energy x-ray experiments at the APS and CHESS. One experiment [10], collected 4 dimensional data sets, tracking the dynamic behavior of individual
grains in a sample of titanium and magnesium alloys under stress at the 1-ID-E beamline at APS. Within these data sets, much of the area of the recorded diffraction in each image had ≪ 1 photon/pixel, allowing a spectral analysis of the recorded signal from the incident 80 keV x-rays, shown as a logarithmic plot in figure 4. X-ray fluorescence peaks and associated fluorescent escape peaks are clearly visible with the corresponding cadmium and tellurium fluorescent energies and escape peaks marked.

Performance of the detector at high flux was tested at APS beamline 6-ID-B. The detector was mounted in the path of the direct beam and the direct beam was imaged with no beamstop as a series of aluminum attenuators was used to vary the transmission from $9 \times 10^{-4}$ to 0.05. A Si $<111>$ monochromator was used to set the photon energy to 13 keV. Although this energy is within the range of a detector with a Si sensor, it is also well within the sensitivity range of CdTe and is useful to interrogate the high-flux behavior of this detector. The incident intensity was monitored by an ion chamber 325 cm upstream of the detector. Slits immediately upstream of the ion chamber restricted the x-ray spot size to $100 \mu m \times 800 \mu m$. With beam divergence, the spot size on the detector face was approximately $330 \mu m \times 970 \mu m$ FWHM. At each attenuator setting, 200 frames with 100 ms integration time were acquired. The detector response was linear over the flux range measured, as shown in figure 5. Due to a lack of absolute ion chamber calibration, the ion chamber readings have been normalized to the average reading recorded at the maximum transmission used here. At maximum transmission, the detector pixel receiving the greatest flux recorded $2 \times 10^7$ ph/pixel/s,

![Figure 5](image_url). Measurement of integrated spot intensity on the CdTe MM-PAD as a function of normalized ion chamber output.
equivalent to $2.6 \times 10^8$ keV/pixel/s. These measurements end well below the maximum flux the detector can accurately measure, $3.2 \times 10^9$ keV/pixel/s. This is well beyond the capabilities of photon counting detectors.

### 3.1 Radiographs

Figure 6 shows a low contrast radiograph of an aluminum coin that was placed in close proximity to a microfocus silver anode x-ray tube biased to 47 kV. The detector was placed to allow the geometrically magnified image of the coin to fill the detector ($\sim 3 \times$ magnification). Figure 6a is the raw radiograph. Figure 6b shows an image taken with the same x-ray source without the coin. This flood illumination shows the non-uniformity inherent to CdTe sensor material, as well as characteristic dark defects (displayed as dark pixels in the lower tiles of the detector). Figure 6c shows the ratio of a and b. This flat field correction significantly improves uniformity. The flat field correction is generally a function of x-ray energy because it depends on the average depth of x-ray conversion in the sensor material and lateral fields and/or traps that distort image. This can be an important consideration for polychromatic x-ray sources because samples can harden the transmitted beam significantly.

Figure 7 is a significantly higher contrast image of a light bulb as is shown without flat field correction. Nonuniformity in the flood illumination images is due to several effects. There are some sensitivity variations, but there are also significant lateral electric fields in the sensor material which cause lateral displacements in the recorded signal, and hence produce areal variations from pixel to pixel on the 1 percent level. For radiography, one properly corrects by dividing the flood illumination. However, since pixel areal variations are integrated intensity conserving, correcting diffraction data by simple division with a flood field may introduce some error [16]; see [17, 18] for a discussion of this effect and methods of calibration. In many cases the effect is small. Quantitative analysis of the CdTe sensor response can be found in [12].

### 3.2 Polarization

CdTe is well known to exhibit dose dependent polarization effects. We have observed and characterized several consequences due to polarization, namely the reduction of signal (count rate deficit) with dose, a deterioration of image uniformity with time and the lateral displacement of signal due to differential dosing [19]. These effects begin to be noticeable when an incident x-ray fluence of $10^{11}$ keV/mm$^2$ is absorbed by the detector and become quite pronounced when the fluence is greater than $10^{12}$ keV/mm$^2$. Onset of polarization can be reduced by operating at a higher sensor bias. 400 V was chosen for operation in this case as a compromise between polarization immunity and the number of pixels which exhibited fluctuating leakage current.

The polarization in the material can be reset through a change in bias applied to the sensor. Our procedure is as follows: apply a slight forward bias (-2 V for 60 s), followed by a return to the operating voltage (400 V in this case). We wait an additional 30 s for the sensor to reach a steady state before taking new images.

Figure 8 shows a sequence of images taken with a standard x-ray line-pair mask (cut from lead foil). These images were taken at two positions (displaced vertically by 4 mm) to highlight various features in the polarization progression. Except for a brief period to monitor at position B in the...
Figure 6. A low contrast image of an aluminum coin (Brazilian 1 Cruzeiro) taken with the CdTe MM-PAD and a silver anode microfocus x-ray tube with a bias voltage of 47 kV, a filament current of 230 µA, and a 2 mm aluminum x-ray filter. The displayed image is an average of one hundred 300 ms images (1.6 × 10^5 x-rays/pixel total impinging on the coin in the dataset). The coin was placed close to the x-ray source to magnify the image (∼3×). (a) The raw image from the detector with a simple dark frame subtraction. (b) A flat field image obtained by imaging the without the coin present. (c) The flat field corrected image, (image (a) divided by image (b)).

figure, all dose was accumulated at position A. Panel A3 shows a decrease in charge collection efficiency, a decrease in the uniformity of response, and a decrease in the resolution of the image. This last effect is due to the induced lateral fields which occur at the boundaries of exposed and unexposed regions. Panel B3 shows the resolution is maintained when a feature is moved into an uniformly polarized region, e.g., where the numbers imprinted on the mask have moved into a flat flood region. Notice also the ghosting at the old number positions. The intensity is higher than expected for an unpolarized region due to a lateral electric field pushing charge into these pixels from neighboring pixels. After a depolarization cycle, the response of the system returns to its initial state.

Displacement of images with many high contrast features is a tool which especially highlights changes due to polarization. We note the polarization sensitivity is often more noticeable with a high-dynamic range imager, but is likely present at the same magnitude in any imager made with this material. We have chosen an aggressive de-polarization step to improve fidelity.

4 Conclusions

The CdTe MM-PAD detector has demonstrated exceptional capability as a high efficiency quantitative imager for high energy (> 20 keV) x-rays. The MM-PAD ASIC combines a charge integrating front-end with threshold driven charge dumps to achieve within each image both the single x-ray detection capability performance expected of photon counters [2] and an instantaneous count-rate
Figure 7. Log scale image of a light bulb with no flat field correction. The image was taken with a silver anode microfocus x-ray tube biased to 47 keV, with an electron current of 500 µA, and 1 mm aluminum x-ray filter. The displayed image is an average of one hundred 800 ms images. A total of $3.2 \times 10^5$ x-rays/pixel illuminated the frame over the data set. In the most absorbing portions, < 2% of the x-rays are transmitted.

performance which exceeds that of photon counting imagers [3], all while framing at a kHz rate. These characteristics make the detector ideally suited for many difficult time resolved experiments, such as the temporal response of individual metal grains to periodic stress [9, 10], and ptychography, both in the sparse and high flux regimes [2, 4].

To our knowledge, all available CdTe materials display non-ideal behavior that can lead to image distortion and charge collection inefficiency, phenomena collectively known as "polarization" [2, 4]. Users should be aware that polarization effects occur in all detectors that use CdTe as the x-ray sensor material. Depolarization by periodically cycling the voltage across the CdTe sensor mitigates these effects significantly and for many applications the magnitude of non-ideal performance does not limit scientific observation [12, 19]. As an example, integration of bright x-ray Bragg spots can mask small scale distortions of details within the spot while yielding good integral values. Care should always be taken to limit the total integrated dose on all regions of the sensor between depolarization cycles.

In sum, the CdTe MM-PAD described in this paper is a valuable, productive tool in the arsenal of scientific x-ray imagers.

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Figure 8. Onset of sensor polarization and efficacy of reset procedure. Images shown are of a standard x-ray line-pair mask taken at two different positions on the detector (A1-A4 at one position, B1-B4 displaced vertically from A by 4 mm). Except for the brief time for exposing images at position B, all dose was accumulated at position A. Images taken after accumulated dose as follows: (A1,B1) — fresh detector; (A2,B2) — a dose of $1 \times 10^{10}$ x-rays/mm$^2$; (A3,B3) — a dose of $8 \times 10^{10}$ x-rays/mm$^2$; (A4,B4). — after a new depolarization cycle. Panel A3 shows a loss in charge collection, a degredation to image resolution (due to lateral fields) and increased nonuniformity in the image. B3 highlights the reduction in charge collection in the dosed regions, but also shows lateral electric field effects through the increased charge collection of pixels at feature boundaries (notice ghosting of numbers which show a brightness greater than 1 in this panel). Images normalized to the signal in the brightest portions of panel A1.

categorize the CdTe MM-PAD. This research used resources of the Advanced Photon Source, a U.S. Department of Energy (DOE) Office of Science User Facility operated for the DOE Office of Science by Argonne National Laboratory under Contract No. DE-AC02-06CH11357.

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