Compartmentalisation-based design automation method for power grid

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Abstract: Power grid design and maintenance are conducted to solve the problems caused by load growth over time and to stay within the constraints of voltage drop, power factor, etc. Typically, solutions to these problems are optimised individually. Considering multiple problems simultaneously and applying different solutions require vast design space exploration. This exclusively needs advanced algorithms and complex global optimisation methods which are not easily-applicable in different scenarios. In the state-of-the-art methods, for solving multiple problems simultaneously, these individually optimised solutions are applied sequentially to the power grid. In this so-called uncoordinated method, the final solution may not be optimal solution considering all the variables, since it is considering the overlapping effect of the solutions on the power grid. To validate the compartmentalisation method, a detailed distribution grid has been modeled. After analysing the possible solutions and optimisation, power loss was reduced 45% and total cost decreased by 71%, compared to the uncoordinated method.

Nomenclature

We provide a variable list that is complementary to [1].

Sets

| Variable | Description |
|----------|-------------|
| TO       | thermal overload |
| VD       | voltage drop |
| PF       | power factor |
| WU       | wire upgrade |
| VU       | voltage upgrade |
| CA       | capacitor addition |
| LT       | load transfer |
| ψBE      | edges of the existing grid |
| ψNE      | nodes of the existing grid |
| ψP       | all problems calculated after an analysis |
| ψD       | load edges |
| ψG       | distributed generation edges |

Real variables

| Variable | Description |
|----------|-------------|
| Ck      | fixed costs for installation for solving a given problem k from the ψ set |
| Zlevj   | indicates the z-level dependency of the jth node |

Complex variables

| Variable | Description |
|----------|-------------|
| Ii       | current of edge i |
| IiMax    | maximum current capacity of edge i (i.e. thermal limit of edge) |
| IiSlack  | the slack of edge i, defined as IiMax − Ii |
| Vj       | voltage measured at node j |
| VjDB     | localised VD measured for edge i with respect to nodes jnom and j0 of the edge |
| VjDIV    | voltage DROP measured for edge i with respect to the nominal voltage and node j0 of the edge |
| PFj      | power factor measured at edge i with respect to node j0 |

| Parameter | Description |
|-----------|-------------|
| φi        | phase angle between voltage and current measured at edge i with respect to node j0 |

| Parameter | Description |
|-----------|-------------|
| Vnom     | nominal voltage of the existing system |
| dj       | nodal load at node j |
| wcost    | designer-defined weight preference for objective function to minimise cost |
| wsolved  | designer-defined weight preference for objective function to maximise number of problems solved |
| ΔV%      | maximum tolerable voltage deviation from Vnom expressed as a percentage |
| IMaxA%   | maximum tolerable current with respect to thermal capacity, expressed as a percentage |
| PFMaxlag | maximum tolerable leading power factor |
| PFMax   | maximum tolerable leading power factor |
| Nsolve   | number of solutions to implement per run |

ListTypeSolve

| ListTypeSolve | Description |
|---------------|-------------|
| an ordered list that indicates the type of power grid problems to solve first |

1 Introduction and related work

Since the beginning of the industrial revolution, the design and maintenance of power grids for the purpose of providing the residential and industrial demands have been very important responsibilities of governments, corporations, utilities, and individuals alike. The problems arising from design and maintenance of these grids, are whether from load growth or from a disturbance caused by adding new loads/components to a stable existing system. Examples of these problems include conductor thermal overload, excessive voltage drops (VD), power factor (PF) regulation, etc. [2–6].

Many regulating bodies and owners of the power grids set specific constraints for reliability and efficiency purposes. Reliability is met by following the operational requirements set by the manufacturers.
of the various components that comprise the power grid (such as the transformers and voltage regulators). Maintaining reliability would help extend the longevity of these devices. Efficiency is met through both meeting reliability standards as well as examining where power losses occur significantly, either due to the system operating at a low-voltage level or due to an inability to maintain a high PF as desirable, etc. Typically, not meeting these constraints are considered as power grid problems [7].

Typically, solutions to these problems are applied by optimising just a single variable of the power grid. Hence, they are considered easily-applicable for power grid designers. On the other hand, when considering multiple problems and variables simultaneously, there may be overlapping effect of the solutions on the power grid which will deviate our solution from the optimum point. These multi-variable problems are typically solved using global optimisation methods [8, 9]. However, considering this wide range of solutions and components simultaneously in order to optimise multiple variables for lower implementation cost is too complex to handle (see Section 3). As a result, multi-variable global optimisation methods are not applied to power grid design and maintenance in practice. Therefore, in the state-of-the-art methods, designers utilise the single-variable optimised solutions sequentially to address the problems [2]. They might implement multiple solutions before checking if the problems are resolved. In this so-called uncoordinated method, since the designers are not considering the overlapping effect of the solutions on the power grid, the final solution may not be optimal in terms of cost. Moreover, the interconnected nature of the power grid adds further complexity to this process [10, 11].

In this paper, we present a novel compartmentalisation-based method to solve multiple problems in the power grid, using the existing optimised single-variable individual solutions for lowering the implementation cost. Compartmentalisation is the method of condensing the possible solutions to the aforementioned power grid problems into solution blocks, such that the optimiser may use them interchangeably when attempting to meet all the system constraints. A critical importance to this method is the possibility of solving a type of problem in multiple ways, e.g. a thermal constraints. A critical importance to this method is the possibility of solving multiple problems sequentially, e.g. when using uncoordinated method.

In summary, the problem of solving multiple power grid design problems simultaneously poses the following challenges:

(i) Complexity of a multi-variable global optimisation as a non-practical method for solving multiple power grid problems while designing or upgrading.
(ii) Considering the overlapping effect of the solutions when solving multiple problems sequentially, e.g. when using uncoordinated method.
(iii) Accounting the order of applying the individual solutions for solving multiple power grid problems simultaneously.

1.2 Our novel contributions

To address the aforementioned challenges, our design automation method of compartmentalisation is proposed to do:

(i) Compartmentalising sets of optimised and standard solutions implemented for power grid design and maintenance in order to tackle each type of design problem based on the desired constraints.
(ii) Analysing the order of applying these sets of solutions and their effects (possible overlapping effect) on power grid considering implementation cost.
(iii) Optimising the order of sequential implementation for these solutions at system level which allows the designers to find the most cost-effective final solution while checking to see if more solution is required.

The components required for our compartmentalisation-based design automation method with further details of its implementation are abstracted in Fig. 1.

![Diagram](image)

**Fig. 1** Our compartmentalisation-based design automation method
2 Definitions, design problems and solutions

Definition 1: The compartmentalisation (see Fig. 1) is a method of representing, solving, and optimising power grid design problems. It uses the existing optimised solutions, to solve the power grid problems and meet all the design-time constraints. It also provides a process for testing different solution types for any given power grid problem. This method requires component databases which contain the possible different types of power grid components to choose from, as well as costing information for the different types of solutions.

2.1 Power grid definition

Defining an existing power grid is the very first step, and requires specifying a netlist that electrically and spatially defines the connectivity. Electrical definition is achieved by specifying the nodes and edges in the system, while spatial definition provides geometrical coordinates of these nodes and edges. Typical X-Y-Z coordinate system may be used for determining distances between nodes. Nodes serve as electrical connective points, where voltage is usually defined and edges are the various electrical components between the nodes. Typically, each component which forms an edge, is connected to a number of nodes, depending on the type of component.

Definition 2: A component database is a set \( (ED_{\text{Type}}) \) of different varieties of one type of electrical component, where \( s' \in ED_{\text{Type}} \) is a unique electrical component. \( s' \) holds parameter information such as: name of the component, the type of the component, electrical parameters relevant to the component, and costing information. The component database also serves to limit the components to consider when solving and optimising the system, as a solution will only be considered if \( \forall s' \in ED_{\text{Type}} \) is true.

The component databases that will be used in this paper are: \( ED_{\text{OH}} \) for overhead conductors, \( ED_{\text{U}} \) for underground conductors, \( ED_{\text{REG}} \) for regulators, \( ED_{\text{TFM}} \) for transformers, and \( ED_{\text{CAP}} \) for capacitors.

For instance, we have generated a database for wire conductors it consists of their electrical characteristics and the associated cost for their implementation, as shown in Fig. 2.

The two aforementioned steps are critical for defining the power grid, but for both the regular power flow simulation and the compartmentalisation method, designer-defined parameters and design-time constraints will need to be defined.

Definition 3: Design-time constraints are the limitations a designer imposes on the power grid to dictate specific power quality or reliability metrics. They may also be related to electrical parameters such as currents and voltages.

They are the maximum tolerances on the operating parameters of any electrical component such as distributed/central consumers and generators whether at transitive or stable state of grid operation. These parameters may include: \( \Delta V_{\text{Max}} \% \) for defining the maximum tolerable voltage deviation from the system nominal voltage \( (V_{\text{nom}}) \), \( P_{\text{Max}} \% \) for defining the maximum tolerable current with respect to an edge’s thermal capacity, and both \( P_{\text{Max}} \% \) and \( P_{\text{Max}} \% \) to define the maximum tolerable lagging and leading PFs, respectively. Common values for these parameters are [22, 23]:

- \( \Delta V_{\text{Max}} \% = 10\% \)
- \( P_{\text{Max}} \% = 100\% \)
- \( P_{\text{Max}} \% = 0.95 \)

2.2 Power grid design problems and solutions

The following are multiple common power grid design problems that many utilities and regulating bodies consider critical. Each type of problem comes with a possible set of easily-applicable solutions. The decision to implement a certain type of solution depends on the electrical parameters, designer’s preference, and the circumstances of each problem.

2.2.1 Thermal overloading: Many components have a maximum acceptable current (i.e. thermal limit) that must be respected for longevity purposes. Utilities have standards to allow for above-normal current during contingency conditions, however, the frequency of these contingencies is supposed to be low. Consequently, building a steady-state power grid that obeys these thermal limits is very important. Typically 100% of the component’s amperage capacity (ampacity) is an established constraint, however some utilities may opt to follow a more stringent constraint (90% of this ampacity capacity) to allow for extra margin.

Thermal overloads are critical problems that may be solved using the following easily-applicable solutions, as seen in Fig. 3:

![Fig. 2](cost_of_upgrading_wires_conductors.png) **Fig. 2** Cost of upgrading wire conductors with different ampacities [21]

![Fig. 3](possible_solutions_example_thermal_overload_problem.png) **Fig. 3** Possible solutions to an example thermal overload problem
Algorithm 1:

```
Input: Grid with wires \( w_i \) ∈ \( \Psi^{BE} \), nodes \( n_j \) ∈ \( \Psi^{NE} \)
Output: New Grid with wires \( w_i \) ∈ \( \Psi^{BE} \), nodes \( n_j \) ∈ \( \Psi^{NE} \)
1 while True do
   2 foreach \( w_i \) ∈ \( \Psi^{BE} \) do
      3 if \( I^{Max}(w_i) \leq 0 \) then \( WU_i = 1 \)
      4 else \( WU_i = 0 \)
   5 foreach \( n_j \) ∈ \( \Psi^{NE} \) do
      6 if \( (PF_{\text{Max}}(n_j) \leq PF_{\text{Min}}(n_j)) \) then \( CA_j = 1 \)
      7 else \( CA_j = 0 \)
      8 if \( (V_U / \Psi_{\text{Feeder}(n_j)} \geq \text{NumMax}(n_j)) \) then \( VU_i = 1 \)
      9 else \( VU_i = 0 \)
   10 if \( \text{load imbalance} \geq \text{Max imbalance} \) then \( LT = 1 \)
   11 else \( LT = 0 \)
   12 if \( \text{No problem} \) then break
   13 foreach \( w_i \) ∈ \( \Psi^{BE} \) based on \( Z_{\text{level}} \) and \( List_{\text{Type,Solve}} \) do
      14 if \( (WU_i = 1) \) then
         15 Find \( WS \) with \( I^{Max}(WS) \geq I(w_i) \)
         16 Upgrade \( w_i \) to a new wire type \( WS \)
   17 foreach \( n_j \) ∈ \( \Psi^{NE} \) do
      18 if \( (CA_j = 1) \) then
         19 Determine capacitor size \( Q \) (VARs) based on \( PF_i \)
         20 Add \( Q \) in parallel with \( w_i \)
      21 if \( (VU_i = 1) \) then
         22 foreach \( V_{\text{level}} \geq V_{\text{num}} \) do
            23 if \( V_{\text{level}} / Z_{\text{num}} \leq I^{Max}(w_i) \) then
               24 Perform voltage upgrade by adding transformer
      25 if \( (LT = 1) \) then
         26 Let \( X_T \) be \( X_i \) with max \( \text{DOWNLOAD}(X_i) \)
         27 Let \( X_S \) be \( X_i \) with min \( \text{DOWNLOAD}(X_i) \)
         28 \( \delta = \text{DOWNLOAD}(X_i) - \text{DOWNLOAD}(X_s) \)
         29 \( S = X_T \)
         30 foreach \( v \in X_s \) sub-tree do
            31 if \( \text{DOWNLOAD}(v) \geq \delta \) then
               32 Add \( v \) to \( S \)
         33 foreach \( v \in X_t \) sub-tree do
            34 if \( I^{Max}(v) \geq \delta \) then
               35 Add \( v \) to \( T \)
         36 Find a pair \((x, y) \in S, T \) with the shortest distance
         37 Add a tie line between \((x, y)\)
   38 return \( \Psi^{BE}, \Psi^{NE} \)
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Fig. 4 Thermal overload solution selection algorithm

(i) Replace the existing conductor with a conductor of a sufficient thermal rating.
(ii) Adding an appropriately-sized capacitor to achieve a more efficient PF and reduce thermal overloading.
(iii) Increasing the voltage level of the section with the thermal overloading problem via a transformer addition or upgrade.
(iv) Transferring load on the overloaded section to another less-loaded section of the whole grid.

As there are multiple types of solutions, an ordering (permutation) of the easily-applicable solutions must be done. It is important to note that while not all solution types fit a specific problem, it is not always true that only one solution perfectly solves a problem. An assessment of a variety of different solutions must be allowed. Consequently, binary terms for each problem \( i \) must be assessed to determine whether they can be implemented or not. Algorithm 1 (see Fig. 4) shows exemplary solutions to thermal overloads, and uses the binary terms \( WU_i, CA_i, VU_i \), and \( LT \) to evaluate the possibility of using wire upgrades, capacitor additions, voltage upgrades, and load transfers, respectively. The designer-defined list \( List_{\text{Type,Solve}} \) specifies the designer’s preference for the order of trying out different solutions; an example of such a list for thermal overloading problem may be \( List_{\text{Type,Solve}} = [WU, LT, VU, CA] \). It needs to be noted that, this ordering shown in Algorithm 1 (Fig. 4) is an example of illustrating the solution applying process. However, the algorithm can be generalised and the solutions can be applied in any ordering defined by the compartmentalisation method and its optimiser.

2.2.2 Voltage drop: VD is the degradation of voltage potential across a series of lines and device components that make up the power grid, due to the inherent impedance of these components (as per Ohm’s law). Many of the devices have a limited VD tolerance from the nominal voltage for proper operation. Therefore, the Utility is responsible for VD correction, in order to ensure a high-standard reliability and power quality. Although ANSI standards specify a \( \Delta V^{Max}_D = 10\% \) from nominal, in general, many utilities usually opt to follow a more strict voltage tolerance (e.g. \( \Delta V^{Max}_D = 5\% \)) instead. Although VD is usually an important constraint to the objective function, its exact implementation is not the same across different utilities. As such, giving the designer flexibility in specifying the constraints is key. VD violations are critical problems that may be solved using the following easily-applicable solutions:

(i) Replace existing conductors upline to the problem area with a conductor of lower impedance so as to reduce the VD of the conductor.
(ii) Adding a voltage regulator upline to the problem area.
(iii) Increasing the voltage level of the section with the VD problem via a transformer addition or upgrade.
(iv) Transferring load from the problem section to another less-loaded section of the whole grid.

For conductor replacement, the objective is to replace a sufficient amount of conductor such that the VD at the problematic area is reduced to the designer-defined system tolerance (\( \Delta V^{Max}_D \)). Placing regulators is another alternative to solve VD problems. Regulators at the distribution level feature negative-feedback control loops that attempt to maintain a constant voltage level. The addition of regulators is a much simpler, and usually a less expensive option, than performing wire upgrades. However, regulators also add more power loss to the system and may not be desirable to add if there are already many regulators for maintaining the voltage levels.

2.2.3 Power factor: PF is another type of constraint typically applied by utilities. It is primarily a reflection of power efficiency (as a very low PF indicates a much higher reactive power component, which entails a higher current running through the system than needed). Some power grid components may have constraints for the PF, they are capable of operating at. Consequently, this constraint is a more useful guide towards improving power efficiency, rather than as a critical power operation constraint.

Achieving higher PF (\( > 1.00 \)), only comes through a reduction in reactive power consumption. Consequently, PF correction, usually in the form of adding capacitor, is typically the best way to solve PF problems. Balancing the reactive power with a suitable sized capacitor to bring lagging PFs back to leading is the best approach, and is similar to the capacitor addition algorithm. The converse situation of needing to correct a leading PF is not as common, but may occur during non-peak load periods, when there are more capacitors added to the grid than are needed. As the focus of the
3 Optimisation formulation and comparisons

To properly validate compartmentalisation, it is necessary to compare its performance to other types of design methods. In one method which utilises multi-variable global optimisation, all power grid problems $\Psi_F$ are solved during post-analysis time simultaneously while considering all the power grid variables [8, 9]. The multi-variable global optimisation method may not be applied in power grid design and maintenance in practice, see the following for the justification: each problem in a power grid can be defined by multiple constraints, where each one of them addresses a specific aspect of the problem. There might be some constraints that overlap (i.e. meeting a constraint or solving the problem in that aspect, may benefit other constraints in other aspects of the problem). Moreover, for each subproblem or constraint, there may be multiple variables to consider, in order to discover the problem and apply an individual solution (or solving the problem in that aspect, may benefit other constraints in other aspects of the problem). Therefore, global optimisation may not be applied in power grid design and maintenance since the single-variable optimisations are done sequentially. However, in our compartmentalisation method the permutation or ordering of the solutions (single-variable optimisation) is also optimised which enables us to consider the overlapping effect of the solutions (see Section 4). This becomes more efficient, since the compartmentalisation is checking the power grid to see if more solution is required before applying. This will result in a solution close to the global optimum point for multi-variable optimisation which was not possible in uncoordinated methods.

4 Present compartmentalisation method

Compartmentalisation method is trying out and substituting different solutions to certain power grid problems. The cost of the final solution is evaluated just by the summation of all the individual solutions recommended. However, the attempt of finding the most cost-effective solution, requires exploration across different scenarios (i.e. implementing a voltage upgrade solution as opposed to a wire upgrade for a thermal overloading problem). Moreover, there is an interest in performing in multi-optimisation evaluation; examining how effective one single implemented solution is to the whole. One possible metric is to evaluate how many subproblems a single solution implementation solves, another metric is to evaluate the cost per subproblem solved. These costs include both the cost of purchasing materials as well as the cost of installing the materials onto the system. When considering wire upgrades on an existing line section, the solution cost also includes the cost of removing the existing conductor. Finally, the selection and ordering of the solutions for a single type of problem need to be optimised.

Definition 4: The cost of a solution $C_{i,k}$ is defined as the total value (monetary and manpower effort) needed to implement a solution $h$ to a power grid problem $k$. It needs to be noted that the maintenance cost for each solution or component can also be included in the capital cost as extra information in order to be considered during the optimisation of the compartmentalisation method.

Definition 5: The designer-defined weights for the objective function parameter $\omega_{\text{out}}$ and $\omega_{\text{ solved}}$ represent how much impact the cost of a solution and the number of solved problems per solution has on guiding the optimisation, respectively. The objective function focuses on minimising the cost of the solution $h$ proposed for each problem $k$ in $\Psi_F$, as well as maximising the number of solved problems per solution. This function is represented through (5), with the constraints listed in (6)–(9). The weights $\omega_{\text{out}}$ and $\omega_{\text{solved}}$ allow the designer to guide exactly how much weight the optimiser should assign to either minimising the cost of the solutions proposed over maximising the problems solved per solution, respectively. The actual meaning of these weights is a joint interest in both the designer’s preferences for monetarily cheaper solutions in the final (by reducing the cost of a solution over all solutions recommended) implementation, as well as to guide the sequential optimisation (by solving the maximum number of problems per solution). It should be acknowledged that the former weight is universally useful to the designer, while the latter weight is useful only to the guidance of the optimisation.

$$
\begin{align}
\text{min. } C_{\text{tot}} &= \sum_{i,k,h} \omega_{\text{out}}^i C_{i,k,h} - \omega_{\text{solved}}^i \text{Num}_{i,k,h}^\text{solved} \\
\text{subject to: } &\forall i \in \Psi_F \\
I_i &\leq (I_{\text{max}}^i)^{\text{Max}_i} \\
V_{\text{DN}} &\leq (V_{\text{nom}}^i)^{\Delta_{\text{Max}_i}} \\
PF_{\text{Max}} &\leq PF \geq |PF_{\text{lead}}|
\end{align}
$$

Although relaxing the solution space in the multi-variable global optimisation method may help the practicality of the method in terms of complexity, it may deviate from the global optimum solution. Therefore, global optimisation may not be applicable in power grid design and maintenance in practice. On the other hand, the complexity of the uncoordinated method increases polynomially,
A critical component of the compartmentalisation is the selection and ordering of the solutions in solution library. It takes in information about the problem being solved and determines the best possible types of solution to implement. In addition, it also has access to the component database in order to know what possible components to upgrade to. Cost information about the component upgrades is also included in the component databases from [21, 25, 26], so that it may be calculated towards the cost-based objective function. The designer can specify the order of trying out individual upgrades by modifying $List_{TO,Solve}$ which not only contains the overarching types (thermal overload, voltage drop, power flow, etc.), but also the individual solutions for each type (wire upgrade, voltage level change, capacitor additions, etc.). The optimisation must be able to analyse and compare multiple solution orderings to find the most cost-effective one. For the purpose of comparison, the power grid simulator will need to re-run to perform the analysis. This is achieved by calling a function such as Algorithm 1 (Fig. 4), applying the solutions in the defined order, and checking the new power grid. As an example, a common $List_{Type,Solve}$ and $List_{TO,Solve}$ may be:

$$List_{Type,Solve} = \{TO, \text{VD}, \text{PF}\}$$

$$List_{TO,Solve} = \{\text{WU}, \text{LT}, \text{VU}, \text{CA}\}$$

Compartmentalisation needs to be able to explore the solution space and attempt different solutions. As demonstrated in the solving methods, multiple solutions may be provided to a given type of problem. Consequently, the need to examine each solution and explore the benefits of each, needs to be represented. The optimisation process should consider not only the total benefits of all solutions proposed, but also the delta benefit provided by a single solution. For instance, one solution implementation that solves five problems could be considered more preferable over one solution implementation that only solves 2 problems. Unfortunately, it is possible that this per-solution optimisation may result in finding only a local optimum as opposed to a global optimum. For a solution $h$ to a problem $i$, the variable representing this number of problems solved by the $h$ solution would be $\text{Num}_{a,h}^{\text{Solved}}$. This value would ideally be at least 1 (solving the problem $i$ itself), but a higher value would be indicative of being more desirable.

To solve the problems, the optimiser performs multiple iterations of modifying the grid via the information provided in $List_{Type,Solve}$. When attempting multiple solutions at once before applying them (as controlled by $N_{solve}$ parameter), different permutations across these solutions are also considered.

5 Experimental results

5.1 Experimental setup

To properly validate the compartmentalisation method, we have used ‘IEEE 123 Node Test Feeder’ [27] model as the base grid, and expanded it by connecting four more of these 123-node systems. Each system has different load growth rate which is generated randomly. The availability of the four systems linked only at the substations, allows us to transfer the load from an overloaded system to another under-loaded one. The power grid behaviour has been defined and simulated by the state-of-the-art power grid simulator software GridLAB-D [15] and GridMat [14, 28] using complex equations inside their libraries. Though many methods of power flow analysis exist, we have used Newton–Raphson method because of its very fast convergence rate [29]. Moreover, the algorithms for the optimiser in compartmentalisation (see Section 4) are implemented using the Python programming language [30]. The main optimisation has been done using an exhaustive search. Multiple Python scripts are implemented to parse the grid model (GLM file), export it as a set of databases, and model the new grid after applying the solutions. These databases holding information about the grid are used as inputs to our compartmentalisation method. The modular programming is used to provide the designer, the capability to add or remove specific problems, constraints or solutions in the algorithm.

5.2 Case studies and method analysis

For each solution applied to a problem, we have used a variable called ($N_{solve}$) that defines the number of individual solutions applied for each subproblem before doing the simulation again to check whether the power grid needs more solutions. We analyse the effect of $N_{solve}$ on the result of the compartmentalisation method. By increasing ($N_{solve}$), more solutions will be applied per iteration, as a result, there might be more solutions overlapping and resulting in higher total cost. On the other hand, by decreasing ($N_{solve}$), less solutions will be applied in each iteration, as a result, some subproblems may be solved by other existing solutions which would mean that the said subproblems would be ignored in following iterations resulting in an overall less total cost (Fig. 5). On the other hand, the number of solutions applied totally may increase with a larger $N_{solve}$. However, there is a point ($\beta$) in which the increase in the number of solutions gets less than the increase in the total cost. This means that for simulations, where $N_{solve}$ is larger than $\beta$, it is observed that more expensive solutions are used.

Analysing $N_{solve}$ while considering all possible permutations of problems and solutions shows that increasing $N_{solve}$ more than a specific point ($\alpha$), increases the total cost significantly (see Fig. 5). The value of $N_{solve}$ at point $\alpha$, depends on the overlapping factor between individual solutions to the problems. Moreover, if two individual solutions to a problem have overlapping effect on the grid (e.g. applying two individual solutions may improve VD on the same node, though the solutions are supposed to improve the VD of two other nodes), applying both in one iteration without checking whether the problem still exists, may increase the total cost, though it could have been solved with less total cost. On the other hand, increasing $N_{solve}$ and total cost may result in further reduction of power loss in the wires. Although excessive power loss reduction benefits the Utility, the total solution may get expensive for the same constraints.

The uncoordinated method is like applying all the individual solutions ($N_{solve} \to \infty$) sequentially (with an arbitrary ordering), with the assumption that they do not overlap. In this case, the total cost reaches the maximum value possible (Fig. 5). The power grid is not checked to see if more type of solution is required or not. Despite the large total cost of implementing the state-of-the-art uncoordinated method, the results show the most reduction in power loss as a result of over-engineering.

As we saw in Figs 5 and 6, increasing $N_{solve}$ too much will result in significant increase in total cost and getting solutions further from optimum point. Therefore, $N_{solve} = 16$ at point ($\alpha$), may be the best simulation setting to find the best solution for this particular grid system. By applying the solutions around the optimum point, we have decreased the total cost of the final solution ($\$ 251$ K) by $70.7\%$ compared with the uncoordinated method ($\$ 846$ K) and minimised the power loss about $45\%$. This value is called the...
Final solutions may vary by changing the order of applying them to address the problems. As shown in Fig. 7, multiple types of solutions may be applied to solve different problems. As it has been stated before, the order of applying solutions is crucial. For instance, the load of the grid connected to node 72_2 is transferred to node 54_4, another portion of the grid. This transfer resulted in one wire conductor upgrade for the edge connecting load 52_4 and load 53_4. On the other hand, if the wire upgrade solution is applied before the load transfer solution, there would be more wire upgrades in the overloaded grid, resulting in a possible greater total cost.

On the basis of the number of solutions and problems, we have analysed 24 different orders with Nsolve assumed to be 16 (the optimum point). Some of them are shown in Table 1.

There are two different types of permutations; permutation of solutions or subproblems within a problem, is called intra-permutation and permutation of problems at the higher level is called inter-permutation.

5.3 Intra-permutation

Changing the order of subproblems or solutions within a problem might have less effect, since the constraints may not overlap significantly. As shown in the 1st and 2nd rows of Table 1, applying the LT (load transfer) solution before applying the WU (wire upgrade) solution for the thermal overload (TO) problem may have less total cost (1.3% decrease). In addition, applying the capacitor addition (CA) solution before the WU (wire upgrade) solution for the VD problem may decrease the total cost (0.5% decrease) [The rates used for evaluating the costs may have a significant effect on the total cost. If a solution is more preferable, it may be assigned less cost or weight in order to get the optimiser to consider that as the best order.].

5.4 Inter-permutation

On the other hand, looking from higher level, by changing the order of problems to be solved, we might get better results. Moreover, the overlap between different problem may be large enough that solving one may benefit another one, resulting in less total cost. Moreover, some problems may have the same solution to meet their constraints, e.g. VD problems and PF problems may have the same solution as CA. For instance, the fifth and sixth rows in Table 1, show that solving PF problems before VD problems would result in a non-significant decrease in cost (0.2% decrease); which can be explained as such since both problems use the same solution to meet a constraint. Moreover, solving the PF problem before the TO problem would decrease the total cost (0.9% decrease) (shown in 7th and 8th rows), though the problems do not overlap enough to change the result significantly. On the other hand, by looking at 9th and 10th rows, solving the VD problems before TO problems may result in a significant decrease in the total cost (6% decrease). The reason for this is suggested to be that these are using different solutions to meet different constraints, which results in overlap in some parts of the grid. Moreover, when TO problems are solved before VD problems, more constraints are met (i.e. the number of total problems decreased). Therefore, it seems the overlapping effect not only improved the total cost but also solved some other problems, which also may be the reason for the observed decrease in total cost. The final solution total cost depends greatly on the cost of the components. Moreover, the labour cost may change the total cost. The difference between the total cost at the point with optimum
simulation settings against that of the uncoordinated method, indicates the efficiency of the method in that specific case. As shown in Fig. 8, by increasing the price of the components, the efficiency of the method increases, resulting in much less total cost compared with the uncoordinated method [2] (better optimisation). On the other hand, in Fig. 9, by decreasing the labour cost we will get better efficiency. This may be due to the fact that increasing the labour cost might dominate the total cost, resulting in no space for optimisation.

6 Conclusion

The problems existing in the power distribution grid are handled individually by single-variable optimised standard solutions which require a significant amount of both information processing and iterations. However, when considering multiple problems and variables simultaneously, the complexity of the global optimisation method increases exponentially. Moreover, using uncoordinated method to solve the problems sequentially may not give the optimal solution, due to the overlapping effect of the solutions. We have presented a novel compartmentalisation-based design method to solve multiple problems and meet the regulatory constraints. The algorithmic formalisation of the design solutions, as well as the ability to experiment with the ordering of these solutions, allow the designers to determine the optimal ordering of these sequential solutions to the holistic system. In our experiments, multiple orders of solutions have been analysed and the best one is chosen, in which the total cost of the solution may decrease about by 71% compared with the uncoordinated method and minimise the power loss about by 45% after optimisation.

7 References

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