Implementation of a Fractional-Order Electronically Reconfigurable Lung Impedance Emulator of the Human Respiratory Tree

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Abstract: The fractional-order lung impedance model of the human respiratory tree is implemented in this paper, using Operational Transconductance Amplifiers. The employment of such active element offers electronic adjustment of the impedance characteristics in terms of both elements values and orders. As the MOS transistors in OTAs are biased in the weak inversion region, the power dissipation and the dc bias voltage of operation are also minimized. In addition, the partial fraction expansion tool has been utilized, in order to achieve reduction of the spread of the required time-constants and scaling factors. The performance of the proposed scheme has been evaluated, at post-layout level, using MOS transistors models provided by the 0.35 µm Austria Mikro Systeme technology CMOS process, and the Cadence IC design suite.

Keywords: CMOS analog integrated circuits; biomedical circuits; low-power analog integrated circuits; low-voltage analog integrated circuits; fractional-order circuits; fractional-order capacitors; lung impedance

1. Introduction

Fractional calculus is a branch of mathematical analysis which forms a more general description of the conventional integer-order calculus. As it is well known, integer-order calculus deals with differentiation that, given a function \( f(x) \), is described by the expression \( \frac{df(x)}{dx} \), where \( a \) is an integer number (\( a \in \mathbb{Z} \)). In fractional calculus, the same expression describes the differentiation, yet the order is now a real (\( a \in \mathbb{R} \)) number, leading to a wider range of values. The idea of fractional derivative dates back to the end of the 17-th century; it is not until the past few decades, however, when fractional calculus started being used for a variety of scientific problems and applications. The cause of its increasing application is that, as mentioned above, it forms a generalization of the traditional calculus, and thus describes more real problems with higher accuracy [1–4]. One of the most popular methods, that makes the analysis of a real system easier, is the Laplace transform. This is an efficient way to describe and solve equations in frequency domain with simple calculations and without transformations to time domain during the process. The Laplace transform of the derivative of a function, \( f(t) \), in the fractional domain is: \( \mathcal{L}[D^a_t f(t)] = s^a F(s) \) for
zero initial conditions \cite{1}. Employing this definition, the impedance in the fractional-domain has the general form:

\[ Z(s) = ks^\alpha, \]

where the variable \( \alpha \) represents the order of the element. For \( 0 < \alpha < 1 \) the element is called fractional-order capacitor, or sometimes Constant Phase Element (CPE), with \( k \equiv C_\alpha \) being the pseudo-capacitance in Farad/sec\(^{1-\alpha}\). In the case that \( -1 < \alpha < 0 \) the expression in (1) represents a fractional-order inductor with \( k \equiv L_\alpha \) being the pseudo-inductance in Henry/sec\(^{1-\alpha}\). When \( \alpha = 0, -1, 1 \), the element is a resistor, capacitor, and inductor, respectively.

Fractional-order modeling considers the effects of the long-term memory, and due to the offered extra degrees of freedom, it is suitable for modeling, analyzing, and synthesizing electrical, chemical, and biological systems \cite{5–12}. In particular, circuit emulators of individual parts of the human body and even the complete human body play an important role for fast and accurate diagnosis of different diseases and morphological changes arising from environmental impacts such as long time exposure to pollution and contamination. For example, the complete human body impedance model studied in \cite{13,14} offers circuits more accurate than the international IEC 60990 safety standard. It is impossible to test the safety of any designed instrument on real humans and therefore, tests are conducted on circuit models. In a similar manner, circuit models for specific human organs have been widely proposed and used to characterize changes that occur in these organs as a result of diseases, pollution or other types of damages. In this regards, the clear relationship between fractional-order models and fractal geometry has already been highlighted in different articles (see for example \cite{15}). Fractal geometry is especially visible in the tissue structure of the lungs \cite{16}. However, the large variations in the circuit model parameters from one human to the other require developing electronically-adjustable circuits that can be re-configured easily to reflect these changes and allow future integration in smart point of care bio-medical devices (see \cite{17}).

Lungs are located in the chest on either side of the heart in the rib cage. Their purpose is to provide oxygen to the blood \cite{18}. Their shape is conical with a narrow rounded apex at the top, and a broad concave base that rests on the convex surface of the diaphragm. Due to the presence of the heart, the left lung is slightly smaller than the right. Lungs are part of the lower respiratory track beginning from trachea and followed by bronchi and bronchioles. The bronchial tree is a series of passages that supplies air to the alveoli of the lungs. It begins with the trachea, which divides into a left and right bronchus. Broncholies are divided into alveolar ducts that create the alveolar sacs, which contains the alveoli, where gas exchanging happens \cite{19}. Oxygen breathed in and carbon dioxide breathed out. Due to its airway’s branches self similarities (fractal geometry), lungs are a great candidate for the application of fractional-order calculus theory \cite{20–26}. At a low frequency range inheritance is insignificant, so each airway can be modeled by a gamma cell resistance and capacitance. In biology, when a circuit shares the same impedance with a tissue in a specific frequency range, then it is called equivalent circuit. Although the model that is used to characterize viscoelastic behavior is composed of two resistors and one capacitor, in the case of lungs the electrical equivalent impedance of each airway is considered to be consisted of one resistor and one capacitor \cite{27}. Theoretically, the air flow will be the electric current and the air pressure will be the voltage, respectively.

Regarding to the above description the electrical equivalent, shown in Figure 1, consists of a series connection of a passive resistor and a fractional-order capacitor. The equivalent impedance is given by (2)

\[ Z_{eq}(s) = R + \frac{1}{C_\alpha s^\alpha}, \]
with \( R \) being an ohmic resistance, \( C_\alpha \) being the pseudo-capacitance and \( 0 < \alpha < 2 \) being the order of the element [16].

\[
\begin{array}{ccc}
R & C_\alpha \\
\end{array}
\]

**Figure 1.** Fractional-order electrical equivalent of the impedance of the lung in the human respiratory tree [16].

The (scaled) values of the resistances and pseudo-capacitances, as well as of the order of the fractional-order capacitors, taken from seven subjects in the range \([0.9 \text{ Hz}, 6 \text{ Hz}]\) are summarized in Table 1.

**Table 1.** Values of the elements of the fractional-order electrical equivalent of the impedance of the lung in the human respiratory tree [16].

| Case | \( R \) (M\( \Omega \)) | \( C_\alpha \) (nF/s\(^{1-\alpha}\)) | Order of Element (\( \alpha \)) |
|------|----------------|-----------------|-----------------|
| #1   | 6.44           | 238             | 0.5808          |
| #2   | 4.292          | 90.4            | 0.7978          |
| #3   | 6.02           | 135             | 0.802           |
| #4   | 9.54           | 206             | 0.81            |
| #5   | 4.89           | 178             | 0.9349          |
| #6   | 5.93           | 17.5            | 0.95            |
| #7   | 6.06           | 26              | 1.134           |

Table 1 of the manuscript shows that the measured circuit parameters for 7 individuals have a variation in pseudo-capacitance of more than 14 times (from 17.5 nFarad/s\(^{0.05}\) to 238 nFarad/s\(^{0.42}\)). Also, the variation in the dispersion coefficient from 0.6 to nearly double the value at 1.1 indicates the importance of re-configurability of any proposed circuit model in order to reflect the measured values of any particular individual, which is the focus of this work.

Owing to the fact that fractional-order capacitors are not available in the market [28], their behavior is approximated through the employment of RC networks [29], or the employment of the gyration techniques implemented using active elements [10]. The first solution is easily realizable, but it does not offer the capability of programming because the values of all passive elements must be changed, in order to change the characteristics of the element. On the other hand, the second method offers electronic adjustment of the characteristics of the element, but this is achieved at the expense of circuit complexity and active component count. The contribution, made in this work, is that a novel solution for implementing emulators of fractional-order capacitors with order in the range \((0, 2)\), which offers significant reduction of the spread of time-constants and scaling factors with regards to the straightforward implementation [30], is presented. This also leads to a significant reduction of the total capacitance area and the total power dissipation. All the above have been achieved keeping: (a) the fully electronic adjustment of the characteristics of the capacitor, including its value as well as its order, and (b) the capability of emulating fractional-order capacitors with order greater than one by the same core.

The paper is organized as follows: the procedure for emulating the behavior of the fractional-order capacitors is presented in Section 2, while the circuit implementation of the proposed scheme is given in Section 3. The performance of the intermediate stages and of the whole system are evaluated at post-layout level in Section 4, using the Cadence IC design suite and MOS transistor models provided by the Austria Mikro Systeme (AMS) 0.35 \( \mu \)m CMOS process.
2. Emulation of Fractional-Order Capacitors

According to [30], the emulation of a fractional-order capacitor is performed through the utilization of the Functional Block Diagram (FBD) depicted in Figure 2. The equivalent impedance is given by the expression in (3)

\[ Z_{\text{CPE}}(s) = \frac{1}{g_{m,VI} \left( \tau \cdot s \right)^q} \]  

with \( g_{m,VI} \) being the transconductance parameter of the voltage-to-current (V/I) converter, \( \tau \) being a time-constant associated to the unity-gain frequency \( (\omega_0) \) of the differentiator according to the formula: \( \omega_0 = \frac{1}{\tau} \), and \( 0 < q < 1 \) being the order of the differentiator. The value of the emulated pseudo-capacitance has the form of (4)

\[ C_\alpha = g_{m,VI} \cdot \tau^q. \]  

\[ \tau_s \]

Figure 2. Functional block diagram for emulating a fractional-order capacitor of order \( \alpha = (0, 1) \) [30].

Due to the restriction on the values range of the variable \( q \), only the cases \#1 to \#6 (i.e., \( \alpha = q < 1 \)) are implementable. The last case in Table 1, where the order of the fractional-order capacitor is greater than one (i.e., \( \alpha = 1.134 \)), cannot be emulated by the present form of the FBD in Figure 2. In order to overcome this obstacle, the modified FBD in Figure 3 will be utilized, using an extra integer-order differentiator stage between the fractional-order differentiator and the V/I converter and, thus, \( \alpha = q + 1 \) [31].

\[ \tau_s \]

Figure 3. Functional block diagram for emulating a fractional-order capacitor of order \( \alpha = (0, 2) \) [31].

As the equivalent in Figure 1 is valid over the frequency range \( f = [0.9 \text{ Hz}, 6 \text{ Hz}] \), an appropriate selection of unity-gain frequency of the differentiator \( (f_0) \) could be 2.5 Hz. The values of the pseudo-capacitance
and the order $\alpha$ of the fractional-order elements, along with the values of the order $q$ of the fractional-order differentiator and the transconductance $g_{m,V1}$ of the FBD in Figure 3, for the seven cases of emulation are summarized in Table 2.

Table 2. Values of the elements of the fractional-order emulator in Figure 3.

| Case | $C_\alpha$ (nF$^{1-\alpha}$) | Order of Element ($\alpha$) | Order of Differentiator ($q$) | $g_{m,V1}$ (\textmu S) |
|------|-----------------------------|-----------------------------|-----------------------------|----------------------|
| #1   | 238                         | 0.5808                      | 0.5808                      | 1.18                 |
| #2   | 90.4                        | 0.7978                      | 0.7978                      | 0.81                 |
| #3   | 135                         | 0.802                       | 0.802                       | 1.23                 |
| #4   | 206                         | 0.81                        | 0.81                        | 1.92                 |
| #5   | 178                         | 0.9349                      | 0.9349                      | 2.34                 |
| #6   | 17.5                        | 0.95                        | 0.95                        | 0.24                 |
| #7   | 26                          | 1.134                       | 0.134                       | 0.59                 |

Taking also into account the results provided in [32], the 2nd-order Continued Fraction Expansion (CFE) approximation will be utilized for approximating the behavior of the fractional-order differentiator with an error less than 5% in the frequency range of interest. The resulted rational transfer function has the form

$$(\tau \cdot s)^q \approx \frac{A_2 s^2 + A_1 s + A_0}{s^2 + B_1 s + B_0},$$

where in (5) the coefficients $A_i$ ($i = 0, 1, 2$) and $B_j$ ($j = 1, 2$) are positive real numbers provided in [33].

The implementation of (5) can be performed using the multi-feedback Inverse-Follow-the-Leader-Feedback (IFLF) structure demonstrated in Figure 4, where the realized transfer function is

$$H(s) = \frac{K_2 s^2 + (\frac{K_1}{\tau_1}) s + \frac{K_0}{\tau_1 \tau_2}}{s^2 + (\frac{1}{\tau_1}) s + \frac{1}{\tau_1 \tau_2}}.$$  

(6)

The values of scaling factors $K_i$ ($i = 0, 1, 2$) and time-constants $\tau_j$ ($j = 1, 2$) are calculated by equating the coefficients of the numerator and denominator of (5) and (6) [10]. The resulted values are summarized in Table 3. Considering the spread (i.e., the ratio of the maximum and minimum value) of both scaling factors and time-constants, it is derived that the spread of scaling factors for all possible cases is equal to 12, 174, while for the time-constants the corresponding value is 388. Taking into account that in the case of employing OTAs these scaling factors will be controlled by dc currents, it is readily obtained that an extremely wide range of bias currents is required towards this goal.

Figure 4. FBD of an Inverse-Follow-the-Leader-Feedback topology for approximating a fractional-order differentiator using the Continued Fraction Expansion tool [30].
Table 3. Values of scaling factors and time-constants for approximating fractional-order differentiators of the FBD in Figure 3, using the FBD in Figure 4.

| Case | Order of Differentiator \((q)\) | \(K_0\) | \(K_1\) | \(K_2\) | \(\tau_1\) (ms) | \(\tau_2\) (ms) |
|------|-------------------------------|--------|--------|--------|-----------------|-----------------|
| #1   | 0.5808                        | 0.146  | 1      | 6.86   | 5.17            | 114.3           |
| #2   | 0.7978                        | 0.048  | 1      | 20.69  | 2.30            | 85.14           |
| #3   | 0.802                         | 0.047  | 1      | 21.29  | 2.25            | 84.65           |
| #4   | 0.81                          | 0.044  | 1      | 22.5   | 2.15            | 83.71           |
| #5   | 0.9349                        | 0.012  | 1      | 81.9   | 0.71            | 70.09           |
| #6   | 0.95                          | 0.009  | 1      | 109.6  | 0.54            | 68.56           |
| #7   | 0.134                         | 0.668  | 1      | 1.5    | 12.9            | 209.5           |

Another possible solution for overcoming this practical problem is the utilization of the Partial Fraction Expansion (PFE) method introduced in [34]. According to this, the transfer function in (5) can be decomposed in the form of (7)

\[ (\tau \cdot s)^q = K_0 + \frac{r_1}{s - p_1} + \frac{r_2}{s - p_2} + \ldots + \frac{r_n}{s - p_n}, \tag{7} \]

where \(r_j (i = 1, 2, \ldots, n)\) are the residues of (5), \(p_j (i = 1, 2, \ldots, n)\) are the poles of (5) and \(K_0 = A_2\). The expression in (7) can be alternatively written as

\[ H(s) = K_0 + \sum_{j=1}^{2} K_j \frac{1}{1 + \tau_i s}, \tag{8} \]

where \(K_j = \frac{r_j}{|p_j|}\) and \(\tau_i = \frac{1}{|p_j|}\). The implementation of (8) can be performed by the functional block diagram given in Figure 5. Using (5), (7) and (8), the resulted values of times constants are summarized in Table 4. The spread of scaling factors is now significantly reduced into the value 884 and the spread of time-constants is now equal to 362, more or less the same as in the previous case. Therefore, the PFE approximation tool is preferable than the CFE tool with regards to reduction of the spread of scaling factors.

Figure 5. FBD for approximating a fractional-order differentiator using the Partial Fraction Expansion tool [34].
Table 4. Values of scaling factors and time-constants for approximating fractional-order differentiators of the FBD in Figure 3, using the FBD in Figure 5.

| Case | Order of Differentiator (q) | $K_0$ | $K_1$ | $K_2$ | $\tau_1$(ms) | $\tau_2$(ms) |
|------|----------------------------|-------|-------|-------|---------------|---------------|
| #1   | 0.5808                     | 6.86  | -6.12 | -0.59 | 5.43          | 108.9         |
| #2   | 0.7978                     | 20.69 | -20.24| -0.4  | 2.37          | 82.78         |
| #3   | 0.802                      | 21.29 | -20.85| -0.39 | 2.31          | 82.34         |
| #4   | 0.81                       | 22.5  | -22.07| -0.38 | 2.21          | 81.50         |
| #5   | 0.9349                     | 81.9  | -81.73| -0.16 | 0.71          | 69.37         |
| #6   | 0.95                       | 109.6 | -109.4| -0.12 | 0.54          | 68.02         |
| #7   | 0.134                      | 1.5   | -0.51 | -0.32 | 13.8          | 195.7         |

3. Circuit Implementation

In order to approximate the fractional-order differentiator stage, which is required in the functional block diagram in Figure 3, the OTA-C topology depicted in Figure 6 will be utilized. This topology implements the transfer function in (8); the values of scaling factors, provided in Table 4, are performed through an appropriate scaling of the corresponding transconductances and, also, their signs are implemented through an appropriate configuration of the input terminals of the corresponding OTAs. The realized time-constants are given by the general expression in (9)

$$
\tau_j = \frac{C_j}{g_{mj}} \quad j = 1, 2, \quad (9)
$$

with $g_{mj}$ and $C_j$ being the transconductance of the OTAs and the capacitance associated to the $j^{th}$-integration stage.

![Figure 6. Implementation of the fractional-order differentiation stage, required for the implementation of the functional block diagram in Figure 3.](image)

The integer-order differentiator is implemented by the topology demonstrated in Figure 7 [35], and the realized transfer function is given by (10)

$$
H_d(s) = \tau \cdot s = \left( \frac{C_d}{g_{md}} \right) \cdot s. \quad (10)
$$

The $V/I$ conversion stage is implemented by the multiple-output OTA configuration depicted in Figure 8, with the conversion gain being equal to $1/g_{m,VI}$.
In order to implement the whole model, which is provided in Figure 1, a floating resistor emulator is also required. This is realized by the topology, given in Figure 9, where the realized resistance is 

\[ R = \frac{1}{g_{m,R}}. \]

A possible OTA topology, in order to implement the topologies in Figures 6–9, is that presented in Figure 10, which is capable of handling input voltages with maximum amplitude 50 mV, with a total harmonic distortion less than 1% [36]. Considering MOS transistors biased in the sub-threshold region, the realized transconductance is

\[ g_m = \frac{5}{9} \cdot \frac{I_B}{n V_T}, \quad (11) \]

with \( I_B \) being the bias current, \( 1 < n < 2 \) being the sub-threshold slope factor, and \( V_T \) (≈26 mV at 27 °C) being the thermal voltage. Using (9) and (11), the resulted expression of the realized time-constants is that given by (12)

\[ \tau_j = \frac{9}{5} \cdot \frac{nC_j V_T}{I_{Bj}} \quad j = 1, 2. \quad (12) \]

According to (11) and (12), the temperature variations affect both the implemented values of transconductance and time-constant due to the dependence of thermal voltage from the temperature. This can be compensated by employing proportional to the absolute temperature (PTAT) dc current sources [37].

![Figure 7](image_url)

**Figure 7.** Implementation of the integer-order differentiation stage, required for the implementation of the functional block diagram in Figure 3 [35].

![Figure 8](image_url)

**Figure 8.** Implementation of the \( V/I \) converter stage using an appropriately configured multiple-output OTA.

![Figure 9](image_url)

**Figure 9.** Implementation of a floating resistor using an appropriately configured multiple-output OTA.
Figure 10. CMOS OTA circuitry used for implementing the topologies in Figures 6–9 [36].

4. Simulation Results

Using the expression in (12) and considering that \( C_1 = 5 \) pF, \( C_2 = 70 \) pF, then the calculated values of the dc bias currents, required for realizing the time-constants in Table 4, are provided in Table 5. The scaling factors \( \{ K_0, K_1, K_2 \} \) were implemented by multiplying the dc bias current \( I_B \) by these factors, as it is derived from (11). Using (10) and (12), and considering that \( C_d = 25 \) pF, the dc bias current of the OTAs employed for implementing an integer-order differentiator with time-constant 63.69 ms (i.e., \( f_0 = 2.5 \) Hz) was equal to 22.1 pA. In addition, the values of the dc bias currents for implementing the \( V/I \) converter stage and the floating resistors, calculated using (4), (11) and Table 1, are given in Table 5. The evaluation of the behavior of the proposed lung impedance model will be performed using the Cadence IC design suite and MOS transistor models provided by the Austria Mikro Systeme (AMS) 0.35 \( \mu \)m CMOS process. Considering that \( V_{DD} = -V_{SS} = 0.75 \) V, the MOS transistors aspect ratios of the OTAs used in Figures 6–8, selected in such way that all transistors will operate in the sub-threshold region, are provided in Table 6.

The layout design of the proposed lung impedance emulator is demonstrated in Figure 11, where the silicon area is 555.35 \( \mu \)m \( \times \) 485.75 \( \mu \)m. As a first-step the correct operation of the building blocks of the model in Figure 1 was verified. The obtained impedance magnitude and phase responses of the CPE emulator are given in Figure 12. The characteristic values of the gain and phase of the fractional-order differentiator at the center frequency of the approximation (i.e., \( f_0 = 2.5 \) Hz) as well as the values of the implemented resistances of the employed \( V/I \) converter stage in Figure 8 and resistor emulator in Figure 9 are summarized in Table 7, with the corresponding theoretically predicted values (i.e., derived using ideal circuit elements) given in parentheses. The gain and phase of the integer-order differentiator in Figure 7 at frequency \( f_0 = 2.5 \) Hz, which is used in the case #7 for implementing the order \( \alpha = 1 + q = 1.134 \), were 1 and +88.3° with the theoretical values being 1 and +90°, respectively.
Table 5. Values of dc bias currents for approximating the fractional-order differentiator, with unity gain frequency \( f_0 = 2.5 \) Hz, in Figure 6 and implementing the \( V/I \) converter and resistor in Figures 8 and 9.

| Case | Order \( q \) | Differentiator | \( I_{B1} \) (pA) | \( I_{B2} \) (pA) | \( I_B \) (pA) | \( V/I \) Converter | \( I_{B,V/I} \) (mA) | Resistor | \( I_{B,R} \) (mA) |
|------|---------------|----------------|------------------|------------------|---------------|----------------|----------------|----------|--------------|
| #1   | 0.5808        | 51.7           | 36.1             | 350              | 66            | 8.7            |
| #2   | 0.7978        | 118.5          | 47.5             | 350              | 45.6          | 13.1           |
| #3   | 0.802         | 121.6          | 47.7             | 350              | 69.1          | 9.3            |
| #4   | 0.81          | 127.1          | 48.2             | 300              | 107.8         | 5.9            |
| #5   | 0.9349        | 395.5          | 56.7             | 700              | 131.3         | 11.5           |
| #6   | 0.95          | 520            | 57.8             | 600              | 13.5          | 9.5            |
| #7   | 0.134         | 20.3           | 20.1             | 600              | 33.2          | 9.3            |

Figure 11. Layout design of the proposed emulator of the lung impedance model of the human respiratory tree in Figure 1 (fractional-order differentiator in red polygon, integer-order differentiator in green polygon, \( V/I \) converter in yellow polygon, and active resistor in blue polygon).

Table 6. Aspect ratio values of MOS transistors of the OTA in Figure 10 used for implementing the topologies in Figures 6–9.

| Transistor | Figure 6 \((\mu\text{m}/\mu\text{m})\) | Figure 7 \((\mu\text{m}/\mu\text{m})\) | Figure 8 \((\mu\text{m}/\mu\text{m})\) | Figure 9 \((\mu\text{m}/\mu\text{m})\) |
|------------|-------------------------------------|-------------------------------------|-------------------------------------|-------------------------------------|
| M1n-M2n    | 8/15                                | 1/5                                 | 1/1                                 | 2/1                                 |
| M3n-M4n    | 40/15                               | 5/5                                 | 5/1                                 | 10/1                                |
| M5n-M7n    | 25/15                               | 25/5                                | 15/15                               | 10/5                                |
| M1p-M2p    | 10/5                                | 0.5/5                               | 25/10                               | 5/15                                |

The second step is the consideration of the total circuit of the lung impedance emulator in Figure 11. The obtained impedance magnitude and phase responses are provided in Figure 13, where the corresponding theoretically predicted plots are given by dashes. The most important performance characteristics are given in Table 8, along with the theoretical values given between parentheses.
Considering the case $R = 6.44 \ \text{M} \Omega$, $C_\alpha = 238 \ \text{nF/s}^{1-\alpha}$ and $\alpha = 0.5808$, the derived statistical plots about the magnitude and phase of the impedance at the center frequency of the approximation (i.e., $f_0 = 2.5 \ \text{Hz}$) are provided in Figure 14.

![Figure 12](image1.png)

**Figure 12.** Post-layout impedance (a) magnitude, and (b) phase frequency responses of the CPE emulator implemented according to the FBDs in Figures 2 and 3.

**Table 7.** Simulated and theoretical (in parentheses) values of the magnitude and phase (at $f_0 = 2.5 \ \text{Hz}$) of the differentiator in Figure 6, of the impedance of the V/I converter in Figure 8, and resistor in Figure 9, used for implementing the FBDs in Figures 2 and 3.

| Case | Order ($q$) | Differentiator Gain $|Z|$ | V/I Converter $I_{g_{m,v1}}$ (MΩ) | Resistor $R$ (MΩ) |
|------|-------------|-----------------|-------------------------------|-----------------|
| #1   | 0.5808      | 1 (1)           | 53.7 (52.3)                   | 0.85 (0.84)     | 6.45 (6.44)    |
| #2   | 0.7978      | 1 (1)           | 71.8 (71.8)                   | 1.22 (1.23)     | 4.27 (4.29)    |
| #3   | 0.802       | 1 (1)           | 71.7 (72.18)                  | 0.81 (0.81)     | 6.01 (6.02)    |
| #4   | 0.81        | 1 (1)           | 72.7 (72.9)                   | 0.51 (0.52)     | 9.54 (9.54)    |
| #5   | 0.9349      | 1 (1)           | 83.7 (84.14)                  | 0.43 (0.43)     | 4.89 (4.89)    |
| #6   | 0.95        | 1 (1)           | 84.2 (85.5)                   | 0.41 (0.42)     | 5.92 (5.93)    |
| #7   | 0.134       | 1 (1)           | 12.5 (11.5)                   | 1.69 (1.69)     | 6.04 (6.06)    |

![Figure 13](image2.png)

**Figure 13.** Post-layout frequency responses of the lung impedance (a) magnitude, and (b) phase.
Figure 14. Statistical histograms about the sensitivity of the (a) magnitude, and (b) phase of the lung impedance, at the center frequency of the approximation \((f_0 = 2.5 \text{ Hz})\).

Table 8. Simulated and theoretical (in parentheses) values of impedance magnitude and phase of the model in Figure 1, at the center frequency of the approximation \((f_0 = 2.5 \text{ Hz})\).

| Case | Order of Element \((\alpha)\) | \(|Z|\) \((\text{M}\Omega)\) | \(\text{arg}(Z)\) \((^\circ)\) |
|------|-------------------------------|--------------------------|------------------|
| #1   | 0.5808                        | 6.9 (6.9)                | −5.64 (−6.13)    |
| #2   | 0.7978                        | 4.70 (4.71)              | −13.12 (−14.7)   |
| #3   | 0.802                         | 6.25 (6.24)              | −6.26 (−7.3)     |
| #4   | 0.81                          | 9.68 (9.66)              | −2.68 (−3)       |
| #5   | 0.9349                        | 4.96 (4.94)              | −5.35 (−4.96)    |
| #6   | 0.95                          | 7.83 (7.42)              | −32.5 (−34)      |
| #7   | 1.134                         | 5.92 (5.96)              | −17.1 (−16.2)    |

These results are derived, by employing the Monte-Carlo analysis offered by the Analog Design Environment of the Cadence IC design suite, for a number of \(N = 500\) runs and considering variations of the channel width and length, gate oxide thickness, threshold voltage, carrier mobility, and substrate and channel doping concentration. The values of standard deviation were 0.09 \(\text{M}\Omega\) and 0.25\(^\circ\), with the nominal values being 6.89 \(\text{M}\Omega\) and −5.62\(^\circ\), confirming that the proposed structure has reasonable sensitivity characteristics.

It must be mentioned at this point that, in the simulations, the tuning operation is performed by altering the bias current of the transconductors. In the case of a practical implementation with large number of circuit elements, this can be performed by using an appropriate digital circuitry as in [38–40] or using an auxiliary analog system as that proposed in [41].

5. Conclusions

The proposed OTA-C emulator of the lung impedance in the human respiratory tree offers design flexibility and versatility, in the sense that the parameters are fully controllable by appropriate dc bias currents. Thus, the same topology is capable of implementing the results derived from subjects without
any restriction about their condition, as well as about the experimental setup. The main contribution with regards to the literature is that the spread of dc bias currents is minimized, resulting in a significant reduction of the dc power consumption. Future research work includes the development of simpler structures where all the aforementioned benefits will be preserved, as well as the employment of the presented concept, which is general, in the implementation of various types of electrical equivalents in biomedical applications. It is important to note that developing sensors for different lung diseases has become an increasingly important topic due to its huge economic impact [42]. In this regards, bio-impedance-based sensors are a competitive solution and the proposed emulators could be an attractive candidate for the implementation of such systems.

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**Abbreviations**

The following abbreviations are used in this manuscript:

- AMS: Austria Mikro Systeme
- CFE: Continued Fraction Expansion
- CMOS: Complementary Metal-Oxide-Semiconductor
- CPE: Constant Phase Element
- FBD: Functional Block Diagram
- IC: Integrated Circuits
- MOS: Metal-Oxide-Semiconductor
- OTA: Operational Transconductance Amplifier
- PFE: Partial Fraction Expansion

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