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Decentralized Circulating Currents Suppression for Paralleled Inverters in Microgrids Using Adaptive Virtual Inductances

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Abstract: Droop-controlled inverters, which are the main interfaces between distributed generators (DGs) and grid AC bus, are widely adopted in today’s microgrids. Because the DGs are usually dispersed along with the microgrids, which may spread out in a wide area, the impedances of transmission lines that connect different DGs and the point of common coupling (PCC) may be different. Large circulating currents among paralleled inverters would be induced eventually, which not only results in decreased system efficiency, but also makes some DGs impossible to operate at their rated power. In this paper, the relationship between circulating current and line impedances among paralleled inverters with conventional droop control is analyzed in detail. To mitigate the circulating current, an adaptive virtual output inductance control method for the DG inverter is proposed. With the help of the added adaptive virtual inductances, huge reduction on the circulating currents can be obtained regardless of the differences in line impedances. The implementation details of the proposed method and the optimum design rules for system parameters are elaborated, which are swiftly followed by the operational principle and stability analyses. Because neither communications nor global signals are needed in the proposed method, it is a total discrete approach, which evidently indicates high flexibility and scalability. The validity of the proposed method is finally verified by simulations and experiments.

Keywords: microgrid; paralleled inverters; circulating current; adaptive virtual inductance; droop control

1. Introduction

Microgrid, which is a promising structure to integrate renewable and distributed energy sources in a more efficient and reliable way, has greatly attracted people’s attention recently [1–4]. The microgrid is very good at improving the system's overall efficiency, power quality, and energy surety for critical loads. It can operate in grid-connected mode or autonomous islanding mode and benefits both the utility and customers [5]. So far, many countries have devoted to develop their own microgrids to ease the integration of fast growing renewable energies to the grid, provide opportunities for economic development in electric power and clean energy industry, and most importantly obtain better power supply reliability to consumer’s loads [6,7].

In a microgrid, the micro-sources such as photovoltaic cells, small wind turbines, energy storage systems are integrated to the grid in the form of distributed generation, which are further defined as distributed generators (DGs) [8]. Typically, DGs are connected to the grid bus through inverter interfaces, or DG inverters, which operate in parallel. To ease the power control of the microgrid,

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droop control is usually adopted [9–11]. With this method, the ‘plug-and-play’ feature for the DG inverter is achieved, which ensures that the installation of new DGs will not change the control strategies of those DGs already in the microgrid. However, as DG inverters are always dispersed along with the microgrids, which may spread out in a wide area, the impedances of transmission lines that connect different DG inverters and the point of common coupling (PCC) may be different. If the conventional reactive power droop control is still adopted, accurate reactive power sharing among DG inverters can hardly be achieved as it is highly influenced by the line impedances and load characteristics [12]. Consequently, large circulating currents, which do not flow through the loads, will be induced. This not only would reduce the overall system efficiency, but also may make some DG inverters impossible to operate under their rated power conditions.

To alleviate or eliminate the influences of line impedances on the reactive power sharing, thus to mitigate the circulating currents, modifications on the conventional droop control are necessary. In [13], an accurate reactive power sharing control strategy is proposed for paralleled inverters. The distinctive feature of this method is integrated with an on-line tuning droop gain. However, as the droop gain is constantly varying, this method may suffer from stability issue. Furthermore, it should be noted that the set of droop gain in islanded mode has to use some tuned information obtained in grid-connected mode. Consequently, this method is not applicable for autonomous islanding microgrid. In [14], a robust droop control method, which adopts an integration element in the reactive power control loop, is proposed. Although this method can fully avoid the influence of line impedances, it features deteriorated flexibility and scalability due to the need of detecting voltage signal at PCC and adding complex communication system to share it with DG inverters. Some similar modified droop control methods that incorporated with integration element in the reactive power control loop can also be evidenced [15–17]. However, they all suffer from the same problem related to low flexibility and scalability. To address this issue, some other researchers proposed novel droop control methods, which avoid the use of common signals or communications among DGs, to mitigate the circulating currents. A new droop control method, namely reactive power–derivative of voltage droop method, is proposed to improve reactive power sharing in [18]. As derivation may amplify noises, the implementation of this method is complicated. Besides, large circulating currents can still be evidenced in the experimental results, which indicate low suppression ability. In [19], an impedance-matching method to achieve accurate reactive power sharing under unequal line impedance situation is proposed. However, it only works on condition that the DGs have equal capacities. In [20,21], virtual output inductances were added to the DG inverters by properly design the control systems. While effective in decoupling active and reactive power regulation, it is also effective in reducing the circulating currents by increasing the virtual inductances and designing them according to some optimization rules. Unfortunately, due to the lack of self-tuning ability, these virtual inductances are usually set to quite large values, which eventually cause excessive voltage drops on the AC bus. In summary, there is still a lack of a simple and flexible method to suppress the circulating currents among paralleled inverters with different capacities.

The intended contribution of this paper is to propose an adaptive virtual output inductance control method to solve the difficulty of suppressing circulating currents among paralleled inverters in the microgrid. Unlike the approaches mentioned above, as the implementation of the proposed method does not rely on any common signals or communications, high flexibility and scalability are achieved. In addition, the proposed method can be applied to paralleled inverters with different capacities. Furthermore, compared with the traditional virtual output inductances control method, the proposed method can automatically adjust the virtual inductance to better suppress circulating currents. The rest of this paper is organized as follows: the principle of the active and reactive power regulation for paralleled inverters with conventional droop control is analyzed in Section 2, in which the rationale of generation of circulating currents is discussed. In Section 3, an adaptive virtual output inductance control strategy is proposed to minimize the circulating currents. Its operational principle and implementation strategy are elaborated, followed by which the system stability and design on the
system parameters are deeply studied. Afterwards, the effectiveness of the proposed control method is verified by simulation and experimental results performed by a 15 kW/4.5 kVar islanded microgrid demonstration platform in Section 4. Conclusions are finally given in Section 5.

2. Paralleled Inverters with Conventional Droop Control

2.1. Conventional Droop Control

In a microgrid, DG inverters are usually paralleled. To simplify the analysis, two DG inverters connected in parallel are used as an example system throughout this paper. Figure 1 depicts the block diagram of the example system. The power capacity ratio between the two DG inverters is 1:2. The detailed system parameters are shown in Table 1 in Section 4. Figure 2 shows the equivalent circuit of the example system (one phase), in which $V_{oi} \angle \delta_i, I_i \angle \theta_i$ stand for the output voltage vector and output current vector of the $i$th DG inverter respectively, where $\delta$ is the power angle, $\theta$ is the impedance angle and $i = 1, 2$; $V_g \angle \theta$ is the voltage vector at PCC; $L_i$ and $R_i$ are the line inductance and resistance between the output terminal of the $i$th DG inverter and PCC respectively; $Z_o$ and $I_o$ are the load impedance and output current (both for one phase), respectively. In particular, in the rest of this paper, subscript $i$ is used to mark the corresponding variables of the $i$th DG inverter.

Table 1. System Parameters Used in Simulations and Experiments.

| System Parameter                  | Inverter 1 | Inverter 2 |
|-----------------------------------|------------|------------|
| DC link voltage                   | 720 V DC   | 720 V DC   |
| Nominal AC bus voltage            | 380 V      | 380 V      |
| Nominal frequency                 | 50 Hz      | 50 Hz      |
| Nominal output power              | 5 kW, 1.5 kVar | 10 kW, 3 kVar |
| Filter inductance                 | 1 mH       | 1.5 mH     |
| Filter capacitance                | 30 µF      | 50 µF      |
| Line impedance                    | 0.22 Ω, 100 µH | 0.235 Ω, 110 µH |
| Initial constant virtual inductance| 2.1 mH     | 2.3 mH     |
| $P$-ω droop coefficient           | $2.5 \times 10^{-4}$ rad/(s·W) | $1.25 \times 10^{-4}$ rad/s·W |
| $Q$-V droop coefficient           | $2 \times 10^{-3}$ V/Var | $1 \times 10^{-3}$ V/Var |
| Voltage regulator                 | $k_{pv1} = 0.1, k_{iq1} = 50$ | $k_{pv2} = 0.15, k_{iq2} = 50$ |
| Current regulator                 | $k_{p1} = 0.1, k_{iq1} = 1000$ | $k_{p2} = 0.15, k_{iq2} = 1500$ |
| Corner frequency of the LPF $\omega_f$ | 30 rad/s | 30 rad/s |
| Set value of the adaptive virtual inductance $X_{set}$ | 2 mH | 2 mH |
| Coefficient $k_v$                 | $4 \times 10^{-6}$ | $4 \times 10^{-6}$ |
| Set value of the reactive power $Q_{set}$ | 1.5 kVar | 1.5 kVar |
For the DG inverter, the active power $P$ and reactive power $Q$ (per phase) integrated to the microgrid can be expressed as [13],

$$
\begin{align*}
P_i &= \frac{V_{oi}}{R_i + X_i} \left[ R_i (V_{oi} - V_g \cos \delta_i) + X_i V_g \sin \delta_i \right] \\
Q_i &= \frac{V_{oi}}{R_i + X_i} \left[ -R_i V_g \sin \delta_i + X_i (V_{oi} - V_g \cos \delta_i) \right]
\end{align*}
$$

(1)

In real applications, low-pass filters (LPFs) are always adopted to filter out the high-frequency harmonics in the calculated active and reactive power $P$ and $Q$, the transfer function of which is,

$$
G_{fi}(s) = \frac{\omega_{fi}}{s + \omega_{fi}}
$$

(2)

where $\omega_f$ is the corner frequency of the LPF.

In (1), as active power and reactive power are both determined by the power angle $\delta$ and the output voltage of DG inverter $V_{oi}$, it is impossible to regulate the active and reactive power in a decoupled way. Therefore, large and constant virtual output inductance $L_v$ is usually added to each DG inverter by its control system to make the system line impedance mainly inductive [20,21], i.e., $(X_v + X_L) >> R$, where reactance $X_v = \omega L_v$ and $X_L = \omega L$.

Then, the active and reactive power can be determined as,

$$
\begin{align*}
P_i &= G_{fi}(s) \frac{V_{oi} V_v \delta_i}{X_i} \\
Q_i &= G_{fi}(s) \frac{V_{oi} (V_{oi} - V_g)}{X_i}
\end{align*}
$$

(3)

where $X \approx (X_v + X_L)$ is the equivalent line impedance after the virtual inductance added.

Equation (3) determines the basics for conventional droop control method that can be used to achieve the “plug-and-play” feature of the DG inverter, i.e., “real power versus frequency ($P - \omega$)” and “reactive power versus voltage ($Q - V$)” droop control, which is [21]

$$
\omega_i = \omega^* - m_i P_i
$$

(4)

$$
V_{oi} = V_o^* - n_i Q_i
$$

(5)

where $\omega^*$ and $V_o^*$ are the nominal frequency and output voltage of the DG inverter respectively; $m$ and $n$ are the droop coefficients for active and reactive power, respectively.

Hence, the active power and reactive power can be regulated by adjusting the frequency and output voltage respectively according to (4) and (5).

2.2. Generating of Circulating Current

Typically, it is expected that the load power can be shared by the DGs according to their own capacities. For the studied system shown in Figure 1, assuming the capacity ratio between the two
DGs is $k_1:k_2$ to be general. To achieve the proportional sharing of load power, the droop coefficients of the DG inverters should be set inverse proportional to their power ratings, which reads [20],

$$m_1 P_1 = m_2 P_2$$

$$n_1 Q_1 = n_2 Q_2$$  \hspace{1cm} (6) \hspace{1cm} (7)

In fact, considering the steady-state frequency of each DG inverter would eventually converge to AC bus frequency, we have

$$-m_i P_i = \omega_i - \omega^* = \text{const}$$  \hspace{1cm} (8)

Equation (8) indicates that the proportional active power sharing can always be guaranteed despite of the differences in line impedances. Alternatively, the active power control loop will not induce circulating currents in the paralleled system.

On the other hand, to achieve the reactive power sharing, the circulating current, which is further represented with the circulating reactive power flow $Q_{cc}$ defined in (9) in this paper, should be zero.

$$Q_{cc} = k_2 Q_1 - k_1 Q_2 = \frac{k_2 V_{o1}(V_{o1} - V_g)}{X_1} - \frac{k_1 V_{o2}(V_{o2} - V_g)}{X_2} \hspace{1cm} (9)$$

Substituting (2) into (4), the output voltage of the DG inverter in steady state can be obtained as,

$$V_{oi} = 0.5 \left[ V_g - \frac{X_i}{n_i} + \sqrt{\left( \frac{X_i}{n_i} - V_g \right)^2 + 4V_o^* \frac{X_i}{n_i}} \right] \hspace{1cm} (10)$$

From (9) and (10), it is obvious that the circulating current can be eliminated only on condition that:

$$\frac{X_1}{X_2} = \frac{k_2}{k_1} = \frac{n_1}{n_2} \hspace{1cm} (11)$$

In real application, it is always the case that the reactive power droop gains are set inverse proportional to the DG capacities. However, as the line impedances are always different and unknown, (11) can hardly be satisfied. As a result, large circulating currents always exist in the autonomous islanding microgrid, among paralleled DG inverters.

To attenuate the circulating currents, one simple method is to set the virtual reactance $X_v$ of each DG inverter to satisfy (11) and at the same time increase its value to ensure that it can dominate the line impedance. However, as limited by the requirements of grid standard on the voltage drop, power factor, and system stability, $X_v$ cannot be too large. The suppressing ability is then limited if only constant virtual inductances are added to the DG inverters. Therefore, an adaptive virtual output inductance control method is proposed to address this issue.

3. Adaptive Virtual Inductance Control Method

3.1. Operational Principle of the Virtual Inductance Control Method

Suppose the number of DG inverters operated in parallel is $l$ and the capacity ratio is $k_1:k_2: \cdots :k_l$. The initial equivalent impedance of the transmission line between the $i$th DG inverter and grid PCC is $X_i \approx (X_{oi} + X_{Li})$, with $X_{oi}$ being the initially added constant virtual output inductance of the $i$th DG inverter. It should be noted that $X_{oi}$ can also be replaced by large coupling inductor physically connected between the output of the DG inverter and the PCC in real application. As mentioned
before, it is always the case that $X_i$ does not satisfy (11) in real application. Therefore, adaptive virtual output inductance control method is proposed, which is

$$\begin{align*}
\Delta X_{v1} &= X_{set} - k_v(Q_{set} - Q_1) \\
\Delta X_{v2} &= \frac{k_1}{k_v} \left[ X_{set} - k_v \left( Q_{set} - \frac{k_1}{k_2} Q_2 \right) \right] \\
&\vdots \\
\Delta X_{vi} &= \frac{k_i}{k_v} \left[ X_{set} - k_v \left( Q_{set} - \frac{k_1}{k_i} Q_i \right) \right] \\
&\vdots \\
\Delta X_{vl} &= \frac{k_l}{k_v} \left[ X_{set} - k_v \left( Q_{set} - \frac{k_1}{k_l} Q_l \right) \right]
\end{align*}$$

(12)

where $\Delta X_v$ stands for the added adaptive virtual inductance; $X_{set}$ and $Q_{set}$ are the pre-set inductance and reactive power for the reference DG inverter (the 1st DG inverter is treated as the reference inverter in this paper) in the proposed control; $k_v$ is a coefficient. The selection of $Q_{set}, X_{set},$ and $k_v$ will be elaborated later.

The basic idea of the proposed method shown in (12) is to adaptively adjust the virtual output inductance of each DG inverter to make the equivalent line impedances satisfy (11), thus to attenuate the circulating currents. It can be seen from (12) that the value of the added adaptive virtual inductance is only related to the output reactive power of each inverter. Consequently, the virtual inductance can adjust itself to meet (11) without any information of other inverters. To better understand the operational principle, the example system shown in Figure 1, is studied here.

In the analyses, two cases are considered, i.e., case 1: $(X_1/X_2) > 2$ and case 2: $(X_1/X_2) < 2$. The reactive power regulating process are summarized in Figure 3, wherein lines $\mathbb{1}$ and $\mathbb{2}$ are the $Q$-$V$ droop lines of DG inverters 1 and 2; lines $\mathbb{3}$ and $\mathbb{4}$ are the characteristics among output voltage $V_o$, PCC voltage $V_g$, and reactive power $Q$ of DG inverters 1 and 2 with conventional droop control (constant virtual inductance is added), whereas lines $\mathbb{5}$ and $\mathbb{6}$ are their characteristics with the proposed adaptive virtual inductance control. In addition, because the variations of output voltages of DG inverters are within a small allowable range (e.g., $\pm 10\%$), it is reasonable to treat $(X_i/V_{o_i})$ as a constant in the analysis.

**Figure 3.** Equivalent circuit of two inverters operated in parallel (one phase) Reactive power regulating process with proposed control method. (a) Case 1: $(X_1/X_2) > 2$; (b) Case 2: $(X_1/X_2) < 2$.

Case 1: $(X_1/X_2) > 2$. In this case, one can easily obtain that $V_{o1} > V_{o2}$ and $Q_2 > 2Q_1$, which leads to considerable circulating current between two DG inverters, as seen in Figure 3a. However, according to (12), virtual inductances $\Delta X_{v1}$ and $\Delta X_{v2}$, which satisfy $(\Delta X_{v1} < 2\Delta X_{v2})$, will be added to the line impedances. The outcome of this control is to make the line impedances closer to the relationship $(X_1 + \Delta X_{v1}) = 2(X_2 + \Delta X_{v2})$. As (11) is more likely to be satisfied, the circulating current can
be greatly reduced. Finally, as the load reactive power remains unchanged, i.e., $Q_1 + Q_2 = Q'_1 + Q'_2$, after adding the virtual inductances, a further voltage drop $\Delta V_g$ at the PCC generates, as shown in Figure 3a.

Case 2: $(X_1 / X_2) < 2$. In this case, it is obtained that $V_{o1} < V_{o2}$ and $Q_2 < 2Q_1$, as shown in Figure 3b. Obviously, circulating current exists in the system. However, thanks to the added virtual inductances, which satisfy $(\Delta X_v1 > 2\Delta X_v2)$ according to (12), the line impedances are automatically adjusted in the direction of making $(X_1 + \Delta X_v1)$ and $2(X_2 + \Delta X_v2)$ close to each other. Evidently, the circulating current is suppressed. Similarly, there is a voltage drop in the PCC voltage.

Hence, it can be concluded that the proposed method has strong ability in attenuating the circulating currents in paralleled inverters. Besides, as no common signals or communications are needed in the proposed control, it is a total discrete method, which makes the system feature high flexibility and scalability. Figure 4 depicts the working flow chart of the proposed control method.

![Figure 4](image)

**Figure 4.** Working flow chart of adaptive virtual inductance control method when two inverters operated in parallel.

### 3.2. Realization of the Adaptive Virtual Inductance

For each DG inverter, the commonly used cascaded control system, which is composed of an outer power droop control loop and inner voltage and current control loops (see Figure 1), is adopted. Because such a control system is well studied in the literature [9–18], its operational principle and design details are neglected. In this part, concentration is only focused on the method to add the optimized virtual inductance to each DG inverter determined by (12).

For one DG inverter, the voltage drop on the virtual inductance in the $ab$ frame is,

$$V_{vl} + jV_{vl} = j\omega L_{vhi} (i_{vl} + j\dot{\phi}_1) = -i_p \dot{\phi}_1 L_{vhi} + j\omega \dot{\phi}_1 L_{vhi}$$  \hspace{1cm} (13)
where $V_{\alpha\beta}$ and $V_{\alpha\beta}$ are the $\alpha$ and $\beta$ components of the voltage across virtual inductance respectively; $i_a$ and $i_b$ are the $\alpha$ and $\beta$ components of output current respectively; $L_s$ is the total added virtual inductance, which contains a constant part $L_c$ and an adaptively changing part $\Delta L_s$.

After subtracting this voltage drop from the reference voltage, which is generated from the reactive power droop control loop, the final voltage reference for DG inverter is obtained. Afterwards, by controlling the output voltage of the DG inverter to follow this voltage reference, the addition of virtual inductance is achieved.

Consequently, one can use (12) to optimally tune the virtual inductance online and use (13) to add the tuned virtual inductance to the output of DG inverter through its own control system.

### 3.3. Stability Analysis

To better understand the stability feature of the DG inverter with the proposed adaptive virtual output inductance control method, small-signal stability analysis is made in this part [22]. To improve the accuracy of the analyzing result, the line impedance is considered. Furthermore, the line inductance is assumed to be part of the total virtual inductance $X_{vt}$ to ease the analysis.

Based on the above assumption and according to (1) and (2), the small signal values of $P$ and $Q$ of the $i$th DG inverter at some steady state can be obtained as [23],

\[
\begin{align*}
\Delta P_i &= \frac{\omega_{fi}}{s + \omega_{fi}} \cdot (k_{PVI} \Delta V_{oi} + k_{PQi} \Delta \delta_i) \\
\Delta Q_i &= \frac{\omega_{fi}}{s + \omega_{fi}} \cdot (k_{QVI} \Delta V_{oi} + k_{QQi} \Delta \delta_i)
\end{align*}
\]

where

\[
\begin{align*}
k_{PVI} &= \frac{1}{R_i + X_{vt}} (2R_i V_{oi0} - R_i V_{g0} \cos \delta_{i0} + X_{vt} V_{g0} \sin \delta_{i0}) \\
k_{PQi} &= \frac{1}{R_i + X_{vt}} (R_i V_{oi0} V_{g0} \sin \delta_{i0} + X_{vt} V_{oi0} V_{g0} \cos \delta_{i0}) \\
k_{QVI} &= \frac{1}{R_i + X_{vt}} (2X_{vt} V_{oi0} - R_i V_{g0} \sin \delta_{i0} - X_{vt} V_{oi0} V_{g0} \cos \delta_{i0}) \\
k_{QQi} &= \frac{1}{R_i + X_{vt}} (X_{vt} V_{oi0} V_{g0} \sin \delta_{i0} - R_i V_{oi0} V_{g0} \cos \delta_{i0})
\end{align*}
\]

In (14) and (15), symbol “$\Delta$” and subscript “0” are used to represent the small signal value and steady-state value of the corresponding variable.

Then, based on the power droop control strategy shown in (4) and (5), it can be deduced that,

\[
\begin{align*}
\Delta \omega_i &= -m_i \Delta P_i \\
\Delta V_{oi} &= -n_i \Delta Q_i
\end{align*}
\]

Substituting (15) into (16) and considering $\Delta \omega_i = s \Delta \delta_i$, we have,

\[
\begin{align*}
\Delta \delta_i \cdot s &= -m_i \cdot \frac{\omega_{fi}}{s + \omega_{fi}} \cdot (k_{PVI} \Delta V_{oi} + k_{PQi} \Delta \delta_i) \\
\Delta V_{oi} &= -n_i \cdot \frac{\omega_{fi}}{s + \omega_{fi}} \cdot (k_{QVI} \Delta V_{oi} + k_{QQi} \Delta \delta_i)
\end{align*}
\]

Substituting (18) into (17) yields,

\[
s^3 + as^2 + bs + c = 0
\]

where

\[
\begin{align*}
a &= \omega_{fi} (2 + nk_{QVI}) \\
b &= \omega_{fi} (mk_{PQi} + nk_{QVI} \omega_{fi} + \omega_{fi}) \\
c &= mk_{PQi} \omega_{fi}^2 (k_{PQi} + nk_{QVI}) - nk_{PVI} k_{QQi}
\end{align*}
\]
Hence, eigenvalues of the system can be obtained by using (19) and (20) at a certain steady state, which can then be used to judge the stability features of the DG inverter. For example, the root locus of DG inverter 1, whose parameters are listed in Table 1 in Section 4, is obtained in four cases:

Case 1: \( n_1 = 5 \times 10^{-3} \text{ V/Var} \), \( L_{vt1} = 2 \text{ mH} \), \( \omega_{f1} = 30 \text{ rad/s} \), \( m_1 \) is varying from \( 1 \times 10^{-5} \) to \( 1 \times 10^{-3} \text{ rad/(s·W)} \);

Case 2: \( m_1 = 2.5 \times 10^{-4} \text{ rad/(s·W)} \), \( L_{vt1} = 2 \text{ mH} \), \( \omega_{f1} = 30 \), \( n_1 \) is varying from \( 1 \times 10^{-4} \) to \( 5 \times 10^{-2} \text{ V/Var} \);

Case 3: \( m_1 = 2.5 \times 10^{-4} \text{ rad/(s·W)} \), \( n_1 = 5 \times 10^{-3} \text{ V/Var} \), \( L_{vt1} = 2 \text{ mH} \), \( \omega_{f1} \) is varying from 0 to 150 rad/s;

Case 4: \( m_1 = 2.5 \times 10^{-4} \text{ rad/(s·W)} \), \( n_1 = 5 \times 10^{-3} \text{ V/Var} \), \( \omega_{f1} = 30 \), \( L_{vt1} \) is varying from 100 \( \mu \text{H} \) to 10 mH.

In the analyzing process, only resistive load is connected to the PCC (\( R_L = 8 \Omega \) on each phase). The analyzing results are shown in Figure 5a–d. It is easily known from Figure 5a–c that the increase of droop coefficients \( (m \) and \( n) \) and filter corner frequency \( \omega_f \) moves some eigenvalues away from the imaginary axis, which makes the system more stable. However, if the droop coefficients further increase, a pair of conjugate complex eigenvalues would move closer to the imaginary axis with increased imaginary values, which decreases the system’s damping eventually. Consequently, vibrations may be stimulated on some specific conditions, e.g., connection of constant power loads. Therefore, droop coefficients and filter corner frequency in the control system of the DG inverter have to be optimally selected.

As for the virtual inductance, it should be optimally designed as well. As can be shown in Figure 4d, if the virtual inductance is too small (<200 \( \mu \text{H} \) in this case), a pair of conjugate complex eigenvalues is located on the right half plane, which indicates that the system is unstable. Although the increase of virtual inductance may make the system stable by moving this pair of conjugate complex eigenvalues to the left, too large value would again moves one eigenvalue back towards the imaginary
axis and deteriorates the stability of system. Hence, the design of the virtual inductance has to satisfy the stability criterion first, that is,

\[ X_{vti_{\text{min}1}} \leq X_{vti} \leq X_{vti_{\text{max}1}} \]  \hspace{1cm} (21)

where \( X_{vti_{\text{min}1}} \) and \( X_{vti_{\text{max}1}} \) are the minimum and maximum value of the virtual inductance added to the \( i^{\text{th}} \) DG inverter to ensure its stability.

### 3.4. Design of the Key Parameters

For the proposed method shown in (12), three parameters have to be well designed to ensure the good performance of the system, they are \( Q_{\text{set}} \), \( X_{\text{set}} \), and \( k_v \).

Firstly, \( Q_{\text{set}} \) is selected to be the reactive power that the reference DG inverter can provide on condition that the whole microgrid needs its rated reactive power \( Q_N \), that is,

\[ Q_{\text{set}} = \frac{k_1}{k_1 + k_2 + \ldots + k_n} Q_N \]  \hspace{1cm} (22)

In the designing process, \( Q_N \) can also be chosen as the rated apparent power of the whole system for simplicity if the reactive power capacity is not given.

Then, parameters \( X_{\text{set}} \) and \( k_v \) should be chosen in a way to ensure the stability of the microgrid and at the same time make sure the bus voltage is within the limit required by the electric standard for power grid. Because the actual line impedance \( (X_L + R) \) is very small comparing to the added virtual reactance \( X_{vt} \), it is neglected in the designing process.

For the virtual inductance, apart from satisfying the stability requirement determined by (21), it has to meet the requirements of achieving decoupled active and reactive power regulation and voltage limit as well, that is:

\[
\begin{align*}
X_{vti_{\text{min}2}} &\geq a \cdot R_i \\
\sqrt{(X_{vti_{\text{max}2}})^2 + \frac{V_{oi}^2}{P_{Ni}}} &\leq 0.436 \Rightarrow X_{vti_{\text{max}2}} \leq 0.48 \sqrt{\frac{V_{oi}^2}{P_{Ni}}} \\
\end{align*}
\]  \hspace{1cm} (23)

where \( X_{vti_{\text{min}2}} \) and \( X_{vti_{\text{max}2}} \) are the minimum and maximum values of the virtual inductance added to the \( i^{\text{th}} \) DG inverter to decouple its active and reactive power regulation and ensure the voltage drop is within the required limit, respectively; \( P_{Ni} \) is the rated active power (per phase) of the \( i^{\text{th}} \) DG inverter. To decouple the active and reactive power regulation, constant \( a \) in (23) is typically set within (5)–(10) to make the inductance dominate the line impedance. The voltage drop at PCC are limited to 10% of the output voltage, which then makes the ratio between the voltage drop on the virtual inductance and the output voltage of the DG inverter to be 0.436 in (23).

Consequently, it is also required that,

\[
\begin{align*}
X_{vt} + \frac{k_1}{k_i} X_{\text{set}} - \frac{k_1}{k_i} k_{\phi} (Q_{\text{set}} - \frac{k_1}{k_i} Q_{\text{min}}) &\geq X_{vti_{\text{min}}} \\
X_{vt} + \frac{k_1}{k_i} X_{\text{set}} - \frac{k_1}{k_i} k_{\phi} (Q_{\text{set}} - \frac{k_1}{k_i} Q_{\text{max}}) &\leq X_{vti_{\text{max}}} \\
\end{align*}
\]  \hspace{1cm} (24)

where \( X_{vi} \) is the initially added constant virtual inductance in the conventional droop control; \( Q_{\text{min}} \) is selected as 0; \( Q_{\text{max}} \) is the maximum reactive power of the DG inverter; \( X_{vti_{\text{min}}} \) and \( X_{vti_{\text{max}}} \) are the minimum and maximum total virtual inductance that is required by the \( i^{\text{th}} \) DG inverter, which can be determined as,

\[
\begin{align*}
X_{vti_{\text{min}}} &= \max\{X_{vti_{\text{min}1}}, X_{vti_{\text{min}2}}\} \\
X_{vti_{\text{max}}} &= \min\{X_{vti_{\text{max}1}}, X_{vti_{\text{max}2}}\} \\
\end{align*}
\]  \hspace{1cm} (25)

Thus, the selection of \( X_{\text{set}} \) and \( k_v \) is hereby determined by considering (21)–(25).
4. Simulation and Experimental Verification

In this section, the proposed adaptive virtual inductance control method is firstly tested by simulations in Matlab/Simulink using a simulation model of an islanded microgrid composed by two DGs. The system structure and the detailed system parameters are given in Figure 1 and Table 1 respectively. As the initial constant virtual inductance of each DG inverter is set in a way to achieve decoupled regulation of its own active and reactive power, it is always the case that (11) is not satisfied.

During the simulation, consecutive simulation events are applied to the considered system. Figure 6 gives out the simulation results. Firstly, in the time interval (0–0.3 s), a load with 5 kW active power and 1.5 kVar reactive power is connected to the system PCC. As can be seen in Figure 6, the load is only supplied by DG inverter 1. After pre-synchronization of voltage and frequency of the two DG inverters using phase-locked-loop (PLL) technique [24], DG inverter 2 is connected to the AC bus at time \( t = 0.3 \) s. While finding the fact that the active power \( P \) is accurately shared by the two DG inverters according to their own power ratings, i.e., \( P_2 = 2P_1 \), the reactive power \( Q \) is not shared unfortunately. The circulating reactive power flow \( Q_{cc} \) (defined in (9)) is about 750 Var in the time interval (0.3 s–1 s). The same result happens in the time interval (1 s–2 s), where the load power has been stepped up to 15 kW and 4.5 kVar. \( Q_{cc} \) increases to 2.1 kVar in this time interval. The simulation results evidently verify that the mismatch of line impedances would result in large circulating current.

However, after the proposed virtual inductance control method is activated at \( t = 2 \) s, not only the active power is proportionally shared, but the circulating reactive power flow \( Q_{cc} \) is reduced to less than 400 Var under both load conditions, which is a huge reduction especially in high reactive load power condition.

Then, experiments are done to verify the effectiveness of the proposed adaptive virtual inductance control. Figure 7 depicts a picture of the experimental platform established in the lab. The system parameters used in experiments are the same as those used in simulations, as shown in Table 1. Similar consecutive scenarios are applied to the considered experimental system in the order of pre-synchronization and connection of DG inverters at \( t = 0.3 \) s, increase of load power at \( t = 2 \) s, activation of proposed adaptive virtual inductance control at \( t = 3 \) s and decrease of load power at \( t = 4 \) s. The results are shown in Figures 8 and 9. In Figure 8, \( C_1, C_2, C_3 \) and \( C_4 \) are the origins. The measuring units of voltage are 300 V/div, and the measuring unit of \( i_a \) in Figure 8a is 30 A/div, while other measuring units of current in Figure 8 is 20 A/div. The measuring unit of active power in Figure 9 is 2.5 kW/div, and the measuring unit of reactive power in Figure 9 is 1 kVar/div. It can be seen from Figure 8 that with the cascade control system shown in Figure 1, the islanded microgrid has good static and dynamic performances. There are no large transients in the dynamic processes. Then in Figure 9, it can be clearly evidenced that in all events, the active power is accurately shared in proportional to their power ratings, i.e., \( P_2 = 2P_1 \), while the reactive power is not. Specifically, there are large circulating reactive power flow \( Q_{cc} \) in the system before the proposed adaptive virtual inductance control method is activated at \( t = 3 \) s. The calculated \( Q_{cc} \) are 600 Var and 1250 Var in the time intervals (1 s–2 s) and (2 s–3 s) respectively. However, thanks to the addition of the proposed adaptive virtual inductance, \( Q_{cc} \) is able to be reduced to less than 150 Var under both load conditions, which can be easily noticed in Figure 9. Hence, the effectiveness and correctness of the proposed method has been verified.
Figure 6. Simulation results of the considered system.

Figure 7. Test-rig setup in the Lab.
Figure 8. Output voltages and currents of the two DG inverters (Phase A). (a) waveforms captured in the whole events; (b) zoomed in waveforms in TP1; (c) zoomed in waveforms in TP2.
Figure 9. Variations of active and reactive power of the paralleled DG inverters with and without the proposed adaptive virtual inductance control method.

5. Conclusions

In today’s microgrids, parallel operation of DG inverters is inevitable. However, due to the mismatch in line impedances of different DG inverters, there are always large circulating currents among DG inverters. This not only reduces the system efficiency, but also may limit the power supply abilities of some DG inverters.

In this paper, a decentralized circulating current suppression strategy, which is based on an adaptive virtual inductance control method, is proposed. According to the operational principle of the proposed control method, it is clearly seen that the value of the virtual inductance can be automatically adjusted with the change of the reactive power of each inverter. The operational principle, design of system parameters and implementation details are all elaborated. Compared with the existing solutions, the distinctive advantage of the proposed method is its high flexibility and scalability as it requires neither communications nor common signals. It is proved by simulations and experiments that the proposed method is correct and effective at the end of this paper.

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Nomenclature

| Symbol | Description |
|--------|-------------|
| DGs    | distributed generators |
| PCC    | the point of common coupling |
| LPF    | low-pass filter |
| PLL    | phase-locked loop |
| $V_{o1}$, $V^{*o}$, $V_g$ | output voltage, nominal output voltage, voltage at PCC |
| $\delta_i$, $\theta_i$ | power angle, impedance angle |
| $P_i$, $Q_i$, $Q_{cc}$ | output active power, output reactive power, circulating reactive power flow |
| $\omega_i$, $\omega^*$, $\omega_f$ | system frequency, nominal frequency, corner frequency of the LPF |
$m_i, n_i$ droop coefficients of active and reactive power

$k_i$ capacity ratio of each inverter

$X_{vi}, X_{Li}$ initially added constant virtual output inductance, line inductance

$X_i, \Delta X_{vi}$ equivalent output inductance, adaptive virtual inductance

$X_{set}, Q_{set}$ pre-set inductance and reactive power for the reference DG inverter

$k_v$ adaptive coefficient

$V_{va, Vb}$ the $\alpha$ and $\beta$ components of the voltage across virtual inductance

$i_{a, b}$ the $\alpha$ and $\beta$ components of output current

$Q_N$ the rated power of the whole system

$X_{vimi}, X_{vimax}$ the minimum and maximum total virtual inductance

$a$ constant within (5)–(10)

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