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Monoidal computer III: A coalgebraic view of computability and complexity*
(Extended abstract)

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Abstract Monoidal computer is a categorical model of intensional computation, where many different programs correspond to the same input-output behavior. The upshot of yet another model of computation is that a categorical formalism should provide a high-level language for theory of computation, flexible enough to allow abstracting away the low level implementation details when they are irrelevant, or taking them into account when they are genuinely needed. A salient feature of the approach through monoidal categories is the formal graphical language of string diagrams, which supports geometric reasoning about programs and computations. In the present paper, we provide a coalgebraic characterization of monoidal computer. It turns out that the availability of interpreters and specializers, that make a monoidal category into a monoidal computer, is equivalent with the existence of a universal state space, that carries a weakly final state machine for all types of input and output. Being able to program state machines in monoidal computers allows us to represent Turing machines, and capture the time and space needed for their executions. The coalgebraic view of monoidal computer thus provides a convenient diagrammatic language for studying not only computability, but also complexity.

1 Introduction

In theory of computation, an extensional model reduces computations to their set theoretic extensions, computable functions, whereas an intensional model also takes into account the multiple programs that describe each computable function [4,29, II.3].

In computer science, this semantical gamut got refined on the extensional side by denotational models, that take into account not just computable functions but also some computational effects, and on the intensional side by operational models, where the meaning of a program is specified up to an operational equivalence [9,48]. Categorical semantics of computation arose from the realization that

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**cartesian closed** categories provide a simple and effective framework for studying the extensional models [24]. Both denotational and operational semantics naturally developed as extensions of this categorical framework [26,45].

The goal of the monoidal computer project is to provide categorical semantics of intensional computation. This turns out to be surprisingly simple technically, but subtle conceptually. In this section, we describe the structure of monoidal computer informally, and try to explain it in the context of categorical semantics. In the rest of the paper, we spell out some of its features formally, in particular the **coalgebraic** part.

### 1.1 Categorical computability: context and concept

The step from a cartesian closed category \( \mathcal{C} \), as an extensional model of computation, to a monoidal computer \( \mathcal{C} \), as an intensional model, can be summarized as follows:

\[
\begin{align*}
\mathcal{C}(X, [A, B]) & \xrightarrow{\lambda_X^{AB}} \mathcal{C}(X \times A, B) \\
\mathcal{C}^*(X, \mathcal{P}) & \xrightarrow{\gamma_X^{AB}} \mathcal{C}(X \otimes A, B)
\end{align*}
\]

The first line says that a category \( \mathcal{C} \) is cartesian closed when it has the (cartesian) products \( X \times A \) and a family of bijections, natural in \( X \) and indexed over the types \( A \) and \( B \), between the morphisms \( X \times A \to B \) and \( X \to [A, B] \).

If a morphism \( X \times A \xrightarrow{f} B \) is thought of as an \( X \)-indexed family of computations with the inputs from \( A \) and the outputs in \( B \), then the corresponding morphism \( \lambda_X^{AB} \circ \varepsilon_X^{AB} \) can be thought of as the \( X \)-indexed family of programs for these computations. This structure is the categorical version of the simply typed extensional lambda calculus: \( \lambda_X^{AB} \) corresponds to the operation of abstraction, whereas \( \varepsilon_X^{AB} \) corresponds to the application [24, Part I]. The equation \( \varepsilon_X^{AB} \circ \lambda_X^{AB} = id \) says that if we abstract a computation into a program, and then apply that program to some data, then we will get the same result as if we executed the original computation on the data. This is the \( \beta \)-rule of the lambda calculus, the crux of Alonzo Church’s representation of program evaluations as function applications of \( \lambda \)-abstractions [10]. The equation \( \lambda_X^{AB} \circ \varepsilon_X^{AB} = id \) says that if we apply a program, and then abstract out of the resulting computation a program, then we will get the same program that we started from. This is the \( \eta \)-rule of the lambda calculus: the extensionality. Dropping the second equation thus corresponds to modeling the **non-extensional** typed lambda calculus, with *weak* exponent types. While this structure was sometimes interpreted as a model of intensional computation, and interesting results were obtained [18], the main result was that every such non-extensional model is **essentially extensional**, in the sense that it contains an extensional model as a retract [15]. In genuinely intensional models, identifying extensionally equivalent programs is not computable.

The structure of a monoidal computer \( \mathcal{C} \) is displayed in the second line of (1). There are **three changes** with respect to the cartesian closed structure:
a) the bijections $\varepsilon^{AB}_X$ are relaxed to surjections $\gamma^{AB}_X$;

b) the exponents $[A, B]$ are replaced with the type $P$ of programs, the same for all types $A$ and $B$; and

c) the product $\times$ is replaced with a tensor $\otimes$, and $C$ is not a cartesian category, but $C^*$ on the left is its largest cartesian subcategory with $\otimes$ as the product.

We try to clarify these changes in the next three paragraphs.

**Change (a)** means that we have not only dropped the extensionality equation $\lambda^{AB}_X \circ \varepsilon^{AB}_X = \text{id}$, but eliminated the abstraction operation $\lambda^{AB}_X$ altogether. All that is left of the bijection between the abstractions and the applications, displayed in the first line of (1), is a surjection from programs to computations, displayed in the second line of (1): for every $X$-indexed family of computations $X \otimes A \xrightarrow{f} B$ there is an $X$-indexed family of programs $X \xrightarrow{F} P$ such that $f = \gamma^{AB}_X(F)$. Could we get away with less? No, because the program evaluation $\gamma^{AB}_X$ has a left inverse $\lambda^{AB}_X$ if and only if the model is essentially extensional (i.e., it contains an extensional retract). We will see in Sec. 3.1 that the program evaluation $\gamma^{AB}_X$ is in fact executed by a universal evaluator $\{\}_X^{AB} \in C(P \otimes A, B)$, and thus takes the form $\gamma^{AB}_X(F) = \{F\}^{AB} = f$.

**Change (b)** means that all programs are of the same type $P$. The central feature of intensional computation is that any program can be applied to any data, and in particular to itself. The main constructions of computability theory depend on this, as we shall see in Sec. 3.4. If computations of type $A \to B$ were encoded by programs of a type depending on $A$ and $B$, let us write it in the form $[A, B]$, then such programs could not be applied to themselves, but they could only be processed by programs typed in the form $[[A, B], C]$. That is why all programs must be of the same type $P$. We will see in Sec. 3.3 that this implies that all types must be retracts of $P$. This does not imply that the type structure of a monoidal computer can be completely derived from an applicative structure on $P$, as an essentially untyped model of computation [24, I.15-I.17]. The type structure of monoidal computer, can be derived from internal structure of $P$ if and only if the model is essentially extensional (i.e., it contains an extensional retract, like before). But where does the monoidal structure come from?

**Change (c)** makes monoidal computers into monoidal categories, not cartesian. Just like cartesian categories, monoidal computers have the diagonals and the projections for all types, which are necessary for data copying and deleting, as explained in Sec. 2. Unlike in cartesian categories, though, the diagonals and the projections in monoidal computers are *not natural*. The projections are not natural because intensional computations may not terminate: they are not *total* morphisms. The diagonals are not natural when the computations are not deterministic: they are then *not single-valued* as morphisms. While intensional computations can be deterministic, and the diagonals in a monoidal computer can all be natural, if all projections are natural, i.e. if all computations are total, then the model contains an extensional retract. A monoidal computer is thus a cartesian category if and only if it is essentially extensional. That is why a genuinely intensional monoidal computer must be genuinely monoidal. On the other hand, even a computation that is nowhere defined has a program, and pro-
grams are always well-defined values. So while the indexed families of intensional computations cannot all be total functions, the corresponding indexed families of programs must all be total functions. That is why the category $C^*$ on the left in (1) is different from $C$: it is the largest subcategory of $C$ for which $\otimes$ is the cartesian product.

In summary, dropping or weakening any of the changes described in (a-c) leads to the same outcome: an essentially extensional model. For a genuinely intensional model it is thus necessary to have (c) a genuinely monoidal structure, (b) untyped programs, and (a) no computable program abstraction operators. It was shown in [36,41] that this is also sufficient for a categorical reconstruction of the basic concepts of computability. Sections 2 and 3 provide a brief overview of this. But our main concern in this paper is complexity.

1.2 Categorical complexity: A coalgebraic view

To capture complexity, we must capture dynamics, i.e. access the actual process of computation. This, of course, varies from model to model, and different models of computation induce different notions of complexity. Abstract complexity [7] provides, in a sense, a model-independent common denominator, which can be viewed as an abstract notion of complexity; but the categorical view of computations as morphisms at the first sight does not even provide a foothold for abstract complexity. We attempted to mitigate the problem by extending the structure of monoidal computer by grading [38], but the approach turned out to be impractical for our goals (indicated in the next section). Now it turns out to also be unnecessary, since dynamics of computation can be captured using the coalgebraic tools available in any monoidal computer.

Coalgebra is the categorical toolkit for studying dynamics in general [42,44], and dynamics of computation in particular [23,40,45]. Coalgebras, as morphisms in the form $X \to EX$ for an endofuctor $E$, provide a categorical view of automata, state machines, and processes with state update [20,39]; the other way around, all coalgebras can be thought of as processes with state update. In the framework on this paper, only a very special class of coalgebras will be considered, as the morphisms in the form $X \times A \to X \times B$, corresponding to what is usually called Mealy machines [8,14,17, ...]. In the presence of the exponents, such morphisms can be transposed to proper coalgebras in the form $X \to [A, X \times B]$. But coalgebra provides a categorical reconstruction of state machines even without the exponents, since the homomorphisms remain the same, and the category of machines is isomorphic to a category of coalgebras even if the objects are not presented as coalgebras in the strict sense. Our ”coalgebras” will thus be in the form $X \times A \to X \times B$, or more generally $X \otimes A \to X \otimes B$.

The crucial step in moving the monoidal computer story into the realm of coalgebra is to replace the $X$-indexed functions $X \times A \xrightarrow{f} B$ with $X$-state machines $X \times A \xrightarrow{m} X \times B$. While a function $f$ mapped for each index $x$ an input $a$ to an output $b$, a machine $m$ now maps at each state $x$ an input $a$ to an output $b$, and updates the state to $x'$. This state update provides an abstract view of dynamics. Continuous dynamics can be captured in varying the same approach
This step from $X$-indexed functions to $X$-state machines is displayed in the first row of the following table.

| models                      | static                                      | dynamic                                      |
|-----------------------------|---------------------------------------------|----------------------------------------------|
| extensional models:         | $[A, B] \times A\overset{f}{\rightarrow} B$| $[A^+, B] \times B\rightarrow\exists [m] \times B$ |
| cartesian                   | $\exists \lambda f \times A \rightarrow X \times A$ | $\exists \lambda [m] \times A \rightarrow X \times A$ |
| closed                      | applications                                | machines                                    |
| intensional models:         | $P \otimes A\rightarrow B\overset{f}{\rightarrow} X \otimes A$ | $P \otimes A\rightarrow X \otimes B\overset{\exists M}{\rightarrow} X \otimes A$ |
| monoidal                    | programs $\rightarrow$ computations        | adaptive programs $\rightarrow$ processes |
| computers                   |                                             |                                             |

The representation of functions from $A$ to $B$ by the elements of $[A, B]$ lifts to the representation of machines with inputs in $A$ and outputs in $B$ by the induced behaviors in $[A^+, B]$, where $A^+$ is the inductive type of the nonempty sequences from $A$. Behaviors are thus construed as *functions extended in time* [20,41,44]. In the presence of list constructors, the representation of functions using the exponents $[A, B]$ induces the representation of machines using the final machines $[A^+, B]$. The other way around, the final machines induce the exponents as soon as the idempotents split.

The rows of the table depict the step from static models to dynamic models. The columns depict the step from the extensional to the intensional. The left-hand column is just a different depiction of (1): the upper triangle unpacks the bijection in the first line of (1), whereas the lower triangle unpacks the surjection in the second line. The right-hand column is the step from the extensional coinduction of final state machines to the intensional coinduction as implemented in the structure of monoidal computer. The bottom row of the table is the step from the monoidal computer structure presented in terms of universal evaluators, the content of Sec. 3, to the monoidal computer structure presented in terms of *universal processes*, the content of Sec. 4. The fact that the two presentations are equivalent is stated in Thm. 9. This coalgebraic view of intensional computation opens an alley towards capturing dynamics of Turing machines in Sec. 5, and a direct internalization of time and space complexity measures in Sec. 6. A general approach through abstract complexity is provided in the full version of the paper. A comment about the role of coalgebra in this effort is in Sec. 7. Some proofs are in the Appendix.

### 1.3 Background and related work

While computability and complexity theorists seldom felt a need to learn about categories, there is a rich tradition of categorical research in computability the-
ory, starting from one of the founders of category theory and his students [13,31], through extensive categorical investigations of realizability [16,19,30], to the recent work on Turing categories [12], and on a monoidal structure of Turing machines [5]. A categorical account of time complexity was proposed in [11], using a special structure called timed sets, introduced for the purpose. While our approach in [38] used grading in a similar way, our current approach seems closer in spirit to [2], even if that work is neither coalgebraic nor explicitly categorical. Our effort originated from a need for a framework for reasoning about logical depth of cryptographic protocols and algorithms [34]. The scope of the project vastly exceeded the original cost estimates [37], but also the original benefit expectations. The unexpectedly simple diagrammatic formalism of monoidal computer turned out to be a very convenient teaching tool in several courses.\footnote{The course materials are available from \url{http://www.asecolab.org/courses/222/}, and the textbook [41] is in preparation.}

This extended abstract is shortened to fit the conference proceedings format. The full text is available on \url{arxiv:1704.04882}.

2 Preliminaries

A monoidal computer is a symmetric monoidal category with some additional structure. As a matter of convenience, and with no loss of generality, we assume that it is a strict monoidal category. Monoidal categories are presented in many textbooks, e.g. [25, Sec. VII.1 and Ch. XI].

We call data service the structure that allows passing the data around in a monoidal category. In computer programs and in mathematical formulas, the data are usually passed around using variables. They allow copying and propagating the data values where they are needed, or deleting them when they are not needed. The basic features of a variable are thus that it can be freely copied or deleted. The basic data services over a type \(A\) in a monoidal category \(C\) are the copying operation \(A \xrightarrow{\Delta} A \otimes A\), and the deleting operation \(A \xrightarrow{T} I\), which together form a commutative comonoid, i.e. satisfy the equations

\[
\Delta; (\Delta \otimes A) = \Delta; (A \otimes \Delta) \quad \Delta; (T \otimes A) = \Delta; (A \otimes T) = id_A \quad \Delta; \sigma = \Delta
\]

The correspondence between variables and comonoids was formalized and explained in [32]. The algebraic properties of the binary copying induce unique \(n\)-ary copying \(A \xrightarrow{\Delta} A^{\otimes n}\), for all \(n \geq 0\). The tensor products \(\otimes\) in \(C\) are the cartesian products \(\times\) if and only if every \(A\) in \(C\) carries a canonical comonoid \(A \xrightarrow{\Delta} A \xrightarrow{T} 1\), where \(1\) is the final object of \(C\), and all morphisms of \(C\) are comonoid homomorphisms, or equivalently, the families \(A \xrightarrow{\Delta} A \times A\) and \(A \xrightarrow{T} 1\) are natural. Cartesian categories are thus just monoidal categories with natural families of copying and deleting operations.
Definition 1. A data service of type $A$ in a monoidal category $C$ is a comonoid structure $A \otimes A \xleftarrow{\Delta} A \xrightarrow{\top} I$, where $\Delta$ provides the copying service, and $\top$ provides the deleting service.

Definition 2. A morphism $f \in C(A,B)$ is a map if it is a comonoid homomorphism with respect to the data services on $A$ and $B$, which means that it satisfies the following equations

\begin{align*}
    f : \Delta_B &= \Delta_A ; (f \otimes f) \\
    f : \top_B &= \top_A
\end{align*}

Given a symmetric monoidal category $C$ with data services, we denote by $C^*$ the subcategory spanned by the maps with respect to its data services, i.e. by those $C$-morphisms that preserve copying and deleting.

Remark. If $C$ is the category of relations, then the first equation says that $f$ is a single-valued relation, whereas the second equation says that it is total. Hence the name. Note that the morphisms $\Delta$ and $\top$ from the data services are maps with respect to the data service that they induce. They are thus contained in $C^*$, and each of them forms a natural transformation with respect to the maps. This just means that the tensor $\otimes$, restricted to $C^*$, is the cartesian product.

3 Monoidal computer

3.1 Evaluation and evaluators

Notation. When no confusion seems likely, we write $AB$ instead of $A \otimes B$, and $C(X)$ instead of $C(I,X)$. We omit the typing superscripts whenever the types are clear from the context.

Definition 3. A monoidal computer is a (strict) symmetric monoidal category $C$, with a data service $A \otimes A \xleftarrow{\Delta} A \xrightarrow{\top} I$ on every $A$, and a distinguished type of programs $P$, given with, for every pair of types $A, B$, an $X$-natural family of surjections $C^*(X,P) \xrightarrow{\gamma^A_{X}} C(X \otimes A, B)$, representing program evaluations.

The following proposition says that program evaluations can be construed as a categorical view of Turing's universal computer [46], or of Kleene's acceptable enumerations [43,29, II.5], or of interpreters and specializers from programming language theory [21].

Proposition 4. Let $C$ be a symmetric monoidal category with data services. Then specifying the program evaluations $\gamma^A_X : C^*(X,P) \rightarrow C(X \otimes A, B)$ that make $C$ into a monoidal computer, as defined in Def. 3, is equivalent to specifying for any three types $A, B, C \in |C|$ the following two morphisms:
(a) a universal evaluator \( \{ \}^{AB} \in C(PA, B) \) such that for every computation \( f \in C(A, B) \) there is a program \( F \in C(\mathbb{P}) \) such that \( f(a) = \{ F \}^{AB} a \).

(b) a partial evaluator \([ \]^{AB} \in C(\mathbb{P}A, P)\) with \( \{ G \}^{(AB)} C (a, b) = \{ [G]^{AB} a \}^{BC} b \).

**Remark.** Note that the partial evaluators \([ \]\) are maps, i.e. total and single valued morphisms in \( C^* \), whereas the universal evaluators \( \{ \} \) are ordinary morphisms in \( C \). A recursion theorist will recognize the universal evaluators as Turing's universal machines [46], and the partial evaluators as Gödel’s primitive recursive substitution function \( S \), enshrined in Kleene’s \( S^m \)-theorem [22]. A programmer can think of the universal evaluators as interpreters, and of the partial evaluators as specializers [21]. In any case, (a) can be understood as saying that every computation can be programmed; and then (b) says that any program with several inputs can be evaluated on any of its inputs, and reduced to a program that waits for the remaining inputs:

\[
\begin{align*}
\frac{h(x,a)}{b} &= \{ H \}^{(x)} a \\
\frac{b}{x} &= \{ [H] x \} a \\
\frac{a}{y} &= \{ [G]^{AB} a \}^{BC} b
\end{align*}
\]

Together, the two conditions thus equivalently say that for every computation \( h \in C(X \otimes A, B) \) there is an \( X \)-indexed program \( \Xi \in C^*(X, \mathbb{P}) \) such that \( h(x,a) = \{ \Xi x \} a \), namely \( \Xi = [H] \).

**Branching.** By extending the \( \lambda \)-calculus constructions as in [36], we can extract from \( \mathbb{P} \) the convenient types of natural numbers, truth values, etc. E.g., if the truth values \( t \) and \( f \) are defined to be some programs for the two projections, then the role of the if-branching command can be played by the universal evaluator:

\[
\{ \xi \}^{(x,y)} (b, x, y) = \begin{cases}
  x & \text{if } b = t \\
  y & \text{if } b = f
\end{cases}
\]

**3.2 Examples of monoidal computer**

Let \( S \) be a cartesian category and \( T : S \to S \) a commutative monad. Then the Kleisli category \( ST \) of free algebras is monoidal, with the data services induced by the cartesian structure of \( S \). The standard model of monoidal computer \( C \) is obtained by taking \( S \) to be the category of finite and countable sets, and \( TX = \bot + X \) to be the \textit{maybe} monad, adjoining a fresh element to every set. The category \( S_1 \) is the category of partial functions, and the monoidal computer \( C \subseteq S_1 \) is the subcategory of \textit{computable} partial functions:
The category $C^*$ is then the category of computable total functions. Assuming that the programs are encoded as natural numbers, the type of programs is $\mathbb{P} = \mathbb{N}$; but any language containing a Turing complete set of expressions would do, mutatis mutandis. The sequence $\{0\}, \{1\}, \{2\}, \ldots$ denotes an acceptable enumeration of computable partial functions [29, II.5]. The universal evaluators can be implemented as partial recursive functions; the partial evaluators are the total recursive functions, constructed in Kleene’s $S_n$-theorem [22]. Other commutative monads $T : S \to S$ induce monoidal computers in a similar way, capturing intensional computations together with the corresponding computational effects: exceptions, nondeterminism, randomness [26]. Some of the familiar computational monads need to be restricted to finite support. The distribution monad must be factored modulo computational indistinguishability. A simple quantum monoidal computer can be constructed using a relative monad for finite dimensional vector spaces [1]. However, in the model where the universal evaluators are quantum Turing machines, the program evaluations cannot be surjective in the usual sense, but only in the topologically enriched sense, i.e., they are dense [6]. We do not know how to derive this model from a computational monad, albeit relative. Another interesting feature is that most computational effects induce nonstandard data services, corresponding to complementary bases, which are, of course, used in randomized, quantum, but also in nondeterministic algorithms [33,35]. More examples are in [36], but most work is still ahead.

3.3 Encoding all types

**Proposition 5.** Every type $B$ in a monoidal computer is a retract of the type of programs $\mathbb{P}$. More precisely, for every type $B \in |C|$ there are computations $e^B : B \rightarrowtail \mathbb{P} : d^B$ such that $e^B$ is a map, and $e^B ; d^B = \text{id}_B$. We often call $e^B$ the encoding of $B$ and $d^B \in C(\mathbb{P}, B)$ is the corresponding decoding.

**Remark.** In [36] we only considered the basic monoidal computer, where all types are powers of $\mathbb{P}$. In the standard model, programs are encoded as natural numbers, and all data are tuples of natural numbers, which can be recursively encoded as natural numbers. Prop. 5 says that this must be the case in every computer. Note that there is no claim that either $e^B$ or $d^B$ is unique. Indeed, in nondegenerate monoidal computers, each type $B$ has many different encoding pairs $e^B, d^B$. However, once such a pair is chosen, the fact that $e^B$ is total and single-valued means that it assigns a unique program code to each element of $B$. The fact that $d^B$ is not total means that some programs in $\mathbb{P}$ may not correspond to elements of $B$. Since Prop. 5 says that the program evaluations make every type into a retract of $\mathbb{P}$, and Prop. 4 reduced the structure of monoidal computer to the evaluators for all types, it is natural to ask if the evaluators of all types can be reduced to the evaluators over the type $\mathbb{P}$ of programs. Can all of the structure of a monoidal computer be derived from the structure of...
the type \( \mathcal{P} \) of programs? E.g., can the program evaluations be "uniformized" by always encoding the input data of all types in \( \mathcal{P} \), performing the evaluations to get the outputs in \( \mathcal{P} \), and then decoding the outputs back to the originally given types? Can the type structure and the evaluation structure of a monoidal computer be reconstructed by unfolding the structure of \( \mathcal{P} \), as it is the case in models of \( \lambda \)-calculus. Is monoidal computer yet another categorical view of a partial applicative structure? The answer to all these question is positive just in the degenerate case of an essentially extensional monoidal computer. If the type structure of monoidal computer can be faithfully encoded in \( \mathcal{P} \), then there is a retract of \( \mathcal{P} \) which supports an extensional model of computation, i.e. allows assigning a unique program to each computation. If all evaluators can be derived by decoding the evaluators with the output type \( \mathcal{P} \), and if the decoding preserves the original evaluators on \( \mathcal{P} \), then all computation representable in monoidal computer must be provably total and single valued: it degenerates into a cartesian closed category derived from a \( C \)-monoid. For details see [24, I.15-I.17], and the references therein.

### 3.4 The Fundamental Theorem of Computability

In this section we show that every monoidal computer validates the claim of Kleene’s "Second Recursion Theorem" [22.27].

**Theorem 6.** In every monoidal computer \( \mathcal{C} \), every computation \( g \in \mathcal{C}(\mathcal{P} \otimes A, B) \) has a Kleene fixed point, i.e. a program \( \Gamma \in \mathcal{C}(\mathcal{P}) \) such that \( g(\Gamma, a) = \{\Gamma\} a \).

![Diagram of the Fundamental Theorem of Computability](image)

**Proof.** Let \( G \) be a program such that

\[
g([p]p, a) = \{G\}([p]p, a)
\]

A Kleene fixed program \( \Gamma \) can now be constructed by partially evaluating \( G \) on itself, i.e. as \( \Gamma = [G] G \), because

\[
g(\Gamma, a) = g([G]G, a) = \{G\}(G, a) = \{[G]G\} a = \{\Gamma\} a
\]
This theorem induces convenient representations of integers, arithmetic, primitive recursion, unbounded search and thus shows that monoidal computer is Turing complete \[41\]. In \[36\], this was done by using the \(\lambda\)-calculus constructions. Next section provides yet another proof, through Turing machines.

4 Coalgebraic view

So far, we formalized the programs \(\mapsto\) computations correspondence from the left hand column of the table in the Introduction. But presenting computations in the form \(XA \xrightarrow{(F)} B\) only displays their interfaces, and hides the actual process of computation. To capture that, we switch to the right hand column of the table, and study the correspondence adaptive programs \(\mapsto\) processes. A process is presented as a morphism in the form \(X \otimes A \to X \otimes B\). We interpreted the morphisms in the form \(X \otimes A \to B\) as \(X\)-indexed families of computations with the inputs from \(A\) and the outputs in \(B\). The indices of type \(X\) can be thought of as the states of the world, determining which of the family of computations should be run. Interpreted along the same lines, a process \(X \otimes A \xrightarrow{p} X \otimes B\) does not only provide the output of type \(B\), but it also updates the state in \(X\). This is what state machines also do, and that is why the morphisms \(X \times A \xrightarrow{m} X \times B\) in cartesian categories are interpreted as machines. In a sufficiently complete cartesian category, every such machine \(m\) induces a machine homomorphism \(X \xrightarrow{[m]} [A^+, B]\), which assigns to each state \(x \in X\) a behavior \([m]x \in [A^+, B]\), unfolded by the final \(AB\)-machine \([A^+, B] \times A \xrightarrow{\xi} [A^+, B] \times B\). The table in the Introduction displayed this. A monoidal computer, though, turns out to provide a much stronger form of representation for its morphisms in the form \(X \otimes A \xrightarrow{P} X \otimes B\): each of them induces a machine homomorphism \(X \xrightarrow{P} [P]\). This \(P\) is a program for the process \(p\). Note that there may be many programs for each process; but on the other hand, all programs, for all processes of all possible input types \(A\) and output types \(B\), are represented in the same type of programs \([P]\). This makes a fundamental difference, distinguishing machines \(m\) from computational processes \(p\), which include life \([28, 47]^2\). Every family of machines is designed in a suitable engineering language; but all computational processes can be programmed in any Turing complete language, just like all processes of life are programmed in the language of genes. That is why the morphisms \(X \otimes A \xrightarrow{P} X \otimes B\) are processes, and not merely machines. Their representations \(X \xrightarrow{P} [P]\) are not merely \(X\)-indexed programs, but they are adaptive programs, since they adapt to the state changes, in the sense that we now describe.

**Definition 7.** A morphism \(XA \xrightarrow{P} XB\) in a monoidal category \(\mathcal{C}\) is an \(AB\)-process. If \(YA \xrightarrow{r} YB\) is another \(AB\)-process, then an \(AB\)-process homomor-

\(^2\) Both Turing and von Neumann devoted a lot of attention to studying life as a computational process. Their ideas have been adopted in biology [3], but most computer scientists remain skeptical.
**Definition 8.** A universal process in a monoidal category $\mathcal{C}$ is carried by a universal state space $S \in |\mathcal{C}|$, which comes with a weakly final $A B$-process $S \otimes A \xrightarrow{[]} S \otimes B$ for every pair $A, B \in |\mathcal{C}|$. The weak finality means that for every $p \in \mathcal{C}(X \otimes A, X \otimes B)$ there is an $X$-adaptive program $P \in \mathcal{C}(X, S)$ where

\[
\begin{align*}
\{P(x)\}_S a &= p_{(P X, x, a)} \\
\{P(x)\}_B a &= p_{B(x, a)}
\end{align*}
\]

**Theorem 9.** Let $\mathcal{C}$ be a symmetric monoidal category with data services. Then $\mathcal{C}$ is a monoidal computer if and only if it has a universal process. The type $P$ of programs coincides with the universal state space $S$.

**Proof.** Given a weakly final $A B$-process $S \otimes A \xrightarrow{[]} S \otimes B$, we show that

\[
\{[]\}^{A B} = \left( S \otimes A \xrightarrow{S \otimes A} S \otimes B \xrightarrow{T \otimes B} B \right)
\]

is a universal evaluator, and thus makes $\mathcal{C}$ into a monoidal computer. Towards proving (2), suppose that we are given a computation $X \otimes A \xrightarrow{h} B$, and consider the process

\[
\hat{h} = \left( X \otimes A \xrightarrow{\Delta \otimes A} X \otimes X \otimes A \xrightarrow{X \otimes h} X \otimes B \right)
\]

By Def. 8, there is then an $X$-adaptive program $\Xi = [h] \in \mathcal{C}^*(X, S)$ satisfying the rightmost equation in the next diagram.

The middle equation holds because $[h]$ is in $\mathcal{C}^*$, i.e. a comonoid homomorphism. Deleting the state update from the process yields (2). The other way around, if $\mathcal{C}$ is a monoidal computer, with universal evaluators for all pairs of types, we claim that the weakly final $A B$-process is

\[
\{[]\}^{A B} = \left( P \otimes A \xrightarrow{P \otimes B} P \right)
\]
To prove the claim, take an arbitrary $AB$-process $X \otimes A \xrightarrow{p} X \otimes B$, and post-compose it with the partial evaluator on $X$, to get
\[ \hat{p} = (\mathbb{P} \otimes X \otimes A \xrightarrow{\mathbb{P} \otimes p} \mathbb{P} \otimes X \otimes B \xrightarrow{\mathbb{P} X \otimes B \otimes B} \mathbb{P} \otimes B) \]

Using the Fundamental Theorem of Computability, Thm. 6, construct a Kleene’s fixed point $\hat{P} \in \mathbb{C}(\mathbb{P})$ of $\hat{p}$.

The $X$-adaptive program $P \in \mathbb{C}^*(\mathbb{P})$ corresponding to the process $p \in \mathbb{C}(X A, X B)$ is now $P(x) = \left[ \hat{P}, x \right]_{X \otimes \mathbb{P}}$.

This completes the proof that $\{ \}^{A(BF)}$ satisfies definition 8 of weakly final $AB$-process, and that $\hat{P}$ is thus not only a type of programs, but also a universal state space.

5 Computability

In the remaining two sections we show how to run Turing machines in a monoidal computer, and how to measure their complexity. But a coalgebraic treatment of Turing machines as machines, in the sense discussed at the beginning of Sec. 4, would only display their behaviors, i.e. what rewrite and which move of the machine head will happen on which input, and it obliterates the configurations of the tape, where the actual computation happens. In terms of Sec. 4, a Turing machine as a model of actual computation should not be viewed as a machine, but as a process. So we call them Turing processes here. While changing well established terminology is seldom a good idea, and we may very well regret this decision, the hope is that it will be a useful reminder that we are doing something unusual: relating Turing machines with adaptive programs, coalgebraically. The presented constructions go through in an arbitrary monoidal computer, but require spelling out a suitable representation of the integers, and some arithmetic. This was done in [36], and can be done more directly; but for the sake of brevity,
we work here with the category \( \mathcal{C} \) of recursively enumerable sets and computable partial functions from Sec. 3.2. The monoidal structure and the data services are induced by the cartesian products of sets, which are, however, not categorical products any more, since the singleton set, providing the tensor unit, is not a terminal object for partial functions. The monoidal category \((\mathcal{C}, \otimes, I)\) will thus henceforth be \((\mathcal{C}, \otimes, 1)\).

Recall that Turing’s definition of his machines can be recast [40, Appendix] to processes in the form \( \rho_\omega \otimes \Sigma \xrightarrow{\rho} Q_\rho \otimes \Sigma \otimes \Theta \), where

- \( Q_\rho \) is the finite set of states, always including the final state \( \checkmark \in Q_\rho \);
- \( \Sigma \) is a fixed alphabet, always including the blank symbol \( \sqsubset \in \Sigma \);
- \( \Theta = \{\triangleleft, \triangle, \triangleright\} \) are the directions in which the head can move along the tape.

Let us recall the execution model: how these machines and processes compute. A Mealy machine \( Q_\kappa \times I \xrightarrow{\omega} Q_\kappa \times O \) inputs a string \( n \xrightarrow{\omega} I \), where \( n = \{0, 1, \ldots, n-1\} \) sequentially, e.g. it reads the inputs \( \iota_0, \iota_1, \ldots, \iota_n \), then it outputs a string \( n \xrightarrow{\omega} O \) in the same order, i.e. \( \omega_0, \omega_1, \omega_2 \), etc. In contrast, a Turing process in principle overwrites its inputs, and outputs the results of overwriting when it halts; therefore, in a Turing process, the input alphabet \( I \) and its output alphabet \( O \) must be the same, say \( I = O = \Sigma \). Both the inputs, and the outputs, and the intermediary data of a Turing process are in the form \( w : \mathbb{Z} \to \Sigma \), where all but finitely many values \( w(z) \) must be \( \sqsubset \). So each word \( w : \mathbb{Z} \to \Sigma \) is still a finite string of symbols, like in the Mealy machine model. The difference is that \( w \) is written on the infinite "tape", here represented by the set of integers \( \mathbb{Z} \), which allows the processing 'head' to move in both directions, or to stay stationary (while in a Mealy machine the head moves in the same direction at each step). We represent the position of the head by the integer 0, and the symbol that the head reads on that position is thus denoted by \( w(0) \). If the process \( \rho_\omega \otimes \Sigma \xrightarrow{\rho} Q_\rho \otimes \Sigma \otimes \Theta \), which is a triple of functions \( \rho = (\rho_\xi, \rho_\omega; \rho_\Theta) \), is defined on a given state \( q \in Q_\rho \) and a given input \( \sigma = w(0) \), then it will

- overwrite \( \sigma \) with \( \sigma' = \rho_\omega(q, \sigma) \),
- transition to the state \( q' = \rho_\xi(q, \sigma) \), and
- move the head to the next cell in the direction \( \theta = \rho_\Theta(q, \sigma) \).

If \( q = \checkmark \), then \( \rho(\checkmark, \sigma) = (\checkmark, \sigma, \square) \), which means that the process must halt at the state \( \checkmark \), if it ever reaches it. To capture this execution model formally, we extend Turing processes over the alphabet \( \Sigma \), first to processes over the set \( \Sigma \) of \( \Sigma \)-words written on a tape, and then to computations with the inputs and the outputs from \( \Sigma \)

\[
\begin{array}{c}
Q_\rho \otimes \Sigma \xrightarrow{\rho} Q_\rho \otimes \Sigma \otimes \Theta \\
\end{array}
\]

where \( \Sigma = \{ w : \mathbb{Z} \to \Sigma \mid \text{supp}(w) < \infty \} \) is the set of \( \Sigma \)-words written on a tape, and \( \text{supp}(w) = \{ z : w(z) \neq \sqsubset \} \). The elements of \( \Sigma \) are often also called the tape configurations. Writing the tuples in the form \( \tilde{\rho} = (\tilde{\rho}_Q, \tilde{\rho}_\Sigma) \), define
The execution of all Turing processes can now be captured as a single process $Q \otimes \tilde{\Sigma} \xrightarrow{p} Q \otimes \tilde{\Sigma}$, where the state space $Q$ is the disjoint union of the state spaces $Q_\rho$ of all Turing processes $\rho \in T$, i.e. $Q = \bigsqcup_{\rho \in T} Q_\rho$. So that the elements of $Q$ are the pairs $\langle \rho, q \rangle$, where $q \in Q_\rho$, and $Q \otimes \tilde{\Sigma} = Q \otimes \tilde{\Sigma}$ is the pair $p = \langle p_\rho, p_\tilde{\Sigma} \rangle$ which, when applied to $\langle \rho, q \rangle \in Q$ and $w \in \tilde{\Sigma}$, gives $p(\langle \rho, q \rangle, w) = \langle \langle \rho, q' \rangle, w' \rangle$ where $q' = \tilde{\rho}_Q(q, w)$ and $w' = \tilde{\rho}_\tilde{\Sigma}(q, w)$.

By applying Thm. 9 to the process $Q \otimes \tilde{\Sigma} \xrightarrow{p} Q \otimes \tilde{\Sigma}$, we get the following

**Proposition 10.** There is an adaptive program $\tilde{P} \in C^\bullet(\mathbb{Q}, \mathbb{P})$ such that $\tilde{P}(\rho, q)$ executes any Turing process $\rho$ starting from the initial state $q \in Q_\rho$. This means that for every tape configuration $w \in \tilde{\Sigma}$ holds $\| \tilde{P}(\rho, q) \|_w = \tilde{P}(\rho, q')$ and $\| \tilde{P}(\rho, q) \|_{\tilde{\Sigma}w} = w'$, where $q' = \rho_Q(q, w(0))$ is the next state of $\rho$, and $w' = \tilde{\rho}_\tilde{\Sigma}(q, w)$.

**Corollary 1.** The monoidal computer $\mathbb{C}$ is Turing complete.

### 6 Complexity

#### 6.1 Evaluating Turing processes

Using the process $Q \otimes \tilde{\Sigma} \xrightarrow{p} Q \otimes \tilde{\Sigma}$, which according to Prop. 10 executes the single step transitions of Turing processes, we would now like to define a computation $Q \otimes \tilde{\Sigma} \xrightarrow{\mathcal{P}} \tilde{\Sigma}$ that will evaluate Turing processes all the way; i.e. should execute all transitions that a process executes, and halt and deliver the output if the process halts, or diverge if the process diverges. The idea is to run something like the following pseudocode

$$\mathcal{P}(\langle \rho, q \rangle, w) = \begin{cases} x := \langle \rho, q \rangle; y := w; & \\
\text{while } (p_\mathcal{P}(x, y) \neq \checkmark) & \\
\{ x := p_\mathcal{P}(x, y); y := p_\tilde{\Sigma}(x, y) \}; & \\
\text{print } y & \end{cases}$$  \hspace{1cm} (3)
We implement this program using the Fundamental Theorem of Computability. The function $\tilde{p}$ is derived as a Kleene’s fixed program for an intermediary function $\tilde{\rho}$, lifting the derivation from Sec. 5, as follows

$$Q \otimes \Sigma \xrightarrow{\tilde{\rho}} Q \otimes \Sigma \xrightarrow{\tilde{\rho}} \Sigma$$

where

$$\tilde{\rho}(\Upsilon, (\rho, q), w) = \begin{cases} w & \text{if } \rho \ U (q, w(0)) = \checkmark \\ \{ \Upsilon \} & \text{otherwise} \end{cases}$$

with $q' = \rho_q (q, w(0))$ and $w' = \rho_\Sigma ((\rho, q), w)$.

Using the if-branching from Sec. 3.1, this schema can be expressed in a monoidal computer, as illustrated in the following figure.

The first equation is obtained by setting $\Upsilon$ to be Kleene’s fixed program $\tilde{P}$ of $\tilde{\rho}$, and defining $\tilde{p} = \{ \tilde{P} \}$. Given $(\rho, q) \in Q$ and $w \in \Sigma$, this $\tilde{p}$ thus runs $\rho$ on $w$, starting from $q$ and halting at $\checkmark$, at which point it outputs the current $w$. If it does not reach $\checkmark$, then $\rho$ runs forever. The second equation proves the following proposition.

**Proposition 11.** There is an adaptive program $\mathcal{P} \in C^*(Q, \Upsilon)$ that evaluates any Turing process $\rho$ starting from a given initial state $q \in Q$. This means that for every tape configuration $w \in \Sigma$ holds $\{ \mathcal{P} (\rho, q) \} w = \rho(q, w)$.

### 6.2 Counting time

To count the steps in the executions of Turing processes, we add a counter $i \in \mathbb{N}$ to the Turing process evaluator $\mathcal{P}$. The counter gets increased by 1 at each execution step, and thus counts them. We call $\overline{t}$ the computation which outputs the final count. If $\mathcal{P}$ halts, then $\overline{t}$ outputs the value of the counter $i$; if $\mathcal{P}$ does not halt, then $\overline{t}$ diverges as well. The pseudocode for $\overline{t}$ could thus look something like this:

$$\overline{t}(\rho, q, w) = \begin{cases} (x := (\rho, q); y := w; i := 0; \\
\text{while } (p_0(x, y) \neq \checkmark) \\
\{ x := p_0(x, y); y := p_\Sigma(x, y); i := i + 1 \}; \\
\text{print } i \end{cases}$$
The implementation of $\tilde{t}$ in a monoidal computer is similar to the implementation of $\tilde{p}$. It follows a similar derivation pattern:

$$Q \otimes \tilde{\Sigma} \overset{\text{Q \otimes \tilde{\Sigma} \otimes N \overset{\iota}{\rightarrow} N}}{\rightarrow} Q \otimes \tilde{\Sigma} \otimes N$$

$$\text{where } \tilde{t}(\langle \rho, q \rangle, w, i) = \begin{cases} i & \text{if } \rho \quad Q(q, w(0)) = \checkmark \\ \{ \tilde{T} \}((\rho, q') , w', i + 1) & \text{otherwise} \end{cases}$$

with $q' = \rho Q(q, w(0))$ and $w' = \rho \tilde{\Sigma}(\langle \rho, q \rangle, w)$. Like before, we set $\tilde{T}(\langle \rho, q \rangle, w) = \{ \tilde{T} \}((\rho, q), w, 0)$, where and $\tilde{T}$ is a Kleene fixed program of $\tilde{t}$. It is easy to see, and prove, that $\tilde{t}(\langle \rho, q \rangle, w)$ halts if and only if $\tilde{T}(q, w)$ halts, and if it does halt, then it outputs the number of steps that $\rho$ made before halting, having started from $q$ and $w$. The string diagrams that implement $\tilde{t}$, $\tilde{T}$, $\tilde{t}$ and $T$ are similar to those in figure (4): just rename $p$ to $t$ and $P$ to $T$, and add a string of type $N$ on the right, with the successor operation on it, to increase the counter at each run. The added string outputs the time complexity $\tilde{t}$. Hence

**Proposition 12.** There is an adaptive program $\tilde{T} \in C^\ast(Q, P)$ that outputs the number of steps that a Turing process $\rho$ makes in any run from a given initial state $q \in Q_\rho$ to the halting state $\checkmark$. If the Turing process $\rho$ starting from $q$ diverges, then the computation $\{ \tilde{T}(\rho, q) \}$ diverges as well. This means that, for every tape configuration $w \in \tilde{\Sigma}$ holds $\{ \tilde{T}(\rho, q) \}w = \tilde{t}(\langle \rho, q \rangle, w)$.

**6.3 Counting space**

So far, we used the integers $\mathbb{Z}$ as the index set for the tape configurations $w : \mathbb{Z} \rightarrow \Sigma$. The position of the head has always been $0 \in \mathbb{Z}$, and whenever the head moves, the tape configuration $w$ gets updated to $w' = \rho \tilde{\Sigma}(q, w)$, where $w'(0)$ is the new position of the head, and the rest of the word $w$ is reindexed accordingly, as described in Sec. 5. At each point of the computation $w$ thus describes the tape content relative to the current position of the head; there is no record of the prior positions or contents. To count the tape cells used by Turing processes, we must make the tape itself into a first class citizen. The simplest way to do this seems to be to add a counter $m \in \mathbb{Z}$, which denotes the offset of the current position of the head with respect to the initial position. This allows us to record how far up and down the tape, how far from its original position, does the head ever travel in either direction during the computation. To record these maximal offsets of the head, we need two more counters: let $r \in \mathbb{Z}$ be the highest value that the head offset $m$ ever takes; and let $\ell \in \mathbb{Z}$ be the lowest value that the head offset $m$ ever takes. The number of cells that the head has visited during the computation is then clearly $r - \ell$. To implement this space counting idea, we need to run a program roughly like this:
\[\varpi((\rho,q),w) = \begin{cases} x := (\rho,q); y := w; \ell, m, r := 0; \\
\text{while } (p_Q(x,y) \neq \checkmark) \\
\{ x := p_Q(x,y); y := p_\Sigma(x,y); \\
\text{if } (\rho_\Theta(q,w(0)) = a) \\
\{ \text{if } (m = \ell) \{ \ell := \ell - 1; m := m - 1 \} \\
\text{if } (\rho_\Theta(q,w(0)) = \triangleright) \\
\{ \text{if } (m = r) \{ r := r + 1; m := m + 1 \} \} \\
\text{print } r - \ell \} \end{cases} \]

The derivation now becomes

\[
\begin{array}{c}
\frac{Q \odot \exists \rightarrow q \odot \exists}{P \odot Q \odot \exists \odot \Sigma^{2} \rightarrow N} \quad \text{where } \bar{s}(X, (\rho,q), w, \ell, m, r) = \begin{cases} r - \ell & \text{if } \rho_Q(q,w(0)) = \checkmark \\
\{ (Y)^{(\rho,q'), \ldots, w', \ell', m', r') & \text{otherwise} \end{cases} \\
\end{array}
\]

and where

\[
q' = \rho_Q(q,w(0)) \\
\ell' = \begin{cases} \ell - 1 & \text{if } m = \ell \text{ and } \rho_\Theta(q,w(0)) = a \\
\ell & \text{otherwise} \end{cases} \\
w' = p_\Sigma((\rho,q),w) \\
m' = \begin{cases} m - 1 & \text{if } \rho_\Theta(q,w(0)) = a \\
m & \text{if } \rho_\Theta(q,w(0)) = \square \\
m + 1 & \text{if } \rho_\Theta(q,w(0)) = \triangleright \end{cases} \\
r' = \begin{cases} r + 1 & \text{if } m = r \text{ and } \rho_\Theta(q,w(0)) = \triangleright \\
r & \text{otherwise} \end{cases}
\]

Kleene’s fixed point \(\tilde{S}\) of \(\bar{s}\) defines \(\varpi((\rho,q),w) = \{\tilde{S}\}((\rho,q),w,0,0,0)\). The construction is summarized in the following figure:

The box \((\cdot)\)'', which computes \(\ell', m'\) and \(r'\) in figure (7), is implemented by composing several branching commands, e.g. as described at the end of Sec. 3.1. Implementing this box is an easy but instructive exercise in programming monoidal computers. Together, these constructions prove the following proposition.
Proposition 13. There is an adaptive program $\mathcal{S} \in C^*(Q, P)$ that outputs the number of cells that a Turing process $\rho$ uses in any run from a given initial state $q \in Q_\rho$ to the halting state $\checkmark$. If the Turing process $\rho$ starting from $q$ diverges, then the computation $\{\mathcal{S}(\rho, q)\}$ diverges as well. This means that, for every tape configuration $w \in \tilde{\Sigma}$ holds $\{\mathcal{S}(\rho, q)\}w = \tilde{s}(\langle \rho, q \rangle, w)$.

Remark. There are many variations of the above definitions in the literature, and several different counting conventions. E.g., an alternative to the above definition of $\tilde{s}$ would be something like

$$\tilde{s}'(\langle \rho, q \rangle, w) = \{\tilde{s}\}(\langle \rho, q \rangle, w, w_\ell, 0, w_r)$$

where

$$w_\ell = \min\{i \in \mathbb{Z} | w(i) \neq \bot\} \quad w_r = \max\{i \in \mathbb{Z} | w(i) \neq \bot\}$$

In contrast with $\tilde{s}$, where the space counting convention is that a memory cell counts as used if and only if it is ever reached by the head, the space counting convention behind $\tilde{s}'$ is that every computation uses at least $|w| = w_r - w_\ell$ cells, on which its initial input is written. If a Turing process halts without reading all of its input $w$, or even without reading any of it, the space used will still be $|w|$.

Some textbooks adhere to the $\tilde{s}$-counting convention, some to the $\tilde{s}'$-counting convention, but many do not describe the process in enough detail to discern this difference. This is perhaps justified by the fact that the resulting complexity classes and their hierarchies are the same for all such subtly different counting conventions. E.g., the difference between $\tilde{s}$ and $\tilde{s}'$ is absorbed by the $O$-notation, and only arises for computations that do not read their inputs.

7 Final comments

A bird’s eye view of algebra and coalgebra in computer science suggests that algebra provides denotational semantics of computation, whereas coalgebra provides operational semantics [23,40,45]. Denotational semantics goes beyond the purely extensional view of computations (as maps from inputs to outputs), and models certain computational effects (such as non-termination, exceptions, non-determinism, etc.). Operational semantics goes further, and models computational operations. While computational effects are thus presented using the suitable algebraic operations in denotational semantics, computational behaviors are represented as elements of final coalgebras in operational semantics. But although both the denotational and the operational approaches go beyond the purely extensional view, neither has supported a genuinely intensional view, envisioned by Turing and von Neumann, where programs are data. Therefore, in spite of the tremendous successes in understanding and systematizing computational structures and behaviors, categorical semantics of computation has remained largely disjoint from theories of computability and complexity.

The claim put forward in this paper is that coalgebra provides a natural categorical framework for a fully intensional categorical theory of computability and complexity. The crucial step that enables this theory leads beyond final coalgebras, that assign unique descriptions to computational behaviors of fixed types,
to universal coalgebras, that assign non-unique descriptions to computations of arbitrary types. These descriptions are what we usually call programs. Our message is thus that programmability is a coalgebraic property, just like computational behaviors are coalgebraic. This message is formally expressed through universal processes; it can perhaps be expressed more generally through universal coalgebras, as families of weakly final coalgebras, all carried by the same universal state space. Thm. 9 spells out in the framework of monoidal computer the fact that every Turing complete programming language provides a universal coalgebra for computable functions of all types; and vice versa, every universal coalgebra induces a corresponding notion of program. Just like abstract computational behaviors of a given type are precisely the elements of a final coalgebra of that type, abstract programs are precisely the elements of a universal coalgebra. Just like final coalgebras can be used to define semantics of computational behaviors [40], universal coalgebras can be used to define semantics of programs.

From a slightly different angle, the fact that universal coalgebras characterize monoidal computers, proven in Thm. 9, can also be viewed as a coalgebraic characterization of computability. There are, of course, many characterizations of computability. The upshot of this one is, however, in Propositions 12 and 13: the coalgebraic view of computability opens an alley towards complexity. In any universe of computable functions, normal complexity measures [38] can be programmed coalgebraically. Combining this coalgebraic view of complexity with the algebraic view of randomized computation seems to open up a path towards a categorical model of one-way functions, and towards categorical cryptography, which has been the original goal of this project [34].

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