Fault characteristics of Inverter-Interfaced Distributed Generation

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Abstract. Distributed generations can be divided into traditional synchronous generators and inverter interfaced distributed generations (IIDG) according to their different operation mode. While the fault characteristics of IIDG is different from traditional Distributed Generators, the in-depth analysis on output characteristics and fault characteristics of IIDG are the foundation of micro grid protection. The mathematical models under P/Q and V/F control strategy are discussed. After simplifying its model, the fault characteristics of IIDG under P/Q and V/F control strategy are studied using the simulation of PSCAD/EMTDC. The analysis on the fault characteristics of IIDG that lays the theoretical foundation for the micro-grid relay protection.

1 Introduction

As a key technology in smart grid, distributed power supply technology has attracted more attention [1]. With the rapid development of power electronics technology, inverter interfaced distributed generation (IIDG) has been widely used [2]. The research on traditional rotating distributed generation (DG) is relatively mature for the time being, while limited research has been conducted on IIDG. Therefore, it is necessary to analyze IIDG and its fault output characteristics for the distribution network protection [3, 4].

2 Modelling of IIDG

In order to study the fault characteristics of IIDG, it is necessary to simplify its model reasonably. Firstly, there is always a capacitor on the bus in front of the DC side of the IIDG, the existence of this energy storage device makes the output voltage of the IIDG DC side basically constant; Secondly, the capacitor between the IIDG inverter and its DC side can maintain the output voltage of IIDG and also provide energy for the IIDG in the transient state; Finally, environmental factors such as illumination and wind speed can be considered as invariable in tens of milliseconds of power system relay protection action, so the impact of environmental factors on IIDG can be ignored. Therefore, in the process of IIDG transient analysis, DC source can be approximately used to replace the output of IIDG DC side without loss of generality [5].

The simplified IIDG model is shown in Fig.1. It can be seen from the figure that the transient characteristics of IIDG mainly depend on the control strategy of the inverter, other factors can be ignored.

2.1 Interface model of IIDG under P/Q control

When the inverter adopts P/Q control strategy, that is, the controller in Figure 1 adopts P/Q control strategy, its control links mainly include three parts, namely power control link, PI control link and limiting control link. The specific control process is shown in Fig.2.

The calculation formula of power is:

\[ P_{in} = v_d i_d + v_q i_q \]  
(1)

\[ Q_{in} = v_d i_q - v_q i_d \]  
(2)

The PI control formula is:

\[ i_d = (k_p + \frac{1}{s T_p}) \Delta p \]  
(3)

\[ i_q = (k_q + \frac{1}{s T_q}) \Delta q \]  
(4)

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2.2 Interface model of IIDG under V/F control

When the inverter adopts V/F control strategy, its control links mainly include four parts, power control link, drop control link, PI control link and integral control link, the specific control process is shown in Fig. 3.

![V/F control strategy](image)

The power calculation process is the same as that of P/Q control.

The drop control formula is:

\[ \omega_i = \omega_k - m_p \omega \]  

\[ v_i = v_n - n_q q \]

Where \( m_p \) is the slope of active power droop curve, \( n_q \) is the slope of reactive power droop curve.

3 Characteristic analysis for IIDG

Since the control strategy of the inverter is the main factor affecting the transient characteristics of IIDG, it is necessary to analyze the fault characteristics of IIDG under different control strategies. In addition, since DG has two operation modes, independent DG generally adopts V/F control strategy to maintain the stability of microgrid frequency and voltage, while grid connected DG usually adopts P/Q control strategy. Therefore, this section mainly analyzes the fault characteristics of IIDG under P/Q control and V/F control strategy in the power system transient simulation software PSCAD/EMTDC without considering the protection of inverter and IIDG itself.

3.1 Simulation analysis on fault characteristics of IIDG under P/Q control strategy

The simulation model is established under PSCAD, as shown in Fig. 4. At this time, IIDG adopts P/Q control strategy for grid connected operation.

The P/Q control parameters are \( k_p=1, k_q=1, T_p=0.4, T_q=0.5, I_{max}=1, I_{min}=1 \). The voltage of the line is 380V and the length of the line is 0.2km, the parameters of the line are \( R=0.7975 \Omega/km \), \( X=0.105 \Omega/km \). The fault characteristics of IIDG under P/Q control strategy are obtained by simulation analysis of various short-circuit faults in transmission lines under P/Q control strategy.

![Simulation model under P/Q control strategy](image)

3.1.1 Three-phase short circuit fault

When the three-phase short circuit fault occurs at 50% of the line, the output current, active power and terminal voltage waveform of the fault phase of the IIDG inverter decreases slightly, and the terminal voltage waveform and active power waveform of the fault phase of the IIDG inverter are shown in Fig. 5.

![Waveforms of P/Q controlled inverter when three-phase short circuit fault occurred](image)

This shows the voltage and output active power of IIDG after fault are significantly reduced compared with normal operation. It can be seen from the figure that the instantaneous maximum fault current is about 1.3 times of that under normal operation.

3.1.2 Two-phase short circuit fault

When the two-phase short circuit fault occurs at 50% of the line, the output current, active power and terminal voltage waveform of the fault phase of the IIDG inverter under P/Q control strategy are shown in Fig. 6.

![Waveforms of P/Q controlled inverter when two-phase short circuit fault occurred](image)
drops greatly. The fault current is about 1.3 times of the normal operation current.

### 3.1.3 Two-phase grounding short circuit fault

When two-phase grounding short circuit fault occurs at 50% of the line, the output current, active power and terminal voltage waveform of the fault phase of the IIDG inverter under P/Q control strategy are shown in Fig. 7.

At this time, the output power and terminal voltage of the fault phase of the IIDG inverter decreases greatly. The fault current is about 1.5 times of the normal operation current.

### 3.1.4 Single-phase grounding short circuit fault

When single-phase grounding short circuit fault occurs at 50% of the line, the output current, active power and terminal voltage waveform of the fault phase of the IIDG inverter under P/Q control strategy are shown in Fig. 8.

At this time, the output power of the fault phase of the IIDG inverter is not changed basically, and the terminal voltage decreases slightly, the fault current is about 1.5 times of the normal operation current.

According to the simulation analysis, the fault characteristics data of IIDG under P/Q control strategy are shown in Table 1.

|                   | P (MW) | U (pu) | I (kA) |
|-------------------|--------|--------|--------|
| Normal operation  | 0.13   | 1.0    | 0.40   |
| Three-phase short circuit | 0.08   | 0.42   | 0.62   |
| Two-phase short circuit   | 0.09   | 0.51   | 0.51   |
| Two-phase grounding short circuit | 0.09   | 0.49   | 0.62   |
| Single-phase grounding short circuit | 0.11   | 0.52   | 0.72   |

It can be seen from Table 1 that the maximum fault current generated by IIDG is about 1.74 times of rated load current in case of single-phase grounding short-circuit fault, and the minimum fault current generated by IIDG under two-phase short-circuit is about 1.24 times of rated load current, indicating that the zero sequence impedance of IIDG is less than its positive sequence impedance.

The terminal voltage of IIDG decreases the least when single-phase grounding short circuit occurs, which is 0.52 times of the rated voltage; the terminal voltage of IIDG decreases the most when the three-phase short circuit occurs, which is 0.42 times of the rated voltage. This shows that the closer the fault point is to the IIDG, the greater the influence on the terminal voltage is, and the output power is reduced compared with the normal value. At this time, the output power of IIDG cannot maintain the setting value.

To sum up, the terminal voltage of IIDG under P/Q control strategy will decrease when its adjacent feeders are short circuited, so the output active power of IIDG will be affected, which is lower than the rated value. The output fault current of IIDG is not only affected by the maximum allowable current of internal power electronic devices, but also closely related to the output power and voltage of IIDG.
3.2 Simulation analysis on fault characteristics of IIDG under V/F control strategy

The simulation model is established under PSCAD, as shown in Fig. 9. IIDG with V/F control strategy and synchronous generator with active power output of 0.01MW are the two power sources of the system. The V/F control parameters are \( k_p = 1 \), \( k_q = 1 \), \( T_p = 10 \), \( T_q = 10 \), \( m_p = 0.001 \), \( n_q = 0.001 \).

![Fig9. The simulation model under V/F control strategy](image)

3.2.1 Three-phase short circuit fault

When the three-phase short circuit fault occurs at 50% of the line, the output current, active power and terminal voltage waveform of the fault phase of the IIDG inverter under V/F control strategy are shown in Fig. 10.

The voltage and output active power of IIDG after fault are significantly reduced compared with normal operation. It can be seen from the figure that the instantaneous maximum fault current is about 1.3 times of that under normal operation.

![Fig10. Waveforms of V/F controlled inverter when three-phase short circuit fault occurred](image)

3.2.2 Two-phase short circuit fault

When the two-phase short circuit fault occurs at 50% of the line, the output current, active power and terminal voltage waveform of the fault phase of the IIDG inverter under V/F control strategy are shown in Fig. 11.

It can be seen from Fig.10 that the terminal voltage of IIDG is basically unchanged at this time.

3.2.3 Two-phase grounding short circuit fault

When two-phase grounding short circuit fault occurs at 50% of the line, the output current, active power and terminal voltage waveform of the fault phase of the IIDG inverter under V/F control strategy are shown in Fig. 12.

It can be seen from Fig.12 that the strategy of V/F control has excellent performance.

3.2.4 Single-phase grounding short circuit fault

When single-phase grounding short circuit fault occurs at 50% of the line, the output current, active power and terminal voltage waveform of the fault phase of the IIDG
inverter under V/F control strategy are shown in Fig. 13. It can be seen from Fig.13 that the strategy of V/F control play a good role in maintaining terminal voltage of IIDG stability.

![Waveforms of V/F controlled inverter when single phase grounding short circuit fault occurred](image)

Fig13. Waveforms of V/F controlled inverter when single phase grounding short circuit fault occurred

According to the simulation analysis, the fault characteristic data under V/F control strategy are shown in Table 2.

**Table 2. Normal and fault data under V/F controlled inverter**

|                  | P (MW) | U (pu) | I (kA) |
|------------------|--------|--------|--------|
| Normal operation | 0.21   | 1.0    | 0.60   |
| Three-phase short circuit | 0.36 | 1.01   | 1.04   |
| Two-phase short circuit | 0.30 | 1.01   | 1.01   |
| Two-phase grounding short circuit | 0.31 | 1.01   | 1.02   |
| Single-phase grounding short circuit | 0.30 | 1.01   | 1.01   |

The simulation experiment shows that that IIDG under V/F control strategy can play a better role, so that the terminal voltage of IIDG can be basically kept constant when the system has a short-circuit fault. In addition, due to the upper limit of active power in this control strategy, and its terminal voltage is stable around the rated voltage, the fault current of IIDG has little difference.

4 Conclusions

Firstly, the model of IIDG is simplified, and the mathematical models of IIDG are established under P/Q control strategy and V/F control strategy. Then, the simulation model of IIDG is established by power system transient simulation software PSCAD. The fault characteristics of IIDG under P/Q control strategy and V/F control strategy are analyzed respectively. Through the above analysis, the following conclusions are obtained:

1. IIDG under P/Q control strategy has the lowest terminal voltage when three-phase short circuit occurs, and the highest terminal voltage when single-phase ground short-circuit occurs; the minimum fault current occurs when two-phase short-circuit occurs, and the maximum fault current occurs when single-phase ground short-circuit occurs. The positive sequence impedance of IIDG is greater than its zero sequence impedance. The controlled voltage source after reactance can be equivalent to IIDG. The controlled condition is that the output power of IIDG is constant, and the constraint condition is that the outlet current value of IIDG is less than 2 times of its rated current value.

2. IIDG under V/F control strategy, the fault voltage of IIDG is basically maintained near the rated voltage when the fault point is far away from the outlet of IIDG. Since the upper limit of active power is set in V/F control strategy, the difference of fault current at IIDG outlet is not obvious when various short circuit faults occur. In this case, the controlled current source after reactance can be used to equivalent to IIDG. Obviously, the controlled condition is that the terminal voltage remains constant, while the constraint condition is that the outlet current value of IIDG is less than 2 times of its rated current value.

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