Driving Voltage Reduction of a Micromechanical Optical Switch Driven by Electrostatic Force Using an S-Shaped Deformable Thin-Film Mirror*

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Abstract
This paper describes the driving voltage reduction of an optical switch that has an S-shaped deformable thin-film mirror driven by an electrostatic force. The prototype optical switch has high-dielectric-constant layers made from Ta_2O_5 for a low applied voltage to generate a sufficiently large attractive force. Although the dielectric layers are effective at increasing the attractive force, they often cause a charging problem. Thus, suitable driving procedures for solving the charging problem were experimentally examined. In the two driving procedures, AC or DC voltage inputs were used. The experimental results indicate that the driving procedures are effective at reducing the driving voltage to 30 V. However, the results also show that the AC components induce a residual vibration of the spot position. Consequently, the driving procedure with the DC voltage input is proven to be more suitable.

Key words: Optical Switch, Electrostatic Force, S Shape, Thin-Film Mirror, Dielectric Layer, Charging, Driving Voltage

1. Introduction

Until now, many types of micromechanical optical switches with solid micromirrors have been investigated for information systems [1-4]. Solid micromirrors provide many advantages when used in optical switches. Micromirrors are often driven by electrostatic actuators. In comparison with electromagnet actuators, electrostatic actuators can be fabricated easily as micro-electro-mechanical systems (MEMS) and produce low heat. Many electrical conducting materials can be used for the actuators. However, electrostatic actuators often require high driving voltages to produce a sufficient force. Many electrostatic actuators for optical switches are driven by a voltage above 80 V [5-11]. The high voltage reduces the usability of the electrostatic actuators, and this poses significant problems for many electrostatic actuators.

In past research, suitable electrode shapes have been designed for large force gains in the electrostatic actuators and reductions of the driving voltage. Comb-shaped electrodes are suitable and are often used for electrostatic actuators in MEMS devices. Optical switches with a combination of vertically positioned mirrors and horizontal comb-shaped actuators [1, 12], or a combination of horizontal attitude mirrors and vertical comb-shaped actuators [13] have been driven by voltages below 60 V. However, these combinations make
the device structures complex and require bulk micromachining techniques to create deep penetrations and steep-sided holes and trenches, such as the LIGA process and deep reactive ion etching (DRIE) process. It is well known that these processes are quite expensive [14, 15].

The reduction of the gap between electrodes is also effective for achieving low driving voltages. When parallel electrodes are used, a small gap decreases not only the driving voltage but also the working range of the rotational mirror [16]. Sloped electrodes can relieve the problem of the working range limitation [17, 18]. However, sloped electrodes have complex parts that require bulk micromachining, not unlike comb-shaped electrodes.

S-shaped deformable elements have a suitable shape for low-voltage driving. The S shape has also been used for a novel micromechanical optical switch [19] and a microvalve [20, 21]. Some RF switches have beam structures that offer the same advantage [22]. The S shape makes the gap between electrodes small at the edge, and the electrostatic force works effectively at the edge. The characteristics can be maintained across a wide working range. A microvalve has been reported to be driven by an applied voltage of 40 V [21]. The sloping part of an S-shaped element in a microvalve forms a small angle with the electrode that increases the driving force and reduces the required driving voltage. However, this small angle requires a wide space and is therefore not suitable for multi-channel devices. Additionally, the sloping part of the mirror in the optical switch needs to form an angle of 45 degrees with the electrodes, which is about 4.5 times greater than the angle of the film to the electrode reported in [21].

This paper describes the driving voltage reduction of a micromechanical optical switch using an S-shaped deformable thin-film mirror. In this research, a high-dielectric-constant material is used to increase the electrostatic force gain and to decrease the minimum driving voltage. This method is considered to be useful for devices requiring a large angle such as multi-channel devices. However, it generally causes a significant charging phenomenon that greatly deteriorates the performance and reliability of the electrostatic actuators [23, 24]. The phenomenon and its repression have often been discussed [25-27]. To repress the negative effects of charging, suitable driving signals must be determined.

This paper is organized as follows: The principles of the micromechanical optical switch with the S-shaped mirror are introduced in Section 2. Section 3 explains the concept of the driving voltage reduction method. In Section 4, the fabrication process and a prototype of the switches are shown. Section 5 discusses the effects of driving procedures with alternating current (AC) and direct current (DC) voltage inputs on the driving characteristics based on experiments. Concluding remarks are then presented in Section 6.

2. Principle

Figure 1 shows the principle of the micromechanical optical switch with an S-shaped thin-film mirror [19]. In this figure, the case of a 1 × 4 switch is illustrated. The core unit of this switch consists of two substrates, which are set parallel to each other, and an S-shaped conductive thin-film mirror located between the substrates. The film mirror between the substrates is set to form an angle of 45 degrees with the substrates for bending light rays at a right angle. At least one of the substrates and its electrodes are made from transparent materials so that light rays can pass through the substrate. The substrates and electrodes that are located under the layer are coated with an insulation layer.

When a voltage is applied between the film mirror and the electrodes in the substrates, the electrostatic force causes the film to stick to the substrates. Using this force, the film mirror between the substrates is set to form an angle of 45 degrees with the substrates. The arrows in the figure indicate that the electrostatic force is produced between the film and the electrode. In Fig. 1(a), the incident light ray parallel to the substrates is reflected by the
thin-film mirror between the substrates and passes through the lower substrate. When the voltage pattern applied to the electrodes is changed as shown in Fig. 1(b), the position of the sloping part of the film mirror (reflecting part) and the spot position of the reflected light ray move to the left side; that is, the spot position is switched. Since the optical switch outputs the light ray out of the plane, it can be used as a scan line for displays and scanners in addition to components of N × N optical switches [19].

3. Reduction Method for Driving Voltage

The S shape leads to a small gap between the film mirror and the electrode at the edge of the mirror part not in contact with the substrates, and the electrostatic force works effectively at the edge. Figure 2 shows the S-shaped thin-film mirror near the edge. When the S-shaped thin-film mirror is driven, the attractive force produced near the edge of the reflecting part very close to the substrate greatly influences the minimum driving voltage. In Area (A) of Fig. 2, where the thin-film mirror faces the electrode, the electrostatic force between a small part of the mirror and the electrode \( \Delta f_{ae} \) can be approximately expressed as

\[
\Delta f_{ae} = \frac{\varepsilon_0}{\varepsilon_a} \cdot \frac{W \Delta x}{2 \left( \frac{t_{ae}}{\varepsilon_a} + \frac{t_{ae}}{\varepsilon_a} \right)^2} V_{in}^2
\]

where

- \( V_{in} \): Applied voltage
- \( \varepsilon_0 \): Vacuum permittivity
\( \varepsilon_a \): Relative permittivity of air  
\( \varepsilon \): Relative permittivity of insulation layer  
\( w \): Width of mirror  
\( \Delta x \): Small length of mirror  
\( t_a \): Air gap between mirror part and substrate  
\( t_i \): Thickness of insulation layer

This equation indicates that the use of a high-dielectric-constant material for the insulation layer increases the attractive electrostatic force. Its use is effective at reducing the minimum driving voltage and does not influence the dimensions of the switch. However, the high-dielectric-constant layer often causes a significant charging phenomenon that greatly deteriorates the response and reliability of the electrostatic actuators. Since the actuator performance is important, a method for suppressing such charging needs to be developed.

The charging phenomenon is caused by the DC component of the driving voltage and is suppressed by the AC component of the driving voltage. Thus, driving using an AC component is effective at suppressing the charging. In Section 5, the usefulness of the AC driving voltage is discussed. Aside from this, the addition of a step for the change of the applied voltage direction will be useful for eliminating the negative charging effect. The usefulness of this addition is also examined experimentally in that section.

4. Prototype Core Switch

4.1 Fabrication of Components

Figure 3 shows a 3-D schematic diagram of the prototype optical core switch fabricated in this study. The prototype was constructed and used to examine the driving characteristics with a high-dielectric-constant insulation layer. The core switch consists of three components: an upper substrate, a lower substrate, and an S-shaped deformable thin-film mirror. The electrodes in the substrates are arranged in a line. The core switch works as a \( 1 \times N \) switch. The incident rays also move along the electrode array.

Figure 4 shows the fabricated substrate used as the upper substrate and lower substrate in Fig. 3 and its structure. The upper and lower substrates have the same structure. They were fabricated using photolithography because of its accessibility. Transparent glass was used as the base material. The electrodes were constructed from InTiO so that light rays can
pass through the substrate. For easy wiring, Cr (indicated by the dark areas) was used as a visible conductor material. Although the insulation layers in the previous prototype optical switches were made from SiO₂ [19], those in this prototype were made from Ta₂O₅, whose dielectric constant is about six times larger than that of SiO₂. Figure 5 shows the fabricated deformable thin-film mirror attached to the upper substrate. In this figure, the mirror surface for reflecting light rays is shown. This mirror is used as the S-shaped deformable thin-film mirror shown in Fig. 3. The fabricated mirror is constructed from Ti–Ni alloys, which have
a large allowable deformation and a high fatigue strength. Two mirrors with different Ti-Ni alloy thicknesses (0.5 μm and 0.3 μm) were manufactured. An Au layer (50 nm) was applied to the alloys and used as the mirror surface.

4.2 Setting

Figure 6 shows the prototype core switch constructed from the fabricated components and the experimental setup including the core switch. A photograph of the prototype core switch from a lateral view is shown in Fig. 6(a). First, the thin-film mirror was bonded to the upper substrate using an electrically conductive adhesive. Next, the gap between the substrates was adjusted, and the center angle of the reflecting part of the mirror was adjusted to be 45 degrees to the substrates. A gap of 0.3 mm was used.

The setting process was performed using the experimental setup illustrated in Fig. 6(b). The experimental setup comprises an XYZθ fine stage, a light source on a mount, and a position sensitive detector (PSD). The upper and lower substrates are positioned on the mount and the fine stage, respectively. The fine stage is used to adjust the components. The light source emits light rays through a 0.25-mm diameter lens. The driving characteristics are examined using light with a 670 nm wavelength because this allows easy operation. The spot position of the light rays reflected by the thin-film mirror is measured using the PSD to examine the characteristics of the core switches.

5. Experimental Discussion

Figure 7 shows the basic driving procedure for moving the reflecting part one step to the left, which is used in [19]. The voltage pattern in Fig. 7(a) makes the angle of the reflecting part 45 degrees, as shown in Fig. 1. For one pitch motion of the reflecting part, the voltage patterns in Figs. 7(b) and (c) are applied to the electrode array in sequence. The voltage pattern in Fig. 7(c) makes the angle of the reflecting part 45 degrees again. The basic driving procedure in Fig. 7 with a DC voltage input can drive the prototype core switch in Fig. 6 for a while, but charging limits the driving time. In this section, the effect of
the two applied driving procedures on the driving voltage reduction is discussed; that is, the driving procedure in Fig. 7 with sinusoidal AC voltage inputs and an improved driving procedure having new voltage patterns with DC voltage inputs.

5.1 Effect of driving procedure with AC voltage inputs

In order to avoid charging the substrates, sinusoidal AC voltages were applied to the electrodes with the voltage input in Fig. 7, and their effects were examined experimentally. In the experiments, two mirror thicknesses (0.35 μm and 0.55 μm) and three sinusoidal AC voltage inputs with different frequencies (100 Hz, 1 kHz, and 10 kHz) were used.

Figure 8 shows the one-pitch (0.5 mm) stepwise response of the light spot using the prototype core switch with a mirror thickness of 0.55 μm. The dimensions of the core switch and the applied voltage are shown at the top of Fig. 8. Leftward movement of the spot in Fig. 1 is defined as positive movement. The amplitude and frequency of the applied voltage are 35 V and 1 kHz, respectively. The switching period for each step height is 48 ms. During this period, the time for the transient state shown in Fig. 7(b) (transient-state time) is 0.75 ms. Figure 9 shows the definition of the mirror position.
experiment, the reflecting part of the mirror was moved from point (A) to (H) and from point (H) to (A) one step at a time. The change of the spot position per period becomes approximately one pitch, i.e., 0.5 mm. In this study, the response of the spot position with respect to the initial spot position is referred to as the displacement. A sinusoidal AC voltage input of 35 V amplitude successfully moves the average spot position of the core switch with the high-dielectric-constant insulation layers to the target position, although the spot position vibrates. This peak-to-peak (p-p) driving voltage is smaller than the minimum driving voltage of 130 V shown in [19].

The effect of the input voltage amplitude on the response is shown in Fig. 10 using a 1 kHz sinusoidal voltage input.

![Graph showing the effect of input voltage amplitude on the response](image1)

Fig. 10  Effect of input voltage amplitude on the response using a 1 kHz sinusoidal voltage input

The response of the spot position is greatly influenced by the frequency of the input voltage. The frequency effect on the response is shown in Fig. 11. A voltage input of 100 Hz

![Graph showing the frequency effect on the response](image2)

Fig. 11  Frequency effect on response
causes significant vibration of the spot position. The vibration amplitude decreases with an increase of the frequency. However, the steady-state vibration is not completely eliminated. The vibration reduction is important for driving the switch using AC voltage.

5.2 Improvement of the driving procedure with DC voltage inputs

In order to overcome the charging problem and reduce the steady-state vibration, the driving procedure was improved by using a DC voltage input. Figure 12 shows the improved driving procedure. The voltage pattern to the electrodes is changed from (a) to (b) to (c) to (d) to (e). In the procedure, the voltage inputs to the electrodes near the edge of the reflecting part are changed to move the reflecting part. The changes from (a) to (b) and from (d) to (e) are performed to eliminate the charging problem. This procedure is expected to effective discharge the insulation layer just prior to the switching motion of the mirror. The use of DC voltage input can stably maintain the mirror position.

Figure 13 shows the experimental one-pitch stepwise response of the spot position with the driving procedure of Fig. 12. As in Fig. 8, the reference spot position was switched. The relationship between the spot position and the reference point is shown on the left side of the figure. The dimensions of the core switch and the applied voltage are shown at the top of the figure. The period for each step height is 8 ms. For each
voltage pattern, it takes 2 ms. Voltage pattern (b) causes a transient response of the spot position. Voltage patterns (c), (d), (e) and (a) provide a stable spot position. Although the spot position command for point (B) did not provide sufficient displacement, the spots are positioned precisely at the other points. The result indicates that an amplitude of 30 V is effective enough for switching the spot. Although a voltage input of 25 V could move the reflecting part, the motion range was limited. This driving procedure provides a similar minimum driving voltage without the steady-state vibration to the driving procedure with an AC voltage input presented in Section 5.1. On the whole, this shows a better performance than the driving procedure in Section 5.1.

In order to examine the behavior of the reflecting part at position (B), the behavior was measured with a high-speed video camera. Figure 14 shows side-view photographs of the reflecting part after the step command inputs for the four positions (A), (B), (C), and (D). All the step commands produce significant step motion. However, the distance between positions (A) and (B) is shorter than the other distances. In addition, the angle between the reflecting part and the substrates at position (B) is similar to those at the other points. This is considered to be caused by fabrication problems and results in a large spot position error for the reference input of position (B).

6. Conclusion

In this paper, the driving voltage reduction of an optical switch driven by an electrostatic force using an S-shaped thin film has been discussed. To achieve this reduction, dielectric layers and suitable driving procedures were used and their usefulness was experimentally evaluated. Since the dielectric layers cause a charging problem, a driving procedure with AC voltage inputs and an improved driving procedure with DC voltage inputs were applied to the prototype core switches. The experimental results show that the driving procedure with an AC voltage input of 30 V can drive the switch. However, AC voltage inputs always cause the spot position to vibrate. In contrast, the improved driving procedure with a DC voltage of 30 V was proven to drive the switch without residual spot vibration. These results will prove useful for many devices with S-shaped thin-film elements driven by an electrostatic force.

The improved driving procedure with a DC voltage input of 25 V provides a stepwise motion, although its working range is smaller than that with a DC voltage input of 30 V. This suggests that minimizing fabrication errors can decrease the minimum driving voltage. Thus, the plan for future work is to improve the fabrication process.

This paper focused on driving voltage reduction, not on spot position error. The spot position error in Fig. 13 is considered to result from fabrication errors. Thus, improvement of the fabrication process is important for the reduction of the spot position error. In contrast to the driving procedure with AC voltage inputs, the improved driving procedure does not produce residual vibration. This has a positive effect on the position error of an optical
switch with a low fabrication error. Conventional optical switches are often required to have a spot position error smaller than 2 μm. Reducing position error is a future goal, although the allowable spot position error depends on the specifications of the receiver units.

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