Quasi-Boundary Method for Design Consideration of Resonant DC-DC Converters

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Abstract: The paper presents a new unified method for designing the two basic types of serial resonant DC-DC converters (series and with a parallel-loaded capacitor). The method is based on the use of a simplified generalized approach for the analysis of this type of power electronic devices. It is characterized by the fact that regardless of the specific operating mode, with a control frequency below or above the resonant of the series resonant circuit to determine the relationship between input and output voltage or current, it is assumed that the converter operates in resonance. In this sense, it is called a quasi-boundary method for analysis and design. The use of the quasi-boundary method significantly accelerates and facilitates the design of the considered power circuits, which is very useful for the purposes of engineering practice, as well as in the training of power electronics. The applicability of this approach is proven by the methods of mathematical modeling and computer simulations.

Keywords: modeling and simulation; resonant DC-DC converters; design consideration; resonant circuits

1. Introduction

Power electronics, and in particular power electronic converters, are one of the most dynamically developing areas of electronics. In view of the increasing penetration of green technologies and the requirements for a circular economy, it is absolutely necessary to repeatedly convert and store electricity with electronic means. In this aspect, it can be considered that power electronics are a key tool for sustainable, efficient and environmentally friendly development of society.

Resonant power converters have become very widespread in both industry and everyday life. They have a number of advantages over other types of power electronic converters such as: the ability to implement “soft” switching, good control characteristics, operation in modes close to short circuit in the load, good energy, size and economic performance, use of parasitic elements such as part of the resonant circuits, etc. [1–10].

On the other hand, power electronic converters of resonant type are characterized by a wide variety of schemes and possible control algorithms, but in the end they are divided into two main groups depending on the type of resonant circuits involved in the formation of output parameters: series (powered by a voltage source) and parallel (powered by a current source). Due to the relatively easier practical implementation of a voltage source than a current source, serial resonant circuits have found wider application in resonant converters [1,4,6,11,12].

In this regard, DC-DC converters realized on the basis of resonant inverters with and without reverse diodes and series resonant circuit are analyzed, modeled, designed and practically implemented for use in various applications in the works of a number of authors [12–18]. The known analyses of this type of power circuits are either very complex and difficult to give a clear idea with a proper physical interpretation of the nature of the ongoing electromagnetic processes, or are associated with a number of assumptions that lead to significant errors. Most often, when deriving the main dependences, the active resistance, which is reduced to the resonant circuit, is neglected [1–5]. Thus, the resonant
frequency depends only on the values of the resonant elements. On the other hand, in the analysis of a DC-DC converter with a parallel-loaded capacitor, which takes into account the losses in the circuit, as well as the different equivalent circuits as a result of switching between transistors and reverse diodes, design expressions are obtained that are applicable only by using on a computer or other computing equipment [19]. Also available for use are design methodologies developed by manufacturers of components for power electronic devices, which take into account only the basic (first) harmonic of the state variables, and are often combined with graphs from which to select the main parameters (Q factor and detuning) [20–22].

This manuscript proposes a method in which the active resistance in the resonant circuit is taken into account and the resonant current is described by its actual shape. The simplification of the calculation procedures is related to the fact that when performing the analysis it is assumed that regardless of the ratio between the resonant frequency and the switching frequency, the reverse diodes in the inverter part do not work. The reason for the expediency of this assumption is given by the fact that the energy and optimal in terms of using circuit elements modes are in cases where we are dealing with a switching frequency close to the resonant-most often at a frequency ratio in the range of 0.85 to 1.15 [1,3,6,9,15,23]. This assumption makes it possible to obtain convenient expressions through which the design of the considered power schemes is carried out much faster, corresponding to the physical nature of the processes.

The main purpose of this manuscript is based on the application of a new method for analysis of resonant DC-DC converters, to create and verify a simplified design methodology with sufficient accuracy for practical needs to design this class of power electronic devices.

2. Basic Ratios

Regardless of the configurations used in the different power circuits, due to the fact that serial resonant DC-DC converters operate at frequencies close to the resonant one, they can ultimately be reduced to two main types: purely serial and with parallel loaded capacitor. The equivalent circuits on which the determination of the main circuit parameters is based are shown in Figure 1, as in the series circuit in the output we have a voltage source $U_0$, and in the parallel loaded capacitor, a current source $I_0$ [1,5,12,16].

![Figure 1. Equivalent circuits of series resonant DC-DC converters (a) series; (b) with a parallel loaded capacitor.](image-url)

The schemes of Figure 1 contain the same elements: resonant capacitor $C_r$, resonant inductance $L_r$ and equivalent resistance $R$, which reflects the presence of active resistances in the circuit elements, and $I_{L0}$ and $U_{C0}$ are the initial conditions of the state variables, the current through the resonant inductance and voltage on the resonant capacitor. The
considered series resonant circuits are described by the following differential equation, regarding the capacitor voltage \([1,3,19]:\)

\[
L_r C_r \frac{d^2 u_c}{dt^2} + R C_r \frac{du_c}{dt} + u_c = u_{SW}
\]  
(1)

where \(u_{SW}\) is the resultant supply voltage applied to the resonant circuit. It is formed by the sequential operation of the semiconductor switches of the inverter with frequency \(f = 1/T\).

In the theory of resonant power converters, more attention is paid to purely series circuits, due to which the basic expressions for the resonant current and voltage on the resonant capacitor for circuits with and without reverse diodes, with and without output rectifier operating in modes with control frequency below and above the resonant \([1,2,5,17,23–26]\). Different parameters were used in performing these analyses. The following are used in the present work: coefficients of hesitation \(k\) and detuning \(\nu\). These parameters have been introduced and used in the scientific papers of scientists working in the Department of Power Electronics at the Technical University of Sofia, Bulgaria \([27,28]\).

The essence of the proposed quasi-limit method for analysis and design of series resonant DC-DC converters is that to determine the relationship between input and output voltage and current of the devices it is assumed that they operate at resonance (no operation of the reverse diodes in the inverter), and taking into account the fact that it works with a control frequency different from the resonant joint when calculating the elements of the resonant circuit.

Estimation of the error of the assumption for resonance operation is made on the basis of graphical constructions shown in Figure 2 (at values of the detuning coefficient \(\nu = 1.1\) and \(\nu = 0.9\)).

![Figure 2. Graphic constructions explaining the difference in the shape of the resonant current when operating with a control frequency equal to the resonant \((\omega = \omega_0)\) and a mode with a control frequency above \((\omega > \omega_0)\) and subresonant \((\omega < \omega_0)\).](image)

The main factors influencing the error are the ratio of the resonant and control frequency and the magnitude of the quality factor of the resonant circuit. The smaller the difference between the switching frequency and the resonant, the smaller the error. The influence of the quality factor is calculated by the coefficient \(k\) and at its higher value;
the active resistance in the resonant circuit decreases (the circuit approaches the ideal) and therefore increases the current flowing through the reverse diodes as the amplitude approaches the current through the transistors. Therefore, when working with large values of \( k \), more accurate results are achieved by using the proposed method of analysis and design compared to smaller ones.

The analysis is performed in an established mode, ignoring the imperfections of all circuit elements, except for their active resistances. The expressions for the voltage and current of the resonant capacitor in the series resonant circuit are as follows [7,28]:

\[
\begin{align*}
    u_C(t) &= U_d - U_0 - (U_d - U_0 + U_C(0)) e^{-\frac{t}{\tau}} \left( \frac{U_0}{\omega_C} \sin \omega_0 t + \cos \omega_0 t \right) + \frac{U_C(0)}{\omega_C} e^{-\frac{t}{\tau}} \sin \omega_0 t \\
    i_L(t) &= i_C(t) = \frac{U_0 - U_d - U_C(0)}{\omega_C \tau} e^{-\frac{t}{\tau}} \sin \omega_0 t - I_L(0) e^{-\frac{t}{\tau}} \left( \frac{U_0}{\omega_C} \sin \omega_0 t - \cos \omega_0 t \right)
\end{align*}
\]

(2)

To determine the initial values of the state variables based on the condition of periodicity of current and voltage of the resonant capacitor: \( i_L(0) = -i_L \left( \frac{\pi}{2} \right) \) and \( u_C(0) = -u_C \left( \frac{\pi}{2} \right) \).

The circular control frequency is denoted by \( \omega = 2\pi f \).

When operating with a control frequency equal to the resonant one, there is no initial value of the current through the resonant inductance \( I_L(0) = 0 \) and thus simplified expressions for the voltage and current of the resonant capacitor are obtained [7,27,28].

The quasi-boundary method for analysis and design of series resonant DC-DC converters will be demonstrated on the basis of a full-bridge transformerless converter shown in Figure 3 in a variant series (Figure 3a) and with a parallel-loaded capacitor (Figure 3b). If it is necessary to use an output transformer or other type of power circuit in the inverter part of the converter (half-bridge, push pool), this is taken into account in the analysis and design using different numerical coefficients, as shown in [1,3,6,7].

![Figure 3. Resonant DC-DC converters: (a) with a series resonant circuit; (b) with a parallel loaded capacitor.](image)

The following notations are used: \( S_1 \div S_4 \)—controllable switches (transistors) with one-way conductivity; \( DS_1 \div DS_4 \)—reverse diodes, connected in parallel to the switches and with reverse their conductivity; \( L_r \)—resonant inductance, \( C_r \)—resonant capacitor, \( D_1 \div D_4 \)—rectifier diodes; \( L_1 \)—output filter inductance; \( C_L \)—output filter capacitor; \( R_L \)—load resistance.
3. Analysis of a Series Resonant DC-DC Converter by the Quasi-Boundary Mode Method

In the analysis of a series resonant DC-DC converter, the same current flows through the resonant circuit—\( i_{Cr} = i_{Lr} \), and also the inverter current has zero initial condition. Therefore, the expressions for the resonant current and the voltage of the resonant capacitor are as follows [7,28]:

\[
\begin{align*}
    i_{Cr}(t) &= i_{Lr}(t) = \frac{U_d - U_0 + U_{Cr}(0)}{U_0 L_{cr}} e^{-\delta t} \sin \omega_0 t, \\
    u_{Cr}(t) &= U_d - U_0 - (U_d - U_0 + U_{Cr}(0)) e^{-\delta t} \left( \frac{\delta}{\omega_0} \sin \omega_0 t + \cos \omega_0 t \right)
\end{align*}
\] (3)

The initial value of its voltage is determined from the condition for periodicity of the voltage of the resonant capacitor \( U_{C}(0) \):

\[
U_{C}(0) = (2k - 1)(U_d - U_0),
\] (4)

where \( k = \frac{1}{1 - e^{-\frac{\delta \pi}{\omega_0}}} \) is a quantity analogous to the quality factor of the series resonant circuit called the coefficient of oscillation [27,28].

In order to obtain design-friendly ratios, it is necessary to present Equation (3) in a normalized form regarding the switching frequency \( \omega \). Using Equation (4) and after transformations, the expressions for the resonant current and the voltage of the capacitor are as follows:

\[
\begin{align*}
    i_{Lr}(\theta) &= \frac{2k(U_d - U_0)}{\omega_0 L_{cr}} e^{-\frac{\theta}{\lambda}} \sin \left( \frac{\pi}{\lambda} \theta \right), \\
    u_{Cr}(\theta) &= U_d - U_0 - 2k(U_d - U_0) e^{-\frac{\theta}{\lambda}} \left( \frac{\delta}{\omega_0} \sin \left( \frac{\pi}{\lambda} \theta \right) + \cos \left( \frac{\pi}{\lambda} \theta \right) \right)
\end{align*}
\] (5)

where \( \theta = \omega t \), and \( \lambda = \pi \nu \), normalized to the switching frequency conduction angle of the transistors.

The basis of any methodology for designing a DC-DC converter is to determine the relationship between the input and output voltage. This is determined taking into account the fact that when working with ideal circuit elements, active power is consumed only in the load resistance. Thus, the average value of the current in the series resonance circuit is equal to the current in the load:

\[
I_0 = \frac{U_0}{R_L} = \frac{1}{\pi} \int_0^\lambda i_{Lr}(\theta) d\theta = (2k - 1)(U_d - U_0)4fC_r,
\] (6)

Equation (6) determines this relationship:

\[
U_0 = U_d - \frac{I_0}{(2k - 1)4fC_r} = U_d - R_0 I_0,
\] (7)

where \( R_0 = \frac{1}{(2k - 1)4fC_r} \) is the output resistance of the considered converter.

A key point of the analysis and design is the determination of the maximum value of the voltage on the resonant capacitor. The dependence on the operation of the device and the expressions for \( u_{Cr,max} \) is valid [7,28]:

\[
U_{Cr,max} = U_{C}(0) = (2k - 1)(U_d - U_0) = \frac{I_0}{4fC_r},
\] (8)

It is evident from the expressions presented above that two coefficients \( k \) and \( \nu \) are used in the analysis of the converter. For the purposes of engineering design, it is necessary to know the relationship between the total active resistance (which includes all active resistances in the resonant circuit) in the series resonant circuit \( R_S \) and the output resistance...
of the converter \(R_0\). This dependence is obtained by expressing all the participating quantities in the expression for the initial resistance by means of the coefficients \(k\) and \(\nu\).

\[
R_0 = \frac{1}{(2k-1)4fC_r} = \frac{\pi}{4\nu(2k-1)} \left( \frac{\ln \left( \frac{1}{\nu} \right)}{\pi} + \frac{\ln \left( \frac{1}{\lambda} \right)}{\pi} \right) R_{\Sigma} = \frac{\pi t g \beta}{2(2k-1)} R_{\Sigma},
\]

(9)

where \(\beta\) is the detuning angle of the series resonant circuit, used in the analysis and design of resonant power inverters [28].

With the help of Equation (9) the thus determined dependence between the input and output voltage of the converter becomes a function of the active resistance in the resonant circuit. In this way, Equation (7) acquires the following form, which is very convenient for design purposes:

\[
U_0 = U_d - R_0I_0 = U_d - \frac{\pi t g \beta}{2(2k-1)} R_{\Sigma} I_0,
\]

(10)

With the help of Equation (10) already at the design stage of the device depending on the specific parameters of the used building elements and limits of change of the input voltage with sufficient accuracy for design purposes to determine the output voltage and hence, if necessary, to coordinate with its value by assignment.

All the necessary values for designing a series resonant transducer can be determined on the basis of the presented expressions. The summaries used allow them to be used for thyristor circuits as well, in which case only intermittent current mode (value of the coefficient \(\lambda\) less than 1) must be operated and thus the necessary circuit time for thyristor recovery must be provided [7,27,28].

4. Analysis of a Parallel-Loaded Resonant DC-DC Converter by the Quasi-Boundary Mode Method

In the analysis of the parallel-loaded resonant DC-DC converter, according to the equivalent scheme of Figure 1b, it should be noted that the current through the resonant inductor is formed as the sum of the current of the resonant capacitor and the output current—\(i_{L_r} = i_{C_r} + I_0\).

\[
i_{L_r}(t) = \frac{U_d + U_{C_r}(0)}{\omega_0C_r} e^{-\delta t} \sin \omega_0 t + (I_L(0) - I_0) e^{-\delta t} (\cos \omega_0 t - \frac{\delta}{\omega_0} \sin \omega_0 t) + I_0
\]

\[
u_{C_r}(t) = U_d - (U_d + U_{C_r}(0)) e^{-\delta t} (\frac{\delta}{\omega_0} \sin \omega_0 t + \cos \omega_0 t) + \frac{I_L(0) - I_0}{\omega_0 C_r} e^{-\delta t} \sin \omega_0 t - I_0 R
\]

(11)

Therefore, when operating in quasi-boundary mode, the expressions for the resonant current and the voltage of the resonant capacitor are:

\[
i_{L_r}(t) = \frac{U_d + U_{C_r}(0)}{\omega_0C_r} e^{-\delta t} \sin \omega_0 t - I_0 e^{-\delta t} (\cos \omega_0 t - \frac{\delta}{\omega_0} \sin \omega_0 t) + I_0
\]

\[
u_{C_r}(t) = U_d - (U_d + U_{C_r}(0)) e^{-\delta t} (\frac{\delta}{\omega_0} \sin \omega_0 t + \cos \omega_0 t) - \frac{I_0}{\omega_0 C_r} e^{-\delta t} \sin \omega_0 t - I_0 R
\]

(12)

From the condition for periodicity of the voltage of the resonant capacitor its initial value is determined: \(U_{C_r}(0)\):

\[
U_{C_r}(0) = (2k - 1) U_d,
\]

(13)

In order to find expressions convenient for design, it is necessary to present (12) in a normalized form with regarding to the switching frequency \(\omega\). Using (13) and after transformations, the expressions for the resonant inductance current and the capacitor voltage are as follows:

\[
i_{L_r}(\theta) = \frac{2kI_d}{\omega_0 C_r} e^{-\frac{\delta}{\omega_0} \sin \left( \frac{\pi}{\lambda} \theta \right)} - I_0 e^{-\frac{\delta}{\omega_0} \sin \left( \frac{\pi}{\lambda} \theta \right)} \left( \cos \left( \frac{\pi}{\lambda} \theta \right) - \frac{\delta}{\omega_0} \sin \left( \frac{\pi}{\lambda} \theta \right) \right) + I_0
\]

\[
u_{C_r}(\theta) = U_d - 2kI_d e^{-\frac{\delta}{\omega_0} \sin \left( \frac{\pi}{\lambda} \theta \right)} \left( \frac{\delta}{\omega_0} \sin \left( \frac{\pi}{\lambda} \theta \right) + \cos \left( \frac{\pi}{\lambda} \theta \right) \right) - \frac{I_0}{\omega_0 C_r} e^{-\frac{\delta}{\omega_0} \sin \left( \frac{\pi}{\lambda} \theta \right)} - I_0 R
\]

(14)

To create a methodology for designing a resonant DC-DC converter with a parallel-loaded capacitor, it is necessary to determine the relationship between the input and
output current of the converter. When working with ideal building blocks, active power is consumed only in the load resistance. In this case, the average value of the resonant inductance current is equal to the input current consumed by the power supply:

\[
I_d = \frac{1}{\pi} \int_0^\lambda i_L(\theta) d\theta = (2k - 1)U_d 4fC_r + 0, \tag{15}
\]

Through Equation (15) is the connection between the input and output current of the converter:

\[
I_0 = I_d - (2k - 1)4fC_r U_d = I_d - \frac{U_d}{R_0}, \tag{16}
\]

where \(R_0 = \frac{1}{(2k-1)4fC_r}\) is the output resistance of a series resonant converter with a parallel loaded capacitor.

In order to create a methodology for designing the considered type of converters, it is necessary to determine the maximum value of the voltage on the resonant capacitor. The following dependence is valid from the principle of operation of the power scheme:

\[
U_{C_{\text{rmax}}} = U_C(0) = (2k - 1)U_d = \frac{I_d - I_0}{4fC_r}, \tag{17}
\]

Since Equations (7) and (16) involve identical quantities, the expression of the output resistance for both types of converters is done with the same Equation (9).

5. Design Consideration

Usually, when designing DC-DC converters, the following parameters are set: output power, the values of the input and output voltage and of the output current (nominal and with their limits of variation). The requirements related to electromagnetic compatibility, mass and size of the devices and efficiency determine the operating frequency. Below is an example sequence for determining the most important parameters needed for the design of the considered devices.

5.1. Serial DC-DC Converter

A number of studies presented in [1–3,7,26,28] give the relationship between the converted power and the current and voltage loads of the building elements. In this way, depending on the design input, it is possible to estimate the component loads. On this basis, the loss resistances that form \(R_\Sigma\) are determined. Figure 4 shows the dependence (10) as a percentage of the value of the input voltage. The graphs are given for parameters of the values of the load current and of the coefficients \(k\) and \(R_\Sigma\). It is recommended that the presented characteristics be used for the initial determination of the output voltage value. In addition, they are useful for quickly checking whether the setpoint parameters can be achieved by directly connecting the load without using an inverter transformer. On the other hand, a value of the coefficient \(\nu\) should be chosen, by which to fix the specific operating mode (most often for a number of reasons with the frequency of super-resonance control).

The graphical constructions of Figure 4 show an interesting dependence of the output voltage: it practically does not depend on the coefficient \(k\), and its value decreases with increasing load current and the active resistance of the series resonant circuit. The operation of an inverter transformer is reflected in Equation (10), as the output voltage is reduced to the equivalent circuit by the transformation coefficient, and the resistances of its windings are added to the value of \(R_\Sigma\).

In this way, the following design sequence is obtained:

1. The value of the coefficient \(k\) is determined by Equation (8) at a given maximum value of the output current and a selected load of the resonant capacitor. Thus found,
it will be used in determining the other quantities. The corresponding value of the resonant capacitor \( C_r \) is determined from the same expression.

2. From Equation (10) the value of the load voltage is determined, using the already determined coefficients.

3. The attenuation factor and the resonant inductance are determined from the following dependence:

\[
\delta = \frac{R_\Sigma}{2L_r} = \frac{\omega_0}{\pi} \ln \left( \frac{k}{k-1} \right) = \frac{\omega}{\pi V} \ln \left( \frac{k}{k-1} \right) \tag{18}
\]

4. The maximum value of the current through the transistors is determined by the maximum value of the voltage on the resonant capacitor:

\[
I_{\text{max}} = U_{C_r \text{max}} \omega C_r \tag{19}
\]

5. Assuming a sinusoidal shape of the resonant current, the value of the output capacitor is determined by [7]:

\[
C_L = \frac{2\lambda}{\omega \Delta U_0} \left( \sqrt{1 - \left( \frac{I_0}{I_{\text{max}}} \right)^2} - \arccos \frac{I_0}{I_{\text{max}}} \right) I_0, \tag{20}
\]

where \( \Delta U_0 \) are the ripple of the output voltage. In the described way, with the presented several steps, the engineering design of the converter is realized.

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**Figure 4.** Dependence of the output on the input voltage of a series resonant DC-DC converter at different values of the parameters \( R_\Sigma \) and the load current \( I_0 \).
5.2. Resonant DC-DC Converter with a Parallel-Loaded Capacitor

The design of the resonant converter with a parallel loaded capacitor proceeds in a similar way to that of the series one. Again, the output parameters, the nominal value of the input and output voltage with the respective variation limits and the minimum and maximum value of the load current are set as input parameters, and also the operating frequency of the converter is selected or set.

Based on the assessment of the loads of the building elements, their active resistances, which participate in \( R_c \), are determined. Figure 5 shows the dependence (Equation (16)) as a percentage of the value of the input current for different values of the supply voltage (respectively the power to be converted) and when the coefficient \( k \) and \( R_c \) change. These characteristics are used to initially determine the output current and to quickly check whether the setpoint parameters can be achieved without the need for coordination.

![Dependence of the output current on the input current resonant converter with a parallel loaded capacitor](image)

**Figure 5.** Dependence of the output current on the input current resonant converter with a parallel loaded capacitor at different values of the parameters \( R_c \) and the input voltage \( U_i \).

The constructions of Figure 5 show a similar dependence of the output current as in the series circuit: it practically does not change with the change of the coefficient \( k \), but decreases significantly with increasing supply voltage and decreases the active resistance of the resonant circuit. The use of an output transformer will not affect the nature of these dependencies, and the resistances of its windings are added to the value \( R_c \).

In this regard, the design is carried out in the following main steps:
1. From Equation (17) at a set maximum load current, selected supply voltage and load of the resonant capacitor, the required value of the coefficient $k$ is determined, for the needs of the design.

2. By Equation (16) the value of the output current is obtained, at the thus selected coefficients and conversion power.

3. From the dependence (Equation (18)) the attenuation coefficient and respectively the resonant inductance are determined.

4. The value of the resonant capacitor $C_r$ is determined by:

$$C_r = \frac{1}{L_r(\omega_0^2 + \delta^2)}$$

(21)

5. The maximum value of the current through the transistors is determined by the maximum value of the voltage across the resonant capacitor and the operating frequency by means of Equation (19).

6. Assuming a sinusoidal shape of the resonant current, the value of the filter elements is determined depending on the required smoothing coefficient as in the case of main voltage rectifiers [5,6].

These few steps are the basis for determining the main quantities involved in the design of the converter in question.

### 6. Verification of the Design Methodology

The validity of the proposed methods will be verified by designing two examples of a series resonant DC-DC converter (Figure 3a) and a resonant DC-DC converter with a parallel-loaded capacitor (Figure 3b), respectively.

Table 1 shows the output data used to design a serial DC-DC converter. In addition, a comparison was made between the design data and the results obtained with the means of mathematical modeling (Matlab/Simulink) and computer simulations (LTspice).

| Initial Data | Mathematical Modeling | Computer Simulation |
|--------------|-----------------------|---------------------|
| $U_d = 110$ V | $U_d = 110$ V         | $U_d = 110$ V       |
| $U_0 = 102.597$ V | $U_0 = 102.45$ V     | $U_0 = 102.68$ V   |
| $\Delta U_0 = 100$ mV | $\Delta U_0 = 48.5$ mV | $\Delta U_0 = 46.5$ mV |
| $I_0 = 20$ A  | $I_0 = 20.386$ A     | $I_0 = 20.43$ A    |
| $U_{Cr\text{max}} = 214.687$ V | $U_{Cr\text{max}} = 219.3$ V | $U_{Cr\text{max}} = 220.95$ V |
| $I_m = 31.416$ A | $I_m = 31.06$ A      | $I_m = 30.96$ A    |

During the design, an operating frequency of 200 kHz was selected, with operation in the mode with supersonic control frequency ($\nu = 1.1$), value of the coefficient $k = 15$ and $R_C = 0.3$ $\Omega$. As a result of the use of the methodology, the following values of the circuit elements were obtained: resonant inductance $L_r = 5.979$ $\mu$H; resonant capacitor $C_r = 116.4$ nF; load resistance (at maximum current) $R_L = 5$ $\Omega$; filter capacitor $C_L = 20$ $\mu$F.

From the data presented in Table 1 it is found that by using the presented design methodology a very good accuracy has been achieved; the largest difference between the values calculated and obtained from numerical experiments is less than 2.5%. The deviations are due not only to the assumptions made in the analysis of the schemes, but also to the differences in the models used for the verification. Taking into account that in the construction of power electronic devices the building elements have a tolerance of their parameters usually around and above 20%, the proposed methodology gives excellent results without the use of complex computational procedures.

Table 2 shows the output data used to design a series DC-DC converter with a parallel-loaded capacitor. Again, a comparison was made between the design data and the results obtained with the means of mathematical modeling (Matlab/Simulink) and computer simulations (LTspice).
Table 2. Initial data for design of a resonant DC-DC converter with a parallel-loaded capacitor and results obtained by modeling and simulations.

| Initial Data | Mathematical Modeling | Computer Simulation |
|--------------|-----------------------|---------------------|
| $U_d = 30 \text{ V}$ | $U_d = 30 \text{ V}$ | $U_d = 30 \text{ V}$ |
| $I_0 = 10 \text{ A}$ | $I_0 = 10.032 \text{ A}$ | $I_0 = 10.05 \text{ A}$ |
| $\Delta U_0 = 10 \text{ mV}$ | $\Delta U_0 = 5 \text{ mV}$ | $\Delta U_0 = 6.5 \text{ mV}$ |
| $U_0 = 60 \text{ V}$ | $U_0 = 60.2 \text{ V}$ | $U_0 = 60.32 \text{ V}$ |
| $U_{Cr_{max}} = 105 \text{ V}$ | $U_{Cr_{max}} = 102.3 \text{ V}$ | $U_{Cr_{max}} = 101.9 \text{ V}$ |
| $I_m = 52.84 \text{ A}$ | $I_m = 51.05 \text{ A}$ | $I_m = 51.06 \text{ A}$ |

During the design, an operating frequency of 200 kHz was selected, with operation in a mode with supersonic control frequency ($\nu = 1.1$), value of the coefficient $k = 2.25$ and $R_\Sigma = 0.3 \Omega$. As a result of the use of the methodology, the following values of the circuit elements were obtained: resonant inductance $L_r = 1.82 \mu\text{H}$; resonant capacitor $C_r = 412.354 \text{nF}$; load resistance (at maximum current) $R_L = 6 \Omega$; filter capacitor $C_L = 20 \mu\text{F}$ and filter inductor $L_L = 100 \mu\text{H}$.

From the presented data it is established that the methodology gives a very good match and is appropriate to determine the circuit elements of the studied power electronic device.

7. Discussion

The presented results confirm the validity and efficiency of the quasi-boundary method for analysis and design of serial resonant DC-DC converters. The reading of the active resistances in the resonant circuit is useful in view of the use of elements with characteristics different from the ideal ones and especially in the case of devices with low power (below 100 W). On the other hand, this approach is also applicable to other types of resonant power converters, which operate in modes close to resonance, as in this way basic relations are obtained, which makes it very easy and convenient to determine the circuit elements. This is important in order to improve both the learning process in power electronics and in engineering practice; that is, to achieve an optimal solution to a particular problem using fast and innovative design methods. A natural development of this method is its application to resonant transducers with complex output circuits in order to obtain a matching or galvanic separation between the input and output such as LLC and LCC. The disadvantages of the proposed approach are mainly related to the increase of the error in operating modes with control frequencies with more than 20% difference from the resonant one.

8. Conclusions

The manuscript proposes a new approach to the analysis and design of serial resonant DC-DC converters, based on the use of expressions valid for operation with a control frequency equal to the resonant. The development and improvement of this method involves the combination of mathematical modeling tools, basic relationships for determining the initial values of circuit elements, based on the analysis of established processes in the circuit, and application of artificial intelligence techniques for the final finding of optimal values of building elements and for control synthesis. This is shown in [29] by using model-based optimization, which realizes, by creating and verifying a model of the converter, a reference (set) trajectory of the parameter to be optimized and the search for a minimum deviation of the parameter from the set optimal trajectory by the method of the smallest squares. Thus, the combination of classical methods for analysis and design and modern means of computational mathematics and modeling eliminates one of the biggest weaknesses in the analysis and design of power electronic devices: the derivation of basic dependencies to be performed with a fixed mode of operation, without taking into account the duration and nature of the transitional processes.

Unlike other methods for analysis and design, the quasi-boundary method takes into account the active resistances in the resonant circuit, which, when using simplified depen-
dencies, achieves a more precise determination of the circuit elements. This method does not deny, but instead complements and develops the classical methods of design, assuming their combination with modern means of applied mathematics and mathematical modeling.

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