Implementation of low-loss superinductances for quantum circuits

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The simultaneous suppression of charge fluctuations and offsets is crucial for preserving quantum coherence in devices exploiting large quantum fluctuations of the superconducting phase. This requires an environment with both extremely low DC and high RF impedance. Such an environment is provided by a superinductance, defined as a zero DC resistance inductance whose impedance exceeds the resistance quantum $R_Q = h/(2e)^2 \approx 6.5 \text{k}\Omega$ at frequencies of interest ($1 - 10 \text{GHz}$). In addition, the superinductance must have as little dissipation as possible, and possess a self-resonant frequency well above frequencies of interest. The kinetic inductance of an array of Josephson junctions is an ideal candidate to implement the superinductance provided its phase slip rate is sufficiently low. We successfully implemented such an array using large Josephson junctions ($E_J >> E_C$), and measured internal losses less than 20 ppm, self-resonant frequencies greater than 10 GHz, and phase slip rates less than 1 mHz.

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The emerging field of quantum electronics exploiting large fluctuations of the superconducting phase is limited by the practical challenge of engineering an electromagnetic environment which suppresses simultaneously the quantum fluctuations of change and the random low-frequency fluctuations of offset charges. The small value of the fine structure constant $\alpha = 1/137$ entails a fundamental asymmetry between flux and charge quantum fluctuations, strongly favoring the latter. To illustrate this, let us consider the simplest case of a dissipationless LC oscillator, where the charge $Q$ on the capacitor plates and the generalized flux $\Phi$ across the inductor are conjugate variables. The ratio between quantum fluctuations of charge, $\delta q = \delta Q/(2e)$, and flux, $\delta \phi = \delta \Phi / \Phi_0$, in the ground state of the oscillator is given by $\delta \phi / \delta q = Z_0 / R_Q$. Here $Z_0 = \sqrt{L/C}$ is the characteristic impedance of the oscillator and $R_Q = h/(2e)^2 = 6.5 \text{k}\Omega$ is the superconducting resistance quantum. Using only geometrical inductors and capacitors, the characteristic impedance of the oscillator $Z_0$ can not exceed the vacuum impedance $Z_{vac} = \sqrt{\mu_0 / \varepsilon_0}$, thus imposing quantum fluctuations of charge at least an order of magnitude larger than flux fluctuations: $\delta \phi / \delta q < Z_{vac} / R_Q = 8\alpha$.

In order to exceed the vacuum impedance and suppress quantum charge fluctuations, circuit elements with extremely high impedance are required. On chip resistors and long chains of Josephson junctions (JJJs) in the dissipative regime have already been used to provide high impedance environments [1-3]. However, these Ohmic components do not help shunting charge offsets and, being dissipative, they tend to destroy the quantum coherence of the devices. In order to simultaneously suppress quantum charge fluctuations and offsets, we need a circuit element which possesses three key attributes: high impedance at frequencies of interest, perfect conduction at DC and extremely low dissipation. These attributes define the so-called “superinductance” [4].

In this Letter, we report the successful implementation and detailed characterization of superinductances using the large kinetic inductance of arrays of Josephson junctions (see Fig. 1(a)), following the proof-of-concept shown in the fluxonium circuit [5]. This first superinductance implementation suffered from coherent quantum phases-slips (CQPS) [6-7], which constitute additional degrees of freedom, difficult to control experimentally. We show that we can completely suppress the CQPS by using large JJJs with Josephson energy $E_J \approx 100 E_C$, where $E_C = \varepsilon^2/(2C_J)$ is the charging energy of one junction.

Superconducting nanowires are also likely candidates for implementing superinductances [8-9]. Unfortunately, they show significant internal dissipation which is not yet well understood, and are more challenging to fabricate. The JJ arrays may also suffer from dissipation, either due to coupling to internal degrees of freedom [10-11] or to a dissipative external bath [12]. Indeed, transport measurements on large arrays of JJ show the appearance of a superconducting to insulating transition (SIT) with decreasing Josephson energy $E_J$ [13-15]. Previous implementations of resonators where the inductive energy is given by JJ arrays have yielded internal quality factors in the range of a few thousands [16-17]. We show here that the problem of dissipation can be alleviated by using large JJJs to realize superinductances far from the SIT region with internal quality factors an order of magnitude above the previously reported values.

The superinductances are formed by an array of closely spaced Josephson junctions on a C-plane sapphire substrate, as shown in Fig. 1. We fabricated the junctions by e-beam lithography and double angle evaporation of aluminum using the bridge-free technique of [18-19]. Prior to aluminum deposition, the substrate is cleaned of resist residues using an oxygen plasma [20]. We minimize the width of the connecting wires between junctions in order to reduce parasitic capacitances to ground, which ultimately lower the self-resonant frequency of the su-
perinductance \[21\].

![Diagram](image)

FIG. 1. (a) Schematic representation of an array of Josephson junctions. Capacitances of islands to ground and across tunnel junctions are denoted with \(C_0\) and \(C_J\), respectively. The junction inductance is given by \(L_J\). (b) SEM image of the Josephson junction array fabricated using the bridge-free technique \[18\], \[19\]. (c) Optical image of an LC resonator. The large pads implement the resonator capacitance as well as coupling capacitances to the CPW feedline. An array of Josephson junctions between the pads implements the superinductance. The ground plane is patterned with flux-trapping holes. (d) Low-frequency model for the device shown in (c). The phase slip element (split diamond) in series with the superinductance represents the collective contribution of phase slips through all junctions in the array. The characteristic energy of the phase-slip element is denoted by \(E_S\).

We characterize our superinductances at low temperatures and microwave frequencies by incorporating them in lumped element LC resonators capacitively coupled to a co-planar waveguide in the hanger geometry. The resonator response is measured in transmission. An optical image of a typical device and its low-frequency circuit model are shown in Figs. 1(c,d).

The samples were mounted on the mixing chamber stage (15 mK) of a dilution refrigerator inside a copper box, enclosed in an aluminum-Cryoperm-aluminum shield with a Cryoperm cap. We used two 4-12 GHz Pamtech isolators and a 12 GHz K&L multi-section low-pass filter before the HEMT amplifier, and installed copper powder filters on the input and output lines.

The internal quality factor of a resonator is extracted by fitting the transmission data about the resonance with the response function \[22\]

\[
S_{21}(f) = 1 - \frac{Q_{ext}^{-1} - 2i\frac{\delta f}{f_r}}{Q_{tot}^{-1} + 2i\frac{\delta f}{f_r}},
\]

where \(Q_{tot}\) and \(Q_{ext}\) are the total and external quality factors, \(f_r\) is the resonant frequency, and \(\delta f\) characterizes asymmetry in the transmission response profile. The internal quality factor is given by \(Q_{int} = \frac{Q_{ext}Q_{tot}}{Q_{ext} - Q_{tot}}\). We show a typical measured response for a resonator with an 80-junction superinductance in Fig. 2(a). This yields an internal quality factor of 37,000 for the resonator at 15 mK (stage temperature), corresponding to a loss in the superinductance of better than 27 ppm. We note that an unknown portion of the internal loss comes from the capacitors. The inset of Fig. 2(a) shows the dependence of the internal quality factor on the stage temperature. A similar resonator with two 80-junction arrays in parallel was measured to have a quality factor of 56,000 (Fig. 2(b)), corresponding to a superinductance loss of better than 18 ppm. The external quality factors, \(Q_{ext}\), of both resonators were 5,000.

We next evaluate the self-resonant modes of an \(N\)-
junc-ion array. The Lagrangian of the array is
\[ L = \sum_{n=1}^{N} \frac{1}{2} C_0 \Phi_n^2 + \frac{1}{2} C_J (\Phi_n - \Phi_{n+1})^2 - \frac{1}{2} \left( \frac{\Phi_n - \Phi_{n+1}}{L_{J0}} \right)^2, \]
where \( \Phi_n \) are the node fluxes associated with each superconducting island. We express each node flux as a superposition of discrete Fourier mode amplitudes,
\[ \Phi_n = \frac{1}{\sqrt{N}} \sum_{k=1}^{N} e^{i \frac{2 \pi k}{N} n} \Phi_k, \]
which leads to a diagonal Hamiltonian of the following form:
\[ H = \sum_{k=-N/2}^{N/2} \left( C_{kk}^{-1} Q_k Q_{-k} + L_{kk}^{-1} \Phi_k \Phi_{-k} \right). \]
Here \( Q_k \) are the canonical conjugate “charge” variables to the fluxes \( \Phi_k \), and
\[ C_{kk} = \delta_{kk'} \left[ C_0 \frac{1}{2} + C_J \left( 1 - \cos \frac{\pi k}{N} \right) \right]; \ k \in \left[ -\frac{N}{2}, \frac{N}{2} \right] \]
\[ L_{kk'} = \delta_{kk'} \frac{L_{J0}}{\left( 1 - \cos \frac{\pi k}{N} \right)}, \]
are respective capacitance and inductance matrices in the Fourier basis. This immediately leads to a dispersion relation of the form,
\[ \omega_k = (L_{kk} C_{kk})^{-1/2} = \omega_0 \sqrt{\frac{1 - \cos \frac{\pi k}{N}}{C_0 + \frac{C_J}{2} \left( 1 - \cos \frac{\pi k}{N} \right)}}, \]
where \( \omega_0 = 1/\sqrt{L_{J0} C_J} \) is the plasma frequency of a single junction. The coupling capacitance pads at each end of the array (see Fig. 2(c)) load down the eigenfrequencies (see supplementary information for calculation details).

The data presented in Fig. 2 corresponds to measurements of the \( k = 1 \) mode. The frequencies of higher modes, \( k \geq 2 \), of the array lie outside the band of our measurement setup. In order to observe these modes we exploit their cross-Kerr interaction, which is induced by the junction nonlinearity. This interaction leads to a frequency shift of the lowest mode (\( k = 1 \)) when higher modes are excited. In Fig. 3(a) we show the results of a two-tone measurement of an 80-junction resonator (same device as presented in Fig. 2(a)), where we continuously monitor the \( k = 1 \) mode while sweeping the frequency of a probe tone. When the probe tone is resonant with a higher mode of the array, we observe a drop in the \( k = 1 \) frequency.

Fig. 3(b) shows the comparison between the measured frequencies of the array modes and the theoretically predicted values. The black crosses show the renormalized mode frequencies calculated after incorporation of the corrections due to coupling capacitance pads (see supplementary information), which are in good agreement with the measured frequencies represented by colored markers.

The parameters extracted from the fit were \( C_0 = 0.04 \) fF, \( C_J = 40 \) fF and \( L_J = 1.9 \) nH, with a confidence range of 20%. The simulated value of the capacitance to ground \( C_0 = 0.09 \) fF agrees within a factor of 2 with the inferred value from the fit. Room temperature resistance measurements of the junction arrays yield a value of \( L_J = 2.1 \) nH, which agrees with the fit within 10%.

Using the fit parameters we calculate the dispersion relation for the bare superinductance, shown as a gray curve in Fig. 3(b). We note that the lowest resonant frequency of the bare superinductance is 14.2 GHz, which meets the design criterion of having self resonances well above the frequency range of interest (1 - 10 GHz).

In order to characterize the phase slip rate of the superinductances, we monitor the frequency of the \( k = 1 \) mode of a resonator formed by two superinductances in parallel, while sweeping an external magnetic field. As flux bias is increased, the persistent current induced in the loop increases, and results in a drop in frequency of the mode (Fig. 4(a)). This behavior can be described to lowest order in junction nonlinearity by the quasi-
FIG. 4. (a) Lowest mode frequency versus applied flux bias for resonator with 160-junction array loop. Flux bias is swept up (red) and down (blue) over the course of several hours. Gray curves represent quasi-classical predictions for resonator frequencies at different integer values of flux quanta in the array loop. The only adjustable parameter is the number of junctions, found to be $150 \pm 10$. (b) Same measurement as in (a) over a smaller flux range, but before each measurement a high power pulse at one of the plasma modes is applied in order to reset the resonator to the lowest flux state.

The classical expression

$$f(\Phi_{ext}) = \frac{f_R}{\sqrt{1 + \frac{1}{2} (\frac{2\pi}{N} (\frac{\Phi_{ext}}{\Phi_0} - m))^2}}, \quad (8)$$

where $\Phi_{ext}$ is the applied flux bias, and $m$ is the integer number of flux quanta inside the loop. A phase slip event is associated with an integer change in $m$, which we observe as a jump in resonator frequency. As we increase the flux bias, the phase slip rate is enhanced and frequency jumps become more probable [24]. We swept the flux bias applied to the loop over several flux quanta before a phase slip event occurred. The typical duration between phase slips recorded in this experiment was over an hour, Fig. 4(a). This is a remarkably low phase slip rate of well under 1 mHz for a loop of 160 junctions, significantly lower than previously reported values on shorter arrays [6, 25].

Due to the extremely low phase slip rate, in order to measure the resonator in the lowest flux state, we employ an active resetting scheme. We apply a high power pulse at one of the $k \geq 2$ modes before measuring the location of the lowest resonant frequency. This high power pulse activates phase slips, allowing the loop to settle into the lowest flux state. Using this protocol, we tracked the resonant frequency in the lowest flux state, as shown in Fig. 4(b). Discrete inverted parabolas are observed, which allow us to unambiguously calibrate the number of flux quanta in the loop. The fitted value for the total number of junctions differs by 6% from the actual number, which can be explained by the classical nature of the theory which does not take quantum fluctuations into account.

In conclusion, we have demonstrated the successful implementation of superinductances using arrays of Josephson junctions, and performed the first detailed characterization of a superinductance. We measured superinductances in the range of 100–300 nH, self resonant frequencies above 10 GHz, internal losses less than 20 ppm, and phase slip rates below 1 mHz. The long lifetimes of persistent current states in the array loop also demonstrate the low DC resistance of the arrays. With these parameters, the Josephson junction array superinductance enriches significantly the quantum electronics toolbox. Applications would include further suppression of offset charges in superconducting qubits [2], high Q and tunable lumped-element resonators, on-chip bias tees, kinetic inductance particle detectors [26], and precise measurements of Bloch oscillations [27].

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SUPPLEMENTAL MATERIAL FOR IMPLEMENTATION OF SUPERINDUCTANCES FOR QUANTUM CIRCUITS

SIMULATION OF PARASITIC CAPACITANCE IN JUNCTION ARRAYS

The parasitic capacitance to ground $C_0$ of the islands in a Josephson junction (JJ) array lowers the self-resonant frequency of the superinductance. We optimize the array geometry in Ansoft Maxwell electromagnetic field simulation software in order to minimize the parasitic capacitance.

Our model for the array includes the connection pads and substrate (Fig. S1(a)). We model the junction oxide layer by a 1 nm thick dielectric material with relative permittivity of 6. Despite only simulating 5 junctions in the array, we can robustly obtain the parasitic capacitance for the full structure. Increasing the number of junctions in the model leads to no significant change in the computed value of the parasitic capacitance.

Numerical simulations revealed that junctions with large, 1:30, aspect ratios minimize the parasitic capacitance (Fig. S1(b)). Fabricating junctions with such large aspect ratios using the Dolan bridge technique is limited by the collapse of the bridge. The bridge-free technique (BFT) [18, 19] allows the fabrication of arbitrary size and aspect ratio junctions. The BFT also minimizes the width of the connecting wires between junctions. Our simulations show that this leads to a reduction of 60% in parasitic capacitance as compared with the standard Dolan bridge technique.

The parasitic capacitance $C_0$ of each island limits the maximum number of junctions in the array to

$$N_{\text{max}} \simeq \pi \sqrt{\frac{C_J}{C_0}}, \quad \text{(S1)}$$

a value such that the first self-resonant mode of the array lies $\sim 30\%$ below the plasma frequency of a single junction [20]. In Fig. S1(b) we present a plot of the simulated $C_0$ and $N_{\text{max}}$ as a function of JJ size for large aspect ratio junctions. For our device we obtain $N_{\text{max}} = 70$.

![Fig. S1](image)

**Fig. S1.** (a) Simplified BFT junction array model. The orange color represents the thin layer of dielectric (1 nm thick AlO$_x$ oxide), and purple represents the perfect conductor (superconducting aluminum). (b) Simulated parasitic capacitance to ground and inferred $N_{\text{max}}$ (Eq. (S1)) for the design in (a). The junction area is varied by sweeping its length. The dashed vertical gray line indicates the parameters for the sample presented in the main text. The lines joining points are a guide for the eye.

CALCULATING THE SCATTERING MATRIX FOR THE HANGER GEOMETRY

In this section we describe the calculation of scattering matrix of an array resonator coupled to a CPW in a hanger geometry. The circuit model is shown in Fig. S2(a), which corresponds to the device shown in Fig. S1(c). Impedance $Z_1$ represents the coupling between the CPW feedline and the nearest pad. Impedance $Z_4$ represents the cross capacitance between the feedline and the opposite pad, while $Z_3$ and $Z_5$ represent coupling of the pads to ground. Impedance $Z_2$ represents the JJ array.

The circuit model in Fig. S2(a) can be simplified by applying the standard Y-$\Delta$ transform, as shown in Fig. S2(c).
FIG. S2. (a) Model of resonator coupled to a CPW in the hanger geometry. The schematic in (b) elucidates the Wheatstone bridge topology of (a). The resultant circuit after a Y-Δ transform of the circuit in (b) is shown in (c).

The resulting impedances $Z'_1, Z'_2, Z'_5$ are given by:

$$Z'_1 = \frac{Z_2 Z_5 + Z_5 + Z_1}{Z_1}$$  \hspace{1cm} (S2)  

$$Z'_2 = \frac{Z_2 Z_5 + Z_5 + Z_1}{Z_2}$$  \hspace{1cm} (S3)  

$$Z'_5 = \frac{Z_2 Z_5 + Z_5 + Z_1}{Z_5}.$$  \hspace{1cm} (S4)  

The total impedance of the structure is given by

$$Z_{\text{total}} = \frac{Z_1 Z_2 Z_3 + Z_1 (Z_2 + Z_3) Z_4 + ((Z_1 + Z_2)Z_3 + (Z_1 + Z_2 + Z_4)Z_4)Z_5}{Z_4 (Z_3 + Z_5) + Z_1 (Z_2 + Z_3 + Z_5) + Z_2 (Z_3 + Z_4 + Z_5)}.$$  \hspace{1cm} (S5)  

We note that the frequency response of $Z_{\text{total}}$ near resonance can be approximated using a simplified model as that shown in Fig. 1(d) of the main text. Given the total impedance $Z_{\text{total}}$ which shunts the CPW feedline, the scattering matrix can be directly computed using standard techniques [30].

DETAILS ON FABRICATION PROCEDURES

The JJ arrays are fabricated by e-beam lithography at 100 kV using a Vistec 5000+ electron beam pattern generator. The use of a high energy electron beam minimizes forward scattering in the PMMA/MMA resist bilayer and allows implementation of a bridge-free double angle evaporation technique [18]. We develop using a mixture 1:3 of IPA:water at 6 °C. Prior to aluminum deposition, the substrate is cleaned of resist residues using a high pressure, low power oxygen plasma [20]. The aluminum films are deposited in a Plassys UMS300 UHV multichamber e-beam evaporation system with a base pressure of $10^{-9}$ Torr. The static oxidation in between the aluminum layer depositions is performed in a separate chamber, in a mixture 1:3 of O$_2$:Ar at 100 Torr for 10 minutes. We obtain critical current densities of the order of tens of amperes per square centimeter. The junctions age by $\lesssim 10\%$ during the first week after fabrication.

MODIFICATION OF ARRAY MODE FREQUENCIES BY COUPLING PAD CAPACITANCES

The end capacitance pads lower the eigenmodes of the JJ array. To evaluate the modified mode frequencies of the $N$-junction array $\omega_k; k \in [-N/2, N/2]$, we model each $k$ mode as a transmission line of characteristic impedance,

$$Z_k = \frac{1}{2} \sqrt{\frac{L_k}{C_k}} = R_q \sqrt{\frac{2E_C/E_J}{(1 - \cos \frac{\pi k}{N}) (1 - \cos \frac{\pi k}{N} + \frac{C_0}{2C_J})}},$$  \hspace{1cm} (S6)
FIG. S3. Transmission line model for \( k \) modes of an \( N \)-junction array shunted by pad capacitances \( C_S \).

terminated by a shunt capacitance \( C_s \) at both ends. Here \( R_q = \hbar/(2e^2) = 1.02 \, \text{k} \Omega \) represents the reduced superconducting resistance quantum (Fig. S3).

![Diagram](image)

FIG. S4. Determination of \( f_{k=1} \) and \( f_{k=2} \) modified by pad capacitances, using Eqs. (S8) and (S9) respectively. The fitting parameters were \( C_0/C_J = 0.001 \), \( C_s = 0.7 \, \text{fF} \) and \( L_J = 1.9 \, \text{nH} \).

In order to calculate a mode frequency of the loaded transmission line, we exploit the symmetry of the problem by considering a half-section of the line. The input impedance of each section can be written as

\[
Z_{\text{in}} = Z_k \frac{Z_L + jZ_k \tan(\beta_k N/2)}{Z_L + jZ_k \tan(\beta_k N/2)},
\]

(S7)

where \( Z_L = 1/j\omega_k^c C_s \) and \( \beta_k \) denotes the wave number associated with the \( k^{\text{th}} \) mode. At resonance frequency \( \omega_k^c \), the impedances of left and right sections of the transmission line satisfy the identity \( Z_{\text{in}}^{\text{left}} = [Z_{\text{in}}^{\text{right}}]^* \). Further, due to symmetry, the input impedances of the left and right sections of the transmission line are equal, \( Z_{\text{in}}^{\text{left}} = Z_{\text{in}}^{\text{right}} \). These two conditions imply \( Z_{\text{in}} = 0 \), which leads to a relationship,

\[
\frac{1}{\omega_k^c C_s Z_k} = \tan \left( \frac{\omega_k^c N}{\nu_k^0} \right).
\]

(S8)

Here \( \nu_k^0 = N\omega_k^0/(k\pi) \) denotes the phase velocity for each mode evaluated using the unloaded array calculation. Equation (S8) is a transcendental equation in \( \omega_k^c \), which can be solved numerically for each of the \( k \) modes to obtain the corrected mode frequency. The above calculation yields the corrected mode frequencies \( \omega_k^c \) only for the odd modes of the array. Similar treatment using the input admittance \( Y_{\text{in}} \) yields the corrected frequencies for the even modes, as solutions of the following transcendental equation

\[
-\omega_k^c C_s Z_k = \tan \left( \frac{\omega_k^c N}{\nu_k^0} \right).
\]

(S9)

Fig. S4 shows example calculations for the first two mode frequencies using the method described above.
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