Impedance Modeling and Stability Analysis for Cascade System of Three-Phase PWM Rectifier and LLC Resonant Converter

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Abstract: In this paper, the impedance model of PWM rectifier and LLC resonant converter are deduced, and the stability analysis of cascade system is studied. The principle of three-phase PWM rectifier is introduced, and the small signal model in d-q coordinate system is deduced. The expression of dc side output impedance model for PWM rectifier is derived. The LLC resonant converter is operated in a fixed-frequency state, and the LLC resonant converter is modeled as a small signal model. On this basis, the input impedance model expression of the LLC resonant converter is derived. According to the impedance stability criterion, it can be seen that the amplitude of input impedance is greater than the amplitude of output impedance in a certain frequency domain. In addition, the Nyquist curve is not around the point (−1,0), which can judge that the cascade system is stable. In simulation software, a cascade system simulation is built and corresponding simulation curves are obtained, which verifies the stability of the cascade system.

Keywords: small signal modeling; impedance model; bird chart; Nyquist plot; impedance stability criterion

1. Introduction

The consumption of primary energy produces great pollution affecting the environment and the reserves are almost exhausted, which have a significant impact on the sustainable development of human beings [1,2]. Therefore, the development and utilization of renewable energy has been increasing. Large-scale distributed renewable energy access to the power grid requires a large number of power electronic devices [3]. Therefore, the interaction stability of power electronic devices and the power grid is extremely important, whose stability is of great significance for research.

At present, the stability analysis methods for power electronic converter cascade systems are mainly divided into time domain analysis and frequency domain analysis [4–6]. The time domain analysis of state space model is based on the small signal state space model of cascade system in time domain. The stability of the cascaded system can be judged by analyzing its the root locus [7]. However, this time domain analysis method requires knowing the details of all circuit topology and control strategies of the whole system and establishing the time-domain state space model. The modeling process is very complex [8–10]. In order to adapt to a complex cascade system structure, a frequency domain analysis method based on the impedance model can be used. The main impedance modeling method is based on converter topology and control circuit diagram to establish the closed-loop transfer function of the whole system [11]. Firstly, the stability of the system is analyzed by the Bode figure. Secondly, according to the need of the impedance ratio criterion, a before and after the converter
output and input impedance model is set up. Finally, for the impedance stability criterion, the stability of the cascade system is analyzed. This method was first proposed by Middlebrook when he studied the design of input filters of DC systems [12,13]. When a cascade system uses impedance analysis, firstly, each subsystem in the cascade system is simplified to set up the equivalent impedance model by using Norton’s and Thevenin’s theorems. Secondly, cascade subsystem’s impedance ratio judging the stability of the system impedance is obtained by the relationship between the equivalent impedance. The system can be analyzed in combination with the Bode diagram [14,15]. Finally, the cascade system stability was studied by Nyquist stability criterion.

This paper puts forward a three-phase balanced voltage PWM rectifier and LLC resonant converter cascade system of power electronic converter. PWM rectifier is widely used because it can provide stable voltage to dc load and also provide unit power factor and sinusoidal current control on the ac grid side [16]. LLC resonant converter can achieve zero-voltage-switching (ZVS) in certain control strategies, and it can be a high efficiency of energy transfer [17,18]. The high frequency transformer in the middle can provide isolation effect, so that the fault of the load will not have a great impact on the power side, and it can also present the characteristics of a dc transformer. When we analyze the stability of the cascade system, the impedance stability criterion method is adopted in this paper [19]. That is, the PWM rectifier output impedance model and the LLC resonant converter input impedance model are deduced, and the stability of the system is analyzed by combining the impedance Bode diagram and Nyquist plot [20]. The cascade system simulation is set up in the simulation software. The correctness of the impedance stability criterion theory and the stability of the cascade system are judged by the simulation experiment.

2. The Stability Criterion of Three-Phase Balanced Voltage PWM Rectifier and LLC Resonant Converter Cascade System

The cascade system structure studied in this paper is shown in Figure 1. In order to study the stability of three-phase balanced voltage PWM rectifier and LLC resonant converter’s cascade system, we use the impedance analysis method. First, we simplify the subsystems in the cascade system by using the Thevenin’s theorem to establish the equivalent impedance model. Therefore, Figure 1 can be equivalent to Figure 2. This is the method that we need to find the PWM rectifier output impedance and the LLC resonant converter input impedance. The former converter (PWM rectifier) and its front-end input system are defined as power supply, while the latter converter (LLC resonant converter) and its back-end output system are defined as load.

![Figure 1. PWM rectifier and LLC resonant converter cascade system structure diagram.](image-url)
The sufficient condition of system stability is simplified: \(|Z_o(s)| < |Z_i(s)|\) is valid in the entire frequency domain.

For a cascade system, we need to derive the output impedance model expression of the PWM rectifier and the input impedance model expression of the LLC resonant converter.

3. Output Impedance Modeling of Three-Phase Balanced PWM Rectifier

Figure 3 shows the circuit topology of a three-phase balanced voltage PWM rectifier with pure resistance. In the Figure 3, PWM circuit of three-phase full bridge consists of 6 switches: \(S_{ah}, S_{bh}, S_{ch}, S_{al}, S_{bl}, S_{cl}\), respectively; \(L\) is the filter inductance of the converter; The current of the ideal grid flows into the converter through the inductance. The currents are \(i_a, i_b, i_c\) respectively; The ideal grid voltages of three-phase balance are \(u_{fa}, u_{fb}, u_{fc}\) respectively; \(u_a, u_b, u_c\) are the three-phase voltage that are modulated by the converter; \(u_{dc}, i_{dc}\) are dc side voltage and dc side current respectively; \(C_{dc}\) is dc side capacitance; \(R\) is the pure resistance load at dc side.

From Kirchhoff’s laws of current and voltage:

\[
i_{dc} = C_{dc} \frac{du_{dc}}{dt} + \frac{u_{dc}}{R}
\]
\[
\begin{bmatrix}
  u_{ga} \\
  u_{gb} \\
  u_{gc}
\end{bmatrix} = \begin{bmatrix}
  L_{d/2} \dot{d} \theta \\
  L_{b/2} \dot{b} \theta \\
  L_{c/2} \dot{c} \theta
\end{bmatrix} + \begin{bmatrix}
  u_a + u_{no} \\
  u_b + u_{no} \\
  u_c + u_{no}
\end{bmatrix}
\]

\(u_{no}\) is the voltage of the point ‘n’ to the point ‘o’.

For PWM rectifier, the switching function is established by using average method, with the system structure diagram, the three-phase ac mathematical model is transformed into two-phase dc mathematical model by d-q transformation.

\[\frac{d}{dt} \begin{bmatrix}
  i_d \\
  i_q
\end{bmatrix} = \frac{1}{L} \begin{bmatrix}
  u_{gd} \\
  u_{gq}
\end{bmatrix} - \frac{1}{L} \begin{bmatrix}
  D_d \\
  D_q
\end{bmatrix} \begin{bmatrix}
  \dot{d} \theta \\
  \dot{q} \theta
\end{bmatrix} \begin{bmatrix}
  i_d \\
  i_q
\end{bmatrix} - \omega \begin{bmatrix}
  0 \\
  0
\end{bmatrix} \begin{bmatrix}
  i_d \\
  i_q
\end{bmatrix}
\]

Similarly, the formula can be given:

\[\frac{du_{dc}}{dt} = \frac{3}{2C} \begin{bmatrix}
  D_d \\
  D_q
\end{bmatrix} ^T \begin{bmatrix}
  i_d \\
  i_q
\end{bmatrix} - \frac{u_{dc}}{RC}
\]

But the system is a non-linear time-invariant system. On the basis of the average model, a small signal model is established. After repeated terms and higher-order terms is simplified, the formulas can be obtained:

\[\frac{d}{dt} \begin{bmatrix}
  \dot{i}_d \\
  \dot{i}_q
\end{bmatrix} = \frac{1}{L} \begin{bmatrix}
  \dot{a}_{gd} \\
  \dot{a}_{gq}
\end{bmatrix} - \frac{1}{L} \begin{bmatrix}
  D_d \\
  D_q
\end{bmatrix} \dot{a}_{dc} - \frac{1}{L} \begin{bmatrix}
  \dot{d} \theta \\
  \dot{q} \theta
\end{bmatrix} \begin{bmatrix}
  i_d \\
  i_q
\end{bmatrix} - \omega \begin{bmatrix}
  0 \\
  0
\end{bmatrix} \begin{bmatrix}
  i_d \\
  i_q
\end{bmatrix}
\]

\[\frac{d\dot{a}_{dc}}{dt} = \frac{3}{2C} \begin{bmatrix}
  D_d \\
  D_q
\end{bmatrix} ^T \begin{bmatrix}
  \dot{i}_d \\
  \dot{i}_q
\end{bmatrix} + \frac{3}{2C} \begin{bmatrix}
  \dot{d} \theta \\
  \dot{q} \theta
\end{bmatrix} \begin{bmatrix}
  i_d \\
  i_q
\end{bmatrix} - \frac{\dot{a}_{dc}}{RC}
\]

The corresponding structure diagram is shown in Figure 4:

![Figure 4. PWM rectifier small signal model equivalent circuit diagram.](image)

Although the linear time-invariant system is obtained, there is still coupling between d axis and q axis. The coupling between d axis and q axis is eliminated by using the feedforward decoupling control method. The small signal control model is obtained by using the control strategy of current inner loop and voltage outer loop. The control block diagram is shown in Figure 5.
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After feed-forward decoupling control cancels out the coupling terms of the controlled object, the d axis and q axis do not affect each other. Therefore, dc voltage can be expressed by d axis. Then, PWM rectifier d axis small signal model is obtained, as is shown in Figure 6.

The Equation (7) can be obtained from Figure 6.

\[
\dot{I}_i = \left( - \frac{G_u(s)G_i(s)1.5D_d}{sL+r} + \frac{1.5I_dG_i(s)G_u(s)}{(sL+r)+G_i(s)[u_{dc}]} - \frac{1.5I_dD_d}{u_{dc}} \right) \dot{u}_{dc} - sCdc\dot{u}_{dc} \tag{8}
\]

Because the three-phase power supply is the voltage in the ideal grid, \( \dot{u}_{gd} = 0 \), \( \dot{u}_{dc} = 0 \).

As a result, the formula can be obtained:

\[
Z_o(s) = \frac{Ls + r + G_i(s)}{(sC + 1.5I_dD_d/u_{dc})(Ls + r + G_i(s)) + [1.5D_d - 1.5I_d(sL+r)/u_{dc}][G_i(s)G_u(s)]} \tag{9}
\]

When the system works in the unit power factor, \( u_{gd} = 0, i_q = 0, u_{gd}, u_{dc}, R, C, L \) are determined by the system parameters. Other steady state operating points can be obtained by the formula:

\[
I_d = \frac{2u_{dc}}{3u_{gd}R} \tag{10}
\]

\[
D_d = \frac{u_{dc}}{u_{gd}} \tag{11}
\]

The following formula can be obtained by replacing the intermediate variable \( I_d \) and \( D_d \) obtained in Equations (10) and (11), with the known quantity such as \( u_{gd} \).

\[
Z_o(s) = \frac{Ls + r + G_i(s)}{(sC + 1/R)(Ls + r + G_i(s)) + [1.5u_{gd} - 1.5I_d(sL+r)]G_i(s)G_u(s)/u_{dc}} \tag{12}
\]
4. Modeling of Input Impedance of LLC Resonant Converter

Figure 7 gives the topology diagram of the full-bridge LLC series resonant converter, in which \( u_{in} \) is the output voltage of the former three-phase balanced voltage-type PWM converter and the input voltage of the full-bridge LLC resonant converter. \( Q_1-Q_4 \) are switches, \( D_1-D_4 \) are inverse parallel diodes of \( Q_1-Q_4 \); \( C_1-C_4 \) are parasitic capacitors of \( Q_1-Q_4 \); \( L_r \) is series resonant inductance, including leakage inductance of transformer, \( C_r \) is series resonant capacitor, which has the function of isolating DC current; \( L_m \) is parallel resonant inductance, which can be realized by transformer excitation inductance, \( T_r \) is transformer, the turn ratio of primary and secondary edges is \( n:1 \); \( D_{R1}-D_{R4} \) makes up the rectifier circuit for the secondary side, \( C_o \) is the Output filter capacity and \( R_{Ld} \) is the pure load resistor. Two pairs of diagonal switches \( Q_1 \) and \( Q_2 \), \( Q_2 \) and \( Q_3 \) are turned on and off at the same time, and the upper and lower switch tubes of the same bridge arm are 180° conducting to each other. Because the specification of four switches are same, therefore \( C_1 = C_2 = C_3 = C_4 = C_o \).

![Figure 7. Circuit topology diagram of full-bridge LLC resonant converter.](image)

LLC resonant converter adopted open-loop control mode, and the operating frequency of the controlled switch is slightly lower than the resonant frequency, that is \( f_s < f_r \). The fundamental vector method is used to establish the system average model, that is to say, the energy of the system is transferred by the fundamental wave of the voltage when the switches turn on, and the average method is used to model the switches. For convenience, as shown in Figure 8.

![Figure 8. Circuit topology of LLC resonant converter.](image)

The fundamental wave vector of \( u_{AB} \) and \( i_{Lr} \) are \( \dot{u}_{AB} \) and \( \dot{i}_{Lr} \) the expressions of them are as follows:

\[
\dot{u}_{AB} = \frac{4}{\pi} u_{in} \angle 0^\circ \tag{13}
\]

\[
\dot{i}_{Lr} = I_{\text{rms}} \angle \phi_{Lr} \tag{14}
\]

\[
I_{\text{in, LLC}} = \frac{2}{\pi} I_{\text{rms}} \cos \phi_{Lr} = \frac{2}{\pi} \text{Re} \left( \dot{i}_{Lr} \right) \tag{15}
\]

\[
\dot{i}_t = I_{\text{rms}} \angle \phi_t \tag{16}
\]
\[
\dot{u}_R = \frac{4}{\pi} nu_0 \begin{vmatrix} \dot{i}_i \\ \dot{i}_t \end{vmatrix}
\] (17)

On the basis of the average model, the small signal model is established. The steady state component is eliminated and the second order AC term is ignored.

\[
\hat{u}_{AB} = \frac{4}{\pi} \hat{u}_{in} \angle 0^\circ
\] (18)

\[
\hat{i}_{\text{in, LLC}} = \frac{2}{\pi} \hat{i}_{Lr}
\] (19)

\[
\hat{u}_R = R_r \hat{i}_{Lr} + r \hat{i}_{Lr} + 2k_r \hat{i}_o + j(R_i \hat{i}_{Lr} + r_i \hat{i}_{Lr} + 2k_i \hat{i}_o)
\] (20)

\[
\hat{i}_R = k_r \hat{i}_{Lr} + k_i \hat{i}_{Lr}
\] (21)

where:

\[
\hat{i}_{Lr} = \text{Re}(i_{Lr})
\] (22)

\[
\hat{i}_i = \text{Im}(i_t)
\] (23)

\[
R_q = \frac{R_e \omega_s L_m^2}{R_e^2 + \omega_s^2 L_m^2}
\] (25)

\[
X = \frac{R_e^2 \omega_s L_m + \omega_s L_r}{R_e^2 + \omega_s^2 L_m^2} - \frac{1}{\omega_s C_r}
\] (26)

\[
R_r = \frac{R_e (R_q R_e - \omega_s L_m X)}{(R_e^2 + \omega_s^2 L_m^2) (R_q^2 + X^2)}
\] (27)

\[
R_i = \frac{R_e (\omega_s L_m R_e - R_q X)}{(R_e^2 + \omega_s^2 L_m^2) (R_q^2 + X^2)}
\] (28)

\[
r = \frac{R_e (\omega_s L_m R_q + R_e X)}{(R_e^2 + \omega_s^2 L_m^2) (R_q^2 + X^2)}
\] (29)

The small signal model of resonant inductance is as follows:

\[
L_r \frac{d\hat{i}_{Lr}}{dt} + j\omega_s L_r \hat{i}_{Lr} + jL_r \hat{i}_{Lr} \omega_s = \hat{u}_{Lr}
\] (32)

where: \( \hat{i}_{Lr} = \hat{i}_{Lr-} + j\hat{i}_{Lr-}, \hat{u}_{Lr} = \hat{u}_{Lr-} + j\hat{u}_{Lr-} \).

Similarly, the small signal model of excitation inductance \( L_m \) can be obtained as follows:

\[
L_m \frac{d\hat{i}_{Lm}}{dt} + j\omega_s L_m \hat{i}_{Lm} + jL_m \hat{i}_{Lm} \omega_s = \hat{u}_{Lm}
\] (33)
In Figure 9, it can be seen that in the small signal number model of LLC resonant transformation, the inductance of the resonant slot is composed of three parts: The inductance, the switching frequency impedance and the controlled voltage source. Among them, the impedance of the inductance forms low frequency disturbance, the impedance of the switch frequency is the impedance of the inductance generated by the switching frequency, and the controlled voltage source is generated by the perturbation of the switching frequency.

\[
\hat{L}_m + j\omega L_m + jL_m \hat{I}_{in} \hat{\omega}_s = \hat{i}_{cr}
\]

**Figure 9.** Small signal model of excitation inductance.

This is similar to the small signal model of the resonant inductance. The small signal model of resonant capacitor is as follows:

\[
C_r \frac{du_{cr}}{dt} + j\omega C_r u_{cr} + jC_r \hat{U}_{cr} \omega_s = \hat{i}_{cr}
\]  

(34)

At this point, the LLC resonant converter circuit can be expanded into the model shown in Figure 10.

**Figure 10.** LLC resonant converter small signal model in real field.

For the resonant capacitor \( C_r \), through the theoretical derivation, the resonant capacitor \( C_r \) can be equivalent to an inductance when it is resonant, so that the seventh order system can be reduced to the third order system. The equivalent simplified LLC resonant converter model is shown in Figure 11. The output impedance model of LLC resonant converter can be deduced.

And we will get the specific factors impacted in the input impedance model. The results are as follows.

\[
Z_{in, LLC}(s) = \frac{\pi^2 L_c^2 R_{ld} C_o s^3 + \pi^2 L_c (L_c + R_e R_{ld} C_o) s^2 + \pi^2 (2L_c R_e + R_e^2 R_{ld} C_o) s + \pi^2 (R_e^2 + R_e^2)}{8 L_c R_{ld} C_o s^2 + 8 (L_c + R_e R_{ld} C_o) s + 8 R_e}
\]  

(35)

where:

\[
R_e = \frac{8}{\pi^2} R_{ld}, \quad R_s = \omega_s L_t - \frac{1}{\omega_s C_t}, \quad L_c = L_t + \frac{1}{\omega_s^2 C_t}
\]
5. Simulation Verification and Stability Analysis of Cascade System

5.1. Simulation Verification and Stability Analysis of PWM Rectifier

The output impedance characteristics of three-phase balanced voltage-source PWM rectifier can be obtained by taking the Table 1 parameters into (12), as shown in Figure 12.

![Figure 11. LLC resonant converter third order small signal model.](image)

**Table 1. Simulation parameters.**

| Parameters                               | Value   |
|------------------------------------------|---------|
| AC line voltage                          | 100 V   |
| DC voltage                               | 200 V   |
| AC inductance                            | 1.8 mH  |
| DC capacitance                           | 2200 uF |
| Resistance of load                       | 36 Ω    |
| Voltage outer loop parameters (proportion)| 0.75    |
| Voltage outer loop control parameters (integral)| 50   |
| Current inner loop control parameters (proportion)| 10  |
| Current inner loop control parameters (integral)| 400 |

![Figure 12. The bode diagram of PWM rectifier DC side’s output impedance model.](image)

The output impedance frequency characteristic amplitude of PWM rectifier is greater than 0° in the middle frequency band, the phase angle is about 0° in low frequency band, at this point, the output...
impedance model takes the form of pure resistance in high frequency band, the phase angle is about $-90^\circ$ in high frequency band, at this point, the output impedance model takes the form of pure capacitance. In the whole frequency domain, the output impedance phase angle margin is greater than $-180^\circ$. It can be seen that the DC side impedance model of the three-phase balanced voltage PWM integrator is stable.

A closed-loop control simulation model of three-phase balanced voltage source PWM rectifier is built in Matlab/Simulink. The simulation parameters are shown in Table 1.

The simulation conditions are as follows: The simulation time is 0.3 s, the initial DC voltage is 160 V, and Q axis current instruction is 0. There is no load in the DC side at start, the DC side is connected with 36 $\Omega$ resistors at 0.15 s and 0.25 s, and the simulation results are shown in Figure 13.

![Simulation diagram of three-phase balanced PWM rectifier](image)

**Figure 13.** Simulation diagram of three-phase balanced PWM rectifier. (a) Steady waveform of Three-phase voltage; (b) Steady waveform of Three-phase current; (c) DC dynamic capacitance voltage waveform.

Figure 13 shows that: (a) is three-phase voltage waveform when DC load resistance is added, and (b) is the three-phase current waveform when DC load resistance is added and the phases are the same, which satisfies the control of AC side unit power factor. At 0.15 s, the three-phase balance current increased by 10 A due to a sudden increase in load, and soon stabilized near 20 A. At the time of 0.25 s, the current increased by 10 amperes due to the sudden increase of load, and the current was stabilized around 30 A quickly, indicating that the system could have the ability of self-regulation and stability, and that the control structure and parameters of the system are correct. (c) is dynamic DC voltage waveform during the whole simulation period. From the graph, it can be seen that DC voltage has a sudden drop of about 10 V when load is added, and the system will soon be restored to its original state, which indicates that the control structure and parameters are reasonable, and the system is stable.
5.2. Simulation Verification and Stability Analysis of LLC Resonant Converter

Figure 14 is the input impedance of LLC resonant converter derived from the parameters given in Table 2.

![Bode Diagram of LLC Resonant Converter](image)

Figure 14. The bode diagram of LLC resonant converter’s input impedance model.

Figure 14 shows that most of the bode chart values of the LLC resonant converter are above 0 and the phase angle is above $-90^\circ$ in the middle frequency band, and the amplitude curve of frequency 1 k–10 k is obtained. Overall, the input impedance is in a stable state, which will be ready for the subsequent analysis of the stability of the system.

The circuit parameters are listed in Table 2, which is used to build the simulation model in simulation software.

Figure 15 shows the output voltage waveform of LLC resonant converter at DC side, and the control strategy is open-loop control. It can be seen from the diagram: The output voltage of the system has fluctuation before 0.02 s, the output curve of dc side is a straight line and the system tends to stable at 0.03 s. This shows that the whole system is stable and the system parameters are correct, so it is ready to analyze the stability of the cascade system.

| Table 2. Parameters of LLC resonant Converter. |
|-----------------------------------------------|
| Parameters                                    | Symbol | Value  |
| LLC switching frequency                       | $f_s$  | 40 k   |
| Transformer turn ratio                        | $n$    | 1      |
| Transformer excitation inductance             | $L_m$  | 1 m    |
| Transformer leakage inductance                | $L_r$  | 30.7 u |
| Resonant capacitance                          | $C_r$  | 0.33 u |
| Output filter capacitance                     | $C_o$  | 500 u  |

![Output Voltage Waveform](image)

Figure 15. LLC resonant converter output simulation curve.

5.3. Simulation Verification and Stability Analysis of Cascaded System

The output impedance model of three-phase balanced PWM rectifier under independent operation and the input impedance model of LLC resonant converter in independent and stable operation are derived from the analysis of the previous two chapters. The two models were taken into the Formula (1)
and simulated by Matlab/Simulink. As shown in Figure 16, (a) the picture is the three-phase ac side a-phase voltage and current waveform, it shows that the voltage and current have the same phase, indicating that the system operates in unit power factor. It can be seen that the PWM rectifier is stable. (b) is the current curve of the AC side, which shows that there is some instability of AC current when the system starts up, but the AC current becomes stable quickly. (c) is the output voltage waveform of the DC side of the PWM rectifier. (d) is the output voltage waveform of LLC resonant converter, it can be seen that there is fluctuation in the system at the beginning, but it tends to be stable soon. Thus, it can be proved that the whole cascade system is stable.

Figure 16. Output voltage simulation curve of cascade system. (a) The three-phase ac side a-phase voltage and current waveform; (b) The current curve of the AC side; (c) The output voltage waveform of the DC side of the PWM rectifier; (d) Output voltage waveform of LLC resonant converter.

A comparison curve of output impedance of three-phase balanced voltage source PWM rectifier and LLC resonant converter is established in Matlab/Simulink environment, as shown in Figure 17. The green curve is the input impedance curve of LLC resonant converter, and the blue curve is the output impedance curve of three-phase balanced PWM rectifier. The amplitude of the input impedance $Z_o(s)$ is always larger than the amplitude of the output impedance $Z_i(s)$, that is $|Z_o(s)| < |Z_i(s)|$. From the above, the stability of the whole cascade system can be determined.
A comparison curve of output impedance of three-phase balanced voltage source PWM rectifier is established in Matlab/Simulink environment, and the right diagram shows the simulated curve amplified at ($-1,0$). It can be seen from the diagram that the impedance Nyquist curve does not surround ($-1,0$), so it can be determined that the cascade system is stable.

Based on the above analysis, combined with the simulation waveform diagram, Bode diagram and the Nyquist curve, the impedance model of the three-phase balanced voltage type PWM rectifier and the LLC resonant converter can be established accurately, and the stability of the cascade system can be determined at the same time.

6. Conclusions

In this paper, the output impedance model of three-phase balanced voltage type PWM rectifier is built. Afterwards, the stability of the output impedance model is verified by using the bird chart. The input impedance model of LLC resonant converter is established, and the stability of it is verified by bird chart either. The simulation model of cascade system is established and corresponding simulation curves are obtained. The simulation output curve verifies the stability of the cascade system and shows that the configuration parameters and control parameters of the converter are reasonable. Also, the impedance diagram and impedance Nyquist curves are obtained. The bird curve meets the requirement of impedance stability criterion. The Nyquist curve does not revolve around the point ($-1,0$), which also verifies the stability of the cascade system. It is confirmed that the stability of the cascade system can be guaranteed by using impedance stability criterion under reasonable parameters. The correctness of the simulation results can provide guidance for building corresponding experiments in the laboratory. In order to realize the application of high-power electric power electronic transformer in the power grid and guarantee the stability of power grid operation, a good theoretical guidance is provided. It provides a powerful method to judge and analyze the stability of cascade systems in the future.

Figure 17. Bode diagram of front stage output impedance and back stage input impedance.

Figure 18. Impedance ratio Nyquist curve of cascade system.
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