Creating electronic oscillator-based Ising machines without external injection locking

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Coupled electronic oscillators have recently been explored as a compact, integrated circuit- and room temperature operation-compatible hardware platform to design Ising machines. However, such implementations presently require the injection of an externally generated second-harmonic signal to impose the phase bipartition among the oscillators. In this work, we experimentally demonstrate a new electronic autaptic oscillator (EAO) that uses engineered feedback to eliminate the need for the generation and injection of the external second harmonic signal to minimize the Ising Hamiltonian. Unlike conventional relaxation oscillators that typically decay with a single time constant, the feedback in the EAO is engineered to generate two decay time constants which effectively helps generate the second harmonic signal internally. Using this oscillator design, we show experimentally, that a system of capacitively coupled EAOs exhibits the desired bipartition in the oscillator phases without the need for any external second harmonic injection, and subsequently, demonstrate its application in solving the computationally hard Maximum Cut (MaxCut) problem. Our work not only establishes a new oscillator design aligned to the needs of the oscillator Ising machine but also advances the efforts to creating application specific analog computing platforms.

The Ising model, originally developed for spin glass systems, has recently experienced renewed attention owing to its application in accelerating computationally hard problems which are still considered intractable to solve using conventional digital computers. This is motivated by the fact that a large number of such problems can be directly mapped to the Ising Hamiltonian: \( H = -\sum_{ij} J_{ij} \sigma_i \sigma_j \), where \( \sigma_i \) is the \( i \)th spin (±1), \( J_{ij} \) is interaction coefficient between spins \( i \) and \( j \), and \( N \) is the total number of spins in the system; the self-interaction term \(( -\sum_{i} h_i \sigma_i ) \) in the Ising Hamiltonian has been neglected here. For instance, consider the combinatorial optimization-based Maximum Cut (MaxCut) problem—the benchmark problem considered in this work—which entails computing a cut that divides the nodes of the graph in two sets (S1, S2) such that the number of common edges among the two sets is as large as possible. This problem can be mapped to \( H \) by considering each node of the graph as a spin. When a node belongs to S1 (S2), then it is assigned a value (spin) \( +1 \) (\( -1 \)), respectively; further, considering only binary weights (relevant to unweighted graphs, considered here), \( J_{ij} = -1 \), if an edge exists between nodes \( i \) and \( j \); else \( J_{ij} = 0 \). This ensures that if the two adjacent nodes (i.e., nodes connected by an edge) are in the same set, then \( \sigma_i \sigma_j = -1 \); if they are in different sets, then \( \sigma_i \sigma_j = 1 \). Thus, computing the MaxCut of the graph entails finding a configuration of nodes (spins) that minimizes \( -\sum_{ij} J_{ij} \sigma_i \sigma_j \), which is equivalent to minimizing the Ising Hamiltonian (\( H \)). Consequently, this has motivated an active effort to realize a physical Ising machine that evolves to minimize its energy (proportional to \( H \)) and attain the ground state, which in turn, should correspond to the optimal solution of the MaxCut problem. Examples of approaches that have been explored to implement Ising machines include quantum annealing (D-Wave)2–4, quantum mechanical Kerr parametric oscillators5–8, optical parametric oscillator based Coherent Ising machines (CIMs)9–11, electromechanical CIMs12, processors based on annealing in-/near-memory13–15, SRAM based Ising machines that rely on CMOS annealing16, memristor-based implementations17, and coupled electronic oscillators- the focus of the present work. Coupled electronic oscillators have recently been investigated as a promising approach for developing Ising machines since oscillators, in principle, can be made low-power, compact22–24, compatible with room-temperature operation33,34, and can be manufactured using integrated circuit technology3,35.

To solve the MaxCut problem directly using the oscillator hardware, each node of the graph is mapped to an oscillator and every edge is represented by a coupling capacitor. The resulting phases exhibit a bipartition (0° or 180°) that can be mapped to the sets S1 and S2 created by the (Max-)Cut. However, coercing the oscillator phases to exhibit the bipartition requires the injection of a second harmonic signal i.e., \( f_{\text{inj}} \sim \approx 2 \times f_R \) (\( f_{\text{inj}} = \text{frequency} \)).
of injected signal; $f_R$ = frequency of resonant circuit) to every oscillator in the network. In this work, we propose a novel electronic autaptic oscillator (EAO) design that eliminates the need for second harmonic injection; the prefix 'autaptic' is inspired from the autapse structures found in biological spiking neurons which are synapses from the excitatory neuron onto itself (unlike their usual synaptic counterparts that connect to a different neuron) and provide feedback to regulate the neuron's spiking activity. The autaptic feature implemented in the EAO through electronic feedback, introduces two decay time constants during the relaxation phase. These dual time constants can be engineered to effectively create the second harmonic signal required to impose the phase bipartition, thus, eliminating the need for an external second harmonic signal source.

**Results**

The role of second harmonic injection is illustrated using the representative 4 node graph shown in Fig. 1. Figure 1a shows the schematic and experimentally measured time domain output of the Schmitt-trigger based relaxation oscillator used in the experiments. The diode in the feedback circuit is used to create an asymmetric output waveform with a rise time that is significantly smaller than the fall time. Subsequently, when the oscillators are coupled capacitively ($C_C = 1 \text{nF}$) in a manner topologically equivalent to the input graph (Fig. 1b), it can be observed that the oscillators are frequency synchronized. Moreover, when no external signal is applied (Fig. 1c), the oscillator phases exhibit a continuous distribution as shown in the corresponding phase plot (the phase ordering, in fact, represents the independent sets of the graph). However, when an external second harmonic signal ($f_{\text{inj}} = 3.4 \text{kHz}$) is injected (Fig. 1d), the oscillator phases exhibit a bipartition that can subsequently be shown to represent the two sets created by the MaxCut ($= 4$, for the graph considered in the experiment). The second harmonic signal effectively modifies the energy landscape by lowering the energy corresponding to the $0^\circ$ and $180^\circ$ oscillator phases.

However, generating, and injecting the second harmonic injection signal to every oscillator can incur a significant energy and area overhead. We therefore explore the possibility of eliminating this requirement by redesigning the fundamental compute block of the system— the oscillator. Figure 2a shows the proposed EAO design which essentially consists of a Schmitt-trigger oscillator (similar to that proposed in Fig. 1a) with an additional non-linear feedback element—a diode connected (p-type) MOSFET. Figure 2a shows the experimentally
measured time domain waveform of the oscillator where, unlike the conventional oscillator, the discharging phase reveals two distinct two-time constants. While the motivation behind modifying the oscillator dynamics will be discussed in the following sections, we first describe the origin of the two-time constants. The time constant of the oscillator is effectively determined by the net resistance and capacitance in the feedback path. Initially, during the discharging phase, the diode connected p-MOSFET is designed to be in the ON state, and hence, forms a parallel conducting path to the feedback resistance, $R_f$; the diode is reverse biased and has negligible contribution to the conduction. Thus, during this initial phase, the load capacitor, $C_L$, effectively discharges with a time constant $\tau_1 = (R_F||R_{FE})C_L$. As the output voltage decreases, the diode-connected MOSFET turns OFF, and the output now begins to decay with a larger time constant $\tau_2 = R_FC_L$ ($\tau_2 > \tau_1$) until it reaches the minimum. Subsequently, the oscillator output begins to rise again with the rise time being governed by the dynamic resistance of the diode.

To analyze the dynamics of the EAO, we consider the control block diagram of the EAO (Fig. 2). We specifically focus on voltage relaxation phase in the time domain waveform characterized by two different relaxation time constants. During this phase, the EAO can be modelled as a control system with a feed-forward gain, $G = 1 + \frac{R_1}{R_2}$ and feedback factor $B = \frac{1}{\tau_s}$ where $\tau = R_C$. In region A, where the p-MOSFET is considered to be in the ON state, $\tau_1 = (R_{FE})C_L$, and in region B, where the p-MOSFET is OFF, $\tau_2 = R_FC_L$ (i.e., FET is considered as an open circuit). We note that while the p-MOSFET switching has been considered abrupt between the ON (with ON resistance $R_{FE}$) and the OFF state in the above discussion for simplicity, the actual resistance evolution will be a continuum. Using this simplistic model, we analyze the frequency spectrum (Fig. 2c) of the output during the voltage relaxation, specifically focusing on the amplitude of the second harmonic relative to that of the fundamental frequency, i.e., $A_{2f_1}/A_{f_1}$ (indicative of the relative power that resides at the two frequencies) as a function of the relative time period of the two phases observed during voltage relaxation of the EAO. It can be observed from Fig. 2c that $A_{2f_1}/A_{f_1}$ is a strong function of $t_A$ (relative to $T$). In a conventional oscillator without the additional feedback, $\tau_1 = \tau_2$ which indicates that the oscillator relaxes with a single time constant. However, as $\tau_2$, and consequently, $\tau_3$ is progressively reduced using the additional diode-connected transistor element in EAO design, the relative amplitude, and thus, the power concentrated at the second harmonic, steadily increases. We also note that the asymmetric output of the EAO (as well as the conventional oscillator used for comparison), characterized by a short rising time (compared to the voltage relaxation time) helps concentrate more power at the second harmonic in comparison to the symmetric waveform (not shown here).

Thus, by engineering $t_A$ to be small, a significant portion of the EAO power can be concentrated at the second harmonic. Consequently, the EAO effectively self-generates a strong second harmonic signal in the network, and facilitates the bipartition in the oscillator phases without the need for external second harmonic injection. Next, we experimentally explore the synchronization dynamics of the EAOs by considering the same representative graph as shown in Fig. 1b, and constructing the corresponding equivalent circuit with the coupled EAOs. Figure 3a shows the schematic of EAO design discussed above and Fig. 3b shows experimentally measured
time domain waveform of the EAO with distinct time constants. It can be observed from Fig. 3c–e, that the EAO phases exhibit a bipartition (instead of a continuous distribution) without requiring any external injection; in other words, the EAO-based coupled oscillators effectively behave as a system under second harmonic injection, without actually requiring any external injection. Further, we measure the MaxCut solutions obtained with the coupled systems of EAO- and conventional oscillators over multiple trials (Fig. 3f) since Ising machines are known to exhibit statistical behavior owing to the system getting trapped in local minima of the high dimensional phase space. Although for the specific graph instance, we observe that the EAO-based system always computes the optimal solution, we observe similar statistical behavior in larger graph instances considered in the following sections. This performance aspect will be investigated further in future work.

Furthermore, we also measure and compare the dynamics of the EAO oscillators with the conventional oscillator design for various graph configurations as shown in Fig. 4. These configurations also exhibit the same bipartition behavior as described earlier as well as show the capability (albeit statistically) to compute the optimal MaxCut solution.
Solving larger graphs with EAOs. Finally, we evaluate using SPICE-based circuit simulations, the functionality of the autaptic oscillators in larger graphs. We analyze randomly generated graph instances of various size (V: 32, 64, 128) and edge densities ($\eta$: ratio of the number of edges in the graph to the number of edges in a complete graph with the same number of nodes) and compare the results with conventional oscillators operating under second harmonic injection; 2 graphs are analyzed for every combination of V, $\eta$; each graph is simulated 10 times. Figure 5 shows a bubble plot comparing the MaxCut solution obtained from the coupled EAOs with that obtained using the conventional oscillators under the influence of second harmonic injection. It can be observed from the simulations that the autaptic oscillators enable the same computational functionality and similar performance as the conventional oscillators under the influence of external second harmonic injection. The EAO based approach exhibits a deviation of $\sim -6.2\%$ to $+9.15\%$ in comparison to the conventional oscillator-based method ($+$ indicates that the EAO solution is better than that produced by the conventional oscillators), with an average deviation of $\sim -1.6\%$. This suggests that electronic autaptic oscillator approach can potentially be scaled further although factors such as noise and the coupling architecture will be important to the eventual scalability of the approach. Additionally, we also note that while the proposed EAO helps reduce the cost of implementing the second harmonic signal generation and routing circuitry, other...
implementation costs such as those associated with the design of the coupling network, which exhibits a square law dependence on the graph size, will also be vital to the feasibility and the competitiveness of the electronic oscillator-based approach to implementing Ising machines as a whole.

Discussion

The electronic autaptic oscillator concept demonstrated here is a manifestation of a compute-centric device optimization approach to overcome challenges and enable efficient implementation of oscillator-based Ising machines. The feedback methodology used here is not limited to the Schmitt trigger oscillator and can be used to augment other oscillator designs. Considering that area and scalability are key questions in realizing parallel computational architectures like coupled oscillators, this work marks an important step towards improving the scalability and reducing the area requirements for oscillator-based Ising machines.

Methods

The EAO as well as conventional oscillators for the experimental demonstration were implemented using LM741 OPAMP based Schmitt trigger where oscillations were stabilized using negative feedback. For the conventional oscillator, the negative feedback was implemented using a parallel combination of a feedback resistor ($R_F = 80 \, k\Omega$) and a diode; the diode was used to create an asymmetric waveform with a small rise time, and a relatively large decay to help accentuate the effect of two decay constants in the EAO design. The EAO oscillator also used an additional feedback element implemented using P-MOS (ALD1107) to help generate the two decay time constants. A supply voltage of 8 V was used. The oscillators were assembled on a breadboard and coupled using discrete capacitors in order facilitate the prototypical demonstration. Simulations utilized in the work were performed using SPICE.

Data availability

The datasets generated during and/or analyzed during the current study are available from the corresponding author on reasonable request.

Code availability

All codes used in this work are either publicly available or available from the authors upon reasonable request.

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References

1. Lucas, A. Ising formulations of many NP problems. Front. Phys. 2, 1–14 (2014).
2. Mallick, A. et al. Using synchronized oscillators to compute the maximum independent set. Nat. Commun. 11, 4689 (2020).
3. Bashar, M. K. et al. Experimental demonstration of a reconfigurable coupled oscillator platform to solve the Max-cut problem. IEEE J. Explor. Solid State Comput. Dev. Circuits 6(2), 116–121 (2020).
4. Wang, T. & Roychowdhury, J. OIM: Oscillator-based Ising machines for solving combinatorial optimisation problems. In Unconventional Computation and Natural Computation (eds. McQuillan, I. & Seki, S.) vol. 11493 232–256 (Springer International Publishing, 2019).

5. Chou, J., Bramhavar, S., Ghosh, S. & Herzog, W. Analog coupled oscillator based weighted Ising machine. Sci. Rep. 9, 1–10 (2019).

6. Albertsson, D. I. et al. Ultrafast Ising Machines using spin torque nano-oscillators. Appl. Phys. Lett. 118(11), 112404 (2021).

7. Johnson, M. W. et al. Quantum annealing with manufactured spins. Nature 473(7346), 194–198 (2011).

8. King, A. D. & McGree, C. C. Algorithm engineering for a quantum annealing platform. arXiv:1410.2628. (2014)

9. Smelyanskiy, V. N. et al. A near-term quantum computing approach for hard computational problems in space exploration. arXiv::1204.2821. http://arxiv.org/abs/1204.2821 (2012).

10. Goto, H. Bifurcation-based adiabatic quantum computation with a nonlinear oscillator network. Sci. Rep. 6, 21686 (2016).

11. Wang, Z., Marandi, A., Takata, K., Byer, R. L. & Yamamoto, Y. A degenerate optical parametric oscillator network for coherent computation. Lect. Notes Phys. 911, 219–249 (2016).

12. Mcmahon, P. L. et al. A fully 100-spin Ising machine with all-to-all connections. Science 354(6312), 614–617 (2016).

13. Haribara, Y., Usunomiya, S. & Yamamoto, Y. A coherent Ising machine for MAX-CUT problems: Performance evaluation against semidefinite programming and simulated annealing. Lect. Notes Phys. 911, 251–262 (2016).

14. Pierangeli, D., Marcucci, G. & Conti, C. Large-scale photonic Ising machine by spatial light modulation. Phys. Rev. Lett. 122(21), 213902 (2019).

15. Mahboob, I., Okamoto, H. & Yamaguchi, H. An electromechanical Ising hamiltonian. Sci. Adv. 2(6), e1600236 (2016).

16. Takemoto, T., Hayashi, M., Yoshimura, C. & Yamaoka, M. A 2 x 30 k spin multichip scalable annealing processor based on a Processing-in-memory approach for solving large-scale combinatorial optimization problems. In IEEE Int. Solid-State Circuits Conf. (ISSCC) Dig. Tech. Papers, Feb. 2019, 52–54 (2019).

17. Yamamoto, K. et al. 7.3 STATICIA: A 512-spin 0.25 M-weight full digital annealing processor with a near-memory all-spin-updates-at-once architecture for combinatorial optimization with complete spin-spin interactions. In IEEE Int. Solid-State Circuits Conf. (ISSCC) Dig. Tech. Papers, 138–140 (IEEE, 2020).

18. Su, Y., Kim, H. & Kim, B. 31.2 CIM-spin: A 0.5-to-1.2 V scalable annealing processor using digital compute-in-memory spin operators and register-based spins for combinatorial optimization problems. In IEEE Int. Solid-State Circuits Conf. (ISSCC) Dig. Tech. Papers, Feb. 2020, 480–482 (2020).

19. Tamaoka, M. et al. A 20k-spin Ising chip to solve combinatorial optimization problems with CMOS annealing. IEEE J. Solid-State Circuits 51(1), 303–309 (2016).

20. Cai, F. et al. Power-efficient combinatorial optimization using intrinsic noise in memristor Hopfield neural networks. Nat. Electron 3, 409–418 (2020).

21. Torrejon, J. et al. Neuromorphic computing with nanoscale spintronic oscillators. Nature 547, 7664 (2017).

22. Lebrun, R. et al. Mutual synchronization of spin torque nano-oscillators through a long-range and tunable electrical coupling scheme. Nat. Commun. 8, 15825 (2017).

23. Coulombe, J. C., York, M. C. & Sylvestre, J. Computing with networks of nonlinear mechanical oscillators. PloS One 12, e0178663 (2017).

24. Csaba, G., Papp, A., Porod, W. & Yeniceri, R. Non-boolean computing based on linear waves and oscillators. In 2015 45th European Solid State Device Research Conference, 101–104 (2015).

25. Mahboob, I. & Yamaguchi, H. Bit storage and bit flip operations in an electromagnetic oscillator. Nat. Nanotechnol. 3, 275–279 (2008).

26. Csaba, G., Ytterdal, T. & Porod, W. Neural network based on parametricallypumped oscillators. In 2016 IEEE International Conference on Electroniccs, Circuits and Systems (ICECS), 45–48 (IEEE, Monte Carlo, 2016).

27. Roychowdhury, J. Boolean computation using self-sustaining nonlinear oscillators. Proc. IEEE 103, 1958–1969 (2015).

28. Pufall, M. R. et al. Physical implementation of coherently coupled oscillator networks. IEEE J. Explor. Solid-State Comput. Devices Circuits 1, 76–84 (2015).

29. Csaba, G. & Porod, W. Noise immunity of oscillatory computing devices. IEEE J. Explor. Solid-State Comput. Devices Circuits 6(2), 164–169 (2020).

30. Csaba, G. & Porod, W. Coupled oscillators for computing: A review and perspective. Appl. Phys. Rev. 7(1), 011302 (2020).

31. Csaba, G., Raychowdhury, A., Datta, S. & Porod, W. Computing with coupled oscillators: Theory, devices, and applications. In 2018 IEEE International Symposium on Circuits and Systems (ISCAS), 1–5 (2018).

32. Bashar, M. K., Hrdy, R., Mallick, A., Farnoud Hassanazadeh, F. & Shukla, N. Solving the maximum independent set problem using coupled relaxation oscillators. In 2019 Device Research Conference (DRC), 187–188 (2019).

33. Vaidya, J., Bashar, M. K. & Shukla, N. Using noise to augment synchronization among oscillators. Sci. Rep. 11, 4462 (2021).

34. Mallick, A. et al. Graph coloring using coupled oscillator-based dynamical systems. IEEE Int. Symp. Circuits Syst. ISCAS 2021, 1–5 (2021).

35. Herrmann, C. S. & Klaus, A. Autapse turns neuron into oscillator. Int. J. Bifurc. Chaos 14(02), 623–633 (2004).

36. Saada, R., Miller, N., Hurwitz, I. & Susswein, A. J. Autaptic excitation elicits persistent activity and a plateau potential in a neuron of known behavioral function. Curr. Biol. 19(6), 479–484 (2009).

37. Yilmaz, E., Ozer, M. & Perc, M. Autapse-induced multiple coherence resonance in single neurons and neuronal networks. Sci. Rep. 6(1), 1–14 (2016).

38. Qin, H., Ma, J., Wang, C. & Chu, R. Autapse-induced target wave, spiral wave in regular network of neurons. Sci. China Phys. Mech. Astron. 57(10), 1918–1926 (2014).

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Author contributions

J.V. performed the experiments, J.V. and R.S.S.K. developed and implemented the analytical model and performed the simulations. N.S. conceived the idea and directed the overall project. J.V. and N.S. wrote the manuscript. All authors discussed the results and commented on the manuscript.

Competing interests

The authors declare no competing interests.
