Abstract—Variational quantum algorithm (VQA), which is comprised of a classical optimizer and a parameterized quantum circuit, emerges as one of the most promising approaches for harvesting the power of quantum computers in the noisy intermediate scale quantum (NISQ) era. However, the deployment of VQAs on contemporary NISQ devices often faces considerable system and time-dependent noise and prohibitively slow training speeds. On the other hand, the expensive supporting resources and infrastructure make quantum computers extremely keen on high utilization.

In this paper, we propose a virtualized way of building up a quantum backend for variational quantum algorithms: rather than relying on a single physical device which tends to introduce temporal-dependant device-specific noise with worsening performance as time-since-calibration grows, we propose to constitute a quantum ensemble, which dynamically distributes quantum tasks asynchronously across a set of physical devices, and adjusting the ensemble configuration with respect to machine status. In addition to reduced machine-dependant noise, the ensemble can provide significant speedups for VQA training. With this idea, we build a novel VQA training framework called EQC that comprises: (i) a system architecture for asynchronous parallel VQA cooperative training; (ii) an analytic model for assessing the quality of the returned VQA gradient over a particular device concerning its architecture, transpilation, and runtime conditions; (iii) a weighting mechanism to adjust the quantum ensemble's computational contribution according to the systems' current performance. Evaluations comprising 500K times' circuit evaluations across 10 IBMQ NISQ devices using a VQE and a QAOA applications demonstrate that EQC can attain error rates very close to the most performant device of the ensemble, while boosting the training speed by 10.5× on average (up to 86× and at least 5.2×). We will release EQC on GitHub.

I. INTRODUCTION

Quantum computing (QC) is poised to offer substantial computational capabilities that classical computing could never feasibly reach in many critical domains, such as database search [19], graph combinatorial optimization [15], quantum chemistry [7], machine learning [4], etc. Recently, Google has showcased quantum supremacy on their 53 qubits Sycamore Quantum Processing Units (QPU), where a quantum sampling problem, which would take an estimate of 10,000 years (rectified to 2.5 days by IBM) running on the ORNL Summit supercomputer now can be finished in ~200 seconds [1].

Although many algorithms that could provide quantum supremacy have been theorised [19], [38]. QPUs in the contemporary NISQ era are susceptible to noise in a multitude of ways. These include (i) Coherence Error, which describes the natural decay of qubit states to their ground states, analogous to retention error in classical devices. Coherence error includes noise-induced state decay (i.e., T1 decay) and spin-spin relaxation (i.e., T2 decay); (ii) Gate Error, which refers to imperfect gate operations. Such gate error is relatively small for 1-qubit gates (e.g., ~0.1% for IBMQ), but can be significant for 2-qubit gates (e.g., nearly 4% for IBMQ [43]); (iii) SPAM Error, short for state preparation and measurement error, is introduced due to unsatisfactory state initialization and measurement sensing. Additionally, non-coherent interference such as cross-talk [29], [37] can also impose computational error, acting as device-specific latent noise.

Due to the high error rate and insufficient qubits in supporting full-scale quantum error correction (QEC), people are searching for quantum algorithms that can harvest the quantum advantages of NISQ devices but are more robust to the errors. The Variational Quantum Algorithm [7], [9], [14], stands out as one of the most promising NISQ algorithms, which employs a classical optimizer to train a parameterized quantum circuit.

VQAs have been applied for several key applications. These include (i) the Variational Quantum Eigensolver (VQE) for investigating molecular & nuclear structures/dynamics in quantum chemistry [2], [44] and nuclear physics [25], [35]; (ii) Quantum Approximate Optimization Algorithm (QAOA) for optimizations such as MaxCut [10] in graph analytics; and (iii) Quantum Neural Networks (QNNs) for quantum machine learning [15], [21], [34], [39].

Although VQAs have been extensively evaluated on platforms such as IBMQ [8], the deployment of VQAs for practical domain utilization (e.g., estimating the ground energy for molecules) on the NISQ platforms is still hampered by three challenges: (i) The training is susceptible to device-
specific bias and noise. Similar to the correlated error caused by mapping a circuit to the same qubits and links repeatedly following a fixed pattern [15], we have observed that QC is also subject to device-specific bias originating from topology, SPAM [16], calibration and running conditions. Figure 1-left shows the error rates of training a VQE circuit on three IBMQ devices (Bogota, x2, and Casablanca) individually with respect to ideal simulation, as well as the error reduction that can be achieved through our EQC framework; (ii) Prohibitively long execution time. Due to the significant deployment, maintenance, and operating costs, most QC platforms are provided as a cloud service and shared by many users. Given that VQA needs to repeatedly test & adjust the parameterized circuit, iterate over enormous input data, train over numerous epochs, and wait for each trial going through the waiting queue, this leads to extremely long execution time. For example, it takes us about 4,623 hours to train a VQE circuit on Manhattan. The congestion, calibration and maintenance can further exacerbate the delay. Figure 1-middle shows the running time for our VQE circuit on the three IBMQ devices, compared to our ensemble approach; (iii) Large utilization variance due to unbalanced workloads. Despite the sharing nature, quantum computers can be underutilized. This is largely due to workload imbalance across multiple devices given user-preference and congestion. As users tend to select the best performing quantum computer, the overall provider processor utilisation can be quite unbalanced. The VQA jobs significantly amplify such imbalance due to iterative testing and long execution time. Considering the expensive cost from the supporting infrastructure and sources such as cryogenic coolers, superconducting wires, microwave devices, and sophisticated control circuitry [13], the under-utilization can be a considerable burden to the QC service providers. State of the art technologies such as Qiskit Runtime improve VQA performance through minimizing classical-quantum communication costs. However, these vertical approaches do not help with machine-specific bias, and may further exacerbate the unbalanced utilization problem.

In this paper, we propose EQC – an Ensembled Quantum Computing framework for Variational Quantum Algorithms. EQC can bring considerable stability, noise-reductions and speed-ups in VQA training over alternative approaches. Being the primary effort in system-managed QPU-aware parallel VQA training, EQC employs a master node to asynchronously manage a set of client nodes paired with backend devices comprising the quantum ensemble. To dampen the QPU bound noise and minimize time-dependent machine drift, EQC further adopts an analytic model to assess the quality of the returned gradient, and builds a management module to allow online adjustment of the quantum ensemble based on the runtime condition of the backend devices. Evaluations on 10 IBMQ devices show that EQC can mitigate system-dependant bias while boosting the speed by over $10 \times$ (up to $86 \times$). This paper thus makes the following contributions:

- The Concept and Design of Quantum Ensemble: we propose a novel way of thinking about a quantum backend: rather than using a single physical device incorporating device-specific bias and slow execution time, a quantum ensemble can serve as a virtualized backend for offering adaptive bias and noise mitigation through calibration-aware device mixtures, improved device choice relating to circuit design, and faster execution through parallelization.

- The EQC Framework: we develop a system architecture for asynchronous VQA optimization that can bring over an order of speedups over single QPU-based optimization, whilst simultaneously reducing error. We provide theoretic proof on the convergence of the approach.

- The Adaptive Weighting System: we propose a weighting approach considering noise level, topological constraint, transpiled circuit structure, calibration time, etc. It thus allows adaptive and dynamic weighting of the gradient with respect to the device condition, mitigating time-dependent drift and overwhelming time-dependdent machine bias.

II. BACKGROUND

A. Quantum Gates and Devices

QC exploits the quantum phenomena such as superposition and entanglement for computing, and manipulates the probabilistic state-vector to accomplish tasks. State-vectors, which represent the quantum states, are linear combinations of orthonormal eigenvectors with normalized square sum coefficients. Quantum computers make use of gates to perform state transformations, represented as unitary matrices relative to some basis, which operate on the quantum state. Gate operations are performed on a physical quantum processing unit (i.e., QPU). There are two main features for QPUs:

(i) Basis gates. Basis gates describe the native hardware operations; all quantum circuits need to be transpiled to basis gates eventually in order to be executed by a QPU. For example, most IBMQ devices use the same basis gate set: CNOT, ID, RZ, RX, SX [12].

(ii) Topology. Applying two-qubit gates such as CNOT in theoretical development usually assumes fully connected systems. However, on real superconducting quantum processors, due to limitations on the physical designs, inter-qubit connectivity constraints exist.

As shown in Figure 3, the three devices (IBMQ Belem.x2 and Manila) all have 5 physical qubits. However, due to the variance of inter-qubit connectivity, the circuit structures for the same function can be different after transpilation. This is because the two-qubit gates such as CNOT, can only be performed on connected qubits; otherwise, the qubits must be moved next to each other using SWAP-gates. The SWAP gate is composed of three CNOT gates, a costly operation. The SWAP-gates allow any two qubits that are connected to swap positions, thus can hide the topology constraint, however at a substantial time and compounding error cost.
B. QPU Noise

QPU Noise: NISQ QPUs are highly diverse in their ability to run quantum routines without compounding a multitude of random errors due to low-level qubit stability and high-level QPU topological constraints. As discussed, there are three types of errors: (i) Coherence Error due to the non-ideal energy exchange between the outside system and the ideal system, which is characterized by exponential decay factors $T1$ and $T2$, and known as thermal dephasing and decoherence [15]; (ii) Gate Error due to depolarization, which is characterized by undesired Pauli-X/Y/Z operations representing bit-flips or phase-flips. (iii) SPAM Error due to state preparation and measurement noise, which is associated with the anharmonicity between the states $|0\rangle$ and $|1\rangle$, and the erroneous process of distinctively and properly distinguishing between the two states correctly.

Additionally, each QPU has its own unique noise profile that changes with frequent calibration. These volatile systems vary in spatial and temporal noise due to the imperfect manufacturing process [29], imperfections of gate implementation and control, and specific external interference [28], [31]. These noise compound with each other, increasing the overall probability of obtaining an erroneous outcome.

C. Variational Quantum Algorithm

VQAs are characterized by a group of parameterized quantum operations, with parameters $\hat{\theta}$, $\theta \subseteq R$. The quantum algorithms goal is to optimize $\hat{\theta}$ to $\hat{\theta}^*$, where $\hat{\theta}^*$ is the optimal learned parameters, such that the trial wave function of $|\psi\rangle$ is transformed closer to some target wave function according to an optimization function. The optimization function represents for example the ground state of a quantum system, and is expressed as parameterized unitaries $U(\theta_1)U(\theta_{i-1})...U(\theta_j)|\psi\rangle$. For Variational Quantum Eigensolvers (VQE) [34] or Quantum Approximate Optimization Algorithm (QAOA) [15], the objective is to optimize towards some Hamiltonian $H$ describing the total energy of the system, or representing some objective cost function, as shown in Equation 1

$$\arg\min(|\hat{\theta}\rangle) \langle \psi(\hat{\theta}) | H | \psi(\hat{\theta}) \rangle$$

This optimization process is tailored for alternative use cases typically in quantum machine learning [21], [16], [39].

The present VQA optimization process, however, encounters two key issues: (i) There is a substantial computational cost in inducing circuits and optimizing each individual parameter; (ii) Machine noise leads to a bias being learnt by a machine during training, where the parameters will deviate compensating for a machines noise. However, when a machine is re-calibrated or the backend device is changed, the learned parameters are no longer appropriate. Furthermore, if the noise is overwhelmingly high on a machine, the gradient computation will be erroneous and the algorithm will not learn.

D. Distributed Optimization

Distributed optimization of classical deep neural networks (DNNs) has been motivated by the need to train large neural networks [6], [23] for exceedingly large datasets [26] through parallel processing. Asynchronous stochastic gradient descent (ASGD) has been shown to provide substantial speedups for DNN training with little accuracy loss [45]. However, the problem of distributing optimization across multiple classical processing units changes substantially when looking at a quantum distributed system. Classical systems primarily deal
with latency, whereas quantum processors deal with both latency and individual noise characterization of each QPUs \cite{20}. The problem is exacerbated when multiple backend device architectures and/or providers are presented. In addition to the different runtimes of the service, the distinct periods of calibration, the diverse range of speed, as well as the noise can make the coordination of the training process significantly more complicated than classical distributive DNN training.

### III. EQC System Design

In this section we describe the EQC architecture, structure and discuss its implementation.

#### A. VQA Task Decomposition

VQA optimization typically adopts stochastic gradient descent to perform step by step parameter differentiation, where concurrent tasks can be extracted from multiple hierarchies of the algorithm for (asynchronously) parallel execution.

Regarding VQE, these problems are commonly related to quantum chemistry, involving a Fermionic Hamiltonian that is decomposed via processes such as Jordan-Wigner decomposition. As a decomposed Hamiltonian is a linear sum of Pauli strings generated by transforming spin operators into a series of quantum computing supported Pauli operators. In this case, we perform the system parallelization at the Pauli string level. To compute the derivative of each respective parameter, the system collects parallelized computation, to perform one gradient descent step on one parameter. This is repeated for each parameter within the VQE ansatz.

Regarding QAOA, comprising of optimization problems such as MaxCut, the learning process aims to optimize over a target Hamiltonian objective. In the case of a single matrix representation of a Hamiltonian, the system aims to minimize or maximise $\langle \psi | H | \psi \rangle$. In doing so, we parallelize at the parameter level, where the derivative for each parameter is parallelized. This results in each parameter being asynchronously optimized, and is repeated over all parameters. Each parallel task is responsible for computing one gradient descent step over one parameter.

Regarding QNN, the learning process involves optimizing over $\frac{\partial f(x, \theta)}{\partial \theta} = \frac{1}{n} \sum_{i=1}^{n} \frac{\delta f(x, \theta)}{\delta \theta}$, where the average cost over a dataset is to be minimized. To parallelize the QNN training, we distribute at the dataset level for parameter optimization. Given a data set, as the parameter gradient is the average of all the data points’ derivative for a parameter, the gradient can be parallelized for each parameter at each data point. Each parallel job is responsible for computing the gradient with respect to its assigned data point for a target parameter. One complete gradient descent step on one parameter is comprised of the average returned gradient for the target parameter over all data points, despite the gradients are applied asynchronously.

#### B. EQC System Architecture

As shown in Figure\cite{3}, EQC is built as a single-master node, multi-client nodes architecture. It is designed for managing and optimizing the training of a VQA circuit with a set of parameters to optimize using a set of NISQ QPUs with different noise profiles, different topology’s and different induced circuit architectures that may vary with time and transpilation. EQC consists of the master node, the client nodes, and the QPUs: the master node distributes workloads to client nodes, which perform target derivative calculation with their paired QPUs for their assigned task. With the computation of a derivative on a NISQ-term QPU comes a degree of confidence in said computation due to unavoidable noise. Therefore, the client node also computes a weight based on the transpiled circuit and reported system noise statistics at circuit induction time as well. The utilization of asynchronous stochastic gradient descent across a set of NISQ QPU’s, whilst also incorporating a topology-aware low-level-performance-aware weighting system ensures the system attains a computational speedup, whilst improving system optimization performance.

#### C. EQC Implementation

We present the design details about the three major components of EQC: master node, client node, and QPU.

1) Master Node: The master node is provided with three key pieces of information: (i) the quantum circuit over which the system is to be optimized with measurements; (ii) the initialized parameters $[\theta]$, $\theta \subseteq \mathbb{R}$; and (iii) a detailed loss function $\ell$ describing the optimization problem.

The master node is responsible for keeping track of all parameters at all times, as well as the ideal (i.e. no topological constraints) circuit layout. Once the master node has been initialized with all of the pre-requisites in place, the master node queries the Quantum Computing service provider(s) based on the ensemble configuration. The available QPUs to form the ensemble should have active qubits larger than the number of qubits required by the parameterized circuit. The backend devices can be homogeneous or heterogeneous. In other words, although we demonstrate EQC using 10 of IBMQ devices, the framework itself supports an ensemble of QPUs from different vendors.

When the master node dictates the formalization of the ensemble, it initializes client nodes one per device with the loss function and circuit template. The master node then distributes the VQA task parameters in a cyclic fashion to the client nodes. The master node adopts a task queue for parallel task distribution. The master node iterates the parameter list and asynchronously assigns the next parameter to a client node that is available until no parameters left. After that, the next epoch can start.

The only information the master node receives from the client nodes are the gradients of the parameters $\theta_i$, and the computed noise model of the QPU associated, which we will discuss in the following section. On receipt of a gradient, the master node applies this gradient using the ASGD rule outlined in Equation\cite{12} and sends a new parameter to differentiate at an idle client. The whole process is outlined in Algorithm\cite{11}.
The procedure of the client node is shown in Algorithm 2. An associated noise factor back to the master node, and in as shown in Figure 3. To accommodate such topological constraints, extra SWAP operations are needed. With respect to noise, each QPU is substantially different. Highly connected QPU’s suffer from cross-talk, degraded CNOT-gate fidelity and T1/T2 lifespans. As a comparison, newer architectures such as the IBM Falcon processor are laid out in a honeycomb fashion to mitigate the cross-talk issue and reduce noise at the cost of a lower connectivity. Nevertheless, noise still applies to each NISQ system, and is correlated to the topological constraints considering SWAPs and circuit depth.

D. Implementation Details

EQC is implemented based on two packages: Qiskit and Ray. Qiskit is responsible for circuit generation, communication management with IBMQ devices, and all other quantum related processes. Meanwhile, Ray [27] provides very flexible and universal APIs for building distributed systems. Ray was originally designed for distributive reinforcement learning. Ray.io creates remote actors generated based on Python’s class structure, which are responsible for managing the QPU-related configuration information, and properties mentioned above with respect to client node responsibilities. Ray.io allows for asynchronous communication between the master node and client nodes, through the use of “ready” calls that indicate the client node has a pending object to be collected. Each client node connects to one running, unreserved QPU by connecting to individual IBMQ backend.

IV. WORKER QPU WEIGHTING

As discussed, NISQ devices are extremely noisy and compound computational errors in a multitude of ways. Furthermore, their noise models change at every calibration. Certain quantum computers are better at producing more accurate resultant probability distributions than others, due to coherent and latent system noise factors. In order to take these noise factors into consideration for EQC, we propose to quantify and weight the gradient based on an analytic model.

A key observation that aligns system noise (measurement, depolarization, and thermal/dephasing decay) here is that they
can be attributed as probabilistic events. Each quantum computer, when calibrated, reports the gate fidelity, measurement fidelity, gate times, state anharmonicity, and T1/T2 decay constants. The event of the identity gate being applied (i.e. no error) is $1 - p$ for each gate fidelity reported. For thermal decay, the exponential mode of $P(\Phi_{x,t} = \Phi_{0,t}) = e^{-t/T1}$ where $\Phi_{x,t}$ represents the probability of a decay occurring between time $0$ and $t$. The probability of a error-free computation occurring thus can be expressed as the product of possible errors not occurring. Therefore, we propose characterizing an entire systems quality by compounding these calibration statistics based probabilistic models with quantum routine properties described below:

$$P_{Correct} = e^{-\frac{CD}{G1/2} + \mu_{G1,2} + \mu_{G2}} (1 - \gamma)^{G1} (1 - \beta)^{G2} (1 - \omega)^{M}$$

(2)

where CD refers to the critical depth of a circuit, $\mu_{G1,2}$ is the average gate time of a 1 or 2 gate operator, $\beta$ is the CNOT fidelity, $\gamma$ is single gate fidelity, $G1/2$ is the number of single or dual gate operations, $\omega$ is the measurement error rate and $M$ is the number of measurements. Since this equation is a probabilistic model, it is bound by $0 \leq P_{Correct} \leq 1$. Furthermore, our model benefits from being topology-aware. By incorporating dual gate count $G2$, topological constraints will drive this value up due to increased SWAP gates increasing probability of error, thereby decreasing weights of computationally consequential topologies.

To validate our analytic model, we apply a 5-qubit GHZ state onto real quantum computers and use our model to predict the probability of error. The GHZ state is characterized by the state vector $|\Psi\rangle = (\frac{1}{\sqrt{2}} (|000\rangle + |111\rangle))$. Therefore, any results with both a 0 and 1 in the bit string have had a computational error occur. We compute our expected $P_{Correct}$ and compare with IBMQ’s QPU’s IBMQ Lima, IBMQ x2, IBMQ Belem, IBMQ Quito, IBMQ Manila and IBMQ Bogota in Figure 4.

As can be seen in Figure 4, a strong linear positive correlation is demonstrated between observed errors and $P_{Correct}$ with a $R^2$ value of 0.605. Furthermore, we calculate the Pearson correlation between our calculated errors and observed

![Fig. 4. Calculated Vs Observed 5-Qubit GHZ State Error. Observed error indicates proportion of erroneous outputs, and calculated error indicates the computed chance of an error.](image)

![Fig. 5. QPU Weighting: Weighting system applied with weights bound [0.5-1.5] over 7 QPU’s transpiling and computing Equation 2 over circuit illustrated in 8](image)

errors. A correlation of 0.784 is attained with a two-tailed p-value of 1.28E-7, indicating that our results and actual error rates are strongly statistically correlated. We observe that our model was substantially better at predicting the chance of an error closer to calibration times. As observed in Figure 4 for a freshly calibrated system we observed an error rate of 73.5%, alongside a calculated error rate of 74.0%. However, for staler calibrations we observe an error rate of 69.0% alongside a calculated error rate of 91.9%. This was only observed on certain machines however, with other machines not suffering from the same problem of stale calibrations. We make use of our characterizing formula by giving each worker node the job of calculating its $P_{Correct}$ at transpilation before each circuit induction. We make use of our model by linearly scaling the distribution of $P_{Correct}$ values to be bound by a range. This weighting system is real-time adaptive, and incorporates the transpiled cost of a circuit and the QPU’s stability. We demonstrate this in Figure 5 where a group of IBMQ machines are bound to have weights between [0.5, 1.5]. We observe that our model was substantially better at predicting the chance of an error closer to calibration times.

V. EQC EVALUATION

| Device      | Qubits | Processor | QV | Topology  |
|-------------|--------|-----------|----|-----------|
| Lima        | 5      | Falcon r4T| 8  | T-shape   |
| Belem       | 5      | Falcon r4T| 10 | Fully-connected |
| Quito       | 5      | Falcon r4T| 10 | T-shape   |
| Manila      | 5      | Falcon r5.1L| 32 | Line      |
| Santiago    | 5      | Falcon r4L| 16 | Line      |
| Bogota      | 5      | Falcon r4L| 32 | Line      |
| Lagos       | 7      | Falcon r5.1H| 32 | H-shape   |
| Casablanca  | 7      | Falcon r4H| 32 | H-shape   |
| Toronto     | 7      | Falcon r4H| 32 | H-shape   |
| Manhattan   | 15     | Falcon r4H| 32 | Honeycomb |

A. VQA Problem Settings

We comprehensively evaluate EQC using the Variational Quantum Eigensolver optimization problem, as well as the
Quantum Approximate Optimization Algorithm. The experimental setup is illustrated in Figure 7. We use up to 10 IBMQ machines in deploying and evaluating our system, listed in Table I. In total, our evaluations for this work comprise ~500,000 circuit executions on IBMQ devices.

B. Variational Quantum Eigensolver Evaluation

The target problem is an application to quantum magnetism, whereby we perform the minimization of a 4-qubit Heisenberg model Hamiltonian on a square lattice [22]. The Hamiltonian is described by:

$$ H = J \sum_{i,j} (X_iX_j + Y_iY_j + Z_iZ_j) + B \sum_i Z_i $$

where $J$ refers to the spin-spin strength and $B$ is the magnetic field along the Z-axis. We evaluate the Hamiltonian that has $J/B = J = B = 1$, the same experiment as already demonstrated in [22], which provides a reliable baseline (i.e., ground energy) for verifying the convergence. The 4-qubit square lattice results can be represented as a graph with $V = \{1, 2, 3, 4\}$ and edges $E = \{(1, 2), (2, 3), (3, 4), (1, 4)\}$. We make use of a hardware-efficient ansatz comprised of a linearly entangled full-bloch sphere rotation style circuit. The parameterized circuit is visualized in Figure 8 composed of

$$ R_\alpha(\theta) R_\beta(\theta) $$

and

$$ \sum_i Z_i $$

where

$$ \theta^{t+1} = \theta^t - P_{\text{Correct}} \alpha g^* (\theta^t) $$

C. VQE Unweighted Performance Evaluation

We show how EQC can contribute to noise-mitigation and machine-bias mitigation, while gaining significant performance improvement over single-machine optimization. The primary results are shown in Figure 6. We show the optimization process of minimizing the system energy as described in Equation 3 for over 250 epochs. The ideal learnt ground state is illustrated at $-4.0$ a.u. (as validated in [22]). We further illustrate the average single machine speed in comparison to EQC. We evaluate the optimization of our system on an
ideal quantum simulator using 8192 shots with an average convergence point of 80 epochs over 10 experiments. This serves as the baseline as plotted in Figure 6. Notably, the standard deviation is plotted, however the value is negligible and hence is difficult to see on the plot. We further run the optimization problem once over 6 single IBMQ machines, each attempting to train the system on their own independent of each other. The optimization task is run once on single machines due to severe time costs of single-machine VQE training.

Overall, IBMQ x2 is shown to be the slowest at converging with an approximate convergence at 175 epochs, a 118.75% increase in the number of epochs to converge. The relatively slow convergence is attributed to IBMQ x2’s older topological architecture and high degree of crosstalk, therefore being substantially more error prone. In comparison, for IBMQ Bogota, we observe convergence substantially quicker at epoch 122, 52.5% slower than an ideal simulator. Certain experiments were infeasible to complete, where in IBMQ Manhattans, running a 250-epoch 16-parameter VQE requires at the order of months to train, with our current expectation being 193 days. Similarly, Santiago would be on the order of weeks, with our approximation being 21 days. These numbers are also susceptible to large time-dependent fluctuations, with Toronto fluctuating from 6.553 epochs per hour to 0.033 epochs per hour over our entire training period.

Due to these large swings in performance, we terminate certain experiments which had not finished after 2 weeks of training, as seen in Figure 6. IBMQ Casablanca was substantially more performant at an average performance of 6,767 epochs per hour, hence only a run time of 36.944 hours, and converging at epoch 130. Although Casablanca was most performant initially in the epochs between 0 and 130, we observe the machine becoming unstable until epoch 215 where it stabilizes back at the ground energy. This is an example of time dependent drift whereby the machine becomes noisier over time, and the learned parameters are biased, compensating for this noise, and avoiding the actual optimal solution.

As demonstrated, two key factors that are observed in a single machine training; machine throughput and machine stability, which are highly variable with time.

We evaluate the same problem on EQC, as represented in Figure 6. We observe an average speed of 46.701 epochs per hour when using our system over 10 IBMQ machines. The fastest IBMQ machine is IBMQ x2, performs at a rate of 9.011 epochs per hour. With EQC providing a worst-case improvement of 518%. We train our system on EQC 3 times and illustrate the standard deviation at each epoch on Figure 6. EQC converges at epoch 135, 69% slower than an ideal simulator. This is substantially better than the noisiest machine IBMQ x2, which also is the fastest machine and hence contributed the largest throughput to EQC. One of our key insights that we demonstrate is the ensemble noise mitigation and machine-bias mitigation of EQC. With EQC being an ensemble of QPU’s each with their own machine specific noise profile the problem of large machine-induced bias is dampened inherently through the mixture, i.e., an ensemble of averaged noise.

Quantum processors tend to have time dependent drift from the ideal parameters. For example, Casablanca converges the fastest, however drifts from the ideal solution after convergence, as shown in Figure 6. This dampening is demonstrated by the final average converged energy of each system. With an ideal ground energy of −4.0, the error of a system is the average of the obtained ground energy divided by the ideal ground energy. The deviation from ground state error is visualized in Figure 7. As can be seen, EQC attains an error rate of 0.379%, whereas IBMQ x2 attains an error rate of 1.798%, Bogota 0.865%, and Casablanca 4.6%. We can see that EQC attains the closest result to the ideal solution when compared with training on single machines, implying that the ensemble can mitigate device-specific noise.

D. VQE Client Node Weighting Evaluation

We evaluate EQC against two different weighting strategies, and compare them to baseline (i.e., no weighting) and the ideal simulation result. As shown in Figure 8, the application of our weighting model can allow for an improvement in the rate of convergence. The weighting system operates by taking the maximum , and the minimum , and linearly normalizing the values to the set weight bound. As a concrete example, to weight a system of values between 0.5 – 1.5, the values over all client nodes are normalized and shifted +0.5. In the case of a modest scaling factor between 0.5 – 1.5, we observe a convergence at 115 epochs, against the regular 140 epochs, resulting in a 17.858% speedup. In the case of 0.25 – 1.75, the speedup is even further improved, converging at the same time as the ideal solution at 80 epochs, a 75% speedup. In evaluating 0.75 – 1.25 we notice an improvement in convergence when comparing with 0.5 – 1.5 and no weights, with convergence at 105 epochs. With respect to converged error rate, we plot this information within the sub figure of Figure 8. As illustrated, utilizing a weighting system of 0.25-1.75 increases error by 0.33%. However, utilizing...
weights of $0.50 - 1.50$ allowed for the system to converge 0.49% closer to the ground energy, and 0.37% closer with weights $0.75 - 1.25$. Increasing range of weights allows for larger steps in parameter updates, which can cause problems of converging on local-minima due to step size being too large. Continuing the discussion on machine-bias mitigation, the introduction of weighting further improves the ability of our system to dampen quantum machine noise at real time. Given that we calculate our weight based on noise statistics, any time dependent drift will lower the impact of the quantum machine. Any machine that tends away from being performant will be trusted less and less, until it is calibrated back to a better noise standard. This benefit is very similar to that of boosting in classical machine learning, where better results are amplified and gradually dominate.

### E. Quantum Approximate Optimization Algorithm

In this section, we demonstrate the Quantum Approximate Optimization Algorithm (QAOA) using a MaxCut problem. QAOA is a quantum algorithm that is used for approximating solutions for combinatorial optimization problems, e.g., MaxCut. In our example, we demonstrate the MaxCut problem over the same graph structure discussed prior with $V = \{1, 2, 3, 4\}$ and $E = \{(1, 2), (2, 3), (3, 4), (1, 4)\}$. Given a graph $G(V, E)$, where $|V| = n$ and with undirected edge weights of $w_{i,j} > 0$, the MaxCut problem optimizes towards the partitioning nodes into two sets, such that the number of edges $E$ connecting two nodes $V_i$ and $V_j$ from different subsets is maximized. This is formalized in the characteristic Equation [5]

$$C(x) = \sum_{i,j} w_{i,j} x_i (1 - x_j)$$

(5)

Where, in Equation [5], $x_i$ is the group of node $i$, and $x_j$ is the group of node $j$. Translating this optimization problem to a diagonal Hamiltonian such that it can be optimized via variational quantum algorithms, it can be mapped as a sum over the edge set $E$.

$$C(x) = \sum_{(i,j) \in E} (x_i (1 - x_j) + x_j (1 - x_i))$$

(6)

Equation [6] is mapped to a spin Hamiltonian, where $x_i = \frac{1-Z_i}{2}$, as well as swapped from a maximization to a minimization problem, resulting in Equation [7]

$$H = - \sum_{j,k \in E} \frac{1}{2} (1 - Z_j Z_k)$$

(7)

We employ this optimization problem over the graph discussed prior on the IBMQ machines Toronto, Santiago, Quito, Lima, Casablanca, Bogota, Manila, and Belem both independently and utilizing the EQC framework. The circuit trained is illustrated in Figure 10 and is motivated by the design outlined in the original QAOA paper [15]. In this circuit, a total of 2 parameters are to be optimized, whilst making use of 8 asynchronous systems. In this experiment, we demonstrate the ability of our system to converge with reduced ground state errors at a substantially higher training performance over single machines, highlighting our noise dampening and system throughput contribution.

![4-Qubit Trainable Circuit](image)

**Fig. 10.** 4-Qubit QAOA Circuit. Connected dots represent ZZ gates parameterized by $\beta$. $R_x$ represent rotations around the X-axis by parameter $\alpha$. $H$ represents Hadamard gates. M gates represent measurements.

We illustrate the optimization process in Figure 11 EQC converges under similar iterations when compared with the independent quantum processors. Due to experiment run times, there is variation in day to day performance, which can lead to difficulty in directly comparing systems. As a concrete example, Toronto trains over multiple days, with multiple calibration cycles, whereas Belem can complete training in one hour. EQC performs at a speed of 135,510.2% faster than the slowest IBMQ machine and 322.4% faster than the fastest machine. With respect to converged performance with system weighting, we compare the best solutions presented by our models in Figure 12. As can be seen, by applying a weighting system based on system quality, the model converges quicker than without, and to a lower final MaxCut cost. Without weighting, EQC attains convergence second to worst, only beating out IBMQ Casablanca. However, by applying our weighting schema, EQC is able to attain solutions close to that of the top 3 IBMQ machines and outperform many worse machines. Weighting approaches improved solutions by 2.863% for a 0.5-1.5 weighting, and 2.343% for a 0.25-1.75 weighting. In comparison, using the best machine of IBMQ Quito was a 4.786% improvement over unweighted EQC. This evidence acts as a strong support for EQC in bolstering distributed quantum computing performance with system-calibration based management.

![4-Node Unweighted Graph MaxCut Optimization](image)

**Fig. 11.** 4-Node Unweighted Graph MaxCut Optimization. Shaded regions represent error over 3 experiment runs. Speed represents system data throughput. Graph illustrates comparison between IBMQ independent machines and an unweighted EQC system.
VI. DISCUSSION

EQC has proven to provide a substantial speedup in training variational quantum routines whilst providing a framework for real time noise dampening. By viewing multiple QPUs as an ensemble, we are able to weight QPUs with lower confidence, when they are trapped in a noisy condition, and increase the confidence when they are recovered or calibrated. A good example is the training of VQE on IBMQ-Casablanca. IBMQ-Casablanca was able to train faster than any other QPUs on IBMQ, as shown in Figure 6. However, it diverges after being converged for about 20 epochs due to the increasingly deleterious running condition of the machine. For single machine execution, such an impact is inevitable. However, in an ensemble situation, the noise would be recognized and dampened, thereby mitigating this problem. This fluctuating condition of QPU performance highlights the necessity of our ensemble-method for today’s erroneous and noisy NISQ devices. Additionally, with our weighting system further applied, it is possible to ensemble many noisy quantum machines alongside a few well-behaved QPUs to create a virtualized quantum device with both high-fidelity and high-performance.

VII. RELATED WORK

Understanding quantum device running behavior and mitigating the errors through algorithms, compilation and architectural approaches is undoubtedly necessary for the success of QC in the NISQ era.

Regarding the characterization of NISQ devices, Patel et al. [32] profile the error and execution time of the IBMQ NISQ devices through benchmarkings and draw nine interesting observations. Cross et al. [11] define “quantum volume”, which quantifies the largest random circuit of equal width and depth that a NISQ device can successfully implement, concerning both gate and SPAM errors. Murali et al. [29] characterize the crosstalk noise of IBMQ devices through random benchmarking, and propose compiler techniques to mitigate the crosstalk impact. Sun and Geller [40] characterize the SPAM error of IBMQ and Rigetti platforms.

Regarding the error mitigation techniques, Tannu et al. [43] observe that the same qubit mapping is often reused for all circuit execution trials, leading to correlated error associated with a certain mapping being amplified to give the incorrect results. Motivated by the concept of diversity, they propose an ensemble of diverse mappings (EDM) for different trials of QC execution to avoid the dominance of single error type. Our work is analogous in that we also attempt to alleviate NISQ error through mixture. However, we focus on device-specific bias, and leverage multi-devices simultaneously for VQA training, significantly speedup the execution. Patel and Tiwari [33] focus on estimating correct output from raw erroneous outputs of the NISQ devices through statistical methods. Comparatively, we propose a weighting system to regularize the gradient from the multiple backends based on their device properties and runtime conditions.

Regarding the acceleration and scaling of NISQ-based QC, Li et al. [24] focus on VQE, and present an application-compiler-hardware codesign for QC program compression with little loss of accuracy. Additionally, they tailor the compiler and architecture according to the problem’s Pauli strings, greatly reducing the execution overhead. Patel et al. [33] proposes VERITAS, a distributed quantum computing framework for estimating correct outputs of quantum routines, which can provide substantially useful to single-output-distribution algorithms or tasks on quantum computers. Tang et al. [42] introduce CutQC, which can decompose a large quantum circuit into several pieces which can then be evaluated on small-scale NISQ devices. The probability distribution of the subcircuits are collected for reconstructing the original distribution. Gokhale et al. [18] aim at accelerating pulse generation for VQA. They propose to pregenerate the optimal pulses for certain blocks of gate tailored towards VQA circuits, serving as partial compilation for accelerating the
classical GRAPE \cite{17} based machine control pulse generation. Britt and Humble \cite{5} investigate the potential architectural design choices, such as CPU/QPU interconnection, for QPU-integrated HPC. Finally, Das et al. \cite{13} propose to multiprogram the same NISQ devices for enhancing NISQ device utilization. In order to mitigate the possible interference, three techniques are presented: picking up the reliable regions; unifying the circuit length; monitoring the reliability and disable multi-programming if necessary. All these techniques, however, are perpendicular to EQC and can be integrated to further mitigate NISQ error, promote utilization rate, and accelerate VQA training speed. For example, if an advanced device (e.g. IBMQ Toronto) can sustain more than one VQA circuit simultaneously, multiple jobs can be distributed to the same backend device for co-execution, further improving the training speed and system utilization.

VIII. CONCLUSIONS

The field of adaptive ensembled quantum computing has not been tackled in literature to the best of our knowledge. We present the first framework for adaptive quantum ensemble training speed and system utilization. For example, if an advanced device such as CPU/QPU interconnection, further improving the training speed and system utilization.

APPENDIX: CONVERGENCE PROOOF

To show that EQC can converge, we first make the following assumptions:

(I): The loss function \( \ell \) must be a differentiable function with \( \ell : \mathbb{R}^d \rightarrow \mathbb{R} \), based on the expectation values of observables \( \langle O \rangle \) from the quantum routine; (II): The application circuit must be in some part comprised of variational gates with some measurements performed, which means

\[
\ell(\bar{\theta}) = \ell(\langle \psi(\bar{\theta}) | O_1 | \psi(\bar{\theta}) \rangle, \ldots, \langle \psi(\bar{\theta}) | O_m | \psi(\bar{\theta}) \rangle)
\]

for quantum states \( | \psi(\bar{\theta}) \rangle \) that are produced using the variational circuit for parameters \( \bar{\theta} \in \mathbb{R}^d \) from a fiducial input state \( | 0 \rangle \); (III): The loss function should be easily computable on a classical computer, and we assume its cost is negligible compared to the time required to estimate \( \langle \psi(\bar{\theta}) | O_i | \psi(\bar{\theta}) \rangle \).

Under these assumptions, we show that our asynchronous EQC system can converge. This proof is motivated by existing works \cite{30, 41}. If the system is parameterized by \( \bar{\theta} \), \( \theta \subseteq \mathbb{R}^d \), and some cost function \( \ell : \mathbb{R}^d \rightarrow \mathbb{R} \), and optimized through stochastic gradient descent rule:

\[
\theta_{t+1}^i = \theta_t^i - \alpha g_t^i(\theta_t^i)
\]

where \( g^i \) is an unbiased gradient estimator for the variable \( \theta_t^i \), and hence \( \mathbb{E}(g_t^i(\theta_t^i)) = \nabla \ell(\theta_t^i) \). Let’s assume \( \ell \) is based on a classical observable taking the form of:

\[
\ell(\langle O_1 | \theta \rangle, \langle O_2 | \theta \rangle, \ldots, \langle O_n | \theta \rangle)
\]

where \( \langle O_j | \theta \rangle \) is the expected value of \( O_j \) generated from the circuit parameterized by \( \theta \). Without losing generality, we assume that for all parameters in \( \theta \), the unbiased estimator of the partial derivative with respect to \( \theta_i \) is calculated using the parameter shift rule \( \frac{\partial}{\partial \theta_i} \ell = r(\theta_i + \frac{\epsilon}{\sqrt{n}}, \ell(\theta_i + \frac{\epsilon}{\sqrt{n}})) \) where for common operators such as \( R_X, R_Y, R_Z, r = 1/2 \). We assume that the system is measuring in a basis comprised of two orthonormal eigenvectors as required by the parameter shift rule, e.g., measuring against \( | 0 \rangle \) and \( | 1 \rangle \).

Since evaluating the exact expectation values from quantum applications becomes rapidly infeasible, we use the sampling mean estimator \( \langle o_i \rangle \) of the output \( \langle O_i \rangle \). We assume sufficient samples are taken such that \( \langle o_i \rangle = \langle O_i \rangle \), and can be used as an unbiased estimator.

Given that we will operate on each parameter \( \epsilon \), and that the probability that parameter \( \theta_i \) fails to be differentiated at any point is \( p \), the probability for a sequence of successes and failures can be expressed as:

\[
P_{\text{Setting}} = (1 - p)^{\tau - n} p^n
\]

Based on this, the probability of all possible combinations of failures and successes with a set number of failures is:

\[
P_{n-\text{failures}} = \frac{\epsilon!}{(1 - \epsilon)^n!} (1 - p)^{\tau - n} p^n
\]

The probability of the parameter failing \( n \) times or less is:

\[
P = \sum_{j=1}^{n} \frac{\epsilon!}{(1 - \epsilon)^j!} (1 - p)^{\tau - j} (p^j)
\]

If \( p \) is reasonably small, and \( \epsilon \) is sufficiently large as in our case, we can see that with \( p \rightarrow 0, \epsilon \rightarrow \infty \), \( P \rightarrow 0 \).

We now discuss the asynchronous SGD. In our case, given the significant difference in QPU execution time, we need to rely on the asynchronous gradient descent update rule:

\[
\theta_{t+1}^i = \theta_t^i - \alpha g^i(\theta_t^i)
\]

where \( \tau \leq t \) and \( t - \tau \) is equivalent to the time delay of the gradient. We assume the stepsize \( \alpha \) is non-increasing, and the time delay \( t - \tau \) is bounded by some unknown positive integer \( D \) such that the model is only a partially asynchronous method \cite{5}. Consequently, the EQC optimization process follows equal distribution of parameter optimization, as all of the parameters are optimized an equal number of times by cycling over each parameter sequentially, where the system operates on each parameter \( 1, 2, \ldots, m \) and repeats starting from 1 again after operating on \( m \). Furthermore, given a sequence obtained by sampling from the cyclic sequence, we denote the sequence used in calculating the updated parameters as \( \pi(t) \). Therefore, for some positive integer \( T \), we maintain the validity of \( | \pi(t) - t | \leq T \) for \( \forall t = 1, 2, \ldots \) (i.e. the asynchronous nature of the system.)

Let’s assume the gradients are bound by a limit of \( C \) which means \( ||g|| \leq C \), \( \forall g \leq \frac{\theta}{\sqrt{n}} \). Therefore, the expectations of quantum systems are strictly bound by \( 0 \leq \langle O \rangle \leq 1 \) and requiring \( \ell(\langle O \rangle) \) to be bound, hence \( ||g|| \) is bounded. We further argue that the difference between updated parameters calculated from time \( \tau \) on parameters at time \( t \) is bounded
by $D$ such that $|t - \tau| \leq D$, $\forall t = 0, 1, 2, \ldots$, etc. Then with respect to [30], it can be shown that, if $\ell^* = -\infty$,

$$\lim_{t \to \infty} \ell(t) = -\infty$$

(13)

otherwise, if $\ell^*$ is finite:

$$\lim_{t \to \infty} \ell(t) = \ell^* + mC^2 \left(\frac{1}{2} + m + 2D + T\right)\alpha$$

(14)

where $\ell^*$ is the optimal solution to the objective function, which demonstrates the convergence.

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REFERENCES

[1] F. Arute, K. Arya, R. Babbush, D. Bacon, J. C. Bardin, R. Barends, R. Biswas, S. Boixo, F. G. Brandao, D. A. Buell et al., "Quantum supremacy using a programmable superconducting processor," Nature, vol. 574, no. 7779, pp. 505–510, 2019.

[2] F. Arute, K. Arya, R. Babbush, D. Bacon, J. C. Bardin, R. Barends, S. Boixo, M. Broughton, B. B. Buckley, D. A. Buell et al., "Hartree-Fock on a superconducting qubit quantum computer," Science, vol. 369, no. 6507, pp. 1084–1089, 2020.

[3] D. P. Bertsekas and J. N. Tsitsiklis, Parallel and distributed computation: numerical methods. Prentice-Hall, Inc., 1989.

[4] J. Biamonte, P. Wittek, N. Pancotti, P. Rebentrost, N. Wiebe, and S. Lloyd, "Quantum machine learning," Nature, vol. 549, no. 7671, pp. 195–202, 2017.

[5] K. A. Britt and T. S. Humble, "High-performance computing with quantum processing units," ACM Journal on Emerging Technologies in Computing Systems (JETC), vol. 13, no. 3, pp. 1–13, 2017.

[6] T. B. Brown, B. Mann, N. Ryder, M. Subbiah, J. Kaplan, P. Dhariwal, A. Neelakantan, P. Shyam, G. Sastry, A. Askell et al., "Language models are few-shot learners," arXiv preprint arXiv:2005.14165, 2020.

[7] Y. Cao, J. Romero, J. P. Olson, M. Degroote, P. D. Johnson, T. B. Brown, B. Mann, N. Ryder, M. Subbiah, J. Kaplan, P. Dhariwal, Y. Shi, D. I. Schuster, H. Hoffmann, and F. T. Chong, "Partial compilation of variational algorithms for noisy intermediate-scale quantum machines," in Proceedings of the 52nd Annual IEEE/ACM International Symposium on Microarchitecture, 2019, pp. 266–278.

[8] D. Castelvecchi, "Ibm’s quantum cloud computer goes commercial," Nature, vol. 543, no. 7644, p. 159, 2017.

[9] S. J. Glaser, U. Boscain, T. Carlarco, C. Emary, and P. Zuliani, "Modelling and simulating the noisy behaviour of near-term quantum computers," arXiv preprint arXiv:2101.02109, 2021.

[10] A. W. Cross, L. S. Bishop, S. Sheldon, P. D. Nation, and J. M. Gambetta, "Validating quantum computers using randomized model circuits," Physical Review A, vol. 100, no. 3, p. 032328, 2019.

[11] A. W. Cross, L. S. Bishop, J. A. Smolin, and J. M. Gambetta, "Open quantum assembly language," arXiv preprint arXiv:1707.03429, 2017.

[12] A. W. Cross, L. S. Bishop, S. Sheldon, P. D. Nation, and J. M. Gambetta, "Variational quantum state eigensolver," arXiv preprint arXiv:2004.01372, 2020.

[13] G. E. Crooks, "Performance of the quantum approximate optimization algorithm on the maximum cut problem," arXiv preprint arXiv:1811.08419, 2018.

[14] J. Biamonte, P. Wittek, N. Pancotti, P. Rebentrost, N. Wiebe, and S. Lloyd, "Quantum machine learning," Nature, vol. 549, no. 7671, pp. 195–202, 2017.

[15] K. A. Britt and T. S. Humble, "High-performance computing with quantum processing units," ACM Journal on Emerging Technologies in Computing Systems (JETC), vol. 13, no. 3, pp. 1–13, 2017.

[16] K. Georgopoulos, C. Emary, and P. Zuliani, "Modelling and simulating the noisy behaviour of near-term quantum computers," arXiv preprint arXiv:2101.02109, 2021.

[17] S. J. Glaser, U. Boscain, T. Carlarco, C. Emary, and P. Zuliani, "Modelling and simulating the noisy behaviour of near-term quantum computers," arXiv preprint arXiv:2101.02109, 2021.

[18] P. Gokhale, Y. Ding, T. Propson, C. Winkler, N. Leung, Y. Shi, D. I. Schuster, H. Hoffmann, and F. T. Chong, "Partial compilation of variational algorithms for noisy intermediate-scale quantum machines," in Proceedings of the 52nd Annual IEEE/ACM International Symposium on Microarchitecture, 2019, pp. 266–278.

[19] K. E. Hamilton, T. Kharazi, T. Morris, A. J. McCaskey, R. S. Bennink, and R. C. Poozer, "Scalable quantum processor noise characterization," in 2020 IEEE International Conference on Quantum Computing and Engineering (QCE). IEEE, 2020, pp. 430–440.

[20] W. Jiang, J. Xiong, and Y. Shi, "A co-design framework of neural networks and quantum circuits towards quantum advantage," Nature communications, vol. 12, no. 1, pp. 1–13, 2021.

[21] A. Krizhevsky, I. Sutskever, and G. E. Hinton, "Imagenet classification with deep convolutional neural networks," Advances in neural information processing systems, vol. 25, pp. 1097–1105, 2012.

[22] G. Li, Y. Shi, and A. Javadi-Abhari, "Software-hardware co-optimization for computational chemistry on superconducting quantum processors," arXiv preprint arXiv:2105.07127, 2021.

[23] H.-H. Lu, N. Kloc, J. M. Lukens, T. D. Morris, A. Bansal, A. Ekstrøm, G. Hagen, T. Papenbrock, A. M. Weiner, M. J. Savage et al., "Simulations of subatomic many-body physics on a quantum frequency processor," Physical Review A, vol. 100, no. 1, p. 012320, 2019.

[24] P. Mannava, "An overview of cloud computing and deployment of big data analytics in the cloud," International Journal of Scientific Research in Science, Engineering, and Technology (IJSRSET), Online ISSN, pp. 2394–4099, 2014.

[25] P. Moritz, R. Nishihara, S. Wang, A. Tumanov, R. Liaw, E. Liang, M. Elibol, Z. Yang, W. Paul, M. I. Jordan et al., "Ray: A distributed framework for emerging [AI] applications," in 13th USENIX Symposium on Operating Systems Design and Implementation (OSDI'18), 2018, pp. 561–577.

[26] P. Murali, N. M. Linke, M. Martonosi, A. J. Abhari, N. H. Nguyen, and C. H. Alderete, "Full-stack, real-system quantum computer studies: Architectural comparisons and design insights," in 2019 ACM/IEEE 46th Annual International Symposium on Computer Architecture (ISCA). IEEE, 2019, pp. 527–540.

[27] P. Murali, D. C. McKay, M. Martonosi, and A. Javadi-Abhari, "Software mitigation of crosstalk on noisy intermediate-scale quantum computers," in Proceedings of the Twenty-Fifth International Conference on Architectural Support for Programming Languages and Operating Systems, 2020, pp. 1001–1016.

[28] A. Nedić, D. P. Bertsekas, and V. S. Borkar, "Distributed asynchronous incremental subgradient methods," Studies in Computational Mathematics, vol. 9, no. C, pp. 381–407, 2000.

[29] J. L. Orrell and B. Loer, "Sensor-assisted fault mitigation in quantum computation," arXiv preprint arXiv:2012.12423, 2020.

[30] T. Patel, A. Potharaju, B. Li, R. B. Roy, and D. Tiwari, "Experimental evaluation of nisq quantum computers: error measurement, characterization, and implications," in SC20: International Conference for High Performance Computing, Networking, Storage and Analysis. IEEE, 2020, pp. 1–15.
[33] T. Patel and D. Tiwari, “Veritas: accurately estimating the correct output on noisy intermediate-scale quantum computers,” in SC20: International Conference for High Performance Computing, Networking, Storage and Analysis. IEEE, 2020, pp. 1–16.

[34] A. Peruzzo et al., “A variational eigenvalue solver on a quantum processor. eprint,” arXiv preprint arXiv:1304.3061, 2013.

[35] A. Roggero, A. C. Li, J. Carlson, R. Gupta, and G. N. Perdue, “Quantum computing for neutrino-nucleus scattering,” Physical Review D, vol. 101, no. 7, p. 074038, 2020.

[36] M. Schuld, I. Sinayskiy, and F. Petruccione, “The quest for a quantum neural network,” Quantum Information Processing, vol. 13, no. 11, pp. 2567–2586, 2014.

[37] S. Sheldon, E. Magesan, J. M. Chow, and J. M. Gambetta, “Procedure for systematically tuning up cross-talk in the cross-resonance gate,” Physical Review A, vol. 93, no. 6, p. 060302, 2016.

[38] P. W. Shor, “Polynomial-time algorithms for prime factorization and discrete logarithms on a quantum computer,” SIAM review, vol. 41, no. 2, pp. 303–332, 1999.

[39] S. A. Stein, B. Baheri, R. M. Tischio, Y. Mao, Q. Guan, A. Li, B. Fang, and S. Xu, “Qugan: A generative adversarial network through quantum states,” arXiv preprint arXiv:2010.09036, 2020.

[40] M. Sun and M. R. Geller, “Efficient characterization of correlated spam errors,” arXiv preprint arXiv:1810.10523, 2018.

[41] R. Sweke, F. Wilde, J. Meyer, M. Schuld, P. K. Fährmann, B. Meynard-Piganeau, and J. Eisert, “Stochastic gradient descent for hybrid quantum-classical optimization,” Quantum, vol. 4, p. 314, 2020.

[42] W. Tang, T. Tomesh, M. Suchara, J. Larson, and M. Martonosi, “Cutqc: using small quantum computers for large quantum circuit evaluations,” in Proceedings of the 26th ACM International Conference on Architectural Support for Programming Languages and Operating Systems, 2021, pp. 473–486.

[43] S. S. Tannu and M. Qureshi, “Ensemble of diverse mappings: Improving reliability of quantum computers by orchestrating dissimilar mistakes,” in Proceedings of the 52nd Annual IEEE/ACM International Symposium on Microarchitecture, 2019, pp. 253–265.

[44] X. Yuan, S. Endo, Q. Zhao, Y. Li, and S. C. Benjamin, “Theory of variational quantum simulation,” Quantum, vol. 3, p. 191, 2019.

[45] S. Zhang, C. Zhang, Z. You, R. Zheng, and B. Xu, “Asynchronous stochastic gradient descent for dnn training,” in 2013 IEEE International Conference on Acoustics, Speech and Signal Processing. IEEE, 2013, pp. 6660–6663.