Virtual Flipped Class and Laboratories for Medical Electronics Course

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Abstract—This paper describes the adaptation of a flipped Biomedical Electronics course with laboratories to remote learning at the start of the Covid-19 pandemic. In class collaborative work on problem sets was replaced by group work (4–5 students) in Zoom breakout sessions. When the groups assembled at random for each class had sufficiently progressed on a problem, a detailed solution was typed on the Multisim circuit simulator desktop (National Instruments) shared on the instructor screen. A laboratory project dealing with the development of an electromyograph (EMG) was redesigned for in-depth exploration of each circuit block composing the EMG circuit that was only feasible with the circuit simulator. The students progressed through the remote section of the course at the same rate as they had in the physical classroom in prior years. Student pairs finished a more complete virtual EMG laboratory project without being hampered by manipulation errors that are typical of novices when assembling hardware circuits. We conclude that a flipped biomedical electronics course can successfully be offered remotely and that virtual electronics laboratories that make use of a circuit simulator can provide a complete and meaningful learning experience.

Keywords—Remote learning, Flipped classroom, Electronics laboratories, Circuit simulator.

BACKGROUND

The Medical Electronics course at our institution is organized as a flipped class (two 80-min sessions/week) with design-based laboratories (one 3-h lab/week—morning or afternoon times) in which junior and senior students analyze, design, assemble, and test prototypes of medical instruments. Online video lessons posted on the learning management system (LMS) Blackboard present methods of analysis and design of fundamental electronic circuits encountered in these instruments, e.g. amplifiers assembled with operational amplifiers, active filters, comparators, diode circuits, and transistor switches. In class, students practice in small groups analyzing variants of these circuits.

The labs are mainly devoted to two multi-week projects (dual-supply electrocardiograph amplifier and single-supply electromyograph amplifier) divided into self-contained functional blocks which are based on circuits studied in class. Students design and implement a functional block each week to satisfy specified requirements and use as reference documentation application notes and professional tutorials. Preparation for the labs includes developing and testing a model of each circuit block with the simulation software Multisim (National Instruments). In lab, student pairs assemble and test the circuit block on a breadboard and compare its experimental performance with design requirements and predictions of the simulation model. Students spend a considerable fraction of each lab identifying and correcting manipulation errors: using a component with the wrong value or the wrong component, wiring errors, etc. Locating these errors is complicated by sloppy wiring habits, which take considerable time to remedy in spite of a tutorial all students study before the first lab and repeated recommendations from the instructor and teaching assistants.

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**CHALLENGE STATEMENT**

The challenge was to reorganize the last six weeks of the course for remote operation while keeping the essential features and processes described above which the students had become accustomed to during the semester’s first nine weeks.

**Novel Initiative**

Addressing the challenge required reorganizing the classroom activities so they could be attended remotely and reformating the labs such that students could design and validate their circuits exclusively with the Multisim circuit simulator (online appendix). All Zoom meetings were scheduled at the regular classroom and laboratory times.

Ahead of each remote class, problem sets were posted on the LMS both as a static document and a Multisim file which comprised the circuits the students would analyze together. Class started with a 5-min summary of the video lesson followed by a brief question-and-answer period. Thereafter, students were randomly grouped into Zoom breakout sessions (4–5 students/session) to work on each problem. The instructor and teaching assistant roamed the breakout sessions with the lab partner who they had chosen component values and the response’s characteristics within certain tolerance ranges. With the whole class, the frequency response of the filter was simulated using Multisim’s simulation tools (voltage and current probes, oscilloscope, etc.) which allowed the instructor to point out differences between simple models used to solve the problems by hand and more complex models used in the simulator. Multisim was also used to validate the solutions of design problems. For instance, student groups designed active filters by hand using established algorithms and chose component values within certain tolerance ranges. With the whole class, the frequency response of the filter was simulated using students’ values and the response’s characteristics (critical frequency, roll-off rate, passband gain) were compared to the design requirements. After each class, the solutions and a link to the Zoom recording of the session were posted on the LMS for review.

The transition to remote learning occurred just prior to starting the electromyograph (EMG) amplifier project which the students develop from documenta-
### TABLE 1. The single-supply electromyograph (EMG) design was divided into 5 functional blocks and the students developed one functional block each week, which they tested first individually and then connected to the previously-developed functional blocks.

| Week 1 - functional block 1: 5V dc single-supply instrumentation amplifier and 2.5 V voltage reference | Circuit design novelty: none |
|---|---|
| N #1: Determine effect of input bias currents ($I_{bias}$) and input offset voltage ($V_{os}$) on amplifier dc output voltage ($V_{out}$) and input offset voltage ($V_{os}$) | T: Use 4 sets of ground return resistors at the amplifier's inputs and measure $V_{out}$ for each set |
| N #2: Experimental amplifier frequency response | A: Use resistor and $V_{out}$ values to compute $I_{bias}$ and $V_{os}$. Compare to nominal values from amplifier data sheet |

| Week 2 - functional block 2: signal conditioning with high-pass and low-pass filters | Circuit design novelty: design 4-pole Chebyshev low-pass filter with Webench filter software design tool (Texas Instruments) |
|---|---|
| N #1: visualize effect of component tolerances on filter frequency response | T: "Monte-Carlo" simulation to generate 20 frequency response curves for randomly selected passive components within tolerances (10% resistors, 20% capacitors) |
| N #2: measure time-domain delay through filters | A: measure highest and lowest values of critical frequencies. Compare with theoretical values determined from filter design |

| Week 3 - functional block 3: Single-supply non-inverting op-amp amplifier, precision rectifier, EMG envelope circuit | Circuit design novelty: design of bandwidth limited op-amp amplifier to extract EMG envelope |
|---|---|
| N #1: measure amplifier frequency response for different gain values | T: "Monte-Carlo" simulation to generate 20 frequency response curves for randomly selected passive components within tolerances (10% resistors, 20% capacitors) |
| N #2: sensitivity of precision rectifier response to component values | T: use "interactive simulation" oscilloscope to measure time difference between input and output waveforms at critical frequencies |
| N #3: validation of EMG envelope detection circuit | A: relate experimental delays to theoretical filter group delays estimated with Webench |

| Week 4 - functional block 4: comparators with hysteresis to detect EMG crossing selected thresholds and energize transistor switches | Circuit design novelty: entire circuitry |
|---|---|
| N #1: design two comparators with hysteresis to detect EMG amplitude crossing pre-selected intensity thresholds | A: determine threshold voltages to theoretical predictions from comparator design calculations |

| Week 5 - functional block 5: optocoupler circuit to isolate electrically EMG amplifier front end | Circuit design novelty: entire circuitry |
|---|---|
| N #1: design and test voltage-to-current converter to control opto-coupler LED | T: apply DC interactive voltage source at comparator input and measure levels at which output switches from "low" to "high" |
| N #2: characterize experimentally current transfer ratio (CTR) of opto-coupler component | A: relate "threshold" voltages to theoretical predictions from comparator design calculations |

The virtual circuit design novelty in comparison to the hardware-based EMG project of previous years is indicated below the block description. "A" refers to the analysis the students completed on the test data.

1. **Zoom recordings**, and (3) augmenting the circuit analyses using the capabilities of Multisim.

Random grouping of the students ensured that on average, students with different ability levels worked together, which was different from patterns observed in the physical classroom. In the latter, juniors and seniors usually worked in separate groups. The stronger students tended to assemble together, resulting in groups of weaker students. This pattern has been previously reported for cooperative learning groups.

Student groups with heterogeneous levels of ability have been found to be more successful in terms of performance and student satisfaction. Low-ability students perform better when included in heteroge-
neous groups which also benefit high-ability students as they practice verbalizing in different ways their approach to solving the problem sets. New student groups were formed at random for each class in this first attempt. It has been recommended to keep stable groups for a month or longer to build group cohesiveness and student interdependence. For future offerings, whether online or in person, instructor-formed heterogeneous student groups will be used and groups will be reshuffled at most a few times in the semester.

Producing a record of all classroom activities (breakout sessions excluded) was effortless and benefited students who could not attend class synchronously. Recording all classroom activities may be difficult to implement in the physical setting without a special infrastructure. However, typing the main steps to reach the problems’ solutions on a permanent record with student input would be easy to implement and preferable to the ephemeral solution written on the classroom whiteboard or to posting instructor’s solutions prepared without student input. Systematic use of the circuit simulator opened new possibilities for computation and visualization which were implemented without breaking the flow of the classroom activities.

In the virtual labs, students took advantage of the capabilities of Multisim to explore empirically practical implementation issues in electronic circuits which are seldom investigated in hardware labs. These included examining the effects of parts tolerance on circuit behavior and the differences between realistic component models and their idealized counterparts. Circuit simulators have been found to be effective tools for teaching electronics and circuit analysis. Thus, to train students in debugging circuits, Hodge et al. developed a virtual environment in which circuits included random wiring errors and components with erroneous values. The students used virtual test instruments to analyze the circuit behavior and identify the faults. Campbell and colleagues investigated students who trained exclusively in physical electronics labs with students who trained in matched simulated labs (80% time) + physical labs (20% time). Student performance was assessed with a written test covering lab theory and skills and by measuring the amount of time needed to complete a new physical lab. Students who had trained in the hybrid format performed better on the written exam than those who only trained in the physical labs. Both groups took the same amount of time to complete the new lab, suggesting lab skills had at a minimum not been adversely affected by training mostly in the virtual environment. Hall divided freshman-level and junior-level electronic technology students at random to complete half of their electronics labs in a simulated environment and the other half using hardware. Post-lab testing demonstrated that the two groups learned the concepts presented in the labs equally well. Analysis of the students’ perceptions of their learning gains revealed that the freshmen felt they learned as much in either context with respect to theory and to troubleshooting skills. The juniors who had prior experience in physical labs thought they learned more in the latter setting and preferred it over the virtual labs. We surmise that students can learn equally well electronics in virtual labs and physical labs but that they learn different things. The simulated labs enable students to explore more deeply the operation of a circuit and issues of importance to engineers and designers such as the effect of parts tolerance on circuit performance. Hands-on troubleshooting skills that are useful for laboratory practice are better acquired in physical labs. When physical labs are not possible, many benefits can still be derived from simulated electronics laboratory experiments. Simulated experiments have been found to be useful and practical to teach other subjects relevant to biomedical engineering, including chemical reactor processes and human pathophysiology. Simulated experimental environments take time and resources to develop. We had the advantage that Multisim was already used in our course, and that, like other SPICE-based simulators, Multisim is relatively inexpensive and widely available.

The simulated EMG project included an additional functional block as compared to the hardware project used in previous years. Yet, all students completed the entire project, which rarely happened in the past. Assembling a hardware circuit from a schematic involves several cognitive tasks beyond validating the circuit design. Components symbols and pin locations must be converted into their physical counterparts. Component values must be translated from letters and numbers to colored bands (resistors) and inscriptions on integrated circuits. Connections on the schematic must be matched to holes on a solderless breadboard. Cognitive load theory asserts that concurrent tasks overload working memory in novices and slow down or reduce learning. The theory distinguishes between germane load which helps learning and extraneous load which makes learning more difficult or impossible. By removing the heavy cognitive effort associated with assembling the hardware, the simulated labs likely reduced cognitive load and enabled the students to work faster and complete an expanded version of the project from the design standpoint. Additional explorations of the circuit properties became possible. When the main cognitive task is to learn to assemble a hardware circuit, we suggest that cognitive load can be reduced by using software that displays a graphical representation of the parts wired on the breadboard.
such as Fritzing or the simulated breadboard of Multisim. Clear identification of the expected learning gains from laboratories can help define the germane load and result in deeper and more satisfying learning experiences for the students.

The course content covered in the six weeks of online learning was equivalent to that covered in the same time period in the physical classroom in prior years. This observation suggests that switching to online learning did not diminish the amount of course material that could be taught and learned. The course learning objectives include: (1) Describe electrical characteristics of transducers, diodes, integrated amplifiers, and bipolar junction transistors; (2) Analyze and design circuits built with these electronic components; (3) Design, implement, and validate electronic circuits using these components with laboratory tools; (4) Utilize a circuit simulation software to analyze and design electronic circuits.

Achievement of the first two objectives is assessed with written examinations (5–7 short quizzes, a midterm exam, and a comprehensive final exam) and can be accomplished in the same manner for content covered in the classroom or remotely. Each examination comprises objective questions that test the students on a learning outcome from one of 13 lessons covered in the semester by having the students solve a short problem and identify the correct answer among 5 choices. Examples of lesson learning outcomes include (1) determine the effects of the input offset voltage and input bias current of a “real” op-amp on the DC output voltage of an op-amp amplifier, (2) design Sallen-Key second-order low-pass and high-pass filters using filter tables, and (3) analyze and design inverting and non-inverting comparators with hysteresis. In the semester, about 80 objective questions are used to directly assess the students on the detailed lesson learning outcomes which in turn reflect the more global course learning objectives.

Course learning objectives (3) and (4) are assessed from the preparatory documents and laboratory reports submitted by the students prior to and after each laboratory. Each laboratory assignment lists several deliverables for the students to include in their reports. Examples include a wiring diagram with component values for a 4-pole low pass filter designed to achieve a specified critical frequency and response, and the analysis of the filter characteristics, including the experimental frequency response curve, critical frequency, and roll-off rate. The students’ reports are assessed with checklists that assign points to each deliverable listed in the assignment. While the design, implementation, and validation testing are different for simulated circuits and hardware-based circuits, comparable methods and tools are used for the design and testing, which the students should highlight in the deliverables they turn in. In the online laboratories, students learned in greater depth the use of the circuit simulation software Multisim which would be reflected in the list of expected deliverables included in the laboratory assignments and in the students’ write-ups.

In conclusion, the flipped class structure of our Medical Electronics course facilitated content delivery by means of virtual collaboration tools. Classroom activities completed online emulated the approach used in the physical classroom. Virtual labs took advantage of the capabilities of the circuit simulation software Multisim to enable students to experiment and investigate features of a complex electronics project that would have been difficult to undertake in physical labs.

**ELECTRONIC SUPPLEMENTARY MATERIAL**

The online version of this article (doi:https://doi.org/10.1007/s43683-020-00037-6) contains supplementary material, which is available to authorized users.

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