Shadow evaporation of epitaxial Al/Al$_2$O$_3$/Al tunnel junctions on sapphire utilizing an inorganic bilayer mask$^a$)

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This letter describes a new inorganic shadow mask that has been employed for the evaporation of all-epitaxial Al/Al$_2$O$_3$/Al superconducting tunnel junctions. Organic resists that are commonly used for shadow masks and other lift-off processes are not compatible with ultra-high vacuum deposition systems, and they can break down at even moderately elevated temperatures. The inorganic mask described herein does not suffer these same shortcomings. It was fabricated from a Ge/Nb bilayer, comprising suspended Nb bridges supported by an undercut Ge sacrificial layer. Utilizing such a bilayer mask on C-plane sapphire, the growth of epitaxial Al tunnel junctions was achieved using molecular beam epitaxy. Crystalline Al$_2$O$_3$ was grown diffusively at 300 °C in a molecular oxygen background of 2.0 mtorr, while amorphous oxide was grown at room temperature and 25 mtorr. A variety of analysis techniques were employed to evaluate the materials, and tunnel junction current-voltage characteristics were measured at millikelvin temperatures.

A common technique for the fabrication of Josephson junctions involves double-angle shadow evaporation of Al through an offset mask, with the tunnel barrier formed by the diffusive oxidation of the Al base layer.$^{1,2}$ Shadow evaporation has been the most successful fabrication approach to date for making long-lived, high-coherence superconducting quantum bits (or qubits).$^{3–6}$ An alternative technique for tunnel junction fabrication involves the selective etching of whole-wafer superconductor/insulator/superconductor (SIS) trilayers.$^{7,8}$ Spectroscopy measurements on qubits fabricated in this manner have shown that epitaxial Al$_2$O$_3$ tunnel barriers have fewer two-level fluctuators (a known source of decoherence) than diffused, amorphous oxides.$^9$ Given the respective successes of these techniques, combining the two would seem ideal. However, the conventional organic bilayers used for shadow masks are incompatible with the ultra-high vacuum (UHV) environment and the high substrate temperatures required for epitaxial film growth.

To address this challenge we have developed an inorganic bilayer mask, and used it to combine epitaxial trilayer growth with shadow evaporation for the fabrication of crystalline Josephson junctions. Inorganic masks have been utilized before by a few different groups, but our interest was developing one consistent with our existing qubit fabrication process.$^{10}$ Those developed by Howard$^{11}$ used Cu for a sacrificial lift-off layer, and Cu is incompatible with our CMOS fabrication facility because of its degradation of minority carrier lifetimes in Si.$^{12}$ Offset masks used by van Wees et al.$^{13}$ employed Al as the lift-off layer, which doesn’t permit the fabrication of Al tunnel junctions. Finally, Hess et al.$^{14}$ utilized SiO$_2$ for the lift-off layer, a dielectric material incorporated into our qubit fabrication process for wiring cross-overs. Given these limitations, we sought to develop a CMOS-compatible process for Al/Al$_2$O$_3$/Al tunnel junctions. In addition to being compatible with our qubit fabrication process, we also desired to resolve suspended bridges down to 100 nm wide and up to 2.5 μm long.

Our inorganic shadow masks were fabricated from Ge/Nb bilayers in the Microelectronics Laboratory at MIT Lincoln Laboratory (see Fig. 1). The sacrificial Ge layer was about 500 nm thick and the Nb bridge layer was about 100 nm thick. To pattern the top Nb layer, we used 248 nm deep-ultraviolet (DUV) photolithography with UV5 resist and an anti-reflective coating (ARC), followed by F-based reactive

![FIG. 1. The process for making an inorganic Ge/Nb bilayer mask for Al tunnel junction shadow evaporation. (a) The bridge pattern is first defined in an ARC/UV5 resist bilayer using 248-nm photolithography. (b) The top metal (Nb) is etched using F-based RIE and the resist layers are stripped. (c) The sacrificial layer (Ge) is undercut with H$_2$O$_2$. (d) Al/Al$_2$O$_3$/Al shadow evaporation is performed, followed by lift-off in H$_2$O$_2$.](image-url)
Amorphous oxide tunnel barriers were formed by oxidizing the Al (111) surface in the MBE load-lock at an O2 pressure of 25 mtorr for 30 min. Crystalline oxide tunnel barriers were formed in the growth chamber by diffusive oxidation of the Al (111) surface at high temperatures.18,19 Several growth conditions were evaluated using both RHEED to measure the surface crystallinity, and x-ray photoelectron spectroscopy (XPS) to measure oxide thickness. Comparing the XPS spectra of crystalline oxide layers with that of an amorphous oxide formed under the conditions above, the crystalline oxide process that gave the desired thickness had a wafer temperature of 300°C, an O2 pressure of 2.0 mtorr, and a duration of 5 hours. After forming the oxide barrier layer and depositing the top Al electrode layer, the Ge/Nb mask layers were lifted off in H2O2, leaving behind the Al/Al2O3/Al overlap Josephson junction.

RHEED measurements of the two junction varieties (with amorphous and crystalline barriers) are shown in Fig. 2. In both cases the Al (111) base layer grows epitaxially on C-plane sapphire with the orientational relationship Al [112] || α-Al2O3 [1120]. An amorphous oxide layer leads, as expected, to a polycrystalline top Al layer, indicated by the faint rings observed in the RHEED pattern (see Fig. 2a). On the other hand, the crystalline oxide gives rise to an epitaxial Al (111) top layer with a RHEED pattern nearly indistinguishable from the bottom Al layer. The oxide layer itself has a very diffuse diffraction pattern, quite different from the sapphire polymorphs are thermodynamically stable at temperatures up to 300°C and beyond. In addition, many polymorphs was used to adjust the wafer’s azimuthal angle such that the mask pattern was aligned to the Al cell position. Epitaxial Al (111) films were achieved through room-temperature deposition without any pretreatment of the wafer in UHV.

For standard, non-epitaxial, Al shadow evaporation processes, we fabricated inorganic masks on both bare Si and Si with thermal oxide. For epitaxial Al growth, we utilized C-plane sapphire – α-Al2O3 (0001) – for several reasons: 1.) it does not react with H2O2; 2.) it is easily cleaned with solvents (the Ge/Nb bilayer is not chemically attacked); 3.) the surface remains crystalline when exposed to air; and 4.) Al (111) films have been demonstrated on C-plane sapphire.15–17 In the center of the sapphire substrate, an area of about 5 cm2 was flood-exposed and completely stripped of the mask layers – this enabled in situ reflection high-energy electron diffraction (RHEED) measurements before, during, and after film growth. Crystalline Al growth was achieved using a molecular beam epitaxy (MBE) system with a base pressure less than 1 × 10−10 torr and an effusion cell for Al deposition. The Al cell was mounted at a fixed angle of 45°, and RHEED ion etching (RIE). After stripping the organic resist layer from the wafer, the final step in preparing the shadow mask was to undercut the Ge layer with a H2O2 wet-etch such that the Nb overhang was roughly equal to the Ge thickness. Fig. 1 shows this process, with which we can routinely make bridges down to 150 nm wide and up to 2.5 μm long. (We have employed e-beam lithography to define even narrower bridges, but that approach was not employed in the work discussed here.)

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and this misfit may be partially accommodated through the growth of one or more polymorphs. X-ray diffraction (XRD) measurements of the Al trilayers after lift-off confirmed the orientation of the Al layers, in both the surface-normal and in-plane directions. Like other close packed metals, Al epitaxy in the (111) growth orientation exhibited stacking faults, as evidenced by the six-fold symmetry observed in ϕ-scans (not shown). The crystalline Al trilayer also showed a slightly wider rocking curve (ω-scan) than did a single layer Al film – 0.21° versus 0.15°. Finally, radial scans of trilayer and single layer Bragg peaks showed a slight shift of the trilayer Al (111) peak to a higher 2θ-ω value (see Fig. 2c). While single-layer Al was found to have a (111) planar spacing, \( d_{(111)} = 2.340 \text{ Å}, \) very close to that of bulk Al (2.338 Å), the crystalline trilayer exhibited a smaller spacing of 2.330 Å. The cause for this contraction of the Al trilayer lattice is not known.

After lift-off the devices were also scanned using atomic force microscopy (AFM) both to measure the Al films’ thickness at various locations across the wafer, and to measure the overlap achieved for a variety of bridge widths. We found that across a 150 mm-diameter wafer the thickness of each Al layer varied about ±20%, and that for a 200 nm-wide bridge the overlap of the two Al layers was about 600 nm. This overlap length is consistent with the estimate \( 2d - w - t - a \) one can make for 45° evaporation, where \( d \) is the thickness of the sacrificial Ge layer, \( w \) and \( t \) are the bridge width and thickness, respectively, and \( a \) is the thickness of the Al base layer. We also observed in the AFM scans that longer bridges tended to bow upward during the crystalline oxide growth process, which led to an increased overlap in those devices. This is shown in Fig. 3 for two junctions formed with a bridge 200 nm wide by 2.5 \( \mu \)m long – one with crystalline oxide formed at 300 °C (Fig. 3a) and one with an amorphous oxide formed at room temperature (Fig. 3b). Eliminating this bow, the subject of future work, may be achieved through better stress control in the Nb and/or Ge layers during deposition, or by thermal treatment of the bilayer prior to shadow-mask patterning.

Electrical measurements of tunnel junction current-voltage characteristics \((I-V)\)s) were performed in a dilution refrigerator operating below 20 mK. Typical \( I-V \)’s for both crystalline and amorphous oxide barriers are shown in Fig. 4. Junctions with an amorphous barrier exhibited the common, hysteretic curve of a SIS tunnel junction, with features at the gap voltage due to self-heating. On the other hand, crystalline tunnel barriers displayed no such hysteresis and had a gap roughly half that observed with the amorphous barrier. While enabling epitaxial growth from bottom to top, the crystalline oxide does not form an electrically insulating film and is most likely littered with electrical pinholes. It is also plausible that the process for forming crystalline \( \text{Al}_2\text{O}_3 \) on Al diffuses a substantial amount of oxygen into the bulk of the metal base layer, to the point where superconductivity is suppressed to a large degree.

In summary, we have developed a new inorganic bilayer mask and utilized it to grow epitaxial Al/\( \text{Al}_2\text{O}_3 \)/Al Josephson junctions. The shadow mask was fabricated from a Ge/Nb bilayer deposited on C-plane sapphire, and while suspended bridges did show some bowing due to thermal stress, the mask held up well under the growth conditions studied. Both electron and x-ray diffraction measurements on epitaxial Al trilayers indicated very good crystalline quality was achieved. However, the high-temperature diffusive oxidation process employed yielded leaky tunnel barriers and may have suppressed superconductivity in the base Al layer. An alternative growth technique such as co-deposition\(^2\) may be more successful in this regard, and is a subject for future research.

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