Design of Substrate Integrated Waveguide (SIW) Antenna

Hafssa Amer
Electrical Engineering Department
Baghdad University
Baghdad, Iraq

Mahmood A. K. Abdulsattar
Electrical Engineering Department
Baghdad University
Baghdad, Iraq

ABSTRACT
The cavity-backed slots SIW antenna that suitable with planar devices is designed, simulated, implemented, and measured. The design is about of two forms of a slotted cavity at bottom side, but the microstrip line is fed at top side. The feeding line and the cavity-backed slots can be implemented on a single substrate using standard printed circuit board (PCB) process. The idea is to merge specific slot models on the cavity back side such as lateraled and meandered slots. An SIW antenna is presented, the simulation results show a 6.35 dB gain, 7.3 dB directivity, and -0.9512 dB (80.33%) radiation efficiency, and the measurements show that the reflection coefficient ($S_{11}$) at 10.2 GHz is -37.23 dB with a bandwidth of 345 MHZ (3.38%). The combination of cavity-back slots, planar feeding, and SIW antenna design, leads to lightweight, size reduction, easy implementation, and simple integration with planar circuits.

Keywords
Cavity-backed, a slot antenna, substrate integrated waveguide (SIW).

1. INTRODUCTION
The development of communication devices has managed engineers to require light mass, strength, and simple integration with planar circuit and low-profile antennas [1]. The narrowband frequency is advanced using printed patch antennas [2]. Later, the bandwidth has improvement without reducing the performance of antenna by using cavity backed slot antennas. In addition, it is a suitable solution using of metallic surfaces [3]. To reduce the antenna radiation, a narrow cavity is located behind the slot antenna. In [4], a process for decreasing the sizes of cavity backed rectangular slot antenna is presented.

The substrate integrated waveguide (SIW) has been development and increase by improvement of a new technologies used in passive circuits. The implementation of SIW is suitable when its fabricate by using printed circuit board (PCB) standards. Different offerings have been accounted, so it prevents the original size of passive structures [5]. The SIW technical has been used for designing and fabrication filters and slot antennas [6]. Lately, SIW antenna with a cavity-backed slot that located at the ground plane was proposed [1]. This SIW antenna has some benefits: low cost, size reduction, and compatibility with the PCB process as well as with common surface mounting techniques.

The present work, the improvement of the matching between the transmission line and antenna is obtained by including the lateral slot lines that etching at ground side of SIW antenna. The method of improve the electrical performance of the antenna has been illustrated in [4] by using the meandered slot and how to reduce the cavity width. Finally, compared to [1], the suggested solution leads to reduce the resonant frequency without change the overall dimensions of SIW antenna.

2. DESIGN PROCEDURE
The design of the SIW antenna is depend on the use of classical rectangular waveguides analysis equations. Figure 1 shows the classical rectangular waveguide. Then, it can be considering ($f_c$) cutoff frequency when working in $TE_{10}$ fundamental mode. A diagram view of SIW is shown in Figure 2. Such a waveguide is emulating of two parallel arrays of via-holes, which define the waveguide TE wave propagation region. The propagation constant and the radiation loss are determined by parameters “w”, “p”, and “d”, which denote the width of the SIW, the pitch length and the diameter of vias, respectively [7].

$$f_c = \frac{c}{2\pi} \sqrt{\left(\frac{mn}{a}\right)^2 + \left(\frac{mn}{p}\right)^2}$$ (1)

$$f_c = \frac{c}{2a} \quad \text{At fundamental mode}$$ (2)

$$a_d = \frac{a}{\sqrt{\varepsilon_r}} \quad \text{Dielectric filled waveguide width}$$ (3)

$$w = a_d + \frac{a^2}{0.98p}$$ (4)

$$w = a_d - 1.08 \frac{d^2}{p} + 0.1 \frac{d^2}{a_d}$$ (5)

Fig 1: Dimensions of an (SIW)

As shown in Figure 2, the SIW is equivalent to a dielectric filled waveguide with slots at its side, so it can be analyzed just by using the width of the equivalent waveguide [8]. The following is an experimental formula that used to normalize the width of the equivalent waveguide.
\[ \hat{\psi} = \frac{\xi_1 + \frac{\xi_2}{\alpha + \frac{\xi_1 + \frac{\xi_3}{\alpha + \xi_1}}{\alpha + 1}}}{\alpha + \frac{\xi_1 + \frac{\xi_3}{\alpha + \xi_1}}{\alpha + 1}} \]  

(6)

Where

\[ \xi_1 = 1.0198 \left( \frac{0.3465}{\alpha} - 1.0684 \right) \]  

(7)

\[ \xi_2 = -0.1183 \left( \frac{0.3465}{\alpha} - 1.0684 \right) \]  

(8)

\[ \xi_3 = 1.0082 \left( \frac{0.9163}{\alpha} + 0.2152 \right) \]  

(9)

These formulae have a relative error less 1% and the width of the equivalent classical rectangular waveguide is [8]:

\[ a = w + \hat{\psi} \]  

(10)

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**Fig 2: Substrate integrated waveguide and the equivalent rectangular waveguide**

Figure 3 shows the analysis results made to implement a cavity with a resonant frequency of 10 GHz, over an FR4 substrate (\( \varepsilon_r = 4.3, \delta = 0.025, h_s = 1\text{mm} \)).

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**3. EXTERNAL COUPLING TO THE CAVITY USING CPW ACCESS**

A transition between microstrip and SIW cavity as defined in [8] is used on the top side. A 50-microstrip line with length of \( \text{Sel} \) that connect to the coplanar waveguide (CPW) as shown in Figure 3. The length \( L_{MS} \) is used to transform the quasi-TEM of the microstrip line into the TE of the SIW antenna. The transition design is easy to achieve, and the quality factor (Q) is largely that effect by the distance \( L_{MS} \) and the trace width \( W_{MS} \). The design circuit is shown in Figure 3.

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**Fig 3: Circuit topology (a) top view (b) bottom view**

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**4. ANTENNA EXPLANATION**

A slot patch antenna is typically seen as an equivalent resonant magnetic dipole with its first operating frequency that occurring corresponding to an overall length of \( \lambda_c /2 \). Where the compact type of a slot patch antenna can be put into the cavity back metallic plane (bottom face). It is suitable to insert the cavity current lines around the SIW antenna. At first the lateral slot etching on the back side of the SIW antenna, that the five slots (current lines) as rectangle shape are circulated around SIW antenna in perpendicular way as shown in Figure 4, which principally responsible for producing a resonant condition, and it is also responsible for increasing the matching condition between antenna and
microstrip transmission line. Second, the meandered slots line is etching that conserves the electrical length (Figure 4). These meander slots are responsible to achieve the compact in size without change the overall dimensions of antenna, the meander and lateral slots that etching at bottom side is shown in Figure 4. In the SIW antenna, the maximum current is flowing complete the upper side. The meandered slot line has an overall length $\lambda_c/2$, and it is of two slots that designed to suitable the cavity width, ending perpendicularly to a lateral slot. Lateral slots are used as inductively load of slot antenna, so the matching of its resonant frequency is change (increased) [9] (Figure 4).

![Figure 4: 3D view of the implemented circuit.](image)

5. SIMULATION RESULTS

Using the structure before described, was designed of FR4 substrate ($\varepsilon_r = 4.3$, $\delta = 0.025$, $h_s = 1$ mm). The dimensions of SIW are 9 mm X 14 mm X 1 mm, while the overall antenna dimensions are 12 mm X 20 mm.

5.1 Parametric Study

At first, the SIW antenna is designed to operate at 12.2 GHz, the simulation result is shown in Figure 5. The lateral slots are including at bottom side of the designed SIW antenna this slots responsible of decreasing the value of the $S_{11}$ parameter from -19 dB to -53 dB (increase matching between transmission line and SIW antenna) as shown in Figure 6. To obtain compact in size the meander slots are included its responsible for shifting the resonant frequency from 12.2 GHz to 10 GHz (X-band).

![Fig 5: Simulation $S_{11}$ parameter of un slotted proposed SIW antenna](image)

The important parameter that effect on the design of SIW antenna is $t_s$. From Figure 7 as $t_s$ increases, the value of $S_{11}$ is an increase (matching decrease), but the value of resonant frequency is decreased.

![Fig 6: Simulation $S_{11}$ parameter of cavity back lateral slots SIW antenna](image)

![Fig 7: Simulation $S_{11}$ parameter of proposed antenna with $t_s$ as a parameter](image)

The other parameter that affected on the design of SIW antenna is $t_p$. From Figure 8 as $t_p$ increases, the value of $S_{11}$ is increase, with the increase of resonant frequency value.

![Fig 8: Simulation $S_{11}$ parameter of proposed antenna with $t_p$ as a parameter](image)
There must be some compromise between the various requirements of wide bandwidth, low frequency, and low return loss (low $S_{11}$). Final values that are obtained through the sweep parameters and which affect the performance of this antenna have been summarized in Table 1. Figure 9 shows a return loss less than -40 dB and a fractional bandwidth of about 2.8% centered at 10 GHz. Figure 10 shows the antenna has gain of 6.36 dB, radiation efficiency -0.9512 dB (80.33%), total efficiency -0.9522 dB (80.31%). As the difference between these two efficiency value is only 0.0011 dB, the loss of impedance mismatch is low.

Table 1 Geometrical parameter of the proposed SIW cavity backed lateral and meander slots antenna

| Parameter  | Value (mm) | Parameter  | Value (mm) |
|-----------|------------|------------|------------|
| $L_1$      | 14         | $L_{P1}$   | 13         |
| $W_1$      | 9          | $W_{P1}$   | 7.5        |
| $P_1$      | 1          | $L_{P2}$   | 12.5       |
| $D$        | 0.3        | $W_{P2}$   | 2.3        |
| $L_{ML}$   | 8          | $t_p$      | 0.4        |
| $L_{SS}$   | 4          | $L_{SL}$   | 1.4        |
| $W_{M1}$   | 1          | $L_{SS}$   | 0.7        |
| $W_{M2}$   | 0.5        | $W_S$      | 0.8        |
| $S_{eL}$   | 4          | $t_s$      | 0.1        |

Fig 9: Simulation $S_{11}$ parameter of cavity back lateral and meander slots (proposed model) SIW antenna

Fig 10: Farfield 3D-plot gain of SIW cavity-backed antenna at 10 GHz

5.2 Electric Field (E-field) and Magnetic Field (H-field) Distributions

The polar plot radiation pattern of E-field and H-field at resonating frequency 10 GHz in three plane XOY, YOZ, and XOZ are shown in Figure 11 and 12.
The surface current distribution of proposed SIW cavity-backed antenna plot at 10 GHz has been simulated as shown in Figure 13. The surface current distribution has a high value along the meander and lateral slots. The proposed antenna rejects the frequency that is located outside of the bandwidth, so the surface current has no longer spread all over the cavity.

Fig 11: Simulation polar plot of the radiation pattern in E-plane and at 10 GHz (a) XOY plane (b) YOZ plane (c) XOZ plane

Fig 12: Simulation polar plot of the radiation pattern in H-plane at 10 GHz (a) XOY plane (b) YOZ plane (c) XOZ plane

Fig 13: Surface current distribution at 10 GHz
6. ANTENA IMPLEMENTATION AND COMPARISON BETWEEN THE MEASUREMENT AND SIMULATION RESULTS

Based on the analysis and simulations shown in this work, an experimental lateral and meander-shaped slot cavity backed SIW antenna has been fabricated. Figure 14 shows the photograph of the fabricated prototype of proposed antenna.

![Photograph of the fabricated prototype of the lateral and meander-shaped slot cavity backed SIW antenna front and back view.](image)

**Figure 14:** Photograph of the fabricated prototype of the lateral and meander-shaped slot cavity backed SIW antenna front and back view.

![Comparison between the simulation(solid) and the measured (dashed) S11-parameter for fabrication backed SIW cavity antenna.](image)

**Figure 15:** Comparison between the simulation(solid) and the measured (dashed) S11-parameter for fabrication backed SIW cavity antenna.

The measured results reach an agreement with the electromagnetic (EM) simulation results in terms of S11-parameter as illustrated fig. 15. gain, and efficiency. However, measured return loss at 10.2 GHz is about -37.23 dB which somewhat higher than the simulation. This is because connector losses and unexpected material losses that could not be accounted in this simulations. this fractional bandwidth of about 3.38 % centered at 10.2 GHz. Good out of gain is probably due to the unspecified values of \( \varepsilon_r \) and possible fabrication errors. Table 2 summarizes simulation results of designed SIW antenna and experimental results of implemented proposed SIW antenna, this table shows the improvement of the performances first with un slotted cavity-backed antenna, then second adding the meander slots, and finally including the lateral slots. Good agreement between simulations and measurements results was obtained.

### Table 2. summarizes simulation and experimental results

| Property | \( f_0 \) GHz | Return Loss (dB) | BW % at -10dB |
|----------|---------------|------------------|---------------|
| Un slotted | 12.2 | -19 | 2.12 |
| Lateral slots | 12.2 | -53 | 1.34 |
| Lateral and meander slots | 10 | -45 | 2.65 |
| Measured | 10.2 | -37.23 | 3.45 |

7. CONCLUSION

A new design method of low profile cavity backed planar slot antenna is presented in this paper. The whole antenna including backed cavity and feeding element is completely constructed at a single substrate by using SIW technique and GCPW structure. Experiment was carried out to validate this design concept. The fabricated antenna has 2.65% bandwidth and keeps high radiation performance of conventional cavity backed antenna such as high gain, and low cross polarization level. It still has advantages of conventional planar antennas such as low profile, light weight, good conformability and seamless integration with planar circuit. It can be easily fabricated by ordinary single layer PCB process with very low cost.

8. REFERENCES

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