Characterization of high-\( k \) materials for the advancement of high-speed ULSIs *

Akira Nishiyama,† Yoshiki Kamata, Ryosuke Iijima, Masahiro Koike, Tsunehiro Ino, Masato Koyama, Yuuichi Kaminuma, Mizuki Ono, Masamichi Suzuki, Chie Hongo, and Akira Takashima

Advanced LSI Technology Laboratory, Corporate R&D Center,
Toshiba Corporation, 8 Shinsugita-cho, Isogo-ku, Yokohama, 235-8522, JAPAN

Akio Kaneko, Seiji Imuniya, and Mariko Takeyana

Semiconductor Company, Toshiba Corporation, 8 Shinsugita-cho, Isogo-ku, Yokohama, 235-8522, JAPAN

(Received 20 September 2003; Accepted 20 November 2003; Published 19 December 2003)

Insulators with high dielectric constants (so-called high-\( k \)) have been intensively investigated for the replacement of SiO\(_2\) gate dielectric of MOS transistors for high-speed ULSIs in near future. The characterization results of the Hf-silicate gate dielectric, especially in terms of its structural transformation during high temperature annealing, are presented. Phase separation and micro-crystallization in Hf-silicates were evaluated using XPS, TEM, and XRD. Impurity diffusion through the dielectric was also examined using the backside SIMS of the p\(^+\)-poly-Si/Hf-silicate/Si-substrate system. It has also been revealed that such thermal stability problems for Hf-silicates can be solved with nitrogen incorporation in the film using the physical analyses outlined above.

Keywords: Sputter deposition; X-ray photoelectron spectroscopy

I. INTRODUCTION

As the device dimensions in ULSIs have been reduced, several intrinsic barriers against further development have emerged. One of these is the leakage current through the SiO\(_2\) gate dielectric due to the quantum mechanical tunneling of the carriers in the inversion layers. This phenomenon results in a high level of power consumption, which is especially problematic in LSIs for mobile applications. Much effort has been expended in developing high-permittivity dielectrics (high-\( k \) materials) as an alternative gate insulator in order to solve this problem [1]. Among various candidates, silicate is considered to be a promising material due to its modest dielectric constant [2] and relatively good interface properties with the silicon substrate [3]. However, a major drawback of this material is its poor thermal stability [4]. Figure 1 shows that several high temperature annealings following the gate dielectric film deposition is necessary for ULSI fabrication. This report focuses on the characterization of Hf-silicates especially in terms of their structural transformation during high temperature annealing. The electrical property change is also presented in relation to the structural change of this material. The effect of nitrogen incorporation in Hf-silicates on the thermal stability was also evaluated with the characterization technique mentioned above.

II. EXPERIMENT

Hf silicate films were deposited on HF-treated Si(100) substrate using reactive co-sputtering from the Hf and Si targets in Ar+O\(_2\) ambient at room temperature. The Hf/Si ratio in the film was varied by changing the power imposed on each target. Nitrogen incorporation was achieved by a mixture of N\(_2\) gas in the sputtering atmosphere. Rapid thermal annealing in N\(_2\) at 1000°C for 30 s was performed after the film deposition as the high temperature annealing in this study. High Resolution TEM (Hitachi HF-2000 field emission, 200 keV) was used to observe the structural change in the film. In-plain XRD was performed in order to evaluate the crystallinity of thin (<10 nm) films. An X-ray beam (Cu K\(_{\alpha}\), 0.154 nm) was incident on the samples at an angle of 0.3° for the total reflection at the sample surface in this measurement. In order to evaluate the chemical bonds in the silicate films, XPS using Al K\(_{\alpha}\) (1.487 keV) as the light source was carried out. Backside SIMS measurement was used to evaluate the boron diffusion through thin Hf-silicate films with high temperature annealing. O\(_2\)\(^+\) with an energy of 3 keV was used for the primary ion in this measurement.

For the electrical measurement, highly-doped poly-Si/Hf-silicate/p-Si(100) MOS capacitors have been fabricated. High temperature annealing was carried out at 1000°C for 30 s for the activation and diffusion of implanted Arsenic and Boron inside the gate electrodes.

[FIG. 1: High temperature processes following the gate dielectric film deposition in the ULSI fabrication.]

*This paper was presented at The 4th International Symposium on Atomic Level Characterizations for New Materials and Devices (ALC ‘03), Kauai, Hawaii, USA, 5-10 October, 2003.
†Corresponding author: a-nishiyama@amc.toshiba.co.jp
V measurement was carried out in order to evaluate the dielectric constant of the films.

III. RESULTS AND DISCUSSION

Figures 2 show the TEM planar view of the Hf-silicate films (a) without nitrogen, (b) with [N] of 5 at.%, and (c) 30 at.%, after the 1000 °C annealing. Relative concentration Hf/Hf+Si was about 25 % for all samples. Micro-crystals formed with a diameter of several nanometers inside the film without nitrogen during the annealing as shown in Fig. 2(a). The diameter was decreased with the nitrogen incorporation in the 5 at.% film and uniform amorphous film was obtained for the 30 at.% nitrogen concentration. Figure 3 shows the in-plane XRD profile of the Hf-silicate films including those shown in Fig. 2. The diffraction peaks of crystal formed inside the sample without nitrogen corresponded to those of orthorhombic HfO$_2$. The average diameter $D_{hkl}$ of the HfO$_2$ crystal inside the surface plane was calculated by Scherrer’s equation,

$$D_{hkl} = \frac{0.9 \lambda}{\beta \cos \theta},$$

where $\lambda$, $\beta$, and $\theta$ are the wavelength of the X-rays, the full width at half maximum of the (211) peak, and the diffraction angle of the peak, namely 15.2 °, respectively. The average diameter turned out to be about 3.5 nm from this calculation, which was consistent with the TEM results in Fig. 2(a). The orthorhombic HfO$_2$ peaks were still observed for the sample with [N] of 5 at.%, however, it diminished for samples with [N] more than 16 at.%. Figure 4 shows the XPS O1S spectra for the samples with Hf/Hf+Si of 25 % and [N] of 30 at.% before and after the 1000 °C RTA. It reveals that the main chemical bond component in the film is Hf-O-Si (Silicate bonds). The fact that the XPS profile after 1000 °C N$_2$ annealing is almost identical to that of sample before the annealing indicates that the chemical bond structure was not changed during the annealing with the presence of nitrogen inside the film. On the other hand, when nitrogen was intentionally replaced by oxygen by the 1000 °C O$_2$ annealing, which is not used in ULSI process, the Hf-O-Si peak diminished and Hf-O-Hf and Si-O-Si increase their intensi-
Characterization (XPS, TEM, and XRD) of the Hf-silicate gate dielectrics especially in terms of their structural transformation during high temperature annealing have revealed that Hf-silicate shows the phase separation as-deposited sample with [N] of about 25 at. % is larger than that of the sample without nitrogen. This suggests that nitrogen itself can enhance the $\epsilon_r$ of the film.

As for the thermal stability of the gate dielectrics, impurity penetration such as that of Boron from the gate electrode through the thin films during high temperature annealing is another issue [7]. Figure 6 shows the Boron profile after 1000 °C annealing for 30 s in the p⁺-poly Silicon/Hf-silicate/Si-substrate system. Hf/Hf+Si was about 30 % and the thickness of the film including the interfacial layer was about 3nm for all three samples. SIMS measurement proceeded from the backside, namely from the Si substrate, in order to extract accurate profiles, avoiding the knock-on effect by the primary ions. The fact that Boron concentration [B] is very high at the interface between the poly-Si gate electrode and the Hf-silicate indicates that the high temperature annealing is long enough for the Boron diffusion inside the electrode. However, Boron severely penetrated into the substrate for the film without nitrogen, which is problematic in terms of transistor threshold voltage shift as well as short-channel effect enhancement. This figure also shows that an increase in [N] can successfully suppress the phenomenon and [B] at the interface between the substrate and the dielectric film could be reduced down to about $2 \times 10^{17}$ cm$^{-3}$, which could be acceptable in future ULSI devices.

IV. CONCLUSION

Characterization (XPS, TEM, and XRD) of the Hf-silicate gate dielectrics especially in terms of their structural transformation during high temperature annealing have revealed that Hf-silicate shows the phase separation...
FIG. 6: Boron profile after 1000 °C annealing for 30 s in the $p^+$-poly Silicon/Hf-silicate/Si-substrate system. Hf/Hf+Si was about 30 % and the thickness of the film including the interfacial layer was about 3 nm.

and micro-crystallization of orthorhombic HfO$_2$ during the subsequent LSI high temperature processes. Related electrical measurement suggested that this phenomenon leads to a decrease in the dielectric constant of the material. Boron diffusion through the dielectric evaluated with the backside SIMS of the $p^+$-poly-Si/Hf-silicate/Si-substrate system indicated that Hf-silicate is not a good diffusion barrier for Boron. It was also revealed that such thermal stability problems for Hf-silicates can be solved with nitrogen incorporation in the film. These physical analyses are quite effective in shedding light on nanoscale structural changes inside high-$k$ films and their surrounding system, and they are indispensable in the development of high-$k$ materials for ultra-high speed CMOS devices.

Acknowledgments

The authors are grateful for the technical assistance of Yuichiro Mitani, Toshiba Corporation and Nobutaka Sato, Toshiba Nanoanalysis Corporation. We also appreciate Noburu Fukushima, Kazuhiro Eguchi and Yoshitaka Tsunashima for their support for this work and their useful comment and discussion.

[1] G. D. Wilk, R. M. Wallace, and J. M. Anthony, J. Appl. Phys. 89, 5243 (2001).
[2] G. D. Wilk and R. M. Wallace, Appl. Phys. Lett. 74, 2854 (1999).
[3] W.-J. Qi, R. Nieh, B. H. Lee, K. Onishi, L. Kang, Y. Jeon, J. C. Lee, V. Kausik, B.-Y. Neuyen, L. Prabhu, K. Eisenbeiser, and J. Finder, Symposium on VLSI Technology, Digest of Technical Papers, p. 40 (2000).
[4] A. I. Kingon and J-P. Maria, Nature, 406,1032 (2000)
[5] J.-P. Maria, D. Wickaksana, J. Parrette and A. I. Kingon, J. Mater. Res. 17, 1571 (2002).
[6] M. Ono, T. Ino, M. Koyama, A. Takashima, and A. Nishiyama, Extended Abst. Int. Conf. Solid State Devices and Materials, p. 710 (2002).
[7] T. Ayama, K. Suzuki, H.Tashiro, Y. Tada and K.Horiuchi, J. Electrochem. Soc. 145, 689 (1998).