Numerical Analysis of a Spiral Tube Damping Busbar to Suppress VFTO in 1000 kV GIS

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Abstract: Gas insulated substations (GISs) are broadly used for transmission and distribution in electric power networks. Very fast transient overvoltage (VFTO) caused by SF$_6$ discharge during switching operations in a GIS may threaten the insulation of electrical equipment. In this paper, a novel VFTO suppression method with great prospects in engineering, called the spiral tube damping busbar, is proposed. The suppressing mechanism of the new method is analyzed. The structure and the design characteristics of the damping busbar are introduced as well. Moreover, a calculation method for the self-inductance of the damping busbar at high frequency is presented. According to the structural characteristics of the damping busbar, the inductance effect on suppressing VFTO is analyzed. A further improvement in damping VFTO is investigated by designing a spiral litz coil connected in series with the busbar, which increases the damping effect. The simulation results show that the improved damping busbar has a significant suppressing effect on the amplitude and the frequency of VFTO.

Keywords: VFTO suppression; GIS; high-frequency inductance calculation; damping busbar

1. Introduction

Power system demands are increasing due to the increase in economic activities and national developments. With the increase in demand, power systems operators have upgraded their transmission systems to higher voltage levels in order to achieve economical and reliable power transfer between the generation side and the demand side [1]. Therefore, high voltage and extra-high-voltage substations are widely deployed in modern systems, and their number will be greatly increased in the near future [2]. Thus, gas insulated substations (GISs) have been widely used over the last three decades in power systems and other fields such as intelligent transportation systems, high-speed trains, and underground, because of their high reliability, easy maintenance, and small ground space requirements. The higher personal and operational safety level and easy installation and commissioning make high-voltage GISs in significant demand, particularly in heavily industrial areas, in comparison with conventional air insulated substations (AIS) [3–7]. Although GISs have been involved in power systems for a long time, with the development of GISs and the voltage level of transmission lines, many new problems are occurring, such as very fast transient overvoltage (VFTO) caused by SF$_6$ discharge during switching operations in GISs. VFTO could cause damage to power devices. A reduction of the insulating capability of the dielectric gas in GISs is caused mainly by the peak magnitude and high-frequency oscillations of VFTO. The internal VFTO causes stress on the main insulation in the GIS, while the external VFTO poses a threat mainly to the main transformer and the secondary equipment within the substation [8]. These transients have extremely short rise time, in the nanoseconds range.

Several methods have been considered to mitigate such overvoltages [9,10]. VFT suppression has been proposed by many researchers using a disconnector equipped with a damping resistor [11],
a disconnector with reduced voltage during the opening operation (the so-called trapped charge voltage (TCV)) [12], surge arresters [13,14], high-frequency resonators [15,16], magnetic rings of different types [17–20], and VFTO mitigation by controlling the voltage conditions preceding voltage breakdown in SF$_6$ gas [21,22]. It could be well observed from the literature that an accurate design using the specific parameters of the GIS DS (Disconnector) contact system is required for VFTO mitigation by controlling the voltage conditions preceding voltage breakdowns in the disconnector contact system. As a consequence, additional costs will increase for new combinations.

Most of the suppression methods may require a change in GIS design and construction, which makes them complicated and expensive with low reliability. Disconnecting switches fitted with damping resistors and grounding switches were developed by Yamagata, Y. et al. [23]. The amplitude of the VFTO was reduced by up to 25% after applying the damping resistor. However, the damping resistor method showed limitations because any increment of the resistance leads the VFTO to be decreased and the dimension of the disconnector to be increased. In addition, the dissipated power requirements for the resistor are increasingly high. Furthermore, the slow operating speed of the GIS DS (2–3 m/s) leads to nonability for arc extinction. As a result, the lifetime of the contactor is reduced because of these nanosecond arcs.

TCV has been noticed on the load side of the disconnector after the occurrence of the last re-strike when the disconnector opening operation is completed. Charge leakage across the insulators leads to decay of the trapped charge, which is a prolonged operation, taking hours or days. During the next closing operation of the disconnector, due to the slow contact speed, the first pre-strike occurs when the source side and the load side have the same voltage with different polarity, where the load-side voltage is TCV resulting from the previous operation. The main challenge in the TCV approach is the optimum design of the disconnector which should be considered to achieve significant VFTO reduction with an acceptable sparking time, as explained clearly by Chen, W. et al. [24].

Moreover, surge arresters can suppress the amplitude of the VFTO, but they have no effect on the steepness. The difficulty in using surge arresters is implementing the optimum number of arrester discs for a noticeable damping effect and at a convenient location. The appropriate location of surge arresters in order to eliminate VFTO was investigated by Yadav, D.N. [25].

It could be well seen from the literature [15,16] that the main disadvantage of the cavity resonator method is the absorption of VFTO energy in only a narrow band of its broad frequency spectrum. Consequently, there is no observed damping effect when the resonant frequency of the resonator does not fit the dominant harmonic component of the VFTO. However, compared with the ferrite ring method, the magnetic rings become saturated in high current under high frequency and lose their suppressing effect, as confirmed by Rama Rao, J. V. G. et al. [26]. Furthermore, ferrite magnetic rings are still in the experimental stage, and they may have no obvious suppressing effect on VFTO.

As a consequence, in order to ensure the reliability of substations, it is essential to carry out research on suppressing VFTO. This paper presents a design of a damping busbar to suppress VFTO. We hollow out the conventional busbar to a spiral tube, and then the busbar conductor is changed into a series circuit with a multiturn hollow inductance coil and multiturn gap; based on this, paralleling the damping resistance with the newly designed spiral tube inductance circuit, the spiral tube damping busbar is formed. The mechanism of VFTO suppression by the proposed damping busbar is analyzed, and the distributed equivalent circuit is established. Furthermore, an illustrative structure of the damping busbar is introduced. VFTO with the proposed damping busbar is simulated, and the suppression effects before and after installing the damping busbar are compared. An improved design of the damping busbar is proposed, and a higher damping effect is verified.

The remainder of this paper is structured as follows. In Section 2, the suppressing mechanism of the new method is analyzed. The simulation results and the damping effect of the damping busbar are discussed in Section 3. In Section 4, the influence of the inductance value of the damping busbar on suppressing VFTO is investigated. An improved design of the damping busbar in order to increase the suppression effect is proposed in Section 5. This section presents an investigation on a spiral coil made
of litz wire, used to damp VFTO. Such winding meets the requirement of sufficient resistance to damp VFTO. Section 6 summarizes the damping effects of the damping busbar and the improved design. Finally, conclusions are drawn in Section 7.

2. The Suppression Mechanism of the New Method

Several studies have shown that VFTO is generated as a superposition of multiple different reflected electromagnetic waves with complex nonharmonic time dependence and covers a wide frequency range from 100 kHz to 100 MHz [27]. The wavefront steepness, amplitude, and high-frequency components of VFTO can be suppressed by inductance and resistance. Based on this, this research proposes a VFTO suppression method—the spiral tube damping busbar—located before the disconnector switch (DS), as shown in Figure 1b. At the rated frequency, the damping busbar transmits current and voltage waves like a normal busbar, but when VFTO passes through the damping busbar, it will be suppressed. Also, the energy of the travelling waves is consumed by the resistance. When the resistance and inductance of the damping busbar match each other, the biggest wave energy consumption can be obtained and the best VFT damping can be achieved.

Figure 1. The new damping busbar installed in a gas insulated substations (GIS): (a) general view of the damping busbar with its components; (b) the placement of the damping busbar.

2.1. The Structure of the Damping Busbar

The conventional GIS busbar was hollowed out into a spiral tube, and the busbar conductor was changed into a series circuit with a multiturn hollow inductance coil and a multiturn gap; then, we paralleled the damping resistance with the spiral tube inductance circuit. This method increases the active losses of the busbar by changing the wave impedance of the busbar. As a result, the transient energy is consumed, and the amplitude of the VFTO is reduced. Thus, the main components of the spiral tube damping busbar are (a) a spiral tube damping busbar made by screwing out the conventional busbar to a spiral slotted solenoid busbar; (b) solid metal oxide resistors with an antipulse voltage function as the noninductive damping resistors, as shown in Figure 2; and (c) electrical connection tools.
2.2. The Equivalent Circuit of the Damping Busbar

The equivalent circuit of the damping busbar is resistance and inductance connected in series with the GIS bus, as shown in Figure 3. $L_i(i = 0,1,2, \ldots)$ represents the inductance of each unit coil of the metal spiral tube conductor. $R_i$ and $L_{R_i}(i = 0,1,2, \ldots)$ represent the noninductive resistance and its residual inductance of the parallel connection of each turn, respectively, and the value of the resistance can be adjusted to achieve the best damping effect. $g_i(i = 0,1,2, \ldots)$ is the hollowing gap of the damping busbar and $r_i(i = 0,1,2, \ldots)$ is the arcing resistance of the gap, which represents the losses formed by the discharge channel.

Inductance Calculation of the Damping Busbar at High Frequency-Damping Busbar Parameters

The purpose of this section is to calculate a fairly precise value of the inductance. Determining the inductance was carried out by studying the effect of high-frequency fields with all the geometrical parameters of the busbar [28]. To this end, a calculation method for the self-inductance of the damping busbar at high frequency was developed and analyzed by the finite element method (FEM). In order to design the damping busbar, we needed to use a simulation method to obtain the VFTO waveform and then calculate the VFTO distributions. Thus, the mathematical expression of the VFTO waveform was theoretically calculated using the curve fitting method to get the fitting data of the VFTO waveform [29]. Then, the curve fitting method was applied to get the fitting data of the associated very fast transient current (VFTC) waveform for a single SF$_6$ gas discharge. Due to VFTC being assigned as current excitation for the simulation procedures, the VFTC equation was obtained for Fourier 8, with goodness of fit parameters R-square = 0.9768 and adjusted R-square = 0.9759.

![Figure 2](image1.png)

**Figure 2.** The structure of the damping busbar.

![Figure 3](image2.png)

**Figure 3.** The distributed parameters of the equivalent circuit of the damping busbar.
\[ VFTC(t) = a_0 + \sum_{i=1}^{8} a_i \cdot \cos(i \omega t) + \sum_{i=1}^{8} b_i \cdot \sin(i \omega t) \quad (1) \]

Table 1 illustrates the coefficients of the VFTC equation.

| Values |
|--------|
| \( i \) | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 |
| \( a_i \) | 1025 | 403.3 | -7.241 | 809.5 | -28.8 | 46.59 | 23.15 | -1149 | 1881 |
| \( b_i \) | 0 | 642.6 | 2540 | -3334 | -808.4 | 161.9 | -859.5 | 1063 | -1489 |
| \( \omega \) | 1.372 \times 10^7 |

The aim of the simulation study was to calculate the equivalent inductance of the damping busbar at high frequency, so some assumptions were proposed for ease of simulation. In this paper, the simple geometry model was imported into ANSYS Maxwell, and the eddy current solution type was chosen. However, this design depends on many variables according to ANSYS analysis: material properties (aluminum); the geometry of the busbar (busbar body); and operating frequency and input current (the VFTC frequency is 2 MHz and the VFTC peak value is 8256 A—see Equation (1)—for singular switching opening operation, as shown in Figure 4). All of these can be included in design sweeps to study how the impedance matrix—and, therefore, the inductance—is affected by changes in these properties (frequency sweep setup: set the frequency field from 1 to 2 MHz with 0.1 MHz step size).

The simulation results show a decrease in the inductance value of the damping busbar taking place for different frequencies, as illustrated in Figure 5. Notably, however, Figure 5 shows that the inductance value of the damping busbar is 0.33656 mH at 2 MHz. As a consequence, the damping busbar parameters are illustrated in Table 2.
3. Simulation Results

3.1. Modelling of a 1000 kV GIS

The equivalent circuit diagram of a 1000 kV GIS was simulated by using EMTP (Electromagnetic Transients Program) simulation in order to study the VFTO with and without installing the damping busbar [30]. However, the high-frequency characteristics of VFTO led to simulating GIS components as capacitances dominating the other parameters. In order to model the GIS bus duct, distributed parameters and lumped elements can be utilized. Surge impedance and wave velocity could be calculated for a GIS section of any length by using the physical dimensions of the bus duct [31,32].

In the following equations, C and L are self-capacitance and inductance, Z is surge impedance, and V is wave velocity in the GIS [33,34]:

\[ C = \frac{2\pi \varepsilon_0 \varepsilon_r}{\ln \frac{R}{r}}, \]  
\[ L = \frac{\mu_0 \mu_r}{2\pi} \ln \frac{2R}{r}, \]  
\[ Z = \sqrt{\frac{L}{C}} = \frac{1}{2\pi} \sqrt{\frac{\mu_0 \mu_r}{\varepsilon_0 \varepsilon_r}} \ln \frac{R}{r}, \]  
\[ V = \frac{1}{\sqrt{LC}}. \] 

Figure 6 shows the equivalent circuit of a 1000 kV GIS using EMTP simulation after installing the equivalent circuit of the damping busbar, as illustrated in Table 2.

Let r be the inner radius of the GIS shell and R be the inner radius of the GIS bus. In addition, \( \varepsilon_0 = 8.85418782 \times 10^{-12} \) F/m, \( \varepsilon_r = 1.0024 \) and \( \mu_0 = 4\pi \times 10^{-7} \) H/m. According to the dimensions of the GIS busbar, the calculations showed that the propagation velocity is 295 m/\( \mu \)s and the surge impedance is about 93.382 ohm/m.

| Parameters                  | Number of Turns | Units | Resistance per Unit [\( \Omega \)] | Inductance [mH] |
|-----------------------------|-----------------|-------|----------------------------------|-----------------|
| Value                       | 28              | 14    | 6 Resistors in parallel Req/unit = 20 \Omega | 0.33656         |

Figure 5. Inductance calculation of the damping busbar at high frequency.

Table 2. Damping busbar parameters.
3.2. Discussion

As shown in Figure 7a, when the disconnector is opened, the VFTO for the whole process at the power supply side appears as a superposition of high-frequency transients and a sine wave. After SF₆ gas discharges, the disconnector gap restores the insulation state; then, the current disappears, the high-frequency transient components gradually decay, and, finally, the voltage wave continues as a sinusoidal wave with the supply voltage.

In addition, the VFTO for the full process at the load side has an approximate step shape, which is a superposition of high-frequency transients and a stepped wave. Each step of the ladder wave corresponds to SF₆ gas breakdown, the step magnitude is the breakdown voltage, and the narrow pulse at the ladder edge is the highest frequency of the VFTO, as shown in Figure 7c.

Moreover, the VFTC for the whole process is the superposition of high-frequency transients with zero current amplitude. When the critical breakdown voltage (BV) of the SF₆ gas is equal to the transient recovery voltage of the gap, the SF₆ gas collapses and the gap transfers from the insulation state into a conductive state; then, the circuit produces a high-frequency transient current, as shown in Figure 7e. Additionally, when the critical breakdown voltage of SF₆ gas exceeds the transient recovery voltage of the gap, the current disappears during discharge of the SF₆ gas.

The simulation results show that the maximum amplitude of the VFTO decreased from 1.554 to 1.13 p.u., which means that the damping busbar functioned as a VFTO suppressor. The damping effect of the new design is evident from the simulation results, and the mitigation effect was about 27%. Table 3 illustrates the transient characteristics of the VFTO waveform under opening operation before and after installing the damping busbar. A singular switching operation (opening) of the disconnector was simulated in EMTP-ATP (Alternative Transients Program) software.
which in turn leads to a reduction in the high-frequency components of the VFTO. The travelling wave leads to a higher voltage on the inductance of the small circuit, which causes the parallel resistance to absorb the travelling wave energy, increase the consumption of active power, and reduce the amplitude of the VFTO. The limitation on the steepness of the transients is a result of the inductance effect on Suppressing VFTO

4. Inductance Effect on Suppressing VFTO

When VFTO passes through the inductance, the wavefront is smoothed, its steepness is reduced, and the amplitude is decreased. The inductance \( L_i \) increases the round-trip time of the travelling wave, which in turn leads to a reduction in the high-frequency components of the VFTO. The travelling wave leads to a higher voltage on the inductance of the small circuit, which causes the parallel resistance \( R_i \) to absorb the travelling wave energy, increase the consumption of active power, and reduce the amplitude of the VFTO.

The inductance value of the damping busbar is an important factor for an obvious VFTO suppression effect. The limitation on the steepness of the transients is a result of the inductance effect of the damping busbar. Figure 8 describes the influence of the inductance value on VFTO suppression at the load side during transient phenomena.

The more considerable value of the equivalent inductance of the damping busbar leads to an obvious suppression effect. Table 4 illustrates the transient characteristics of the VFTO waveform under an opening operation for different values of equivalent inductance of the damping busbar.

Table 3. The transient characteristics of VFTO under an opening operation at the load side before and after installing the damping busbar.

| VFTO Characteristic Parameters | Before Installing the Damping Busbar | After Installing the Damping Busbar |
|-------------------------------|--------------------------------------|-------------------------------------|
| Rise time [ns]                | 10                                   | 30                                  |
| Average breakdown steepness [kV/\( \mu \)s] | 71.53      | 48.725                             |
| Maximum amplitude of VFTO [p.u.] | 1.554    | 1.13                               |
| Average voltage overshoot coefficient | 0.487   | 0.244                              |
| Delay time [\( \mu \)s]       | 24.4                                 | 47.2                                |

Figure 7. Very fast transient overvoltage (VFTO) and VFTC waveforms: VFTO at the source side (a) before installing the damping busbar and (b) after installing it; VFTO at the load side (c) before installing the damping busbar and (d) after installing it; VFTC (e) before installing the damping busbar and (f) after installing it.
The flowchart of the procedure used to calculate the spiral coil design parameters is shown in Figure 9.

Figure 8. Influence of the inductance value on VFTO suppression at the load side of a 1000 kV GIS: at inductance values (a) \(L = 0\) mH, (b) \(L = 0.3\) mH, (c) \(L = 3\) mH.

Table 4. Influence of the equivalent inductance of the damping busbar on VFTO suppression.

| VFTO Characteristics | Simulation Results |
|----------------------|-------------------|
| Parameters           | \(L = 0\) mH | \(L = 0.3\) mH | \(L = 3\) mH |
| Rise time [ns]       | 10              | 30              | 35             |
| Maximum amplitude of VFTO [p.u.] | 1.554 | 1.13 | 1.0 |

It can be observed that a higher damping effect was achieved by increasing the equivalent inductance of the damping busbar. Furthermore, increasing the inductance value can damp the VFT waveform’s front. It is also helpful for absorbing the sharp spikes of the VFT since surge arresters do not act quickly enough to prevent steep-fronted switching transients. Therefore, installing an additional spiral coil with the damping busbar is highly valuable to attenuating the effects of VFTs. For this reason, a spiral coil of resistive litz wire was designed to be installed in the helical groove and connected in series with the damping busbar.

5. The Improved Design of the Damping Busbar

A further enhancement of the suppression effect was investigated by designing a spiral litz coil connected in series with the busbar, which increases the total inductance value. In order to design the spiral coil, an algorithm based on air-gap calculation was developed. This algorithm designs a spiral coil for a specific inductance value, which was determined as an input value. Other parameters, such as VFTC characteristics and the number of turns of the damping busbar, were also inserted in the input data.

Furthermore, litz wire was chosen for the improved design because it is mostly used for the frequency range 10kHz to 2 MHz. The main advantage of this configuration is to minimize the power losses due to “skin and proximity effect”, and it is desirable in high-frequency applications as well. Sullivan, C.R. and Zhang, R.Y applied a simple method for a suitable litz wire design [35]. The flowchart of the procedure used to calculate the spiral coil design parameters is shown in Figure 9. By determining the inductance value and the number of turns, then magnetic flux density could be calculated. After that, the eddy power losses in the litz coil were calculated for the optimal design.
However, we can divide the strand-level proximity effect into the internal proximity effect (the effect of other currents within the bundle) and the external proximity effect, but the total proximity effect may be considered a result of the total field at any given strand [36–38]:

\[ P_{\text{eddy, strand}} = \frac{\pi \omega B_2^2 d_s^4 n l}{128 \rho_c} \left(1 + \frac{\pi^2 n d_s^2}{4 K_a p^2}\right) \]  

(6)

where \( \omega \) is the angular frequency, \( l \) is the length of the bundle, \( \rho_c \) is the resistivity of copper, \( p \) is the pitch of the twisting, \( n \) is the number of strands in a bundle, \( d_s \) is the diameter of each strand, \( B_2^2 \) is the peak magnetic flux density, and \( K_a \) is the strand packing factor. Thus, the AC resistance factor, \( F_r = R_{ac} / R_{dc} \), for a litz-wire winding for arbitrary waveforms and 2-D or 3-D field geometries can be expressed as follows:

\[ F_r = R_{ac} / R_{dc} = 1 + \frac{(\pi n N_s)^2 d_s^6}{192 \delta^4 b^2}. \]  

(7)

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**Table 5**

| Design Specification | Specific Parameters of VFTC: Frequency and Peak Value |
|----------------------|------------------------------------------------------|
| Air gap calculation | \( I_g = \frac{N^2 A_s \mu_0}{L} \)                        |
| Calculate magnetic flux density | \( \overline{B^2} \)                                        |
| Determine power losses in stranded litz winding |                                      |
| Apply the simple method outlined by Sullivan et al. for a suitable wire design with critical condition |                                      |
| Output the litz coil configuration by determining the diameter of litz wire and number of bundles |                                      |

**Figure 9.** The flowchart of the procedure to calculate the litz coil design.

The power loss in a stranded-wire winding is derived from \( P = F_t P_1 = F_t \frac{I_{\text{rms}}^2}{n A_s} \), where \( A_s \) is the cross-sectional area of a strand. Consequently, by inserting the operating frequency, the AWG (American wire gauge) can be chosen (i.e., when frequency range is from 1.4 MHz to 2.8 MHz, then the...
best choice is AWG = 48), which means that about 70% loss reduction is achieved. Eddy power losses were calculated, and the final design was determined according to the critical factor (diameter of litz wire no more than the width of the spiral slot on the damping busbar surface).

The aim of calculating the air gap was to determine the magnetic flux density in order to calculate the power losses in the litz wire for the optimal design of the coil. Thus, for a typical design, we completed the following:

1. Determine \( A_c \) of the core, which was an aluminum cylinder (busbar body) in our study.
2. Determine the number of turns which was commensurate with the design.
3. Determine the inductance value which achieves an effective design to avoid saturation.
4. Calculate the air gap length as a first step to calculate the magnetic flux density in order to calculate the power loss in the litz wire for the optimal design of the coil. Thus, the length of the air gap was calculated by using Ampere’s law, as shown below:

\[
l_g = \frac{N^2 A_c \mu_0}{L}.
\]  

(8)

Many different combinations of strand diameter and number of strands could be designed and manufactured for any given cost. Thus, the number of bundles and, finally, the litz coil diameter could be obtained using a critical condition (diameter of the litz wire no greater than the width of the spiral slot on the damping busbar surface). The previously described technique was used to calculate the litz coil design. Simulations were performed using MATLAB software by keeping the length of the coil, number of turns, and transient current parameters constant. The results are listed in Table 5.

| Inductance Value [mH] | Simulation Results for Litz Coil Design |
|-----------------------|----------------------------------------|
|                       | Number of Bundles | Number of Strands | Strand Diameter [mm] | Construction Type |
| 0.1                   | 5                  | 140              | 0.899                | Stranded litz wire, type 2 |
| 0.3                   | 5                  | 140              | 0.838                |                     |
| 3.0                   | 5                  | 140              | 0.702                |                     |

Table 5. Simulation results for the litz coil design.

It is worth noting that the inductance value of both the damping busbar and the spiral litz coil was calculated at 2 MHz. An accurate choice of construction type of the litz design leads to a higher capability for greater current carrying capacities. The larger Type 2 and 3 litz designs have this essential feature for high-frequency power supply, inverter, and grounding applications. A Type 2 litz construction is distinguished by bundles of twisted wires together, while Type 3 features individually insulated bundles of Type 2 litz wire [39]. As a consequence, in this study we aimed to add a spiral litz coil located in the sweeping spiral on the surface of the damping busbar in order to improve its performance in VFTO mitigation. EMTP simulation was carried out to study the VFTO with and without installing the improved damping busbar. \( L_{\text{coil}} = 0.1 \) mH is the equivalent inductance of the spiral litz coil and \( L_{\text{busbar}} = 0.33656 \) mH is the equivalent inductance of the damping busbar. Thus, \( L_{\text{eq}} = 0.43656 \) mH is the equivalent inductance of the improved damping busbar. The simulation was set up based on \( L_{\text{damp}} = 0.43656 \) mH, as presented in Figure 6. Thus, Figure 10b shows the higher damping effect of the improved damping busbar. In order to clarify the damping effect, Figure 10d illustrates the damping effect of the improved damping busbar for a singular switching operation at approximately the same moment (0.013 s.). In order to clarify the higher damping effect shown in Figure 10b, further details about the damping effect of the improved design are discussed in the next section.
The transient characteristics of the VFTO after adding a 0.1 mH litz coil are illustrated in Table 6.

The main advantages of this improved design are the higher damping effect as compared with other existing expensive VFTO suppression methods that require changes in GIS design and construction, due to the extra cost. So, in future work, it will be fundamental to develop the design to improve the estimation quality to meet the industry’s requirements for low costs. However, improving the design and material type could be useful to achieving an optimal suppressing effect. 

Another obvious advantage of this improved design is no magnetic saturation problem and mitigation of the overvoltage magnitude due to dissipation of the transient energy in the damping resistance. On the other hand, litz wire has been classified as a cost-effective choice to reduce eddy current loss in high-frequency transformer and inductor windings. Some solutions could be successful in increasing the dielectric strength for a high voltage level or for safety requirements, like adding thermoplastic insulation such as PTFE, which could be used for bundle insulation on litz wire in some applications [36].

The proposed improved damping busbar has limitations when thermoplastic insulation is added due to the extra cost. So, in future work, it will be fundamental to develop the design to improve the estimation quality to meet the industry’s requirements for low costs. However, compared with other existing expensive VFTO suppression methods that require changes in GIS design and construction,
the proposed method can be easily implemented in any existing GIS without any change in its structure, making it very cost-effective. Therefore, it is necessary to use simulation calculations to optimize the insulation reliability of the uniform electric field and increase the insulation of the busbar. Table 7 shows the mitigation effect of different damping methods, including the new method.

Table 7. Comparison of different mitigation methods with the new damping busbar.

| Method                          | Mitigation Effect          |
|---------------------------------|-----------------------------|
| Damping resistor                | Up to 25%                   |
| Slow-acting disconnector (low trapped charge voltage) | (15–25)%                  |
| High-frequency resonator        | Up to 20%                   |
| Ferrite rings                   | Up to (10–30)%              |
| Damping busbar                  | (22.22–52.94)%              |

7. Conclusions

A new method to suppress VFTO was proposed, called a damping busbar. The mechanism of this new method was analyzed. A calculation method for the self-inductance of the damping busbar at high frequency was presented. Moreover, the inductance effect of the damping busbar on VFTO suppression was studied as well. As a result, the amplitude of the VFTO was reduced by approximately 27% after applying the damping busbar. A further enhancement of the suppression effect was investigated by designing a spiral litz coil connected in series with the busbar, which increased the total inductance value. Consequently, a good damping effect was achieved by utilizing the inductive and resistance arrangement which was formed by the improved design of the spiral tube damping busbar. The conclusions are as follows.

1. We proposed a new design which has a significantly better damping effect when compared with other existing suppression methods. The proposed damping busbar can be easily implemented in any existing GIS without any change in its structure and is therefore very cost-effective.
2. We investigated a calculation method for the self-inductance at high frequency.
3. We improved the performance of the damping busbar by adding a spiral litz coil connected in series with the busbar in order to enhance the suppression effect, and we developed an algorithm based on air gap calculation to design the litz coil.
4. We comparing the damping effect with and without the damping busbar and the improved design.

However, research on improving the proposed design to increase the equivalent inductance value by increasing the length of the busbar and the number of turns has to be considered. Many different adaptations, tests, and experiments should be investigated in future work (i.e., experimental study of a 1000 kV GIS station VFTO after installing the improved damping busbar).

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