Adaptive Merging on Phase Change Memory

Wojciech Macyna*, Michal Kukowski
Faculty of Information and Communication Technology
Wrocław University of Technology
Wrocław, Poland
wojciech.macyna@pwr.edu.pl michal.kukowski@pwr.edu.pl

Abstract. Indexing is a well-known database technique used to facilitate data access and speed up query processing. Nevertheless, the construction and modification of indexes are very expensive. In traditional approaches, all records in the database table are equally covered by the index. It is not effective, since some records may be queried very often and some never. To avoid this problem, adaptive merging has been introduced. The key idea is to create an index adaptively and incrementally as a side-product of query processing. As a result, the database table is indexed partially depending on the query workload.

This paper faces the problem of adaptive merging for phase change memory (PCM). The most important features of this memory type are limited write endurance and high write latency. As a consequence, adaptive merging should be investigated from the scratch. We solve this problem in two steps. First, we apply several PCM optimization techniques to the traditional adaptive merging approach. We prove that the proposed method (eAM) outperforms a traditional approach by 60%. After that, we invent the framework for adaptive merging (PAM) and propose a new variant of the PCM-optimized index. It further improves the system performance by 20% for databases where search queries interleave with data modifications.

Keywords: phase change memory, adaptive merging, database indexing

*Address for correspondence: Wrocław University of Science and Technology, Wybrzeże Wyspiańskiego 27, 50-370 Wrocław, Poland.

Received April 2022; accepted December 2022.
1. Introduction

Database indexing is a very mature technique for accelerating query processing. However, index creation and maintenance can decrease the performance of the database. In traditional approaches, an index covers all records equally. However, in real applications, some records are queried often and some never. Let us assume that the database records selling transactions in the table `Sales` containing the attributes: `Product`, `Quantity`, `Value` and `Date`. If the records from the last year are queried, it is not necessary to index the whole table by `Date`. In this case, it would be more profitable to index only the records of the last year and leave the older ones not indexed.

To solve this problem, two main approaches of partially indexing were introduced: database cracking and adaptive merging. In the database cracking (see [1], [2], [3], [4]), the index is incrementally adapted in response to the actual query workload: only such tables, columns and key ranges inside the table are indexed which are really queried. In the case of adaptive merging ([5], [6], [7], [8]) the data are split into $n$ sorted partitions before starting the merging process. Each merge step affects only such key ranges that are relevant to actual queries, leaving records in all other key ranges in their initial places. Figure 1 shows an example of adaptive merging. First, the data entries are loaded into partitions and sorted inside them. Then, the entries fetched from the partitions by query Q1 are stored in the final partition. These entries are depicted using the shadowed font. After that, the query Q2 operates on the partitions updated as a result of Q1. Subsequent queries may continue indexing until all data entries for the given workload are stored inside the final partition. Thus, indexing is a side effect of the executed queries.

Phase change memory (PCM) is one of the most promising non-volatile storage media. Due to its byte addressability, high access speed and low energy consumption, PCM is the best candidate to cooperate with DRAM as a main memory storage type. However, the drawbacks of PCM are: limited write endurance, high write latency, and asymmetry between read and write operation time. A PCM cell can endure only $10^7 - 10^8$ writes. Thus, DRAM cannot be replaced by PCM completely.
Nowadays, the research of PCM is concentrated on many aspects. To prolong the memory lifetime, we need a mechanism that distributes write operations to PCM cells uniformly (see [9], [10], and [11]). Consequently, many data management techniques must be changed in order to reduce the write traffic (see [12], [13], and [14]).

As PCM is a relatively new storage media, many issues related to databases should be optimized. The paper faces a problem of adaptive merging on PCM. We resolve the problem in two steps. First, we improve the traditional adaptive merging (AM) by applying several PCM optimization techniques. We call this method: extended adaptive merging (eAM). The detailed descriptions of AM and eAM are presented in subsection 5.2.2. Then, we further improve the adaptive merging by inventing a framework that takes into consideration crucial PCM limitations such as: data access asymmetry and limited memory cell endurance. Although the framework can cooperate with any index type, we use a new index called BB+tree. The index is an extended version of DPTree [15] optimized for adaptive merging. In our model, we consider DRAM as a main memory and PCM as the secondary storage.

The key contribution of this paper is the adaptive merging framework described in section 3. Most of the approaches to partial indexing aim at fast and effective index convergence after receiving a set of range queries and do not consider database modification. Our framework, on the contrary, is very efficient also in the dynamic database environment where updates interleave with range queries. The second contribution is the adaptation to PCM. We do this by applying some PCM-optimized techniques and proposing a modified variant of PCM index (see section 4). Section 5 presents the experiments which prove the suitability of both the framework and the new techniques for adaptive merging on PCM.

2. Background

2.1. Phase Change Memory (PCM)

Phase Change Memory (PCM) has attracted attention due to non-volatility, high density, shock-resistivity, and low leakage power. PCM is built out of chalcogenide-based materials. It stores bits by heating a piece of chalcogenide glass with a large current. After cooling down, two forms with different electrical resistance are created: a crystalline state corresponds to "1" and an amorphous state corresponds to "0" [16].

PCM has been considered as a promising candidate to replace DRAM due to its lifetime and access latency. Nevertheless, the lifetime of PCM is about $10^{9}$ times of writes per cell. It is relatively short compared with DRAM. Apart from this, PCM consumes more energy than DRAM for each write. To cope with these problems, the current research is divided into two categories. The first category targets on reducing the total number of writes to PCM. The second category is focused on evenly distributing writes to memory cells. This technique is called wear-leveling.

To reduce the total number of memory writes, a hybrid memory architecture consisting of DRAM and PCM is proposed (see [17] and [18]). Zhou et al. [19] introduce bit-writes removal to avoid unnecessary writes. Namza et al. [20] create a DRAM-based system that is used as a cache of PCM. In this way, the write-intensive data could be held in DRAM and the read-intensive one in PCM.
Wear-leveling is a mechanism for extending the lifetime of PCM. Memory blocks should be worn out equally. This is because if one block of PCM wears out, the whole PCM is regarded as broken. Wear-leveling can be implemented at the hardware (see [19]) or operating system level (see [21]). In [22], Liu et al. provide an application-specific wear-leveling method, which gradually changes the mapping of hot regions in address space to a different part of physical memory. In [23], the most important works about prolonging PCM lifetime are reviewed.

2.2. Indexing on PCM

To reduce PCM write traffic, database index structures must be redesigned.

In [12], the authors deal with B+tree index optimized for PCM. They present improved algorithms that reduce both execution time and energy on PCM while increasing write endurance.

The authors in [24] enhance the performance of B+-tree by introducing unsorted and overflow nodes. The unsorted nodes are used to avoid sorting of items in insert operations. The overflow nodes reduce the write count in the cases where the existing schemes are ineffective.

In [25], the authors are studying indexing on PCM/DRAM-based hybrid memory and propose an improved version of the B+tree called XB+tree (eXtended B+tree). The key idea is to detect the read/write tendency of the index nodes and organize read-intensive nodes on DRAM while putting write-intensive nodes on PCM. To effectively move nodes between DRAM and PCM, they propose a new node management and migration algorithm. In this way, the read and write operations on PCM are reduced.

The authors in [26] propose a hybrid persistent and concurrent Fingering Persistent Tree (FPTree). The FPTree uses fingerprinting, a technique that limits the expected number of in-leaf probed keys to one. The leaf nodes of FPTree are stored on persistent memory while inner nodes are placed in DRAM.

The paper [15] proposes a new index fully optimized for PCM called DPTree. The index consists of two components: the B+ tree in DRAM and the main index on PCM. First, the operations like insert, delete and update are accumulated in the B+tree. Then, the entries stored in the B+tree are merged into the main index on PCM. Worth mentioning is the fact that the index modification is performed in batches which drastically amortizes persistence overhead. DPTree can work concurrently for multicore processors and includes several techniques to achieve crash consistency.

In [27], a PCM-variant of the R-tree is proposed. R-tree is a well-known index that can handle spatial data. The proposed PCR-tree has a more compact structure with fewer overlaps. It reduces the number of PCM writes 30 times in comparison to the traditional R-tree. Moreover, the insert performance is improved by 70% on average.

The paper [14] proposes an implementation of the aggregated R-tree optimized for PCM. The aggregated R-tree extends the original R-tree with the aggregated values connected with the nodes. The proposed method records the most profitable set of aggregated values in the case when the memory size is limited. The method chooses to store frequently asked values and the aggregated values with high calculation cost.
2.3. Partially indexing

In traditional indexing, all data of a particular column or columns are indexed immediately after index creation. Such an approach is not effective in situations when not all data are queried. Database cracking is an approach where index maintenance is a part of query processing using continuous physical reorganization. In [2], the first mature cracking architecture is presented. The approach is implemented as a relational system with minor enhancements to its relational algebra kernel. In [1], the authors go beyond the static database. They introduce several novel algorithms for data modification against a cracked database and prove that the good performance of a cracked database can be maintained in a dynamic environment where updates interleave with queries. Further improvements are made in [28] where many different experimental settings, including high and low selectivity queries, and multiple query access patterns are tested.

In [5], an alternative approach called adaptive merging is introduced. The main difference is that the data are split to \( n \) sorted partitions before starting this process. Each merge step affects only such key ranges that are relevant to actual queries, leaving records in all other key ranges in their initial places.

Both approaches have some weaknesses. Adaptive merging has a high initialization cost but converges rapidly. On the other hand, database cracking enjoys a low initialization cost but converges relatively slowly. To overcome such limitations, a hybrid approach is proposed (see [7]). The authors implement several hybrid algorithms for a column-store database system. They compare a performance of their system against database cracking and adaptive merging, as well as against both traditional full index lookup and scan of unordered data. Another hybrid approach is proposed in [8]. It utilizes a cost model to determine the best operation to refine the index. Experiments show that the hybrid approach can achieve appropriate performance trade-offs between database cracking and adaptive merging. In [6] the authors propose three alternative parallel algorithms for adaptive indexing designed for the multi-core system. The proposition of partial indexing for flash memory can be found in [29]. Nevertheless, the authors concentrate on the block addressable memory type which has completely different characteristics than PCM.

Our paper presents a method for adaptive merging for PCM. The approach is not just limited to optimizing the traditional adaptive merging methods for PCM (see [7] and [5]), but it can be treated as a framework where many different index types can be utilized. We do not take into account database cracking since this approach is not suitable for memory types where the write operation is much more time-consuming than the read one (see [5]). As a further optimization, we improve DPTree index ([15]) to exploit its potential for bulkloading, which is a dominant operation in adaptive merging.

3. Adaptive merging framework

In this section, we propose a new adaptive merging framework (PAM) tailored for phase change memory.

Adaptive merging is a process that creates or refines the database index as a side effect of query execution. Thus, the index is not created for all records of the table at once, but gradually and only for records retrieved by consecutive database queries. Adaptive merging is applied for the secondary
index in databases. So, we consider an entry as a pair: \( \langle \text{key}, \text{ptr} \rangle \), which denote the key value and a pointer to the record, respectively. In the subsequent part of the paper, we will call such an index as "merge index".

Two kinds of events may affect the merge index.

- A search query can invoke adaptive merging process. Intuitively, a query of the key range \( <k_1, k_2> \) can add all entries with keys between \( k_1 \) and \( k_2 \) to the merge index.

- As with traditional indexes, operations such as insert, update and delete may affect the merge index. Clearly, adding a record with key \( k \) to the database results in inserting an entry into the merge index. On the other hand, removing a record from the database deletes the respective entry from the merge index if the entry has been inserted into it before.

3.1. System concept

The proposed framework consists of two parts. The first part is mainly stored on PCM and has the following components:

- **Partition.** A structure for storing merge index entries. The partitions are created at the beginning of the adaptive merging process. The entries within each partition are sorted by the key. Each partition holds in DRAM the minimal (\( \text{min} \)) and maximal (\( \text{max} \)) key value. So, it is very easy to determine which partitions should be accessed during the particular query execution. When all entries of the partition are moved to the merge index, the partition can be removed from PCM. The adaptive merging process starts with copying all entries to the sorted partitions.

- **Deletion Journal.** If some data is removed from the database, the merge index entry corresponding to that data must be deleted as well. The deletion journal holds the entries deleted from the partition or the entries deleted from the index that was previously copied to it from the partition. The deleted entries must be held on PCM so that they could be restored after the system crash.

- **Index.** Any index optimized for PCM. Although the framework can work with any index, we propose our version of PDTree that shows very good performance in the adaptive merging process (see section 4).

The second part is stored in DRAM and contains:

- **Sorting buffer.** The DRAM resident buffer is used to sort the merge index entries before inserting them into the partition. We assume that the maximal partition size should not exceed the sorting buffer size.

- **Insertion Journal.** It holds a list of key ranges already copied by the query from the partitions to the merge index. Such values may be stored in DRAM because they can be easily restored after the system crash using the merge index. To speed up access to the insertion journal, the values are stored in a tree-like structure. For simplicity, we do not reflect this fact in the subsequent description.
• **Partition set.** The partition set is used to manage the partitions. It contains a list of partitions with their attributes. The attributes depict the minimal (\(\min\)) and maximal (\(\max\)) key values inside the partition. In this way, it is very easy to identify which partition holds the required data. Apart from that, the memory addresses of the first and the last entry in the partition are stored to speed up the memory search. If all entries from the partition are copied to the merge index, the partition is removed from PCM.

![Figure 2](image)

**Figure 2.** Adaptive merging for PCM

Figure 2 shows an example of our method. Let us consider a database table containing entries with the following keys: \((b, s, z, e, i, a, c, k, q, u, x, n, p, l, d, f, r, t, m, z)\). In the initial state, the structures like the merge index, deletion and insertion journal, and the partition set are empty. Let us assume that we receive a query of the range \(f - m\). The entries are sorted in the sorting buffer and stored in the partitions: \(P1, P2, P3, P4\). The selected entries \(f, i, k, l, m\) are added to the merge index and the range \(f - m\) is stored in the insertion journal. Additionally, the partition set is updated: it holds a partition \(id\) and the minimal and maximal key value in the partition. As we mentioned above, the partition set holds also the PCM addresses of the first and last entry in the partition. For simplicity, we do not show it in the example. In the second query, the values between \(a\) and \(h\) are selected. In that case, the content of the particular partitions changes. The values: \(b\) and \(e\) from \(P1\), \(a\) and \(c\) from \(P2\), and \(d\) from \(P3\) are copied to the merge index and become invalid. This is marked using the shadowed font. Please note that \(P4\) remains intact since it contains data out of the required range. Moreover, the values \(f, g, h\) are inside the insertion journal. It means that they can be fetched directly from the merge index by subsequent queries. After the query execution, a new range \(a - h\) is added to the insertion journal. The last query is an example of a delete operation. The deleted values of the range \(r - t\) are removed from \(P4\) and added to the deletion journal.
As we can see in this example, the merge index is created as a side-product of query execution and contains only the entries fetched by the queries.

### 3.2. Database operations

In this subsection, we describe basic database operations that affect the merge index.

#### 3.2.1. Search

A search method (Algorithm 1) takes the merge index and a key range as an input. It returns the entries whose keys are inside the key range. The requested entries may be fetched either from the partition or from the merge index directly. The algorithm works as follows. If the key is found in the merge index (line 4), it can be added to the result set immediately (line 6). The condition (line 7) checks if the required key has not been copied from the partition to the merge index and has not been deleted from the partition. If it is true, the partitions which may hold the required entry are determined using their min and max attributes (line 8). If the required entry is found in the partition, it is added to toInsert.

```
Algorithm 1: search(input: Index ind, Key keys[])
1   Entry result[] := ∅
2   Entry toInsert[] := ∅
3 foreach Key k ∈ keys do
4      Seek Entry e with key k in ind
5      if e exists in ind then
6         result := result ∪ e
7      if k not in InsertionJournal and k not in DeletionJournal then
8         // The selection is made using partition attributes: min and max
9            Select Partition par[] containing k
10        foreach Partition p ∈ par do
11           Seek Entry e with key k in p
12           if e exists in p then
13              toInsert := toInsert ∪ e
14              result := result ∪ e
15        // Adding entries from toInsert into the merge index starting from the root
16        bulkload(root(ind), toInsert)
17        // Journal and partition set updating
18        Pair p := (min(keys), max(keys))
19        updateInsertionJournal(p)
20        updatePartitionSet()
21   return result
```
array (line 12) and to the result set (line 13). After finding all the keys, the entries are inserted into the merge index (line 14). To do this, the bulkload described in the next section is invoked. At the end of the algorithm, the insertion journal and the partition set are updated (lines 16 to 17). The algorithm returns all entries of the required key range and modifies the merge index as a side effect.

3.2.2. Insertion

The insert operation is invoked when a new record must be added to the database. In our method, the insertion is made directly to the merge index without changing any partition.

3.2.3. Deletion

The delete operation is executed when the data must be removed from the database. The data may be deleted either from the merge index or from the partition. In the first case, the index is modified in a way specific to the index type. Typically, it is accomplished by inserting an entry marked as "ToDelete" into the merge index and then removing this entry during index reorganization. In the second case, the entry is simply deleted from the partition. This fact is stored in the deletion journal. It may be a situation that an entry has been copied from the partition to the merge index and then removed from the index. In this case, the entry is stored in the deletion journal as well.

3.2.4. Update

The update operation is executed when a modification of the record occurs. In this situation, the old version of the record is deleted and the new one is inserted into the database.

3.3. Crash recovery

This subsection sketches a problem of crash recovery. We do not consider transaction issues because they are specific for the index type and are out of the scope of this paper. We concentrate only on recovering the data structures stored in DRAM:

- **Insertion Journal.** It can be recovered by traversing all the keys in the index and creating key ranges from them. For example, if the crash happens after the search \( a - h \) (see Figure 2), index traversing creates \( \langle (a - f), (i - i), (k - m) \rangle \) in the insertion journal. Although we lose some information about query ranges processed in the past (e.g. \( (f - m) \)), we still have correct information about real values inside the index.

- **Partition Set.** To recover the partition set, we must pass every partition. For each entry in the partition, we check whether the entry is written in the deletion journal. If so, we don’t consider it for min and max value calculation.

- **Entry Log.** An entry can be added to the B+tree in DRAM but not merged with the main index yet. In this case, it would be lost in the system crash. To prevent it, we use the entry log stored on PCM similar to [30].
4. Index choosing

The proposed framework can work with any index type. However, to exploit the PCM characteristics, we adapt and improve DPTree proposed in [15]. The core idea of DPTree is to batch multiple writes in DRAM persistently and, then, merge them into the main index on PCM. The index consists of two components (see Figure 3). The first component is the B+ tree in DRAM where the data from operations like insert, delete, and update are collected. The second component is the main index, which is stored partly in RAM and partly in PCM. If the B+tree extends the fixed threshold, its entries are merged with the entries of the main index. In this way, index modification is performed in batches which drastically amortizes persistence overhead. Worth mentioning is the fact that a leaf node level is stored persistently on PCM and all inner nodes are inside DRAM. Thus, the inner node’s reorganization cost is avoided.

We did some improvements in DPTree. We observed that adaptive merging can insert many batches with entries sorted by key. As a result, the sorted entries once inserted into a leaf node remain intact for a long time. On the other hand, database modifications can add some new entries into the sorted leaf nodes. In this case, it is better to insert such entries at the end of the node in an unsorted fashion than to reorganize the entire node. To accomplish this, we divide a node into two sections: sorted and unsorted. To find an entry in the sorted section, we utilize a binary search. Additionally, each node has a bitmap that indicates if the entry is valid or not. When the entry is deleted from the node, it is not removed immediately from PCM but marked as invalid in the bitmap. The size of both sections is aligned with the multiplication of the cache line size. In this way, the number of cache misses is reduced during linear searching.

The node size is an important parameter that affects the performance of the tree. Previous works suggest that a tree node size should be equal to the size of a few cache lines. For modern computers, the cache line size is usually 32, 64, or 128 bytes. We fix the cache line as 64 bytes for all the methods. Consequently, the node size of the BB+tree in PAM is 512 bytes.

In summary, our index, named BB+tree, has the following features:

- It contains the DRAM B+tree where the data are collected.
- It contains the main index with leaf nodes on PCM and inner nodes in DRAM.
- A leaf node has two sections: sorted and unsorted.
- The bulkload operation merges the batches stored in B+tree with the entries in the main index.

4.1. Merge index modification algorithms

As we mentioned before, two types of database operations can affect the merge index: searching and modification. The data obtained from these operations are first collected in the B+tree in DRAM. When the B+tree is full, the bulkload is invoked.

In this subsection, we present two algorithms for index modification. The first algorithm describes bulkload. The second one reflects the inserting of the entry into the main index.
Algorithm 2: bulkload(input: Node node, Entry entries[ ])

1 if length(entries) = 0 then
2     return
3 if isLeaf(node) = TRUE then
4     insert(node, entries)
5     return
6 Key lastMax := −∞
   // Go down to the children nodes with the subset of entries with keys
   // between minKey and node.keys[i]
7 for i ← 1 to FANOUT do
8     Key minKey := lastMax
     // node.keys[FANOUT] is always equal to ∞
9     Key maxKey := node.keys[i]
10    lastMax := maxKey
11    Entry slice[ ] := sliceMake(entries, minKey, maxKey)
12    bulkload(node.child[i], slice)

Algorithm 3: insert(input: Node node, Entry entries[ ])

1 Node n = node
2 foreach Entry e ∈ entries do
3     if nmbValidEntries(n) < FANOUT/2 then
4         n = MergeNodes()
5         UpdateInnerNodes()
6     if nmbValidEntries(n) > FANOUT then
7         n = SplitNode()
8         UpdateInnerNodes()
9     if eToDelete = TRUE then
10        delete(n,e)
11     else
12        if existsGapInUnsorted(n) = TRUE then
13           insertIntoUnsortedGap(n,e)
14        else if unsortedNotFull(n) = TRUE then
15           insertIntoUnsorted(n,e)
16        else if existsGapInSorted(n) = TRUE then
17           insertIntoSortedGap(n,e)
Algorithm 2 describes bulkload as a dominant operation in adaptive merging. The method inserts many entries into the main index in batches. It starts in the root node and works recursively. The procedure takes a node and an entry set as an input. As the root and inner nodes have pointers to the child nodes, the input entry set is split according to the key ranges of the child nodes. It is reflected in lines 6 to 12. When the leaf level is reached, the appropriate entries are added to the leaf (lines 3 to 5). For this, algorithm 3 is responsible.

Algorithm 3 inserts the entries into the leaf nodes. When the inserted entry is marked as "ToDelete", it is removed from the leaf node of the main index. It is reflected in lines 9 and 10.

When some entry has been removed from the leaf and the leaves have not been reorganized yet, memory gaps are created. They can exist either in the sorted or in the unsorted section of the node. When the normal entry is inserted into the leaf node, the algorithm checks the gaps in the unsorted section. If it finds them, the entry is inserted into the gap (lines 12 and 13). If not, the entry is inserted into the free place in the unsorted section (lines 14 and 15). When the unsorted section is full, the algorithm seeks the gap in the sorted section of the node (lines 16 and 17). When there is no place in the requested leaf node, a new one must be created. In the other case, the index must be reorganized in the following way. If the number of valid entries in the node is less than $fanout/2$, the nodes are merged (lines 3 to 5). On the other hand, if the number of valid entries in the node is more than $fanout$, the node is split (lines 6 to 8). In both cases, the inner nodes up to the root node can be reorganized (lines 5 and 8).

![Figure 3. Bulkload example](image-url)
Figure 3 shows an example of the index modification. We assume that the fanout of the main index is equal to 4. Each node of the main index is divided into sorted (left) and unsorted (right) sections. The example consists of six steps. In the first step, a batch containing seven new entries is added to the B+ tree in DRAM. After that (step 2), the main index is created and the entries are inserted into it using the bulkload method. Please note that the main index has three leaf nodes on PCM and one inner node in DRAM. As it is the first batch, all entries inside the nodes remain sorted. In step 3, a batch consisting of two entries to insert and two entries to delete is added to the B+tree. The merging (step 4) is performed as follows. Algorithm 2 is invoked for the root node and the entry set. The entry set is split into three subsets because the root node has three children. This is reflected in the recursive section of the bulkload method (lines 7 to 12). After that, the insertion to the leaf nodes is performed in the following way. Entry with key 3 is removed from the leftmost leaf node. As the number of entries in the node must be at least two, this node must be merged with the middle node. Now, the newly created node contains the entries: 1, 5, 7. After that, entries 6 and 8 are inserted to this node. Since the node has five entries, a node split is needed. As a consequence, two nodes containing the entries: 1, 5, 7, and 6, 8 are created. At last, entry 13 is marked as deleted. This fact is denoted using the light font. The leaf node update results in the reorganization of the inner nodes. Thus, the root node is changed: key 5 is removed and key 6 is added to the unsorted section of the root node. In the last batch, two entries: 10 and 14 are added to the B+ tree (step 5). Both entries should be inserted into the rightmost leaf node. Entry 10 is inserted into the unsorted section of the node and entry 14 fills the gap formed by removing entry 13 in the sorted section (step 6).

5. Performance evaluation

The section consists of two parts. In the first one, we describe the implementation of the PCM simulator. The second part presents some experiments which confirm the effectiveness of the proposed methods.

5.1. Implementation issues

5.1.1. PCM simulator

The core of our system is a PCM simulator. The implementation is written in C and compiled with gcc 9.3. We simulate PCM in the same way as proposed in [12]. First, we model data comparison writes for PCM writes. We write a 64B cache line to PCM and compare this line with the original one to compute the number of modified bits and the number of modified words. Second, we model eight PCM memory ranks with parallel access. At last, we model the details of cache line write back operations. Since we have eight parallel 8B ranks, we can do a flush cache (64B) as fast as one 8B write. In modern PCM, most of the work is done by the controller. However, it depends on the software developer how the entries in the write queue are arranged.

Typically, the controller manages 32 or 64-entry FIFO write queue. Even if the controller can do writing in the background, it is fully stressed during write-intensive workloads (the write queue is almost always full). That is why, we favor perfect cache utilization, i.e. writes that need to change
64B are better than several writes changing 8B sequentially one after another. To accomplish this utilization, we introduce a new way to calculate fill factor for each index. Typically, the bulkload creates the node with a fixed 70 - 80% fill factor. In our method, we use 80% with alignment to 64B. We initialize a node with an unsorted area aligned to several cache lines. In this way, the linear search works on full cache lines.

In the simulator, we fixed the following PCM parameters:

- Page size: 64B
- Page read latency: 50 ns
- Page write latency: 1 $\mu$s
- Write bandwidth: 64 MB/s per die

5.2. Experiments

We divide the experiments into three parts. The first part compares PCM-optimized indexes. We consider three candidate index types and choose the best for our purpose. In the second part, we compare all implemented methods of adaptive merging under different workloads. Each experiment is carried out until the merge index is completed. In the third part, we place our method in a dynamic database environment where database modifications interleave with search queries.

The experiments were conducted on Intel Core i7-6700k 4.0Ghz (8CPUs) equipped with 32 GB RAM. For each experiment, we use a set of 100 million entries. The size of one entry is 16 bytes making the total size of the dataset equal to 1.6GB.

5.2.1. Index comparison

In this section, we experimentally compare different index types. Some of them were already mentioned in the previous subsections. Following PCM tendency, the inner nodes of all indexes are held in DRAM. The goal of the experiment is to justify that BB+tree is the most suitable index for adaptive merging on PCM. We do not consider the indexes like CB+tree (see [31]) with inner nodes stored on PCM since their reorganization cost is high.

We consider the following index types:

- UB+tree - B+tree with unsorted leaves.
- SB+tree - B+tree with two section nodes: sorted and unsorted.
- BB+tree - buffered B+tree with two section nodes: sorted and unsorted.

In the experiments, we used the following workloads:

- Write intensive workload: 10 batches, each batch contains 40000 inserts, 40000 deletes, and 20000 point searches.
- **Read intensive workload**: 10 batches, each batch contains 10000 inserts, 10000 deletes, and 80000 point searches.

- **Balanced workload**: 10 batches, each batch contains 25000 inserts, 25000 deletes, and 50000 point searches.

We estimate index performance without executing an adaptive merging process. Like in the traditional indexing, we first create and fill each index with 100 million entries using bulkload. As a consequence, we obtain the index with a completely filled sorted section and an empty unsorted section. Then, each of the above mentioned workloads is applied. It contains batches with a different number of operation types.

As we can observe (see Figures 4, 5, and 6), introducing sorted and unsorted sections of the node (SB+tree) slightly decreases the elapsed time for the balanced and write-intensive workload. The read-intensive workload shows better performance since it exploits the binary search in the sorted section of the node. The application of the buffered index with sorted and unsorted sections (BB+tree) significantly improves the performance of the whole system. It is because the data are collected and merged with entries of the main index in batches. In this way, we experimentally prove that BB+tree is a strong candidate for adaptive merging on PCM.

![Figure 4. Balanced workload](image1)

![Figure 5. Read intensive workload](image2)

![Figure 6. Write intensive workload](image3)

![Figure 7. Invalidation](image4)
5.2.2. Adaptive merging implementations

In this subsection, we compare three different adaptive merging implementations. All of them use different index types. Following the current tendency, the inner nodes of each index are stored not on PCM but in DRAM. In the initial step of each implementation, all merge index entries are sorted and inserted into partitions. Thus, each partition holds the entries sorted by key.

We consider the following methods:

• **Original adaptive merging (AM).** The implementation is based on the method proposed for traditional block memory types [3]. In this method, a partitioned B+tree is built over the partitions and each of its nodes corresponds to one partition. As the merge index entries are sorted only in a partition’s scope, the entries with the same keys can be stored in many partitions. Thus, an entry of the partitioned B+ tree may point to many partitions. The approach is not suitable for PCM, since it shifts the data after each modification like in the traditional B+-tree. To make the implementation PCM friendly, we did some improvements. We created a memory pool for storing entries for deletion. The memory pool is useful for late materialization. This technique is used when the modification is not immediately reflected in the index but is postponed until the memory pool is full. Thus, if a range query is performed, the memory pool is scanned. The deleted merge index entries are removed from the result provided that they fulfill the search condition. Utilizing the memory pool drastically improves the method for PCM.

• **Extended Adaptive Merging (eAM).** The goal of this method is to adjust the standard adaptive merging (AM) to PCM characteristics. We observed that the operations like insert, delete, and search cause node modifications in the partitioned B+tree. Such a situation has a negative impact on PCM performance and increases the number of memory writes. To prevent it, we replaced the partitioned B+tree by UB+tree with a bitmap. The entries inside the leaf node are unsorted to avoid unnecessary memory overhead. Each entry is connected with one bit of the bitmap. If the entry is valid, the bit is set to 1. Otherwise, the bit is set to 0. In this way, any index modification results in changing one bit instead of reorganizing the entire tree node. Moreover, each partition is equipped with a bitmap as well. The bit in the bitmap indicates whether the entry is already moved from the partition to the index.

• **PCM Adaptive Merging (PAM).** The implementation is based on the framework proposed in this paper (see subsection [3]). As the merge index, we use BB+tree described in more detail in section [4].

This experimental series considers index convergence. It means that each experiment is conducted until all entries from the database table are inside the index. To do this, we use range queries with selectivity equal to 5% and data patterns described below. By selectivity, we denote which fraction of records is fetched by the query. We assume that the database is not modified during the adaptive merging. A combination of adaptive merging and database modification is considered in the next subsection.
For these experiments, we use the following data patterns:

- Random pattern - we draw a minimal key \((min)\) with a uniform distribution. Then, we select the records starting with this key so that the fixed selectivity is reached. Thus, we obtain the range \([min, min + rows]\) where \(rows = totalRows \times selectivity\).

- Sequential pattern - we create the sequential access pattern as follows (see [28]): starting from the beginning of the value domain, the queried range is shifted for each query by half of its size towards the end of the domain. When the end is reached, the query range restarts from the beginning. The position to begin is randomly set in the first 0.01% of the domain. In this way, the repetition of the same sequence in subsequent rounds is avoided.

- New keys pattern - each query asks for non-index data.

In the first experiment (Figure 7), we compare three invalidation methods for the data fetched from the partitions:

- Flag - the invalid data has an invalidation bit set to \(false\).

- Bitmap - each partition contains its own bitmap where data invalidation is registered (see [24]).

- Journal - the invalidation technique applied in PAM that uses the journal to indicate valid data.

We execute AM with the random pattern and selectivity 5% until the full index is created. Figure 7 presents the total time of invalidation operations for three invalidation methods. The worst performance shows the flag invalidation method (Fig. 7). It comes from the fact that one byte must be updated after the invalidation of each data. For bitmap invalidation, each data of the partition is associated with a single bit. Since selectivity is 5%, a lot of entries are marked by the same query. Thus, the bitmap can fully utilize the cache line. As the cache line is 64B, we can mark 512 entries at the same time. The best performance is notified by utilizing the journal.

![Figure 8. Sequential total](image)

In the next experiment, we measure the elapsed time and PCM wear-out for the different data patterns (see Figures 8, 9 and 10). As we can see, the traditional adaptive merging (AM) exhibits the worst performance. It comes from the fact that the method is not optimized for byte-addressable
memory. Both methods: eAM and PAM show similar performance (the difference is about 1.2 seconds in favor of PAM). The same trend can be noticed for memory wear-out. In the experiment presented in Figure 11, we applied the random pattern for PAM with different index types. As we can see, the index type does not affect PAM in the case when the adaptive merging without database modifications is considered.

5.2.3. Update cracking database

In this subsection, we show that the nice performance properties of adaptive merging can be maintained in a dynamic environment where database modifications interleave with search queries. We carry out the experiments for the workloads with database modifications. Each workload consists of a fixed number of batches, whereas each batch is a collection of delete, insert and range searches with selectivity from 1% to 5%. Using the different workloads, we check when the framework and when index type affect the performance of adaptive merging. For that, we compare eAM and PAM with the different index types inside. Additionally, we measure the impact of search selectivity on the performance of the methods. For each workload, we perform five experiments for selectivity from 1% to 5% in the range search. We do not show the results for AM, since its performance is very bad. It comes from the fact that AM is not optimized for database modification on PCM.

We use the following workload types:

- **WorkloadA**: 100 batches, each batch contains: 5 inserts, 5 deletes, and 10 range searches.
- **WorkloadB**: 5 batches, each batch contains: 100000 inserts, 100000 deletes, and 5 range searches.
- **WorkloadC**: 10 batches, each batch contains: 100000000 inserts, 100000 deletes, and 20 range searches.
- **WorkloadD**: 10 batches, each batch contains: 1000000000 inserts, 100000 deletes, and 10 range searches.
Figure 12 presents the elapsed time for WorkloadA. In this case, we need 5 to 10 (depending on selectivity) batches to complete the merge index. So, the framework works very short and the system hardly benefits from using it. As a result, the performance is similar for all methods: eAM and PAM with the different index types. The execution time does not depend on the index type, since the number of data operations is too small. Additionally, we can observe that the execution time increases with increasing selectivity. When the selectivity grows, the range queries need more time to fetch the data.

Figure 13 presents WorkloadB where database modifications are dominant. Since the number of range searches is not large, we do not reach a state where the merge index is completed. When the selectivity increases, the range queries fetch more data. As a consequence, more data is inside the merge index and less data is stored in the partitions. Thus, the overall system performance decreases. Moreover, PAM outperforms eAM. It is due to the optimization proposed in the framework. The use of the deletion and insertion journals significantly improves the system’s performance. When the merge index is not completed, the framework can manage the partitions very efficiently. Furthermore, selectivity affects cache utilization in eAM. As we mentioned before, each partition is equipped with a bitmap. A bit in the bitmap indicates whether the entry is already moved from the partition to the merge index. When the selectivity is small, fewer data are fetched from the partitions and fewer bits in the bitmap are updated at the same moment. As a consequence, the number of cache misses is large. This is not the case in PAM because it has the insertion journal instead of the bitmap. We can also
observe that all index types show the same performance. They behave similarly because the number of range searches is too small to fully exploit the advantages of using the two-section index.

Figures 14 and 15 present the performance of WorkloadC and WorkloadD, respectively. In both cases, the framework works very short and after that, many database modifications are performed. As we can see, all methods except PAM with BB+tree look similar. Since the framework has no impact on the performance, eAM works quite well in comparison to PAM with SB+tree and PAM with UB+tree. However, the large number of insertions and deletions is amortized by the utilization of BB+tree. This is why, the performance of PAM with BB+tree increases significantly.

From the latest experiment we can draw the following conclusions. When the database is large, the framework works for a long time. In this case, the system performance benefits from the optimization techniques used in the framework. When the database is small or heavily modified, the framework works for a short time and does not play a key role in the system. In this case, the system performance depends on the applied index.

5.2.4. Summary of experiments

We can summarize all the experiments as follows. We consider three aspects. The first aspect is index choosing for PCM. We applied three different workloads for each index type. The best performance exhibits BB+tree. Thus, we justify the utilization of this index during adaptive merging on PCM. In the second part of our experiments, we compare the different adaptive merging frameworks. We assume that the database is not modified during adaptive merging. As we can observe, eAM and PAM exhibit similar performance and overpowers the traditional approach (AM). The last case, where index merging interleaves with database modifications, is more common in the real world. In this situation, the proposed framework with BB+tree drastically improves the overall system performance.

6. Related work

6.1. Alternative partial indexing strategies

In this paper, we concentrate on the framework for the adaptive merging of the secondary index on PCM. The key parts of the framework are partitions with the data and the adaptive index. Each partition contains secondary index entries sorted by the key. The incoming query copies the entries from the data partitions to the adaptive index. Worth mentioning is the fact that the indexed data are not removed from the data partitions but are marked as invalid.

Index cracking is an alternative approach (see [28], [32], [33], and [34]). The core idea of index cracking is to consider a query as a hint for data reorganization which can lead to a full index creation. The index is created adaptively, i.e. each range query can potentially make two new sub-partitions using logic very similar to partitioning in quicksort. It leads to a write-heavy workload. Unfortunately, such a situation is unfavorable for PCM where the write operation is several times more time and energy-consuming than the read operation. As it was proved in [5], index cracking is not suitable for memory with read-write asymmetry.
In the hybrid cracking (see [7], [32], [8], [35], and [28]), the data are stored in the data partitions but, unlike in the case of adaptive merging, they are not initially sorted. When a range query arrives, the selected data are copied to the sorted final partition where the index is created or modified. After that, the indexed data are deleted from the data partition and the other data in this partition are sorted. The data partitions can be merged if they are too small. The final partition can be also rewritten as a result of sorting or merging. Such an approach leads to write-heavy workload. As a consequence, the overall system performance is decreased on PCM.

Our approach differs from the hybrid cracking in the following aspects. First, we do not rewrite or merge the initial partitions since it would lead to the write overhead on PCM. Instead of removing the already indexed data from the partition, we mark them as invalid using a journal. This technique is significantly faster than a bitmap invalidation (see [12]). Second, we initially sort the data in each partition. In this way, we avoid linear scanning of each partition during the search operation and can apply binary search instead. Moreover, keeping the data sorted is essential to use the journal. Fortunately, the partitions are initially sorted in the cache which causes a very small overhead. Third, unlike most approaches, our framework works in the dynamic database environment where updates interleave with range queries.

6.2. Comparison with other index types

In our approach, we treat the initial data partitions and the secondary merge index as independent parts of the framework. In this context, we can easily change the underlying index type and utilize the best one for the memory type.

To highlight the optimization, we do the following steps. We start from the traditional adaptive merging with the partitioned B+tree (AM). After that, to make the technique PCM friendly, we consider extended adaptive merging (eAM). This is a traditional adaptive merging that utilizes the B+tree with unsorted leaves (UB+tree) and a bitmap instead of the partitioned B+tree (see [12]). At last, to further improve the performance, we use our framework with a new variant of DPTree called BB+tree. This index is equipped with a buffer and two types of sections in the leaves: sorted and unsorted. It turns out that BB+tree is very suitable for adaptive merging where the data are inserted in batches. Additionally, we create a journal that holds the key ranges already stored in the merge index. Such a strategy works drastically better than a bitmap. Furthermore, we carry out some experiments which compare different index types utilized inside the framework. The experiments clearly show that BB+tree works better than the others. Worth mentioning is the fact that our framework is a very flexible solution. So, the BB+tree can be easily replaced by any other index type in the future.

7. Conclusion

The paper faces adaptive merging optimized for PCM. We implement two methods to overcome PCM limitations in the context of adaptive merging: eAM and PAM. In the first method, we utilize several PCM optimization techniques like the B+tree with unsorted leaves and inner nodes stored in DRAM. We show that eAM outperforms a traditional approach (AM) by 60%. The second method is a framework equipped with the insertion and deletion journal to handle the adaptive merging process on PCM.
Although the framework can work with any index type, we propose a new version of DPTree named BB+tree. We observe that PAM is about 20% faster than eAM in the databases where search queries interleave with data modifications. For future work, we plan to make further research on BB+tree as a promising candidate for the PCM-optimized index type.

References

[1] Idreos S, Kersten ML, Manegold S. Updating a Cracked Database. In: Proceedings of the 2007 ACM SIGMOD International Conference on Management of Data, SIGMOD ’07. ACM, New York, NY, USA. ISBN 978-1-59593-686-8, 2007 pp. 413–424. doi:10.1145/1247480.1247527.

[2] Idreos S, Kersten ML, Manegold S. Database Cracking. In: CIDR 2007, Third Biennial Conference on Innovative Data Systems Research, Asilomar, CA, USA, January 7-10, 2007, Online Proceedings. 2007.

[3] Idreos S, Kersten ML, Manegold S. Self-organizing Tuple Reconstruction in Column-stores. In: Proceedings of the 2009 ACM SIGMOD International Conference on Management of Data, SIGMOD ’09. ACM, New York, NY, USA. ISBN 978-1-60558-551-2, 2009 pp. 297–308. doi:10.1145/1559845.1559878.

[4] Kersten ML, Manegold S, Kersten M, Manegold S. Cracking the Database Store. In: In CIDR. 2005.

[5] Graefe G, Kuno H. Self-selecting, Self-tuning, Incrementally Optimized Indexes. In: Proceedings of the 13th International Conference on Extending Database Technology, EDBT ’10. ACM, New York, NY, USA. ISBN 978-1-60558-945-9, 2010 pp. 371–381. doi:10.1145/1739041.1739087.

[6] Alvarez V, Schuhknecht FM, Dittrich J, Richter S. Main memory adaptive indexing for multi-core systems. In: Tenth International Workshop on Data Management on New Hardware, DaMoN 2014, Snowbird, UT, USA, June 23, 2014. 2014 pp. 3:1–3:10. doi:10.1145/2619228.2619231.

[7] Idreos S, Manegold S, Kuno HA, Graefe G. Merging What’s Cracked, Cracking What’s Merged: Adaptive Indexing in Main-Memory Column-Stores. PVLDB, 2011. 4(9):585–597. doi:10.14778/2002938.2002944.

[8] Xue Z, Qin X, Zhou X, Wang S, Yu A. Optimized Adaptive Hybrid Indexing for In-memory Column Stores. In: Hong B, Meng X, Chen L, Winiwarter W, Song W (eds.), Database Systems for Advanced Applications - 18th International Conference, DASFAA 2013, International Workshops: BDMA, SNSM, SeCoP, Wuhan, China, April 22-25, 2013. Proceedings, volume 7827 of Lecture Notes in Computer Science. Springer, 2013 pp. 101–111. doi:10.1007/978-3-642-40270-8\_9.

[9] Qureshi MK, Karidis J, Franceschini M, Srinivasan V, Lastras L, Abali B. Enhancing Lifetime and Security of PCM-based Main Memory with Start-gap Wear Leveling. In: Proceedings of the 42Nd Annual IEEE/ACM International Symposium on Microarchitecture, MICRO 42. ACM, New York, NY, USA. ISBN 978-1-60558-798-1, 2009 pp. 14–23. doi:10.1145/1669112.1669117.

[10] Ou Y, Chen L, Xu J, Härder T. Wear-Aware Algorithms for PCM-Based Database Buffer Pools. In: Chen Y, Balke W, Xu J, Xu W, Jin P, Lin X, Tang TY, Hwang E (eds.), Web-Age Information Management - WAIM 2014 International Workshops: BigEM, HardBD, DaNoS, HRSUNE, BIDASYS, Macau, China, June 16-18, 2014 Revised Selected Papers, volume 8597 of Lecture Notes in Computer Science. Springer, 2014 pp. 165–176. doi:10.1007/978-3-319-11538-2\_16.
[11] Wu Z, Jin P, Yue L. Efficient Space Management and Wear Leveling for PCM-Based Storage Systems. In: Wang G, Zomaya AY, Pérez GM, Li K (eds.), Algorithms and Architectures for Parallel Processing - 15th International Conference, ICA3PP 2015, Zhangjiajie, China, November 18-20, 2015. Proceedings, Part IV, volume 9531 of Lecture Notes in Computer Science. Springer, 2015 pp. 784–798. doi:10.1007/978-3-319-27140-8_54.

[12] Chen S, Gibbons PB, Nath S. Rethinking Database Algorithms for Phase Change Memory. In: CIDR 2011, Fifth Biennial Conference on Innovative Data Systems Research, Asilomar, CA, USA, January 9-12, 2011, Online Proceedings. www.cidrdb.org, 2011 pp. 21–31. URL http://cidrdb.org/cidr2011/Papers/CIDR11_Paper3.pdf.

[13] Viglas SD. Write-limited Sorts and Joins for Persistent Memory. Proc. VLDB Endow., 2014. 7(5):413–424. doi:10.14778/2732269.2732277.

[14] Jurga M, Macyna W. Implementation of the Aggregated R-Tree for Phase Change Memory. In: Hartmann S, Ma H, Hameurlain A, Pernul G, Wagner RR (eds.), Database and Expert Systems Applications - 29th International Conference, DEXA 2018, Regensburg, Germany, September 3-6, 2018, Proceedings, Part II, volume 11030 of Lecture Notes in Computer Science. Springer, 2018 pp. 301–309. doi:10.1007/978-3-319-98812-2_26.

[15] Zhou X, Shou L, Chen K, Hu W, Chen G. DPTree: Differential Indexing for Persistent Memory. Proc. VLDB Endow., 2019. 13(4):421–434. doi:10.14778/3372716.3372717.

[16] Yu S, Xiao N, Deng M, Liu F, Chen W. Redesign the Memory Allocator for Non-Volatile Main Memory. JETC, 2017. 13(3):49:1–49:26. doi:10.1145/2997651.

[17] Qureshi MK, Srinivasan V, Rivers JA. Scalable high performance main memory system using phase-change memory technology. In: Keckler SW, Barroso LA (eds.), 36th International Symposium on Computer Architecture (ISCA 2009), June 20-24, 2009, Austin, TX, USA. ACM, 2009 pp. 24–33. doi:10.1145/1555754.1555760.

[18] Park H, Yoo S, Lee S. Power management of hybrid DRAM/PRAM-based main memory. In: Stok L, Dutt ND, Hassoun S (eds.), Proceedings of the 48th Design Automation Conference, DAC 2011, San Diego, California, USA, June 5-10, 2011. ACM, 2011 pp. 59–64. doi:10.1145/2024724.2024738.

[19] Zhou P, Zhao B, Yang J, Zhang Y. A Durable and Energy Efficient Main Memory Using Phase Change Memory Technology. In: Proceedings of the 36th Annual International Symposium on Computer Architecture, ISCA ’09. ACM, New York, NY, USA. ISBN 978-1-60558-526-0, 2009 pp. 14–23. doi:10.1145/1555754.1555759.

[20] Sohail HB, Vamanan B, Vijaykumar TN. MigrantStore: Leveraging Virtual Memory in DRAM-PCM Memory Architecture. CoRR, 2015. abs/1504.04297. [1504.04297] URL http://arxiv.org/abs/1504.04297.

[21] Pan C, Xie M, Hu J, Qiu M, Zhuge Q. Wear-leveling for PCM main memory on embedded system via page management and process scheduling. In: 2014 IEEE 20th International Conference on Embedded and Real-Time Computing Systems and Applications, Chongqing, China, August 20-22, 2014. IEEE Computer Society, 2014 pp. 1–9. doi:10.1109/RTCSA.2014.6910513.

[22] Liu D, Wang T, Wang Y, Shao Z, Zhuge Q, Sha EH. Curling-PCM: Application-specific wear leveling for phase change memory based embedded systems. In: 18th Asia and South Pacific Design Automation Conference, ASP-DAC 2013, Yokohama, Japan, January 22-25, 2013. IEEE, 2013 pp. 279–284. doi:10.1109/ASPDAC.2013.6509609.
[23] Rashidi S, Jalili M, Sarbazi-Azad H. A Survey on PCM Lifetime Enhancement Schemes. *ACM Comput. Surv.*, 2019. 52(4):76:1–76:38. doi:10.1145/3332257.

[24] Chi P, Lee WC, Xie Y. Making B+-tree Efficient in PCM-based Main Memory. In: Proceedings of the 2014 International Symposium on Low Power Electronics and Design, ISLPED '14. ACM, New York, NY, USA. ISBN 978-1-4503-2975-0, 2014 pp. 69–74. doi:10.1145/2627369.2627630.

[25] Li L, Jin P, Yang C, Wu Z, Yue L. XB+-Tree: A Novel Index for PCM/DRAM-Based Hybrid Memory. In: Cheema MA, Zhang W, Chang L (eds.), *Databases Theory and Applications - 27th Australasian Database Conference, ADC 2016, Sydney, NSW, Australia, September 28-29, 2016, Proceedings*, volume 9877 of *Lecture Notes in Computer Science*. Springer, 2016 pp. 357–368. doi:10.1007/978-3-319-46922-5_28.

[26] Oukid I, Lasperas J, Nica A, Willhalm T, Lehner W. FP-Tree: A Hybrid SCM-DRAM Persistent and Concurrent B-Tree for Storage Class Memory. In: Özcan F, Koutrika G, Madden S (eds.), Proceedings of the 2016 International Conference on Management of Data, SIGMOD Conference 2016, San Francisco, CA, USA, June 26 - July 01, 2016. ACM, 2016 pp. 371–386. doi:10.1145/2882903.2915251.

[27] Jabarov E, Park M, On B, Choi GS. PCR*-Tree: PCM-Aware R*-Tree. *J. Inf. Sci. Eng.*, 2017. 33(5):1359–1374. URL http://jise.iis.sinica.edu.tw/JISESearch/pages/View/PaperView.jsf?keyId=158_2074.

[28] Schuhknecht FM, Jindal A, Dittrich J. The Uncracked Pieces in Database Cracking. *Proc. VLDB Endow.*, 2013. 7(2):97–108. doi:10.14778/2732228.2732229.

[29] Macyna W, Kukowski M. Partially Indexing on Flash Memory. In: Database and Expert Systems Applications - 30th International Conference, DEXA 2019, Linz, Austria, August 26-29, 2019, Proceedings, Part I. 2019 pp. 95–105. doi:10.1007/978-3-030-27615-7_7.

[30] O’Neil PE, Cheng E, Gawlick D, O’Neil EJ. The Log-Structured Merge-Tree (LSM-Tree). *Acta Informatica*, 1996. 33(4):351–385. doi:10.1007/s002360050048.

[31] Li L, Jin P, Yang C, Wu Z, Yue L. Optimizing B+-Tree for PCM-Based Hybrid Memory. In: Proceedings of the 19th International Conference on Extending Database Technology, EDBT 2016, Bordeaux, France, March 15-16, 2016, Bordeaux, France, March 15-16, 2016. 2016 pp. 662–663. doi:10.5441/002/edbt.2016.75.

[32] Halim F, Idreos S, Karras P, Yap RHC. Stochastic Database Cracking: Towards Robust Adaptive Indexing in Main-Memory Column-Stores. *Proc. VLDB Endow.*, 2012. 5(6):502–513. doi:10.14778/2168651.2168652.

[33] Schuhknecht FM, Jindal A, Dittrich J. An experimental evaluation and analysis of database cracking. *VLDB J.*, 2016. 25(1):27–52. doi:10.1007/s00778-015-0397-y.

[34] Pirk H, Petraki E, Idreos S, Manegold S, Kersten ML. Database cracking: fancy scan, not poor man’s sort! In: Kemper A, Pandis I (eds.), Tenth International Workshop on Data Management on New Hardware, DaMoN 2014, Snowbird, UT, USA, June 23, 2014. ACM, 2014 pp. 4:1–4:8. doi:10.1145/2619228.2619232.

[35] Haffner I, Schuhknecht FM, Dittrich J. An analysis and comparison of database cracking kernels. In: Lehner W, Salem K (eds.), Proceedings of the 14th International Workshop on Data Management on New Hardware, Houston, TX, USA, June 11, 2018. ACM, 2018 pp. 10:1–10:10. doi:10.1145/3211922.3211930.