Gas-sensitive Field-effect Transistor Incorporating Polymer Layer and Porous Metal Electrode in the Gate Structure

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Field-effect transistor (FET)-based sensors have attractive potential for use in fabricating miniaturized sensor arrays by semiconductor processes. In this work, a gas-sensitive FET incorporating a polyethylene glycol (PEG) film and porous Pt electrode in the gate structure is proposed. In this new type of gas-sensitive FET, the PEG layer provides a dielectric layer (i.e., a stationary phase) that interacts with gaseous molecules, while the porous Pt gate electrode allows gaseous molecules to effectively access the PEG layer. Features of the response pattern, such as peak height, recovery time, and peak shape, unique to the PEG-modified FET sensor were observed when exposed to vapor of volatile organic compounds (VOCs). The sensing mechanism of the gas-sensitive FET is discussed in terms of capacitance changes of the gate structure induced by gaseous molecule adsorption onto the stationary phase. A variety of polymeric materials might be used to modify the gate, and the proposed structure shows promise as a platform for cross-reactive FET-based gas sensor arrays for pattern recognition.

1. Introduction

Sensors for gases and vapors of volatile organic compounds (VOCs) have been intensively studied for applications in fields such as disease diagnosis[1–3] and environmental monitoring related to industrial safety and home security.[4] Field-effect transistor (FET)-based sensors have also been widely studied for gas sensing applications. In 1975, Lundström and co-workers first reported a hydrogen-sensitive Pd-gate FET.[5,6] This pioneering work on catalytic gate FETs triggered research into other catalytic gate field-effect devices such as Schottky diode-[7,8] and capacitor-type devices.[9,10] In previous reports, several models were proposed to explain the gas-sensing mechanisms of the catalytic gate field-effect devices.[11,12]

The combination of cross-reactive sensing with pattern recognition methods is a promising way to identify gaseous target analytes and estimate their concentrations.[3,13–16] Cross-reactive sensor arrays used for this purpose require broadly responsive sensors with differentiated response profiles.[3] The obtained signals can then be extracted by preprocessing before the
computational processing.\textsuperscript{(13–16)} FET-based sensors can be downsized and integrated into sensor arrays by standard semiconductor processes, which makes them an ideal platform for cross-reactive sensing systems.\textsuperscript{(17)} Furthermore, properties of catalytic gate FETs can be tuned by several methods to achieve broad and differential responses. Several parameters are available for tuning the gas-responsive properties of catalytic gate field-effect devices, including the type of catalytic metal,\textsuperscript{(11,12)} the type of insulating material,\textsuperscript{(10)} and the operation temperature.\textsuperscript{(12)}

Here, we describe a new approach to fine-tuning the gas-responsive properties of FETs in an approach compatible with the cross-reactive sensor array applications [Fig. 1 (a)]. The gas-sensitive FET we propose incorporates a structure based on a porous Pt gate electrode embedded into a gas-adsorbing layer, i.e., polyethylene glycol (PEG). The FET operates in the $n$-channel depletion mode. As shown in Fig. 1(a), the insulating layers are composed of SiO$_2$, Si$_3$N$_4$, and Ta$_2$O$_5$; note that the presence of the Ta$_2$O$_5$ layer is not an absolute requirement for the present proof-of-concept study. PEG, which is widely used as a stationary phase in capillary columns for gas chromatography (GC),\textsuperscript{(18)} is used as a gas-adsorbing layer in the gate structure. Furthermore, a porous Pt layer is formed as the gate electrode directly on the PEG layer, which thus allows gaseous analytes to access the PEG layer.

2. Materials and Methods

To develop the new gas-sensitive FET combined with a PEG layer and porous Pt gate electrode, we used a commercially available base FET without a gate electrode. To form a PEG layer, 2 µL of PEG (PEG 20M, Shinwa Chemical Industries) -dissolved Milli-Q pure water (0.3 g L$^{-1}$) was dropped onto the surface of the Ta$_2$O$_5$ layer and the solution was evaporated in an oven at 110 °C for 1 h. After the formation of a PEG layer, a porous Pt gate electrode was deposited by RF sputtering at a power of 50 W for 30 or 60 s (Table 1) in a vacuum chamber (Ar atmosphere of ~1 Pa). In the characterization of the new gate structure, the 1-min-sputtered FET (device 1) was observed with a scanning electron microscope (SEM; JSM-7500F, JEOL), transmission electron microscope (TEM; JEM-2100F, JEOL), and scanning TEM (STEM; JEM-2100F, JEOL). To obtain cross sections of the gate structure, the FET device was protected

![Fig. 1](image_url)
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with resin and then processed with a focused ion beam system (JIB-4501, JEOL). Elemental mapping of the cross section was performed in the STEM mode combined with energy-dispersive X-ray spectroscopy (EDX; JED-2300T, JEOL). The vapor samples were generated with a vapor-generator apparatus (Permeater PD-IB-2, GASTEC) and introduced into the gas-flow cell containing the developed FET sensor with dry N$_2$ carrier gas. Dry N$_2$ carrier gas was selected as the inert atmospheric gas for avoiding the effect of moisture. During measurements of the electronic properties, we heated the FET device to 70 °C on a hot plate and the temperature of the device was measured with a thermocouple. Drain current ($I_D$)–gate voltage ($V_G$) curves of device 1 were measured using a semiconductor parameter analyzer (4155C, Agilent Technologies) at a drain-source voltage ($V_{DS}$) of 1 V. Real-time response measurements of the FET devices to vapor samples were conducted with a measuring instrument based on the electronic circuit shown in Fig. 2. In the electronic circuit, changes in output voltage ($\Delta V_{out}$) occur alongside a shift in the threshold voltage ($\Delta V_T$) of the device. During response measurements, $V_G$ and $V_{DS}$ were fixed at 0.2 and 4 V, respectively.

3. Results and Discussion

The SEM image in Fig. 1(b) shows a top view of the surface of the Pt gate electrode. A markedly porous morphology consisting of discontinuous Pt is apparent in Fig. 1(b). Figure. 3 shows STEM images and EDX elemental map of N, O, Si, Ta, and Pt. The elemental mapping revealed that the gate structure was composed of multiple layers in the desired arrangement. It was confirmed that the porous Pt gate electrode occupied the top layer. In carbon mapping, unfortunately, the PEG layer could not be distinguished from the resin layer used to support the structure in the cutting process. Cross-sectional TEM images shown in Fig. 1(c) indicate that the porous morphology of the Pt electrode consisted of a network of Pt nanoparticles (NPs), including some Pt NP structures that partially infiltrated the PEG layer.

Representative $I_D$–$V_G$ curves of device 1 upon exposure to ethanol vapor (38 ppm) are shown in Fig. 4. The introduction of ethanol vapor produced a negative shift of $V_T$ in the FET device. A plausible explanation for this behavior may be given by taking into account the change in the overall capacitance of the gate environment as follows.

The working principle of a FET in the unsaturated region can be generally expressed as

$$I_D = \mu C_{OX} \frac{W}{L} \left[ (V_G - V_T) V_{DS} - \frac{1}{2} V_{DS}^2 \right],$$

(1)

| Device No. | PEG Layer | Sputtering time |
|------------|-----------|-----------------|
| 1          | Formed    | 1 min           |
| 2          | Formed    | 30 s            |
| 3          | None      | 30 s            |

Table 1

PEG layer and sputtering time details of device.
Fig. 2. Electronic circuit used for real-time measurements of gas response characteristics of the FET.

Fig. 3. (Color online) (a) STEM image and (b)–(f) elemental mapping of cross section of the FET (device 1) by STEM-EDX: (b) Nitrogen, (c) oxygen, (d) silicon, (e) tantalum, and (f) platinum mapping. Scale bar is 200 nm.

Fig. 4. (Color online) $I_D-V_G$ curves ($V_{DS}: 1$ V) of the FET of device 1 when ethanol vapor with dry N$_2$ carrier gas was introduced into the gas-flow cell container. (Red) 38 ppm ethanol vapor with dry N$_2$ and (black) only dry N$_2$. 
where $I_D$ is the drain current, $\mu$ is the electron mobility in the channel, $C_{OX}$ is the gate oxide capacitance per unit area, and $\frac{W}{L}$ is the channel width-to-length ratio. $V_G$, $V_T$, and $V_{DS}$ are the applied gate, threshold, and drain-source voltages, respectively. Furthermore, $V_T$ can be expressed as

$$V_T = V_{FB} - \frac{Q_B}{C_{OX}} + 2\phi_f,$$

where $V_{FB}$ is the flat band voltage, $Q_B$ is the bulk depletion charge per unit area, and $\phi_f$ is the Fermi potential difference between the doped bulk silicon and intrinsic silicon.

Here, we assume that the intrinsic dipole moment of a polar molecule adsorbed onto a polymer can generate changes in the overall capacitance value of the stationary phase. For a polymer-modified FET, Eqs. (1) and (2) should be modified as

$$I_D = \mu C_{com} \frac{W}{L} \left[ (V_G - V_T) V_{DS} - \frac{1}{2} V_{DS}^2 \right],$$

$$V_T = V_{FB} - \frac{Q_B}{C_{com}} + 2\phi_f,$$

with

$$C_{com} = \frac{C_{in} \times C_{st}}{C_{in} + C_{st}} = \frac{C_{st}}{1 + \frac{C_{st}}{C_{in}}},$$

$$C_{st} = \varepsilon_0 \varepsilon_r \frac{S}{d},$$

where $C_{in}$ and $C_{st}$ are the capacitances of the insulating layer and the stationary phase (polymer layer), respectively. $C_{com}$ is the sum capacitance of $C_{in}$ and $C_{st}$. The parameters $\varepsilon_0$, $\varepsilon_r$, $S$, and $d$ are the vacuum permittivity, relative static permittivity, area, and thickness of the polymer layer, respectively.

Adsorption of polar molecules onto the stationary phase induces a change in the permittivity in the stationary phase, resulting in a change in $C_{st}$ ($\Delta C_{st}$). As expressed in Eq. (5), this result qualitatively demonstrates that $\Delta C_{st}$ can generate a change in $C_{com}$ ($\Delta C_{com}$), that affects $V_T$ and $I_D$ of the FET.

The thickness and density of the Pt layer depend on sputtering time and can affect the sensitivity of the FET-based sensor. For example, it has been reported that a smaller thickness of the Pt gate electrode provides higher sensitivity in a solid-electrolyte-based FET oxygen sensor. Figure 5(a) shows the real-time response of the developed FET sensors, 30-s-sputtered devices (devices 2 and 3), when exposed to hexanal vapor samples of various concentrations. The red-highlighted regions represent intervals during which the vapor samples...
were introduced. In the principle of the electronic circuit shown in Fig. 2, the rise of $V_{out}$ induced by VOC exposure indicates a negative shift of $V_T$. As evident from Fig. 5(a), the $\Delta V_{out}$ responses to hexanal vapor were reversible and could be cycled multiple times. A comparison between the two devices with (device 2) and without (device 3) the PEG layer suggested that the PEG layer contributed to considerable differences in the response patterns, including the peak height, recovery time, and peak shape. The relationship between the hexanal vapor concentration and $\Delta V_{out}$ of device 2 is shown in Fig. 5(b). The value of $\Delta V_{out}$ increased as the hexanal concentration increased. Furthermore, the recovery time of $\Delta V_{out}$ to the baseline also increased as the hexanal concentration increased, as observed in Fig. 5(a). These results can be attributed to greater amounts of vapor adsorbing on the PEG layer and Pt NPs. The increased adsorption of gaseous molecules led to an increase in $\Delta V_{out}$ and a longer recovery time for desorption. To evaluate the repeatability in Fig. 5(a), we calculated the coefficient of variation (CV) of $\Delta V_{out}$ between the baseline and peak using the standard deviation and mean value. The CV values in measurements of 7, 14, and 41 ppm of hexanal vapor were 16, 24, and 12%, respectively.

To demonstrate the response of the developed gas-sensitive FET to a broad range of gaseous analytes, we exposed device 2 to several VOC samples and monitored $\Delta V_{out}$. Figure 6 shows real-time measurements of the response characteristics of the gas-sensitive FET (device 2) to vapors of ethanol (17 ppm), 1-propanol (10 ppm), tetrahydrofuran (42 ppm), acetone (64 ppm), and nonanal (2 ppm). Figure 6 reveals that the $\Delta V_{out}$ signals were reproducible with repeated exposure to the VOCs. Furthermore, the FET-based sensor was broadly responsive toward these VOCs. As mentioned in the introduction, these broadly responsive properties could be useful for the fabrication of cross-reactive sensor arrays.
4. Conclusions

Here, we reported a new gas-sensitive FET incorporating a polymer film with a porous electrode in the gate structure. PEG, which is widely used as a stationary phase material for GC, was applied to the gate structure to provide adsorption sites for gaseous molecules. Furthermore, a porous Pt layer that allows gaseous molecules to access the PEG layer was used as the gate electrode. When exposed to vapor of VOCs, the responsive patterns were unique to the PEG-modified FET in terms of peak height, recovery time, and peak shape. In addition, the proposed FET showed a broad response toward VOC vapors. A variety of different types of polymers can be applied to tailor adsorption properties and, thus, the profile of response. Although the response mechanism has yet to be clarified in detail, the FET structure described herein could be useful as a platform for cross-reactive sensor arrays coupled with pattern recognition methods, which require individual sensing components that can detect a broad range of analytes with differential responses.

Acknowledgments

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