Two-step Commutation for Three-phase-to-single-phase Matrix Converter with High-frequency Isolation

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This paper proposes a two-step commutation method for a three-phase-to-single-phase matrix converter. Conventional two-step commutation cannot be applied at all operation regions because a commutation failure still owing to detection error of grid voltages. In our proposed two-step commutation, we modulate only one of two devices in a bidirectional switch and utilize a zero vector to let the switches naturally turn off, in this manner, the commutation failures are avoided completely regardless of the voltage detection error. From experimental results, it is confirmed that the proposed two-step commutation undertakes safe operation to avoid commutation failure. The input current total harmonic distortion at 10 kW with the proposed two-step commutation is improved by 38% in comparison with the conventional four-step commutation.

Keywords: two-step commutation, current direction estimation, commutation failure.

1. Introduction

A lot of studies on Electric vehicles (EVs) and Plug-in hybrid vehicles (PHEVs) have been accelerated over the past decade. Compared to the gasoline vehicle, EVs or PHEVs still faces one of the main challenges, i.e. the long battery charging time. In order to solve this problem, high-power low-profile battery chargers are required [1-4]. In [5-9], isolated AC-DC converters using a matrix converter as a medium frequency AC-AC converter connected with the transformer at the primary side have been proposed. The volume of the matrix converter is expected to be greatly reduced compared to other topologies which employ a buffer capacitor because the buffer capacitor in the high-power application such as the rapid battery charger usually has to withstand a high current, which increases the capacitor volume.

Generally, the matrix converters are required a commutation sequence at the switching timing of the power devices to prevent a commutation failure, i.e. short-circuit at a voltage source and open-circuit at inductive components. The conventional commutation method is separated into two types, the voltage commutation method based on the input voltage polarity and the current commutation method based on the output current direction [10-13]. The voltage commutation works reliably when the relationship of the input voltages is accurately obtained. These commutation sequences are the four-step commutation which is divided into the four steps to avoid the open-circuit and the short-circuit. Each step is turned-on or turned-off a switch into the two bi-directional switches depended on the voltage polarity or the current direction.

The commutation time which is longer than the switching speed of the switching devices is inserted among the first-step, the second-step, the third-step and the fourth-step to prevent the commutation failure. Therefore, the four-step commutation which is the voltage commutation or the current commutation is a complex commutation algorithm which greatly restricts the applicable control hardware. A life time and a reliability of the switching devices are decreased by the commutation failure which is based on the detection error. In order to simplify the control hardware for the matrix converter, two-step commutation methods have been proposed [5-11]. Commutation time in the two-step commutation is half of that in the four-step commutation. This results in a short commutation time with a simpler commutation algorithm. In particular, the two-step commutation in [11] is achieved by zero vectors which is the switching pattern of the additional circuit outputting the zero voltage at the input terminal. Another two-step commutation in [6-10] uses both the voltage polarity and the current direction for the commutation. However, the main problem of these conventional two-step commutations is that either the additional circuit is required or the commutation failures still occur at the critical area due to a detection error of the input voltage. A solution for this problem is that the commutation method is switched between the conventional two-step commutation and the four-step commutation. However, this solution increases the number of switching step and requires the implementation of the two commutation methods.

The error between a reference and actual voltage occurs by the dead-time which is longer than the duration of each switching pulse of the matrix converter. In the low modulation index region, the input current is distorted by the output voltage error due to the conventional several-step commutation. It is necessary to reduce the number of the commutation step in order to reduce the output voltage error.

In this paper, the two-step commutation is proposed for a three-phase to single-phase matrix converter in order to improve the input current total harmonics distortion (THD) in the low modulation index region. The output voltage error is decreased by the proposed two-step commutation due to less commutation. In addition, the commutation failure does not occur regardless of the voltage detection error. The original idea of this paper is to turning-on only one of two devices in the bi-directional switches based on the current direction and to use a voltage vector which let the switches naturally turn-off. Therefore, the proposed two-step commutation is unnecessary to switch between the conventional two-step
Two-step commutation for Three-phase-to-single-phase Matrix Converter (Shunsuke Takuma et al.)

![Diagram of isolated AC-DC converter with three-phase-to-single-phase matrix converter.](image)

**Fig. 1.** Isolated AC-DC converter with three-phase-to-single-phase matrix converter.

Commutation and another commutation method to prevent the short-circuit at the critical area. The effectiveness of the proposed two-step commutation is evaluated with a 10-kW prototype thought experimental results.

2. Circuit configuration and control method

Figure 1 shows the isolated AC-DC converter with the three-phase to single-phase matrix converter. The proposed circuit consists of a LC filter to eliminate switching ripple component of the input current, a three-phase to single-phase matrix converter with bi-directional switches, a medium frequency transformer, a diode rectifier, and a smoothing inductor in the output DC side. In particular, the three-phase grid voltage is directly converted to medium frequency single-phase voltage by the matrix converter. Consequently, the volume of the transformer is significantly minimized because this transformer is operated with this medium frequency single-phase voltage.

3. Two-step commutation method

3.1 Problem of conventional commutation method

Figure 2 shows the equivalent circuit at each arm of the matrix converter. The equivalent circuit consists of the three voltage sources (maximum-phase-voltage $V_{\text{max}}$, middle phase-voltage $V_{\text{mid}}$, minimum phase-voltage $V_{\text{min}}$), current source which represents as the current at the transformer, and three bi-directional switches. Figure 2(b) and 2(c) show the transition situation from $V_{\text{max}}$ to $V_{\text{mid}}$ with the four-step voltage commutation and the conventional two-step commutation. If the actual voltage polarity does not agree with the detection voltage polarity, the short-circuit via the grid occurs in the commutation state of the four-step voltage commutation or in the steady state of the conventional two-step commutation until turning-on $S_{\text{on}}$.

3.2 Principle of two-step commutation

Figure 3 depicts the principle of the proposed two-step commutation, which is divided into three modes. Figure 3(a) shows the transition from the high voltage to the low-voltage. As shown in Fig. 3 (a), the switch $S_{\text{on}}$ turns-on at $V_{\text{mid}}$-phase when the matrix converter starts to transit from $V_{\text{max}}$-phase to $V_{\text{mid}}$-phase with the positive output current. At first step, the switch $S_{\text{on}}$ at $V_{\text{mid}}$-phase turns-on. Then, at second step, the switch $S_{\text{on}}$ turns-off and the current commutates from $V_{\text{max}}$-phase to $V_{\text{mid}}$-phase. The output voltage error occurs due to the commutation from $V_{\text{max}}$-phase to $V_{\text{mid}}$-phase. Figure 3(b) shows the transition from the low voltage to the high voltage. In contrast, the switch $S_{\text{on}}$ turns-on at $V_{\text{mid}}$-phase when the matrix converter starts...
to transit from $V_{\text{min}}$-phase to $V_{\text{max}}$-phase with the positive output current. At first step, the switch $S_{pr}$ at $V_{\text{max}}$-phase turns-on and the current commutates from $V_{\text{min}}$-phase to $V_{\text{max}}$-phase. Then, at second step, the switch $S_{mr}$ turns-off. The output voltage error is zero when the output voltage is changed from $V_{\text{max}}$-phase to $V_{\text{mid}}$-phase. Figure 3(c) shows the transition from zero-vector state to an input phase. The output current decreases until zero in the initial state. After that, the output current keeps to zero because the current path is nothing regardless of the switching state of $S_{pr}$. Therefore, the zero-vector state $V_t$ is defined as an equivalent off state of all switching devices of the matrix converter. The zero-vector state depend on the input voltage polarity. At first step, the all switches turn-off because the output current is already zero. At second step, $S_{pr}$ turns-on and the output voltage is changed from zero to Vmax. Therefore, the output voltage error should occur by the zero-vector. In this paper, the commutation from the low to high voltage is excluded to achieve the simple duty compensation because the output voltage error is always a constant duty using other commutation.

In these commutation modes, the short-circuit and the open-circuit are avoided regardless of the voltage detection error by the modulation of only one of two devices in the bi-directional switches. Therefore, the transition from an input phase to another input phase without any commutation failures is achieved. The initial state is on state of $S_{pr}$ and then similar switching sequence are applied when the output current direction is negative. The initial state of the equivalent commutation model for each arm is express by (1)

$$
\begin{align}
S_{mr} = 1 & \quad S_{mr} = 0 \quad i_{\text{load}} > 0 \\
S_{mr} = 0 & \quad S_{mr} = 1 \quad i_{\text{load}} < 0 \\
& \quad x = p, m, n
\end{align}
$$

where subscript $x$ indicates $p$ ($V_{\text{max}}$-phase) or $m$ ($V_{\text{mid}}$-phase) or $n$ ($V_{\text{min}}$-phase) depend on the output line to line voltage $V_{pr}$. If the output voltage of the matrix converter is $V_{\text{max}}$-$V_{\text{min}}$, the upper side arm is $p$. In addition, $x$ of the lower side arm is $n$.

Figure 4 shows the half-cycle operation of one switching period including commutation in sector I. The relationship of phase voltage is $v_p > v_m > v_n$. The inductor current $I_{Ll}$ is assumed to be ideal as no ripple current.

(i) $t_0 - t_1$
$S_{pr}$ and $S_{mr}$ are on, whereas the output current direction is positive. Therefore, $S_{mr}$ and $S_{mr}$ can only conduct the current through the diode connected anti-parallel with the switching devices.

(ii) $t_1 - t_2$
$S_{pr}$ is turned-on. The current flow does not change because the R-phase voltage is higher than the S-phase voltage.

(iii) $t_2 - t_3$
$S_{pr}$ is turned-off. The output voltage is changed from $V_{\text{max}}$-phase to $V_{\text{mid}}$-phase. This matrix converter successfully outputs vector $V_2$.

(iv) $t_3 - t_4$
$S_{mr}$ and $S_{mr}$ is turned-on, whereas the output current direction is still positive.

(v) $t_4 - t_5$
$S_{pr}$ and $S_{mr}$ is turned-off. The output voltage polarity is changed to be opposite of the output current. Thus, the output current quickly decreases to zero during $t$. The time $t_5$ is expressed by

$$
t_5 = I_{Ll} \frac{L_{pr}}{N_{\text{max}}}
$$
minimize the time \( t_i \). This output voltage during \( t_i \) is error. However, this output voltage error is ignored because this period is lower than \( 1/10 \) of a dead-time using the small leakage inductance and the high output voltage.

(vi) \( t_5 = t_6 \)

The output current keeps to zero regardless of the switching states of \( S_{tp} \) and \( S_{mr} \). The output voltage is also clamped to zero because the inductor current at DC side is circuited at the diode rectifier in secondary side during zero-vector state. The commutation failure occurs by next commutation sequences when the output current is not zero. The time \( t_i \) should be shorter than zero-vector state from \( t_4 \) to \( t_6 \) to suppress the commutation failure.

(vii) \( t_6 = t_7 \)

\( S_{tp} \) and \( S_{mr} \) can safely turned-off because the output current has become zero when the time \( t_i \) is shorter than zero-vector state.

(viii) \( t_7 = t_8 \)

\( S_{sn} \) and \( S_{mr} \) are turned-on and the matrix converter outputs the negative voltage.

In consequence, the proposed two-step commutation operates the matrix converter without any commutation failures regardless of the detection voltage error at the input voltage.

Figure 5 shows the proposed two-step commutation sequence. The output voltage error due to the dead time is considered. The delay of one-step time occurs from the first step to the second step as shown in Fig. 2 (a) when the matrix converter transits from an input phase to another input phase in the positive output current. In the transition from the zero-vector state to another input phase, all switches have to be turned-off at the first step. Similarly, the delay of one-step time occurs from the first step to the second step as shown in Fig. 2 (b). Consequently, the compensation for the voltage error due to the delay of one-step time of the proposed two-step commutation is similar to that in the back-to-back converter, which is significantly simpler than the output voltage error compensation in the four-step commutation.

4. Circuit configuration and control method

4.1 Input current control for matrix converter Figure 6 shows the space vector modulation (SVM) applied to the three-phase to single-phase matrix converter. The operation mode of SVM is divided by every 60 deg. (Sector I, II, III, IV, V and VI) of the input voltages. Output vectors which are close to the input voltage vector are selected. In sector I, \( V_1 \) and \( V_2 \) are used during the first half of the control period as the positive voltage, whereas \( V_4 \) and \( V_5 \) are used during the second half of the control period as the negative voltage. Note that the zero vector \( V_z \) which outputs the zero voltage, is decided by the sector. These duty reference \( T_1, T_2 \) and \( T_z \) are calculated by

\[
T_1 = \frac{1}{|a|} \begin{bmatrix} v_{\alpha} & v_{2\alpha} \\ v_{\beta} & v_{2\beta} \end{bmatrix}
\]

(3)

\[
T_2 = \frac{1}{|a|} \begin{bmatrix} v_{\alpha} & v_{\alpha} \\ v_{\beta} & v_{\beta} \end{bmatrix}
\]

(4)

where \( v_{\alpha} \) and \( v_{\beta} \) are \( \alpha-\beta \) components of the input current reference, \( v_{1\alpha}, v_{1\beta}, v_{2\alpha} \) and \( v_{2\beta} \) are also \( \alpha-\beta \) components of the vectors \( V_1 \) and \( V_2 \) which are selected based on the area located the input current
reference.

4.2 Current direction estimation  Figure 7 shows the current estimation method to achieve the proposed two-step commutation. The current direction estimation is required for reducing the current sensor which has the high current ratio and the wide bandwidth. The output current is synchronized with the switching carrier by SVM. The switching signal for the zero vector is selected to reduce the output current up to zero. In order to achieve the two-step commutation, the zero current timing at the output side is necessary until the end of the zero vector. The output voltage is clamped at the grid voltage during the zero vector when the output current thought via grid voltage. After that, the output current and voltage are also zero. The current direction estimation is achieved to keep the zero current at the output side until end of the zero vector.

4.3 Implementation of proposed two-step commutation

Table I shows the switching table for the proposed two-step commutation. The switching table depends on the sector and the selected output vector. The switching pulse of the same vector is changed by the sector. According to the principle of the proposed two-step commutation as shown in Fig. 3, the gate signals are decided for the six bidirectional switches. For example, the output vector V1 is selected in the sector I, S_{op}, S_p and S_o are turned-on. S_{op}, S_p and S_o are turned-off.

Figure 8 shows the gate signal generation for the proposed two-step commutation which requires the complex commutation algorithm as the four-step commutation because the proposed two-step commutation only uses a switching table and the current direction estimation. The one of two devices in the bi-directional switches is operated by the gate signals depending on the current direction. Another one of that is turned-off during half cycle of the switching frequency. Finally, the short-pulse in the gate signals is masked because the short-pulse wide more than the sum of the raise time and the turn-on delay time required between the switching and next switching on the three-phase to single-phase matrix converter.

5. Experimental results

Table II shows the experimental conditions for 10 kW. The switching devices at the three-phase to single-phase matrix converter uses IGBT (MITSUBISHI ELECTRIC: CM400CY-24S). The one-step time t_d is decided by the switching characteristics of IGBT. The sum of the raise time and the turn-on delay time of this IGBT is shorter than 1.0 μs. Therefore, the one-

| Sector | I  | II  | III | IV  | V  | VI  |
|--------|----|----|-----|-----|----|-----|
| Vector | V_1 V_2 V_3 | V_2 V_3 V_4 | V_3 V_4 V_5 | V_4 V_5 V_6 | V_5 V_6 V_7 | V_6 V_7 V_8 |
| Switching signal | 100 010 001 | 010 010 001 | 010 010 001 | 010 010 001 | 010 010 001 | 010 010 001 |

Fig. 8. Signal generation for proposed two-step commutation.

Fig. 9. Comparison of device current at matrix converter.
The surge current is approximately 0.4 to 0.6 to 0.8 to 1.0

\begin{align*}
\text{Input current THD} & = 2.9\% \\
\text{Input voltage} & = 1.0\% \text{ at high modulation index.}
\end{align*}

It is clear that the output distortion is low value by 2.9\%. In addition, surge current is always suppressed by the proposed two-step commutation. Moreover, it has been confirmed from the experimental results that the proposed two-step method based on estimation of the output current direction achieves zero vector. Consequently, the input current distortion is low value by 2.9\%. In addition, surge current is always suppressed by the proposed two-step commutation.

Figure 10(a)-(b) show the experimental waveforms with the four-step commutation method based on estimation of the output current direction achieves zero vector. Consequently, the input current distortion is low value by 2.9\%. In addition, surge current is always suppressed by the proposed two-step commutation.

Figure 11(a)-(b) shows the experimental waveforms of the three-phase to single-phase matrix converter at 10 kW with the four-step commutation method and with the proposed two-step commutation method respectively. The commutation failure applying the voltage commutation occurs in the regions where the relationship of the grid voltages changes. As a result, the surge current is approximately 220 A. Consequently, the life time and the reliability of the switching devices is decreased. In addition, the input current is distorted by the commutation failure. The proposed method based on estimation of the output current direction achieves to avoid short-circuit at the grid voltage. Consequently, the input current distortion is low value by 2.9\%. In addition, surge current is always suppressed by the proposed two-step commutation.

Figure 11(a)-(b) shows the experimental waveforms with the three-phase to single-phase matrix converter at 10 kW with the four-step commutation method and with the proposed two-step commutation method respectively. The commutation failure applying the voltage commutation occurs in the regions where the relationship of the grid voltages changes. As a result, the surge current is approximately 220 A. Consequently, the life time and the reliability of the switching devices is decreased. In addition, the input current is distorted by the commutation failure. The proposed method based on estimation of the output current direction achieves to avoid short-circuit at the grid voltage. Consequently, the input current distortion is low value by 2.9\%. In addition, surge current is always suppressed by the proposed two-step commutation.

Figure 12 compares the grid current THD at high modulation index. The input current THDs of the proposed two-step commutation and the conventional four-step commutation are also same. It can be seen from this result that the proposed two-step commutation method has a lower THD compared to the conventional method. Additionally, it is clear that the proposed method suppresses the surge current at the zero vector state quickly decreases to zero. Therefore, the surge voltage due to the output current does not occur when all switches turn-off. It confirms from this result that the proposed two-step commutation method is effective in suppressing the surge voltage and improving the reliability of the matrix converter.

![Fig. 12](image_url)
Two-step commutation method operates the matrix converter without any commutation failures regardless of the input voltage detection error.

Figure 12 shows the characteristics of the input current THD with each commutation method at the high modulation index. The input current THD of the proposed two-step commutation is similar to one of the conventional four-step commutation. The commutation step is decreased by the proposed two-step commutation to keep the performance of input current control.

Figure 13(a)-(b) show the experimental waveforms with the conventional four-step voltage commutation, and the extended waveforms at low modulation index, respectively. The input current is distorted by the masking of the short-pulse.

Figure 14(a)-(b) show the experimental waveforms with the proposed commutation, and the extended waveforms at low modulation index, respectively. It confirms from this result that the proposed two-step commutation method operates the matrix converter regardless of the modulation index.

Figure 15 shows the distortion characteristics of each commutation methods at the low modulation index. The input current THD of the proposed two-step commutation is 7.7% at 10 kW. In the low modulation index, the proposed two-step commutation has the high performance in comparison with the conventional four-step commutation at entire loads.

Figure 16 shows the output voltage error of each commutation method. The output voltage error is less than 0.4% regardless of the
modulation index. It is clear that the proposed two-step commutation method is unnecessary to compensate the output voltage error.

Figure 17 shows the distortion characteristics of each commutation methods against the modulation index at rated 10 kW. The input current THD of the proposed two-step commutation is 7.7% at 10 kW. The output voltage range is extended by 36% in case of same input current THD.

Figure 18 shows the efficiency characteristics of each commutation methods. The efficiency characteristics with the conventional four-step voltage commutation and proposed two-step commutation are almost same. It is clear that the switching loss of the matrix converter is not increased to improve the input current THD by applying the proposed commutation.

6. Conclusions

In this paper, the two-step commutation was proposed in the three-phase to single-phase matrix converter. Compared to the conventional two-step commutation, the proposed two-step commutation is always the safety operation regardless of the voltage detection error. In addition, the commutation algorithm of the proposed two-step commutation is much simpler than the conventional commutation. On the other words, the simple control hardware is employed for the matrix converter with the proposed two-step commutation. The input current THD with the proposed two-step commutation is improved by 38% at the low modulation index.

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