Very Static Enforcement of Dynamic Policies

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Abstract. Security policies are naturally dynamic. Reflecting this, there has been a growing interest in studying information-flow properties which change during program execution, including concepts such as declassification, revocation, and role-change.

A static verification of a dynamic information flow policy, from a semantic perspective, should only need to concern itself with two things: 1) the dependencies between data in a program, and 2) whether those dependencies are consistent with the intended flow policies as they change over time. In this paper we provide a formal ground for this intuition. We present a straightforward extension to the principal flow-sensitive type system introduced by Hunt and Sands (POPL ’06, ESOP ’11) to infer both end-to-end dependencies and dependencies at intermediate points in a program. This allows typings to be applied to verification of both static and dynamic policies. Our extension preserves the principal type system’s distinguishing feature, that type inference is independent of the policy to be enforced: a single, generic dependency analysis (typing) can be used to verify many different dynamic policies of a given program, thus achieving a clean separation between (1) and (2).

We also make contributions to the foundations of dynamic information flow. Arguably, the most compelling semantic definitions for dynamic security conditions in the literature are phrased in the so-called knowledge-based style. We contribute a new definition of knowledge-based termination insensitive security for dynamic policies. We show that the new definition avoids anomalies of previous definitions and enjoys a simple and useful characterisation as a two-run style property.

1 Introduction

Information flow policies are security policies which aim to provide end-to-end security guarantees of the form “information must not flow from this source to this destination”. Early work on information flow concentrated on static, multi-level policies, organising the various data sources and sinks of a system into a fixed hierarchy. The policy determined by such a hierarchy (a partial order) is simply that information must not flow from $a$ to $b$ unless $a \sqsubseteq b$.

1.1 Dynamic policies

Since the pioneering work of Denning and Denning [DD77], a wide variety of information-flow policies and corresponding enforcement mechanisms have been proposed.

This is the Technical Report of [DHSTS].
Much recent work on information-flow properties goes beyond the static, multi-level security policies of earlier work, considering instead more sophisticated, dynamic forms of policy which permit different flows at different points during the execution of a program. Indeed, this shift of focus better reflects real-world requirements for security policies which are naturally dynamic.

For example, consider a request for sensitive employee information made to an employer by a regulatory authority. In order to satisfy this request it may be necessary to temporarily allow the sensitive information to flow to a specific user in the Human Resources department. In simplified form, the essence of this example is captured in Figure 1.

Here $x$ contains the sensitive information, channel $a$ represents the HR user, and the policy is expressed by the annotations $x \rightarrow a$ ($x$ may flow to $a$) and $x \not\rightarrow a$ ($x$ must not flow to $a$). It is intuitively clear that this program complies with the policy.

Consider two slightly more subtle examples, in each of which revocation of a permitted flow depends on run-time data:

In program A, the revocation of $x \rightarrow a$ is controlled by the value of $y$, whereas in program B it is controlled by the value of $x$ itself. Note that the policy for A explicitly allows $y \rightarrow a$ so the conditional output (which reveals information about $y$) appears to be permissible. In program B the conditional output reveals information about $x$ itself, but this happens before the revocation. So should program B be regarded as compliant? We argue that it should not, as follows. Consider “the third output” of program B as observed on channel $a$. Depending on the initial value of $x$, the observed value may be either 2 (line 8) or 3 (line 9). Thus this observation reveals information about $x$ and, in the cases where revocation occurs, the observation happens after the revocation.

Unsurprisingly, increasing the sophistication of policies also increases the challenge of formulating good semantic definitions, which is to say, definitions which both match our intuitions about what the policies mean and can form the basis of formal reasoning about correctness.

At first sight it might seem that increasing semantic sophistication should also require increasingly intricate enforcement mechanisms. However, all such mechanisms must somehow solve the same two distinct problems:

1. Determine what data dependencies exist between the various data sources and sinks manipulated by the program.
2. Determine whether those dependencies are consistent with the flows permitted by the policy.

Ideally, the first of these problems would be solved independently of the second, since dependencies are a property of the code, not the policy. This would allow reuse at two levels: a) reuse of the same dependency analysis mechanisms and proof techniques for...
different *types* of policy; b) reuse of the dependency properties for a given program across verification of multiple *alternative* policies (whether of the same type or not).

In practice, enforcement mechanisms are typically not presented in a way which cleanly separates the two concerns. Not only does this hamper the reuse of analysis mechanisms and proof techniques, it also makes it harder to identify the *essential* differences between different approaches.

**Central Contribution** We take a well-understood dependency type system for a simple while-language, originally designed to support enforcement of static policies, and extend it in a straightforward way to a language with output channels (§5). We demonstrate the advantages of a clean separation between dependency analysis and policy enforcement, by establishing a generic soundness result (§6) for the type system which characterises the meaning of types as dependency properties. We then show how the dependency information derived by the type system can be used to verify compliance with dynamic policies. Note that this means that the core analysis for enforcement can be done even before the policy is known: we dub this *very static* enforcement. More significantly, it opens the way to reuse of dependency analyses across verification of multiple types of information flow policy (for example, it might be possible to use the dependency analyses performed by advanced slicing tools such as Joanna and Indus).

**Foundations of Dynamic Flow Policies** Although it was not our original aim and focus, we also make some contributions of a more foundational nature, and our paper opens with these (§2–§4). The semantic definition of security which we use is based on work of Askarov and Chong [AC12], and we begin with their abstract formulation of dynamic policies (§2). In defining security for dynamic policies, they made a convincing case for using a family of attackers of various strengths, following an observation that the intuitively strongest attacker (who never forgets anything that has been observed) actually places weaker security demands on the system than we would want. On the other hand they observe that the family of *all* attackers contains pathological attacker behaviours which one certainly does not wish to consider. Due to this they do not give a characterisation of the set of all *reasonable* attackers against which one should protect.

We make the following two foundational contributions:

**Foundational Contribution 1** We focus (§3.3) on the pragmatic case of *progress insensitive* security (where slow information leakage is allowed through observation of computational progress [AHSS08]). We argue for a new definition of progress insensitive security (Def11), which unconditionally grants all attackers knowledge of computational progress. With this modification to the definition from [AC12], the problematic examples of pathological attackers are eliminated, and we have a more complete definition of security. Consequently, we are able to prove security in the central contribution of the paper for *all attackers*.

**Foundational Contribution 2** The definitions of security are based on characterising attacker knowledge and how it changes over time relative to the changing policy. As argued previously e.g., [BS09], this style of definition forms a much more intuitive basis for a semantics of dynamic policies than using two-run characterisations. However, two-run formulations have the advantage of being easier to use in proofs. We show (§4) that our new knowledge-based progress-insensitive security definition enjoys a simple two-
run characterisation. We make good use of this in our proof of correctness of our central contribution.

2 The Dynamic Policy Model

In this section we define an abstract model of computation and a model of dynamic policies which maps computation histories to equivalence relations on stores.

2.1 Computation and Observation Model

Computation Model The computation model is given by a labelled transition system over configurations. We write \( \langle c, \sigma \rangle \xrightarrow{\alpha} \langle c', \sigma' \rangle \) means that configuration \( \langle c, \sigma \rangle \) evaluates in one step to configuration \( \langle c', \sigma' \rangle \) with label \( \alpha \). Here \( c \) is a command and \( \sigma \in \Sigma \) is a store. In examples and when we instantiate this model the store will be a mapping from program variables to values.

The label \( \alpha \) records any output that happens during that step, and we have a distinguished label value \( \epsilon \) to denote a silent step which produces no output. Every non-silent label \( \alpha \) has an associated channel \( \text{channel}(\alpha) \in \text{Chan} \) and a value \( \text{value}(\alpha) \). Channels are ranged over by \( a \) and values by \( v \). We abbreviate a sequence of evaluation steps

\[
\langle c_0, \sigma_0 \rangle \xrightarrow{\alpha_1} \langle c_1, \sigma_1 \rangle \xrightarrow{\alpha_2} \ldots \xrightarrow{\alpha_n} \langle c_n, \sigma_n \rangle
\]

as \( \langle c_0, \sigma_0 \rangle \xrightarrow{\alpha} \langle c_n, \sigma_n \rangle \). We write \( \langle c_0, \sigma_0 \rangle \xrightarrow{\alpha} \langle c' \rangle \) if \( \langle c_0, \sigma_0 \rangle \xrightarrow{\alpha} \langle c', \sigma' \rangle \) for some \( n \geq 0 \). We write the projection of a single step \( \langle c, \sigma \rangle \xrightarrow{\alpha} \langle c', \sigma' \rangle \) to some channel \( a \) as \( \langle c, \sigma \rangle \xrightarrow{\alpha \mapsto a} \langle c', \sigma' \rangle \) where \( \alpha \mapsto a \) if \( \text{channel}(\alpha) = a \) and \( \text{value}(\alpha) = v \), and \( \alpha \mapsto \epsilon \) otherwise, that is, when \( \alpha \) is silent or an output on a channel different from \( a \).

We abbreviate a sequence of evaluation steps

\[
\langle c_0, \sigma_0 \rangle \xrightarrow{\alpha_1} \langle c_1, \sigma_1 \rangle \xrightarrow{\alpha_2} \ldots \xrightarrow{\alpha_n} \langle c_n, \sigma_n \rangle
\]

as \( \langle c_0, \sigma_0 \rangle \xrightarrow{\alpha} \langle c_n, \sigma_n \rangle \) where \( t \) is the trace of values produced on channel \( a \) with every silent \( \epsilon \) filtered out. We write \( \langle c_0, \sigma_0 \rangle \xrightarrow{\alpha} \langle c' \rangle \) if \( \langle c_0, \sigma_0 \rangle \xrightarrow{\alpha} \langle c', \sigma' \rangle \) for some \( n \geq 0 \).

We use \( |t| \) to denote the length of trace \( t \) and \( t_1 \preceq t_2 \) to denote that trace \( t_1 \) is a prefix of trace \( t_2 \).

Attacker’s Observation Model We follow the standard assumption that the command \( c \) is known to the attacker. We assume a passive attacker which aims to extract information about an input store \( \sigma \) by observing outputs. As in [AC12], the attacker is able only to observe a single channel. A generalisation to multi-channel attackers (which would also allow colluding attackers to be modelled) is left for future work.

2.2 Dynamic Policies

A flow policy specifies a limit on how much information an attacker may learn. A very general way to specify such a limit is as an equivalence relation on input stores.

Example 1. Consider a store with variables \( x \) and \( y \). A simple policy might state that the attacker should only be able to learn the value of \( x \). It follows that all stores which agree on the value of \( x \) should look the same to the attacker. This is expressed as the equivalence relation \( \sigma \equiv \rho \) iff \( \sigma(x) = \rho(x) \).

A more complicated policy might allow the attacker to learn the value of some arbitrary expression \( e \) on the initial store, e.g. \( x = y \). This is expressed as the equivalence relation \( \sigma \equiv \rho \) iff \( \sigma(e) = \rho(e) \).
Definition 1 (Policy). A policy $P$ maps each channel to an equivalence relation $\equiv$ on stores. We write $P_a$ for the equivalence relation that $P$ defines for channel $a$.

As defined, policies are static. A dynamic policy changes while the program is running and may dictate a different $P$ for each point in the execution. Here we assume that the policy changes synchronously with the execution of the program. That is, the active policy can be deterministically derived from the execution history so far.

Definition 2 (Execution History). An execution history $\mathcal{H}$ of length $n$ is a transition sequence $\langle c_0, \sigma_0 \rangle \xrightarrow{\alpha_1} \langle c_1, \sigma_1 \rangle \xrightarrow{\alpha_2} \ldots \xrightarrow{\alpha_n} \langle c_n, \sigma_n \rangle$.

Definition 3 (Dynamic Policy). A dynamic policy $D$ maps every execution history $\mathcal{H}$ to a policy $D(\mathcal{H})$. We write $D_a(\mathcal{H})$ for the equivalence relation that is defined by $D(\mathcal{H})$ for channel $a$, that is to say, $D_a(\mathcal{H}) = P_a$ where $P = D(\mathcal{H})$.

Most synchronous dynamic policy languages in the literature determine the current policy based solely on the store $\sigma_n$ in the final configuration of the execution history $[AC12,BvDS13]$. Definition 3 allows in principle for more flexible notions of dynamic policies, as they can incorporate the full execution history to determine the policy at each stage of an execution (similar to the notion of conditional noninterference used by $[GM84,Zha12]$). However, our enforcement does assume that the dynamic policy can be statically approximated per program point, which arguably is only feasible for policies in the style of $[AC12,BvDS13]$. Such approximations can typically be improved by allowing the program to branch on policy-related queries.

Since programs are deterministic, an execution history of length $n$ is uniquely determined by its initial configuration $\langle c_0, \sigma_0 \rangle$. We use this fact to simplify our definitions and proofs:

Definition 4 (Execution Point). An execution point is a triple $(c_0, \sigma_0, n)$ identifying the point in execution reached after $n$ evaluation steps starting from configuration $\langle c_0, \sigma_0 \rangle$. Such an execution point is considered well-defined iff there exists $\langle c_n, \sigma_n \rangle$ such that $\langle c_0, \sigma_0 \rangle \xrightarrow{\alpha}^n \langle c_n, \sigma_n \rangle$.

Lemma 1. Each well-defined execution point $(c_0, \sigma_0, n)$ uniquely determines an execution history $\mathcal{H}(c_0, \sigma_0, n)$ of length $n$ starting in configuration $\langle c_0, \sigma_0 \rangle$.

In the rest of the paper we rely on this fact to justify a convenient abuse of notation, writing $D(c_0, \sigma_0, n)$ to mean $D(\mathcal{H}(c_0, \sigma_0, n))$.

3 Knowledge-Based Security Conditions

Recent works on dynamic policies, including $[AC12,BDLG11,BNR08,BS10]$, make use of so-called knowledge-based security definitions, building on the notion of gradual release introduced in $[AS07]$. This form of definition seems well-suited to provide intuitive semantics for dynamic policies. We focus in particular on the attacker-parametric model from Askarov and Chong in $[AC12]$.

Suppose that the input state to a program is $\sigma$. In the knowledge-based approach, an attacker’s knowledge of $\sigma$ is modelled as a knowledge set $K$, which may be any set of states such that $\sigma \in K$. Note that the larger the knowledge set, the less certain
is the attacker of the actual value of $\sigma$, so smaller $K$ means more precise knowledge. (Sometimes, as we see below, it can be more intuitive to focus on the complement $\overline{K}$, which is the set of a-priori possible states which the attacker is able to exclude, since this set, which we will call the exclusion knowledge, grows as the attacker learns more).

Now suppose that the currently active policy is $\equiv$. The essential idea in any knowledge-based semantics is to view the equivalence classes of $\equiv$ as placing upper bounds on the attacker’s knowledge. In the simplest setting, if the actual input state is $\sigma$ and the attacker’s knowledge set is $K$, we require:

$$K \supseteq \{\sigma' \mid \sigma' \equiv \sigma\}$$

Or, in terms of what the attacker is able to exclude:

$$K \subseteq \{\rho \mid \rho \not\equiv \sigma\}$$

(1)

How then do we determine the attacker’s knowledge? Suppose an attacker knows the program $c$ and observes channel $a$. Ignoring covert channels (timing, power, etc) an obvious approach is to say that the attacker’s knowledge is simply a function of the trace $t$ observed so far: $k(t) = \{\rho \mid \langle c, \rho \rangle \stackrel{t}{\rightarrow} a\}$

(2)

We define the exclusion knowledge as the complement of this: $ek(t) = \overline{k(t)}$. Note that, as a program executes and more outputs are observed, the attacker’s exclusion knowledge can only increase; if $\langle c, \sigma \rangle \stackrel{t \cdot v}{\rightarrow} a$ then $ek(t) \subseteq ek(t \cdot v)$, since, if $\rho$ can already be excluded by observation of $t$ (because $c$ cannot produce $t$ when started in $\rho$), then it will still be excluded when $t \cdot v$ is observed (if $c$ cannot produce $t$ it cannot produce any extension of $t$ either).

But this simple model is problematic for dynamic policies. Suppose that the policies in effect when $t$ and $t \cdot v$ are observed are, respectively $\equiv_1$ and $\equiv_2$. Then it seems that we must require both $ek(t) \subseteq \{\rho \mid \rho \not\equiv_1 \sigma\}$ and $ek(t \cdot v) \subseteq \{\rho \mid \rho \not\equiv_2 \sigma\}$. As observed above, it will always be the case that $ek(t) \subseteq ek(t \cdot v)$, so we are forced to require $ek(t) \subseteq \{\rho \mid \rho \not\equiv_2 \sigma\}$. In other words, the observations that we can permit at any given moment are constrained not only by the policy currently in effect but also by all policies which will be in effect in the future. This makes it impossible to have dynamic policies which revoke previously permitted flows (or, at least, pointless; since all revocations would apply retrospectively, the earlier “permissions” could never be exercised).

Askarov and Chong’s solution has two key components, outlined in the following two sections.

### 3.1 Change in Knowledge

Firstly, recognising that policy changes should not apply retrospectively, we can relax (1) to constrain only how an attacker’s knowledge should be allowed to increase, rather than its absolute value. The increase in attacker knowledge going from $t$ to $t \cdot v$ is given by the set difference $ek(t \cdot v) - ek(t)$. So, instead of (1), we require:

$$ek(t \cdot v) - ek(t) \subseteq \{\rho \mid \rho \not\equiv \sigma\}$$

(3)

where $\equiv$ is the policy in effect immediately before the output $v$. (Some minor set-theoretic rearrangement gives the equivalent

$$k(t \cdot v) \supseteq k(t) \cap \{\sigma' \mid \sigma' \equiv \sigma\}$$

which is the form of the original presentation in [AC12].)
3.2 Forgetful attackers

Focussing on change in knowledge addresses the problem of retrospective revocation but it creates a new issue. Consider the following example.

Example 2. The program in Figure 2 produces the same output many times, but only the first output is permitted by the policy. Assume that the value of $x$ is 5. Before the first output, the knowledge set of an observer on channel $a$ contains every possible store. After the first output the observer’s knowledge set is reduced to include only those stores $\sigma$ where $\sigma(x) = 5$. This is allowed by the policy at that point.

By the time the second output occurs, the policy prohibits any further flow from $x$. However, since the attacker’s knowledge set already includes complete knowledge of $x$, the second output does not actually change the attacker’s knowledge at all, so (3) is satisfied (since $k(t \cdot v) = k(t)$). Thus a policy semantics based on (3) would accept this program even though it continues to leak the value of $x$ long after the flow has been revoked.

Askarov and Chong address this by revisiting the assumption that an attacker’s knowledge is necessarily determined by the simple function of traces (2) above. Consider an attacker which forgets the value of the first output in example 2. For this attacker, the second output would come as a revelation, revealing the value of $x$ all over again, in violation of the policy. Askarov and Chong thus arrive at the intriguing observation that security against a more powerful attacker, one who remembers everything that happens, does not imply security against a less resourceful attacker, who might forget parts of the observations made.

Forgetful attackers are modelled as deterministic automata.

Definition 5 (Forgetful Attacker ⊲ § III.A [AC12]). A forgetful attacker is a tuple $A = (S_A, s_0, \delta_A)$ where $S_A$ is the set of attacker states; $s_0 \in S_A$ is the initial state; and $\delta_A : S_A \times \text{Val} \rightarrow S_A$ the (deterministic) transition function describing how the attacker’s state changes due to the values that the attacker observes.

We write $A(t)$ for the attacker’s state after observing trace $t$:

$$A(\epsilon) = s_0$$
$$A(t \cdot v) = \delta_A(A(t), v)$$

A forgetful attacker’s knowledge after trace $t$ is defined as the set of all initial stores that produce a trace which would result in the same attacker state $A(t)$:

Definition 6 (Forgetful Attacker Knowledge ⊲ § IIIA [AC12]).

$$k(A, c, a, t) = \{ \rho \mid \langle c, \rho \rangle \xrightarrow{\cdot \cdot t} \chi_a \land A(t') = A(t) \}$$

(Note that, in preparation for the formal definition of the security condition, program $c$ and channel $a$ now appear as explicit parameters.)

The proposed security condition is still essentially as given by (3), but now relative to a specific choice of attacker. Stated in the notation and style of the current paper, the formal definition is as follows.
Definition 7 (Knowledge-Based Security ▷ Def. 1 [AC12]). Command $c$ is secure for policy $D$ against an attacker $A$ on channel $a$ for initial store $\sigma$ if for all traces $t$ and values $v$ such that $\langle c, \sigma \rangle \xrightarrow{t} \langle c', \sigma' \rangle \xrightarrow{n} 1$ we have
\[
ek(A, c, a, t \cdot v) - \ek(A, c, a, t) \subseteq \{ \rho \mid \rho \not\equiv \sigma \}\]
where $\equiv = D_a(c, \sigma, n)$.

Having relativized security to the power of an attacker’s memory, it is natural to consider the strong notion of security that would be obtained by requiring Def. 7 to hold for all choices of $A$. However, as shown in [AC12], this exposes a problem with the model: there are attackers for which even well-behaved programs are insecure according to Def. 7.

Example 3. Consider again the first example from the Introduction (Section 1.1). Here, for simplicity, we assume that the variable $x$ is boolean, taking value 0 or 1.

It is intuitively clear that this program complies with the policy. However, as observed in [AC12], if we instantiate Def. 7 with the forgetful attacker displayed, the attacker’s knowledge increases with the second output when $x = 0$.

After observing the value 0, the attacker’s state is $A(0) = q_0$. Since $A(\epsilon) = q_0$, the knowledge set still holds every store possible. After the second observation, only stores where $x = 0$ could have led to state $q_2$, so the knowledge set shrinks (i.e., the attacker’s knowledge increases) at a point where the policy does not allow it.

This example poses a question which (so far as we are aware) remains unanswered: if we base a dynamic policy semantics on Def. 7 for which set of attackers should we require it to hold?

In the next section we define a progress-insensitive variant of Def. 7. For this variant it seems that security against all attackers is a reasonable requirement and in Section 6 we show that progress-insensitive security against all attackers is indeed enforced by our type system.

3.3 Progress Insensitive Security

Since [VSI95], work on the formalisation and enforcement of information-flow policies has generally distinguished between two flavours of security: termination-sensitive and termination-insensitive. Termination-sensitive properties guarantee that protected information is neither revealed by its influence on input-output behaviour nor by its influence on termination behaviour. Termination-insensitive properties allow the latter flows and thus provide weaker guarantees. For systems with incremental output (as opposed to batch-processing systems) it is more appropriate to distinguish between progress-sensitive and progress-insensitive security. Progress-insensitive security ignores progress-flows, where a flow is regarded as a progress-flow if the information that it reveals can be inferred solely by observing how many outputs the system produces.
Two examples of programs with progress-flows are as follows:

*Example 4.* Programs containing progress-flows:

```plaintext
// Program A
out 1 on a;
while (x == 8) skip;
out 2 on a;

// Program B
out 1 on a;
if (x != 8) out 2 on a;
out 2 on a;
```

Let $\sigma$ and $\rho$ differ only on the value of $x$: $\sigma(x) = 4$ and $\rho(x) = 8$. Note that, if started in $\sigma$, both programs produce a trace of length 2 (namely, the trace $1 \cdot 2$) whereas, if started in $\rho$, the maximum trace length is 1. Thus, for both programs, observing just the length of the trace produced can reveal information about $x$. Note that, since termination is not an observable event in the semantics, A and B are actually observably equivalent; we give the two variants to emphasise that progress-flows may occur even in the absence of loops.

In practice, most enforcement mechanisms only enforce progress-insensitive security. This is a pragmatic choice since (a) it is hard to enforce progress-sensitive security without being overly restrictive (typically, all programs which loop on protected data will be rejected), and (b) programs which leak solely via progress-flows, leak slowly [AHSS08].

Recall that Knowledge-Based Security (Def. 7) places a bound on the increase in an attacker’s knowledge which is allowed to arise from observation of the next output event. Askarov and Chong show how this can be weakened in a natural way to provide a progress-insensitive property, by artificially strengthening the supposed previous knowledge to already include progress knowledge. Their definition of progress knowledge is as follows:

**Definition 8 (AC Progress Knowledge ⊲ IIIA [AC12]).**

$$k^+(A, c, a, t) = \{ \rho \mid \langle c, \rho \rangle \xrightarrow{t \cdot v} a \land A(t') = A(t) \}$$

Substituting this (actually, its complement) in the “previous knowledge” position in Def. 7 provides Askarov and Chong’s notion of progress-insensitive security:

**Definition 9 (AC Progress-Insensitive (ACPI) Security ⊲ Def. 2 [AC12]).** Command $c$ is AC Progress-Insensitive secure for policy $D$ against an attacker $A$ on channel $a$ for initial store $\sigma$ if for all traces $t$ and values $v$ such that $\langle c, \sigma \rangle \xrightarrow{t \cdot v} a$ we have

$$ek(A, c, a, t \cdot v) - ek^+(A, c, a, t) \subseteq \{ \rho \mid \rho \neq \sigma \}$$

where $\equiv = D_a(c, \sigma, n)$.

Now consider again programs A and B above. These are examples of programs where the only flows are progress-flows. In general, we say that a program is quasi-constant if there is some fixed (possibly infinite) trace $t$ such that every trace produced by the program is a prefix of $t$, regardless of the choice of initial store. Thus, for a quasi-constant program, the only possible observable variation in observed behaviour is trace length, so all flows are progress-flows. Since PI security is intended explicitly to allow progress-flows, we should expect all quasi-constant programs to satisfy PI security, regardless of the choice of policy and for all possible attackers. But, for Def. 9 this fails to hold, as shown by the following counterexample.
Example 5. Consider the program and attacker below. The attacker is a very simple bounded-memory attacker which remembers just the last output seen and nothing else (not even whether it has seen any previous outputs).

```plaintext
// x \not\rightarrow a
out 1 on a;
out 1 on a;
while (x) skip;
out 1 on a;
out 2 on a;
```

Clearly, the program is quasi-constant. However, it is not ACPI secure for the given attacker. To see this, suppose that \( x = 0 \) and consider the trace \( t = 1 \cdot 1 \cdot 1 \). The attacker has no knowledge at this point (\( k(t) \) is the set of all stores) since it does not know whether it has seen one, two or three 1's. It is easily verified that \( k^+(t) \) is also the set of all stores for this attacker (intuitively, giving this attacker progress knowledge in the form \( k^+ \) doesn’t help it, since it still does not know which side of the loop has been reached). But \( k(t \cdot 2) \) is not the set of all stores, since in state \( q_2 \) the attacker is able to exclude all stores for which \( x = 1 \), thus ACPI security is violated.

What has gone wrong here? The attacker itself seems reasonable. We argue that the real problem lies in the definition of \( k^+(A, c, a, t) \). As defined, this is the knowledge that \( A \) would have in state \( A(t) \) if given just the additional information that \( c \) can produce at least one more output. But this takes no account of any previous progress knowledge which might have been forgotten by \( A \). (Indeed, the above attacker forgets nearly all such previous progress knowledge.) As a consequence, the resulting definition of PI security mistakenly treats some increases in knowledge as significant, even though they arise purely because the attacker has forgotten previously available progress knowledge.

Our solution will be to re-define progress knowledge to include what the attacker would know if it had been keeping count. To this end, for any attacker \( A = (S, s_0, \delta) \) we define a counting variant \( A^\omega = (S^\omega, s^\omega_0, \delta^\omega) \), such that \( S^\omega \subseteq S \times \mathbb{N} \), \( s^\omega_0 = (s_0, 0) \) and \( \delta^\omega((s, n), v) = (\delta(s, v), n + 1) \). In general, \( A^\omega \) will be at least as strong an attacker as \( A \):

**Lemma 2.** For all \( A, c, a, t \):

1. \( k(A^\omega, c, a, t) \subseteq k(A, c, a, t) \)
2. \( e k(A, c, a, t) \subseteq e k(A^\omega, c, a, t) \)

**Proof.** It is easily seen that \( A^\omega(t) = (q, n) \Rightarrow A(t) = q \). Thus \( A^\omega(t') = A^\omega(t) \Rightarrow A(t') = A(t) \), which establishes part 1. Part 2 is just the contrapositive of part 1.

Our alternative definition of progress knowledge is then:

**Definition 10 (Full Progress Knowledge).**

\[
 k^#(A, c, a, t) = \{ \rho | (c, \rho) \xrightarrow{t \cdot a} A^\omega(t') = A^\omega(t) \}
\]

Our corresponding PI security property is:
Definition 11 (Progress-Insensitive (PI) Security). Command $c$ is progress-insensitive secure for policy $D$ against an attacker $A$ on channel $a$ for initial store $\sigma$ if for all traces $t$ and values $v$ such that $\langle c, \sigma \rangle \xrightarrow{v} (c', \sigma')$ we have

$$ek(A, c, a, t \cdot v) - ek^\#(A, c, a, t) \subseteq \{ \rho \mid \rho \not\equiv \sigma \}$$

where $\equiv = D_a(c, \sigma, n)$.

This definition behaves as expected for quasi-constant programs:

Lemma 3. Let $c$ be a quasi-constant program. Then $c$ is PI secure for all policies $D$ against all attackers $A$ on all channels $a$ for all initial stores $\sigma$.

Proof. It suffices to note that, from the definitions, if $t \cdot v$ is a possible trace for $c$ and $c$ is quasi-constant, then $k^\#(A, c, a, t) = k(A^ω, c, a, t \cdot v)$. The result follows by Lemma 2.

As a final remark in this section, we note that there is a class of attackers for which ACPI and PI security coincide. Say that $A$ is counting if it always remembers at least how many outputs it has observed. Formally:

Definition 12 (Counting Attacker). $A$ is counting if $A(t) = A(t') \Rightarrow |t| = |t'|$.

Now say that attackers $A$ and $A'$ are isomorphic (written $A \cong A'$) if $A(t_1) = A(t_2) \iff A'(t_1) = A'(t_2)$ and note that none of the attacker-parametric security conditions distinguish between isomorphic attackers (in particular, knowledge sets are always equal for isomorphic attackers). It is easily verified that $A \cong A^ω$ for all counting attackers. It is then immediate from the definitions that ACPI security and PI security coincide for counting attackers.

4 Progress-Insensitive Security as a Two-Run Property

Our aim in this section is to derive a security property which guarantees (in fact, is equivalent to) PI security for all attackers, and in a form which facilitates the soundness proof of our type system. For this we seek a property in “two run” form.

First we reduce the problem by establishing that it suffices to consider just the counting attackers.

Lemma 4. Let $c$ be a command. Then, for any given policy, channel and initial store, $c$ is PI secure against all attackers iff $c$ is PI secure against all counting attackers.

Proof. Left to right is immediate. Right to left, it suffices to show that

$$ek(A, c, a, t \cdot v) - ek^\#(A, c, a, t) \subseteq ek(A^ω, c, a, t \cdot v) - ek^\#(A^ω, c, a, t)$$

Since $A^ω \cong (A^ω)^ω$, we have $ek^\#(A^ω, c, a, t) = ek^\#(A, c, a, t)$. It remains to show that $ek(A, c, a, t \cdot v) \subseteq ek(A^ω, c, a, t \cdot v)$, which holds by Lemma 2.

Our approach is now essentially to unwind Def. 11. Our starting point for the unwinding is:

$$ek(A, c, a, t \cdot v) - ek^\#(A, c, a, t) \subseteq \{ \rho \mid \rho \not\equiv \sigma \}$$

where $\equiv$ is the policy in effect at the moment the output $v$ is produced. Simple set-theoretic rearrangement gives the equivalent:

$$\{ \sigma' \mid \sigma' \equiv \sigma \} \cap k^\#(A, c, a, t) \subseteq k(A, c, a, t \cdot v)$$
Expanding the definitions, we arrive at:
\[
\rho \equiv \sigma \land \langle c, \rho \rangle \xrightarrow{t \cdot v'} a \land A^{v'}(t') = A^{v'}(t) \Rightarrow \exists s. \langle c, \rho \rangle \xrightarrow{a} s \land A(s) = A(t \cdot v)
\]

By the above lemma, we can assume without loss of generality that \( A \) is counting, so we can replace \( A^{v'}(t') = A^{v'}(t) \) by \( A(t') = A(t) \) on the lhs. Since \( A \) is counting, we know that \( |t| = |t'| \) and \( |s| = |t \cdot v| \), hence \( |s| = |t' \cdot v'| \). Now, since \( c \) is deterministic and both \( s \) and \( t' \cdot v' \) start from the same \( \rho \), it follows that \( s = t' \cdot v' \). Thus we can simplify the unwinding to:

\[
\rho \equiv \sigma \land \langle c, \rho \rangle \xrightarrow{t \cdot v'} a \land A(t') = A(t) \Rightarrow A(t' \cdot v') = A(t \cdot v)
\]

Now, suppose that this holds for \( A \) and that \( v' \neq v \). Let \( q \) be the attacker state \( A(t') = A(t) \) and let \( r \) be the attacker state \( A(t' \cdot v') = A(t \cdot v) \). Since \( |t| \neq |t \cdot v| \) and \( A \) is counting, we know that \( q \neq r \). Then we can construct an attacker \( A' \) from \( A \) which leaves \( q \) intact but splits \( r \) into two distinct states \( r_v \) and \( r_{v'} \). But then security will fail to hold for \( A' \), since \( A'(t' \cdot v') = r_v \neq r_{v'} = A'(t \cdot v) \). So, since we require security to hold for all \( A \), we may strengthen the rhs to \( A(t' \cdot v') = A(t \cdot v) \land v = v' \). Then, given \( A(t') = A(t) \), since \( A \) is a deterministic automaton, it follows that \( v = v' \Rightarrow A(t' \cdot v') = A(t \cdot v) \), hence the rhs simplifies to just \( v = v' \) and the unwinding reduces to:

\[
\rho \equiv \sigma \land \langle c, \rho \rangle \xrightarrow{t \cdot v'} a \land A(t') = A(t) \Rightarrow v' = v
\]

Finally, since \( A \) now only occurs on the lhs, we see that there is a distinguished counting attacker for which the unwinding is harder to satisfy than all others, namely the attacker \( A_{\#} \), for which \( A_{\#}(t') = A_{\#}(t) \iff |t'| = |t| \). Thus the property will hold for all \( A \) if it holds for \( A_{\#} \) and so we arrive at our two-run property:

**Definition 13 (Two-Run PI Security).** Command \( c \) is two-run PI secure for policy \( D \) on channel \( a \) for initial store \( \sigma \) if whenever \( \langle c, \sigma \rangle \xrightarrow{n} \langle c_n, \sigma_n \rangle \xrightarrow{a} \) and \( \rho \equiv \sigma \land \langle c, \rho \rangle \xrightarrow{t \cdot v'} a \land |t'| = |t| \), then \( v' = v \), where \( \equiv = D_a(c, \sigma, n) \).

**Theorem 1.** Let \( c \) be a command. For any given policy, channel and initial store, \( c \) is PI secure against all attackers iff \( c \) is two-run PI secure.

**Proof.** This follows from the unwinding of the PI security definition, as shown above.

## 5 A Dependency Type System

Within the literature on enforcement of information flow policies, some work is distinguished by the appearance of explicit dependency analyses. In the current paper we take as our starting point the flow-sensitive type systems of [HS06], due to the relative simplicity of presentation. Other papers proposing similar analyses include [CHH02], [AB04], [AR80] and [BBL94]. Some of the similarities and differences between these approaches are discussed in [HS06].

The original work of [HS06] defines a family of type systems, parameterised by choice of a multi-level security lattice, and establishes the existence of principal typings within this family. The later work of [HST1] defines a single system which produces only principal types. In what follows we refer to the particular flow-sensitive type system defined in [HST1] system as FST.
Values  \( v ::= n \)
Expressions  \( e ::= v | x \)
Commands  \( c ::= \text{skip} | c_1; c_2 | x ::= e | \text{if } e | c_1 \mid \text{while } c | \text{out } e \text{ on } a \odot p \)

\[
\begin{align*}
\langle \text{skip}; c, \sigma \rangle & \not\rightarrow \langle c, \sigma \rangle \\
\langle c_1; c_2, \sigma \rangle & \not\rightarrow \langle c_1'; c_2', \sigma' \rangle \\
\sigma(e) & = v \\
\langle \text{out } e \text{ on } a \odot p, \sigma \rangle & \not\rightarrow \langle \text{skip}, \sigma' \rangle \\
\langle \text{if } e, \sigma \rangle & \not\rightarrow \langle \text{skip}, \sigma' \rangle \\
\sigma(e) & \neq 0 \\
\langle \text{if } e, \sigma \rangle & \not\rightarrow \langle c_1, \sigma \rangle \\
\langle \text{if } e, \sigma \rangle & \not\rightarrow \langle c_2, \sigma \rangle
\end{align*}
\]

Fig. 4: Language and semantics.

The typings derived by FST take the form of an environment \( \Gamma \) mapping each program variable \( x \) to a set \( \Gamma(x) \) which has a direct reading as (a conservative approximation to) the set of dependencies for \( x \). All other types derivable using the flow-sensitive type systems of [HS06] can be recovered from the principal type derived by FST. Because principal types are simply dependency sets, they are not specific to any particular security hierarchy or policy. This is the basis of the clean separation we are able to achieve between analysis and policy verification in what follows.

Consider the simple program shown in Figure 3. The type inferred for this program is \( \Gamma \), where \( \Gamma(x) = \{\} \), \( \Gamma(y) = \{y, z\} \), \( \Gamma(z) = \{z\} \). From this typing we can verify, for example, any static policy using a security lattice in which \( \text{level}(z) \sqsubseteq \text{level}(y) \).

FST is defined only for a simple language which does not include output statements. This makes it unsuitable for direct application to verification of dynamic policies, so in the current paper we describe a straightforward extension of FST to a language with output statements. We then show how the inferred types can be used to enforce policies such as those in [ACT12] and [BvDS13], which appear very different from the simple static, multi-level policies originally targeted.

### 5.1 Language

We instantiate the abstract computation model of Section 2.1 with a simple while-language with output channels, shown in Figure 3. We let \( x \in \text{PVar} \) range over program variables, \( a \in \text{Chan} \) range over channels (as before) and \( p \in \text{PPoint} \) range over program points. Here non-silent output labels have the form \( (a, v, p) \), \( \text{channel}(a, v, p) = a \), and \( \text{value}(a, v, p) = v \).

The language is similar to the one considered in [ACT12], except for the absence of input channels.

Outputs have to be annotated with a program point \( p \) to bridge between the dependency analysis and the policy analysis, described in Section 6.

### 5.2 Generic typing

Traditional type systems for information flow assume that all sensitive inputs to the system (here: program variables) are associated with a security level. Expressions in
the command to be typed might combine information with different security levels. To ensure that all expression can be typed, the security levels are therefore required to form at least a join-semilattice, or in some cases a full lattice. The type system then ensures no information of a (combined) level \( l_1 \) can be written to a program variable with level \( l_2 \) unless \( l_1 \sqsubseteq l_2 \).

The system FST from Hunt and Sands \[HS11\] differs from these type systems in two ways. Firstly, it does not require intermediate assignments to respect the security lattice ordering. As an observer is assumed to only see the final state of the program, only the final value of a variable must not depend on any information which is forbidden by the lattice ordering. For example, suppose \( \text{level}(y) \sqsubseteq \text{level}(z) \sqsubseteq \text{level}(x) \) but \( \text{level}(x) \not\sqsubseteq \text{level}(z) \) and consider the first two assignments in the example from Fig. 3.

\[
\begin{align*}
x & = z + 1; \\
z & = x;
\end{align*}
\]

A traditional type system would label this command as insecure because of the assignment \( z = x \) and the fact that \( \text{level}(x) \not\sqsubseteq \text{level}(z) \), even though the value of \( z \) after this assignment does not depend on the initial value of \( x \) at all. FST however is flow-sensitive and allows the security label on \( x \) to vary through the code.

Secondly, and more significantly, by using the powerset of program variables as security lattice, FST provides a principal typing from which all other possible typings can be inferred.

Thus the typing by FST is generic: a command needs to be typed only once and can then be verified against any static information-flow policy. Since the ordering among labels is not relevant while deriving the typing, FST is also able to verify policies which are not presented in the shape of a security lattice, but any relational ‘may-flow’ predicate between security labels can be verified.

### 5.3 Generic typing for dynamic policies

We now present an extended version of FST which includes an additional typing rule for outputs. All the original typing rules of FST remain unchanged.

Intuitively, an output on a channel is like the final assignment to a variable in the original FST, that is, its value can be observed. Since types are sets of dependencies, we could simply type an output channel as the union of all dependencies resulting from all output statements for that channel. This would be sound but unduly imprecise: the only flows permitted would be those permitted by the policy at all times, in effect requiring us to conservatively approximate each dynamic policy by a static one. But we can do better than this.

The flow-sensitivity of FST means that a type derivation infers types at intermediate program points which will, in general, be different from the top-level type inferred for the program. These intermediate types are not relevant for variables, since their intermediate values are not observable. But the outputs on channels at intermediate points are observable, and so intermediate channel types are relevant. Therefore, for each channel we record in \( \Gamma \) distinct dependency sets for each program point at which an output statement on that channel occurs. Of course, this is still a static approximation of runtime behaviour. While our simple examples of dynamic policies explicitly associate policy changes to program points, for real-world use more expressive dynamic policy languages may be needed. In Section 2.2 we formally define the semantics of a dynamic
policy as an arbitrary function of a program’s execution history, which provides a high
degree of generality. However, in order to apply a typing to the verification of such
a policy, it is first necessary to conservatively approximate the flows permitted by the
policy at each program point of interest (Definition 16).

Let $X$ be the dependency set for the channel-$a$ output statement at program point $p$.
The meaning of $X$ is as follows:

Let $\sigma$ be a store such that execution starting in $\sigma$ arrives at $p$, producing the
$i$’th output on $a$. Let $\rho$ be any store which agrees with $\sigma$ on all variables in $X$
and also eventually produces an $i$’th output on $a$ (not necessarily at the same
program point). Then these two outputs will be equal.

Two key aspects of our use of program points should be highlighted:

1. While the intended semantics of $X$ as outlined above does not require correspond-
ing outputs on different runs to be produced at the same program point, the $X$ that
is inferred by the type system does guarantee this stronger property. Essentially
this is because (in common with all similar analyses) the type system uses control-
flow dependency as a conservative proxy for the semantic dependency property of
interest.

2. Our choice of program point to distinguish between different outputs on the same
channel is not arbitrary; it is essentially forced by the structure of the original type
system. As noted, program point annotations simply allow us to record in the final
typing exactly those intermediate dependency sets which are already inferred by
the underlying flow-sensitive system. While it would be possible in principle to
make even finer distinctions (for example, aiming for path-sensitivity rather than
just flow-sensitivity) this would require fundamental changes to the type system.

The resulting type system is shown in Figure 5. We now proceed informally to
motivate its rules. Definitions and proofs of formal soundness are presented in Section 6.

The type system derives judgements of the form $\vdash \{c\} \Gamma$, where $\Gamma : \text{Var} \rightarrow 2^{\text{Var}}$ is
an environment mapping variables to a set of dependencies. The variables we consider
are $\text{Var} = \text{PVar} \cup \text{CPoint} \cup \{pc\} \cup \text{Chan}$ with $\text{CPoint} = \text{Chan} \times \text{PPoint}$. We
consider the relevance of each kind of variable in turn.

- As program variables $\text{PVar}$ form the inputs to the command, these are the depen-
dencies of interest in the typing of a command. For program variables themselves,
$\Gamma(x)$ are the dependencies for which a different initial value might result in a dif-
ferent final value of $x$.

- Pairs of channels and program points $(a, p) \in \text{CPoint}$ are denoted as $a_p$. The
dependencies $\Gamma(a_p)$ are those program variables for which a difference in initial
value might cause a difference in the value of any observation that can result from
an output statement for channel $a$ with annotation $p$.

- Whenever the program counter $pc \in \Gamma(x)$ this indicates that this command poten-
tially changes the value of program variable $x$. Similar, if $pc \in \Gamma(a)$ then $c$ might

\[ This is progress-insensitive dependency (see Section 3). A progress-sensitive version can be defined in a similar way. \]
produce an output on channel $a$ and if $pc \in \Gamma(a)$ then $c$ might produce an output on $a$ caused by a statement annotated with $p$. We use the program counter to catch implicit flows that may manifest in these ways.

- We use $Chan$ to capture the latent flows described in example program B in the introduction. The dependencies $\Gamma(a)$ are those program variables for which a difference in initial value might result in a different number of outputs produced on channel $a$ by this command. This approach to address latent flows was first introduced in [AC12] as channel countext bounds.

We first explain the notation used in the unchanged rules from FST before turning our attention to the new TS-OUTPUT rule. All concepts have been previously introduced in [HST].

The function $fv(e)$ returns the free variables in expression $e$. The identity environment $\Gamma_{id}$ maps each variable to the singleton set of itself, that is $\Gamma_{id}(x) = \{x\}$ for all $x \in Var$. Sequential composition of environments is defined as:

$$\Gamma_2; \Gamma_1(x) = \bigcup_{y \in \Gamma_2(x)} \Gamma_1(y)$$

Intuitively, $\Gamma_2; \Gamma_1$ is as $\Gamma_2$ but substituting the dependency relations already established in $\Gamma_1$. We overload the union operator for environments: $(\Gamma_1 \cup \Gamma_2)(x) = \Gamma_1(x) \cup \Gamma_2(x)$. We write $\Gamma^*$ for the fixed-point of $\Gamma$, used in TS-WHILE:

$$\Gamma^* = \bigcup_{n \geq 0} \Gamma^n \quad \text{where } \Gamma^0 = \Gamma_{id} \text{ and } \Gamma^{n+1} = \Gamma^n; \Gamma$$

It is only in the typing TS-OUTPUT of the output command that the additional channel and program point dependencies are mentioned; this underlines our statement that extending FST to target dynamic policies is straightforward.
We explain the changes to $\Gamma_{id}$ in TS-OUTPUT in turn. For $a_p$, clearly the value of the output and thus the observation is affected by the program variables occurring in the expression $e$. We also include the program counter $pc$ to catch implicit flows; if we have a command of the form $if e (out 1 on a @ p) (out 2 on a @ q)$ the value of the observation caused by output $a_p$ is affected by the branching decision, which is caught in TS-IFELSE.

We include the channel context bounds $a$ for the channel on which this output occurs to capture the latent flows of earlier conditional outputs, as demonstrated in the introduction. Observe that by the definition of sequential composition of environments, we only add those dependencies for conditional outputs that happened before this output. That is, not the ones that follow this output, since it cannot leak information about the absence of future observations.

Finally, we include the dependencies of output point $a_p$ itself. By doing so the dependency set of $a_p$ becomes cumulative: with every sequential composition (including those used in $\Gamma^*$) the dependency set of $a_p$ only grows, as opposed to the dependencies of program variables. This makes us sum the dependencies of all outputs on channel $a$ annotated with the same program point, as we argued earlier.

The mapping for channel context bounds $a$ is motivated in a similar manner. The $pc$ is included since the variables affecting whether this output occurs on channel $a$ are the same as those that affect whether this statement is reached. Note that we are over-approximating here, as the type system adds the dependencies of $e$ in

$$\text{if } e \ (\text{out } 1 \text{ on } a @ p_1) \ (\text{out } 2 \text{ on } a @ p_2)$$

to context bounds $a$, even though the number of outputs is always one.

Like for $a_p$, we make $a$ depend on itself, thus accumulating all the dependencies that affect the number of outputs on channel $a$.

As the TS-OUTPUT rule does not introduce more complex operations than already present, the type system has the same complexity as FST. That is, the type system can be used to construct a generic type in $O(nv^3)$ where $n$ is the size of the program and $v$ the number of variables in $Var$.

6 Semantic Soundness and Policy Compliance

We present a soundness condition for the type system, and show that the type system is sound. We then describe how the generic typings that the type system derives can be used to check compliance with a dynamic policy that is approximated per program point. We begin by showing how an equivalence relation on stores can be created from a typing:

Definition 14. We write $\equiv_{\Gamma(x)}$ for the equivalence relation corresponding to the typing $\Gamma$ of variable $x \in Var$, defined as $\sigma \equiv_{\Gamma(x)} \rho$ iff $\sigma(y) = \rho(y)$ for all $y \in \Gamma(x)$.

As we are using $\Gamma(a_p)$ as the approximation of dependencies for an observation, the soundness of the PI type system is similar to the PI security for dynamic policies, except that we take the equivalence relation as defined by $\Gamma(a_p)$ rather than the policy $D$.

Definition 15 (PI Type System Soundness). We say that the typing $\vdash \{c\} \Gamma$ is sound iff for all $\sigma, \rho$, if $(c, \sigma) \xrightarrow{\cdot} (c, \sigma') \xrightarrow{\langleage, \varphi \rangle} \sigma$ and $(c, \rho) \xrightarrow{\cdot} (c, \rho') \xrightarrow{\langleage, \varphi' \rangle} \rho$ and $|t| = |t'|$ then $\sigma = \Gamma(a_p) \Rightarrow \rho \Rightarrow \varphi = \varphi'$.
Theorem 2. All typings derived by the type system are sound.

The proof for Theorem 2 can be found in Appendix A.

To link the typing and the actual dynamic policy, we rely on an analysis that is able to approximate the dynamic policy per program point. A sound approximation should return a policy that is at least as restrictive as the actual policy for any observation on that program point.

Definition 16 (Dynamic Policy Approximation). A dynamic policy approximation $A : \text{CPoint} \to 2^{\Sigma \times \Sigma}$ is a mapping from channel and program point pairs to an equivalence relation on stores. The approximation $A$ on command $c$, written $c : A$, is sound for dynamic policy $D$ iff, for all $\sigma$ if $\langle c, \sigma \rangle \xrightarrow{\bullet}^{n} \langle c', \sigma' \rangle \xrightarrow{(a, v, p)}$ then $A(a_p)$ is coarser than $D(c, \sigma, n)$.

We now arrive at the main theorem in this section. Given a typing $\vdash \{c\} \Gamma$, we can now easily verify for command $c$ its compliance with any soundly approximated dynamic policy, by simply checking that the typing’s policy is at least as restrictive as the approximated dynamic policy for every program point.

Theorem 3 (PI Dynamic Policy Compliance). Let $c : A$ be a sound approximation of dynamic policy $D$. If $\vdash \{c\} \Gamma$ and $\Gamma(a_p)$ is coarser than $A(a_p)$ for all program points $a_p$, then $c$ is two-run PI secure for $D$ on all channels and for all initial stores.

Proof. Given a store $\sigma$ such that $\langle c, \sigma \rangle \xrightarrow{\bullet}^{n} \langle c', \sigma' \rangle \xrightarrow{(a, v, p)}$ and a store $\rho$ such that $\langle c, \rho \rangle \xrightarrow{\bullet}^{n} \langle c', \rho' \rangle \xrightarrow{\bullet}^{\gamma_a}$ and $|t| = |t'|$ and $\sigma D(c, \sigma, n) \rho$, we need to show that $v = v'$. Since $c : A$ is a sound approximation of $D$, we have that $\sigma A(a_p) \sigma$ and as $\Gamma(a_p)$ is coarser than $A(a_p)$ we also have $\sigma = \Gamma(a_p) \rho$. Which by Theorem 2 gives us that $v = v'$.

Corollary 1. If the conditions of Theorem 3 are met, then $c$ is PI secure for $D$ for all attackers. This is immediate by Theorem 1.

7 Related Work

In this section we consider the related work on security for dynamic policies and on generic enforcement mechanisms for information-flow control. We already discuss the knowledge-based definitions by Askarov and Chong \cite{AC12} in detail in Section 3.

The generality of expressing dynamic policies per execution point can be identified already in the early work by Goguen and Meseguer \cite{GM82}. They introduce the notion of conditional noninterference as a noninterference relation that should hold per step in the system, provided that some condition on the execution history holds. Conditional noninterference has been recently revisited by Zhang \cite{Zha12} who uses unwinding relations to present a collection of properties that can be verified by existing proof assistants.

Broberg and Sands \cite{BS09} developed another knowledge-based definition of security for dynamic policies which only dealt with the attacker with perfect recall. The approach was specialised to the specific dynamic policy mechanism Paralocks \cite{BS10} which uses part of the program state to vary the ordering between security labels.

Balliu et al. \cite{BDLG11} introduce a temporal epistemic log to express information flow policies. Like our dynamic policies, the epistemic formulas are to be satisfied per
execution point, suggesting that we are able express a similar set of dynamic policies. Dynamic policies can be individually checked by the ENCoVER tool [BDLG12].

The way in which we define dynamic policies matches exactly the set of synchronous dynamic policies: those policies that deterministically determine the active policy based on an execution point. Conversely, an asynchronously changing policy cannot be deterministically determined from an execution point, but is influenced by an environment external to the running program.

There is relatively little work on the enforcement of asynchronous dynamic policies. Swamy et al. [SHTZ06] present the language RX where policies are defined in a role-based fashion, where membership and delegation of roles can change dynamically.

Hicks et al. [HTHZ05] present an extension to the DLM model, allowing the acts-for hierarchy among principals to change while the program is running.

Both approaches however need a mechanism to synchronise the policy changes with the program in order to enforce information-flow properties. RX uses transactions which can rollback when a change in policy violates some of the flows in it, whereas the work by Hick et al. inserts automatically derived coercions that force run-time checks whenever the policy changes.

One of the beneficial characteristics of our enforcement approach is that commands need to be analysed only once to be verified against multiple information-flow policies. This generality can also be found in the work by Stefan et al. [SRMM11] presenting LIO, a Haskell library for information-flow enforcement which is also parametric in the security policy. The main differences between our approach and theirs is that LIO’s enforcement is dynamic rather than static, while the enforced policies are static rather than dynamic.

8 Conclusions

We extended the flow-sensitive type system from [HS06] to provide for each output channel individual dependency sets per point in the program and demonstrated that this is sufficient to support dynamic information flow policies. We proved the type system sound with respect to a straightforward two-run property which we showed sufficient to imply knowledge-based security conditions.

As our approach allows for the core of the analysis to be performed even before the policy is known, this enables us to reuse the results of the dependency analysis across the verification of multiple types of policies. An interesting direction for future research could be on the possibility to use the dependency analyses performed by advanced slicing tools such as JOANA [JOA] and Indus [Ind].

References

AB04. T. Amtoft and A. Banerjee. Information Flow Analysis in Logical Form. In 11th Static Analysis Symposium, volume 3148 of LNCS, pages 100–115. Springer, 2004.

AC12. A. Askarov and C. Chong. Learning is change in knowledge: Knowledge-based security for dynamic policies. In Computer Security Foundations Symposium, pages 308–322. IEEE, 2012.

AHSS08. A. Askarov, S. Hunt, A. Sabelfeld, and D. Sands. Termination-Insensitive Noninterference Leaks More Than Just a Bit. In The 13th European Symposium on Research in Computer Security, number 5283 in LNCS, pages 333–348. Springer, 2008.
AR80. G. R. Andrews and R. P. Reitman. An axiomatic approach to information flow in programs. *ACM TOPLAS*, 2(1):56–75, January 1980.

AS07. A. Askarov and A. Sabelfeld. Gradual release: Unifying declassification, encryption and key release policies. In *Proc. IEEE Symp. on Security and Privacy*, pages 207–221, May 2007.

BBL94. J.-P. Banâtre, C. Bryce, and D. Le Métyayer. Compile-time detection of information flow in sequential programs. In *Proc. European Symp. on Research in Computer Security*, volume 875 of *LNCS*, pages 55–73. Springer-Verlag, 1994.

BDLG11. M. Balliu, M. Dam, and G. Le Guernic. Epistemic temporal logic for information flow security. In *Programming Languages and Analysis for Security, PLAS ’11*, pages 6:1–6:12. ACM, 2011.

BDLG12. M. Balliu, M. Dam, and G. Le Guernic. Encover: Symbolic exploration for information flow security. In *Computer Security Foundations Symposium (CSF), 2012 IEEE 25th*, pages 30–44. IEEE, 2012.

BNR08. A. Banerjee, D. Naumann, and S. Rosenberg. Expressive declassification policies and modular static enforcement. In *Proc. IEEE Symp. on Security and Privacy*, pages 339–353. IEEE Computer Society, 2008.

BS09. N. Broberg and David S. Flow-Sensitive Semantics for Dynamic Information Flow Policies. In *Programming Languages and Analysis for Security*, 2009.

BS10. N. Broberg and D. Sands. Paralocks – role-based information flow control and beyond. In *Symposium on Principles of Programming Languages*. ACM, 2010.

BvDS13. N. Broberg, B. van Delft, and D. Sands. Paragon for Practical Programming with Information-Flow Control. In *Programming Languages and Systems*, volume 8301 of *LNCS*, pages 217–232. 2013.

CHH02. D. Clark, C. Hankin, and S. Hunt. Information flow for Algol-like languages. *Journal of Computer Languages*, 28(1):3–28, April 2002.

DD77. D. E. Denning and P. J. Denning. Certification of programs for secure information flow. *Comm. of the ACM*, 20(7):504–513, July 1977.

DHS15. B. van Delft, S. Hunt, and D. Sands. Very Static Enforcement of Dynamic Policies. In *Principles of Security and Trust*, LNCS. Springer, 2015. To appear.

GM82. J. A. Goguen and J. Meseguer. Security policies and security models. In *Proc. IEEE Symp. on Security and Privacy*, pages 11–20, April 1982.

GM84. J. A. Goguen and J. Meseguer. Unwinding and inference control. In *Proc. IEEE Symp. on Security and Privacy*, pages 75–86, April 1984.

HS06. S. Hunt and D. Sands. On Flow-sensitive Security Types. In *Symposium on Principles of Programming Languages*, pages 79–90. ACM, 2006.

HS11. S. Hunt and D. Sands. From Exponential to Polynomial-time Security Typing via Principal Types. In *Programming Languages and Systems. ESOP*, number 6602 in *LNCS*. Springer Verlag, 2011.

HTHZ05. M. Hicks, S. Tse, B. Hicks, and S. Zdancewic. Dynamic updating of information-flow policies. In *Foundations of Computer Security Workshop*, pages 7–18, 2005.

Ind. Indus homepage. [http://indus.projects.cis.ksu.edu/](http://indus.projects.cis.ksu.edu/) Accessed: 2015-01-09.

Joa. JOANA homepage. [http://pp.ipd.kit.edu/projects/joana/](http://pp.ipd.kit.edu/projects/joana/) Accessed: 2015-01-09.

SHTZ06. N. Swamy, M. Hicks, S. Tse, and S. Zdancewic. Managing Policy Updates in Security-Typed Languages. In *Proceedings of the 19th IEEE Workshop on Computer Security Foundations*, 2006.

SRM11. D. Stefan, A. Russo, J. C. Mitchell, and D. Mazières. Flexible dynamic information flow control in Haskell. In *Proceedings of the 4th ACM symposium on Haskell*, 2011.
A Type System Soundness

We need a collection of lemmas on the type system, which we prove correct in appendix B.

Lemma 5 (Preserving Dependencies). The dependencies of program points only increase with sequential composition. That is, if $\vdash \{e_1\} \Gamma_1$ and $\vdash \{e_1; e_2\} \Gamma_1$ then $\Gamma_1(\alpha_p) \subseteq \Gamma_1(\alpha_p)$ for all $\alpha_p$.

Lemma 6 (Non-Branching Reduction). For all configurations $c$ and stores $\sigma, \rho$ such that $\vdash \{c\} \Gamma$ with $\langle c, \sigma \rangle \xrightarrow{\alpha} \langle c', \sigma' \rangle$ and $\langle c, \rho \rangle \xrightarrow{\beta} \langle c', \rho' \rangle$, i.e. to the same command $c'$, where $\alpha, \beta$ is either an output or $c$. Then $\sigma = \Gamma_1(\alpha_p) \rho$ implies that $\vdash \{c'\} \Gamma'$ and $\sigma' = \Gamma_1(\alpha_p) \rho'$.

Lemma 7 (Branching Reduction). For all configurations $c$ and stores $\sigma, \rho$, such that $\vdash \{c\} \Gamma$ with $\langle c, \sigma \rangle \xrightarrow{\alpha} \langle c', \sigma' \rangle$ and $\langle c, \rho \rangle \xrightarrow{\beta} \langle c', \rho' \rangle$, where $c' \neq c'$. For all $\alpha_p$ such that $\langle c', \sigma' \rangle \xrightarrow{\frac{\alpha(v_p)\beta}{\alpha}} \langle c', \sigma' \rangle$, it holds that if $\sigma = \Gamma_1(\alpha_p) \rho$ then either:

- There exists a joining command $c_j$ with $t_1 \leq t$, such that $\langle c', \sigma' \rangle \xrightarrow{\frac{\alpha_1}{\alpha}} \langle c_j, \sigma_j \rangle$ and $\langle c', \rho' \rangle \xrightarrow{\frac{\alpha_2}{\alpha}} \langle c_j, \rho_j \rangle$ with $|t_1| = |t'|$ and $\vdash \{c_j\} \Gamma_j$ with $\sigma_j = \Gamma_1(\alpha_p) \rho_j$. I.e. both executions join again at equal command $c_j$ after an equal number of outputs with equivalent dependencies for $\alpha_p$, or

- For all $\tau'$ such that $\langle c, \rho \rangle \xrightarrow{\frac{\alpha_1}{\alpha}} \langle c, \sigma \rangle$, $|\tau'| \geq |t'|$.

We restate the semantic soundness property for the progress-insensitive type system:

\[
\begin{align*}
\text{For all } n \geq 0, \text{ for all commands } c \text{ with typing } \vdash \{c\} \Gamma, \text{ we have for all stores } \sigma, \rho, \text{ if } \langle c, \sigma \rangle \xrightarrow{\frac{\alpha_1}{\alpha}} \langle c, \sigma' \rangle \xrightarrow{\frac{\alpha_2}{\alpha}} \langle c, \rho \rangle \xrightarrow{\frac{\beta}{\beta}} \langle c, \rho' \rangle \xrightarrow{\gamma} \text{ and } |\tau| = |\tau'| \text{ then } \sigma = \Gamma_1(\alpha_p) \rho \Rightarrow v = v'.
\end{align*}
\]

We show this by complete (strong) induction on $n$.

\((n = 0)\) When $c$ produces an output in a single step for some store, it does so for any store and at the same program point (Fig. 4). We need to show

\[
\langle c, \sigma \rangle \xrightarrow{\frac{\alpha_1(v_p)}{\alpha}} \wedge \sigma = \Gamma_1(\alpha_p) \rho \wedge \langle c, \rho \rangle \xrightarrow{\frac{\alpha_2(v_p)}{\alpha}} \Rightarrow v = v'.
\]

By induction on the step taken from $\langle c, \sigma \rangle$. Due to the output, there are only two cases that do not lead to immediate contradiction:

- **out e on a @ p** — By TS-OUTPUT, $fv(e) \subseteq \Gamma(l, p)$. Therefore, $\sigma(e) = \rho(e)$ and thus $v = v'$.

- $c_1; c_2$ — By Lemma B $\Gamma_1(l, p) \subseteq \Gamma(l, p)$ for $\vdash \{e_1\} \Gamma_1$. Therefore $\sigma = \Gamma_1(\alpha_p) \rho$ and the property follows by induction on $\langle c_1, \sigma \rangle$.
We have \( \langle c, \sigma \rangle \xrightarrow{\alpha_1} \langle c', \sigma' \rangle \xrightarrow{\alpha_2} \langle c'', \sigma'' \rangle \xrightarrow{(a,v,p)} \) and \( \langle c, \rho \rangle \xrightarrow{\alpha_1} \langle c', \rho' \rangle \xrightarrow{\alpha_2} \langle c'', \rho'' \rangle \xrightarrow{(a,v,p)} \) with the induction hypothesis \( \text{Lemma 4} \) for evaluations of length \( \leq n \). Here \( \alpha \) could be either silent or an output. We only consider the case where \( \alpha \) is silent; for the case where \( \alpha \) is an output the proof is similar except in the induction step we have that traces produced by \( \sigma' \) and \( \rho' \) are both 1 shorter in length.

When \( c \) produces no output in a single step for some store, it does so for any store (Fig. 4). We case split on \( c' = c'_{\rho} \);
\[ c' = c'_{\rho} \]
By Lemma \( \text{Lemma 6} \) \( \vdash \{c'_{\rho}\} \Gamma' \) and \( \sigma' = \Gamma'(a_p) \rho' \). The case follows by induction on \( n \).

\( c' \neq c'_{\rho} \) Since \( \langle c, \rho \rangle \) produces a trace longer than \( |t| \), by Lemma \( \text{Lemma 7} \) there exists a \( t_1 \leq t \) such that \( \langle c', \sigma \rangle \xrightarrow{\delta_1} (c_j, \sigma_j) \xrightarrow{\delta_2} (c''_j, \sigma''_j) \xrightarrow{(a,v,p)} \) and there is a \( t'_1 \leq t' \) such that \( \langle c', \rho' \rangle \xrightarrow{\delta_1} (c_j, \rho_j) \xrightarrow{\delta_2} (c''_j, \rho''_j) \xrightarrow{(a,v,p)} \) with \( |t_1| = |t_1| \) and \( \vdash \{c_j\} \Gamma_j \) with \( |\rho_j|' = |\rho_j| \rho_j' \).
Since \( |t| = |t'| \) and \( |t_1| = |t_1| \) we have \( |t_2| = |t_2| \). Then the case follows by induction on some \( n_2 \leq n \).

\section{Proofs of auxiliary lemmas}

We prove our auxiliary lemmas as part of a series of necessary properties of the type system. In this section, the Lemmas \( \text{Lemma 5,6} \) and \( \text{Lemma 7} \) can be found with proof as Lemmas \( \text{Lemma 15,16} \) and \( \text{Lemma 17} \) respectively.

\begin{lemma}
For all \( \vdash \{c\} \Gamma \), we have \( \Gamma (pc) = \{pe\} \).
\end{lemma}

\begin{proof}
By induction on the typing derivation. This property holds clearly for TS-Skip, TS-Assign and TS-Output as \( \Gamma (pc) = \Gamma \_id (pc) = \{pc\} \). It is also clear for TS-If-Else and TS-While as they explicitly reset the dependencies of \( pc \) to \( \{pc\} \). For TS-Seq, by induction we have \( \Gamma_2 (pc) = \{pc\} \) thus \( \Gamma_2 \_id (pc) = \Gamma_1 (pc) \), which by induction is \( \{pc\} \).
\end{proof}

\begin{lemma}
For all \( \vdash \{c\} \Gamma \), if \( pc \notin \Gamma (x) \) then \( \Gamma (x) = \Gamma \_id (x) = \{x\} \), for all variables, channels and program points \( x \).
\end{lemma}

\begin{proof}
By induction on the derivation of \( \vdash \{c\} \Gamma \):
\begin{itemize}
\item Case: TS-Assign
\( \Gamma \) is different from \( \Gamma \_id \) only for variable \( x \). For \( x, pc \in \Gamma (x) \) so the property holds trivially.
\item Case: TS-Output
\( \Gamma \) is different from \( \Gamma \_id \) only for channel \( a \) and program point \( a_p \). For them, \( pc \in \Gamma (a) \) and \( pc \in \Gamma (a_p) \) so the property holds trivially.
\item Case: TS-Seq
By Lemma \( \text{Lemma 8} \) \( \Gamma_1 (pc) = \Gamma_2 (pc) = \{pc\} \). Thus \( pc \in \Gamma_2 (x) \) implies \( pc \in \Gamma_2 ; \Gamma_1 (x) \). Reversely, \( pc \notin \Gamma_2 \); \( \Gamma_1 (x) \) implies \( pc \notin \Gamma_2 (x) \). By induction we then have \( \Gamma_2 (x) = \{x\} \). Thus \( \Gamma_2 \_id (x) = \{x\} \) and we have \( pc \notin \Gamma_1 (x) \). Therefore by induction \( \Gamma_1 (x) = \Gamma \_id (x) \) which gives \( \Gamma_2 \_id (x) = \{x\} \).
\end{itemize}
\end{proof}
Lemma 11.

The composition with $\Gamma_{id}[pc\mapsto\{pc\} \cup fv(e)]$ only adds dependencies and does not remove the dependency on pc. So $pc \in \Gamma_1(x)$ implies $pc \in \Gamma'_1(x)$. $\Gamma$ is the union of both typing, so $pc \in \Gamma'_1(x)$ implies $pc \in \Gamma(x)$. Reversely, $pc \notin \Gamma(x)$ implies $pc \notin \Gamma'_1(x)$. By induction, $\Gamma_1(x) = \{x\}$, thus $\Gamma'_1(x) = \{x\}$, thus $\Gamma(x) = \{x\}$.

- Case: TS-IFELSE

The composition with $\Gamma_{id}[pc\mapsto\{pc\} \cup fv(e)]$ only adds dependencies and does not remove the dependency on pc. So $pc \in \Gamma_c(x)$ implies $pc \in \Gamma'_c; \Gamma_{id}[pc\mapsto\{pc\} \cup fv(e)](x)$. The reflexive transitive closure operation takes the union over all $\Gamma^n$, so $pc \in \Gamma_c(x)$ implies $pc \in \Gamma_f(x)$. Reversely, $pc \notin \Gamma_f(x)$ implies $pc \notin \Gamma_c(x)$. By induction, $\Gamma_c(x) = \{x\}$, hence neither the composition with $\Gamma_{id}[pc\mapsto\{pc\} \cup fv(e)]$ nor the closure operation modifies the dependencies of $x$, and $\Gamma_f(x) = \{x\}$.

Lemma 10. If $pc \notin \Gamma(x)$ with $\vdash\{e\} \Gamma$ and $\langle c, \sigma \rangle \Rightarrow \langle c', \sigma' \rangle$, then $pc \notin \Gamma'(x)$ with $\vdash\{c'\} \Gamma'$, for all $x$ in variables, channels and program points.

Proof. By induction on the structure of $e$:

- Case: skip
  Holds trivially as there is no progress.
- Case: $c_1; c_2$
  $\Gamma = \Gamma_2; \Gamma_1$ with $\vdash\{c_1\} \Gamma_1$ and $\vdash\{c_2\} \Gamma_2$. If $pc \notin \Gamma(x)$ then by Lemma 8 we have $pc \notin \Gamma_1(x)$ and $pc \notin \Gamma_2(x)$. By Lemma 5 we have $\Gamma_2; \Gamma_1(x) = \{x\}$; $\Gamma_2(x) = \{x\}$ and thus $pc \notin \Gamma_1(x)$. The case follows by induction on $c_1$.
- Case: $x := e$
  Becomes $\text{skip}$ after evaluation. For all variables $y$ other than $x$, $pc \notin \Gamma(y)$ and also $pc \notin \Gamma'(y)$ as $\Gamma'(y) = \Gamma_{id}(y)$. Similar for program points and channels
- Case: $\text{out } e$ on $a \otimes p$
  $\Gamma(x) = \Gamma_{id}(x)$ for all variables, channels and program points other than $a$ and $a_p$ so the property holds. For $a$ and $a_p$, we have $pc \in \Gamma(a)$ and $pc \in \Gamma(a_p)$ so holds trivially.
- Case: $\text{if } e \ c_1 \ c_2$
  If $pc \notin \Gamma(x)$ then $pc \notin \Gamma_1(x)$ and $pc \notin \Gamma_2(x)$; see proof of Lemma 9 Thus property holds for both $\sigma(e) = 0$ ($\Gamma' = \Gamma_1$) and $\sigma(e) = 0$ ($\Gamma' = \Gamma_2$).
- Case: while $e \ c_1$
  If $pc \notin \Gamma(x)$ then $pc \notin \Gamma_1(x)$ with $\vdash\{c_1\} \Gamma_1$; see proof of Lemma 9 Thus $\Gamma_1(x) = \Gamma(x) = \{x\}$ by Lemma 9 and $\Gamma; \Gamma_1(x) = \{x\}$. By inspecting the typing of $c'$ it follows that $\Gamma'(x) = (\Gamma; \Gamma_1; \Gamma_{id}[pc \mapsto \{pc\} \cup fv(e)] \cup \Gamma_{id})(x) = \{x\}$, thus $pc \notin \Gamma'(x)$.

Lemma 11. Let $\vdash\{\text{while } e \ c\} \Gamma$ and $\vdash\{\text{if } e \ (\text{while } e \ c) \ (\text{skip})\} \Gamma'$. Then $\Gamma'(x) \subseteq \Gamma(x)$ for all $x$ in variables, channels and program points.

Proof. Here, $\Gamma = \Gamma_f[pc \mapsto \{pc\}]$, with $\Gamma_f = (\Gamma_c; \Gamma_{id}[pc \mapsto \{pc\} \cup fv(e)])^*$. We refer to the individual typings collected by the union in $\Gamma_f$ with $\Gamma^n$.

Let $\vdash\{c\} \Gamma_c$. We need to show that $\Gamma'(x) \subseteq \Gamma(x)$ which by substituting $\Gamma'$ is:

$$
((\Gamma; \Gamma_c); \Gamma_{id}[pc \mapsto \{pc\} \cup fv(e)])
\cup \Gamma_{id}[pc \mapsto \{pc\} \cup fv(e)](x) \subseteq \Gamma(x)
$$
Observing that $\Gamma_{id}; \Gamma = \Gamma$, $x \neq pc$ and that $\Gamma_{id}(x) \subseteq \Gamma(x)$ since $\Gamma = (\Gamma; \Gamma_{id}[pc \mapsto \{pc\} \cup fv(e)])^*$, this becomes:

$$(\Gamma; \Gamma_{id}[pc \mapsto \{pc\} \cup fv(e)])(x) \subseteq \Gamma(x)$$

If $y \in \Gamma(x)$, this means there exists a $\Gamma^n$ such that $y \in \Gamma^n(x)$. Thus, $(\Gamma; \Gamma_{id}[pc \mapsto \{pc\} \cup fv(e)])(y) \subseteq \Gamma^{n+1}(x) \subseteq \Gamma(x)$. As $\Gamma_{id}(y) \subseteq (\Gamma; \Gamma_{id}[pc \mapsto \{pc\} \cup fv(e)])(y)$ we have that for all $y \in \Gamma(x)$, $\Gamma_{id}(y) \subseteq \Gamma(x)$. Thus $\Gamma; \Gamma_{id}(x) \subseteq \Gamma(x)$, so the relation we need to show is implied by

$$(\Gamma; \Gamma_{id}[pc \mapsto \{pc\} \cup fv(e)])(x) \subseteq \Gamma(x)$$

(if we want to be more precise, it holds that $(\Gamma; \Gamma_{id}[\Gamma]])(x) = \Gamma(x)$, so this relation is in fact equivalent to the one we started out with.) If $pc \in \Gamma(x)$, then there exists a $\Gamma^n$ such that $pc \in \Gamma^n(x)$. As by Lemma 8, $\Gamma_{id}(pc) = \{pc\}$, it follows that $(\Gamma; \Gamma_{id}[pc \mapsto \{pc\} \cup fv(e)])(pc) = \{pc\} \cup \Gamma$. Thus since $\Gamma^{n+1} = \Gamma^n; (\Gamma; \Gamma_{id}[pc \mapsto \{pc\} \cup fv(e)])(pc)$, it follows that $(pc) \cup fv(e) \subseteq \Gamma^{n+1}(x)$ and therefore $(pc) \cup fv(e) \subseteq \Gamma(x)$. So the relation simplifies to $\Gamma(x) \subseteq \Gamma(x)$ which holds trivially.

**Lemma 12 (Program Counter).** The typing $\vdash \{c\} \Gamma$ guarantees that:

1. If $pc \notin \Gamma(x)$ and $\langle c, \sigma \rangle \xrightarrow{\star} \langle \text{skip}, \sigma' \rangle$ then $\sigma(x) = \sigma'(x)$.
2. If $pc \notin \Gamma(x)$ and $\langle c, \sigma \rangle \xrightarrow{\star} \langle \text{skip}, \rho' \rangle$ then $\sigma(x) = \rho'(x)$.
3. If $pc \notin \Gamma(x)$ and $\langle c, \sigma \rangle \xrightarrow{\star} \langle \text{skip}, \rho' \rangle$ then $a_p \notin \Gamma(x)$.

**Proof.** Property 1 by induction on $n$ in $\langle c, \sigma \rangle \xrightarrow{\star} \langle \text{skip}, \sigma' \rangle$. If $n = 0$ the property holds trivially as $\sigma = \sigma'$. We thus have: $\langle c, \sigma \rangle \xrightarrow{\alpha} \langle c', \sigma' \rangle \xrightarrow{\star} \langle \text{skip}, \sigma'' \rangle$ and by induction if $pc \notin \Gamma'(x)$ with $\langle c' \rangle \Gamma'$, then $\sigma'(x) = \sigma''(x)$. By Lemma 10 $pc \notin \Gamma(x)$ then $pc \notin \Gamma'(x)$ so we can use the induction hypothesis.

Similar for properties 2 and 3.

**Lemma 13 (Variables).** For all $c, \sigma, \rho$ such that $\vdash \{c\} \Gamma$ with $\langle c, \sigma \rangle \xrightarrow{\star} \langle \text{skip}, \sigma' \rangle$ and $\langle c, \rho \rangle \xrightarrow{\star} \langle \text{skip}, \rho' \rangle$, then $\sigma = \Gamma(x) \rho$ implies $\sigma'(x) = \rho'(x)$.

**Proof.** By complete induction on $n$ in $\langle c, \sigma \rangle \xrightarrow{\star} \langle \text{skip}, \sigma' \rangle$. If $n = 0$ the property holds trivially as $\sigma = \sigma'; \rho = \rho'; \Gamma = \Gamma_{id}$ and thus $\rho(x) = \sigma(x)$. We then have $\langle c, \sigma \rangle \xrightarrow{\alpha} \langle c', \sigma' \rangle \xrightarrow{\star} \langle \text{skip}, \sigma'' \rangle$ and $\langle c, \rho \rangle \xrightarrow{\alpha} \langle c', \rho' \rangle \xrightarrow{\star} \langle \text{skip}, \rho'' \rangle$. By induction if $\sigma' = \Gamma(x) \rho'$ with $\vdash \{c'\} \Gamma'$ then $\sigma'(x) = \rho''(x)$.

By cases on the first evaluation step:

- $\langle \text{skip}; c, \sigma \rangle \xrightarrow{\star} \langle c, \sigma \rangle$.
  $\Gamma = \Gamma'; \Gamma_{id}$ so $\sigma = \Gamma(x) \rho$ implies $\sigma = \Gamma'(x) \rho$ and as $\sigma = \sigma'$ and $\rho = \rho'$ the case follows by induction.

- $\langle c_1; c_2, \sigma \rangle \xrightarrow{\star} \langle c_1', c_2, \sigma' \rangle$ with $\langle c_1, \sigma \rangle \xrightarrow{\alpha} \langle c_1', \sigma' \rangle$.
  $\Gamma = \Gamma_2; \Gamma_1$. By definition of $\alpha$ this means that $\sigma = \Gamma(x) \rho$ implies that $\sigma = \Gamma(y) \rho$ for all $y \in \Gamma_2(x)$. Thus in the computations $\langle c_1; c_2, \sigma \rangle \xrightarrow{\star} \langle \text{skip}; c_2, \sigma' \rangle \xrightarrow{\star} \langle \text{skip}, \sigma'' \rangle$ and $\langle c_1; c_2, \rho \rangle \xrightarrow{\star} \langle \text{skip}; c_2, \rho' \rangle \xrightarrow{\star} \langle \text{skip}, \rho'' \rangle$ we have by induction on $c_1$ that $\sigma'(y) = \rho'(y)$. Therefore $\sigma' = \Gamma_2(x) \rho'$ and the case follows on the shorter execution trace produced by $c_2$. 


Lemma 14 (Channel Context Bounds). The typing $\vdash \{c\} \Gamma$ guarantees that:

1. If $(c, \sigma) \xrightarrow{\alpha} (c', \sigma')$ and $\sigma = \Gamma(a) \rho$ then $(c, \rho) \xrightarrow{c'} (c', \rho')$ has $|t| \geq |t'|$, and if also $(c, \rho) \xrightarrow{c'} (c', \rho')$ then $|t| = |t'|$.
2. If $\alpha \notin \Gamma(a_p)$ and $(c, \sigma) \xrightarrow{\alpha} (c', \sigma')$ then $a_p \notin \sigma$.

Proof. For property 1 the reasoning is analogous to that for property 3 in Lemma 12.

The rest of the proof is for property 11.

By complete induction on $n$ in $(c, \sigma) \xrightarrow{\alpha} (c', \sigma')$. If $n = 0$ the property holds trivially. We then have $(c, \sigma) \xrightarrow{\alpha} (c', \sigma')$ and $(c, \rho) \xrightarrow{\alpha} (c', \rho')$.

By cases on the first reduction step:

- $(\text{skip}; c, \sigma) \xrightarrow{\alpha} (c, \sigma)$.
  - $\Gamma = \Gamma'$; $\Gamma$; $\alpha$ so $\sigma = \Gamma(a) \rho$ implies $\sigma = \Gamma'(a) \rho$ and as $\sigma = \sigma'$, $\rho = \rho'$ and $c = c'$, the case follows by induction.
- $(c_1; c_2, \sigma) \xrightarrow{\alpha} (c_1', c_2', \sigma')$ with $(c_1, \sigma) \xrightarrow{\alpha} (c_1', \sigma')$.
  - Let $\vdash \{c_1\} \Gamma_1$. By property 3 of Lemma 15 $\Gamma_1(a) \subseteq \Gamma(a)$, thus $\sigma = \Gamma(a) \rho$ implies $\sigma = \Gamma'(a) \rho$. As the command fully evaluates, so does $c_1$. $(c_1, \sigma) \xrightarrow{c_1} (c_1', \sigma')$ and therefore also $(c_1; c_2, \sigma) \xrightarrow{c_1} (c_2', \sigma')$. As $n_1 \leq n$, by induction we have that $(c_1, \rho) \xrightarrow{c_1} (c_1', \rho')$ with $|t'_1| \leq |t_1|$ and if $(c_1, \rho) \xrightarrow{c_1} (c_1', \rho')$ then $|t'_1| = |t_1|$.
  - As $\Gamma = \Gamma_2; \Gamma_1$, for all $x \in \Gamma_2(a)$ we have $\sigma = \Gamma'(x) \rho$, and by Lemma 13 $\sigma''(x) = \rho''(x)$ which means that $\sigma'' = \Gamma_2(a) \rho''$ and the property follows by induction on the execution of length $n - n_1$.
- $(x := e, \sigma) \xrightarrow{\alpha} (\text{skip}, \sigma[x \mapsto v])$ with $\sigma(e) = v$.
  - As this configuration never produces any output, this property holds trivially.
- $(\text{out } e \text{ on } a @ p, \sigma) \xrightarrow{\alpha} (\text{skip}, \sigma)$ with $\sigma(e) = v$.
  - As this configuration produces only one output, always on the same channel, this property holds trivially.
Lemma 15 (Preserving Dependencies). The dependencies of program points and context bounds only increase with sequential composition:

1. If \( \vdash \{ c \} \Gamma \) and \( \vdash \{ c_1; c_2 \} \Gamma \) then \( \Gamma_1(a_p) \subseteq \Gamma(a_p) \) for all \( a_p \).
2. If \( \vdash \{ c \} \Gamma \) and \( \vdash \{ c_1; c_2 \} \Gamma \) then \( \Gamma_1(a) \subseteq \Gamma(a) \) for all \( a \).

Proof. For property 1, First, we establish that for all typings \( \vdash \{ c \} \Gamma \), \( a_p \in \Gamma(a_p) \) by induction on the typing. This property clearly holds for TS-Skip, TS-Assign and TS-Output. For TS-Seq, by induction \( a_p \in \Gamma_2(a_p) \). Thus \( \Gamma_1(a_p) \subseteq \Gamma(a_p) \). By induction we also have \( a_p \in \Gamma_1(a_p) \) and thus \( a_p \in \Gamma(a_p) \). For TS-If-Else, we have \( \Gamma_1(a_p) \subseteq \Gamma(a_p) \) and it follows by induction. For TS-While, we have \( \Gamma'_c(a_p) \subseteq \Gamma(a_p) \) since \( \Gamma'_c(a_p) \cup \{ pc \mapsto \{ \{ pc \} \cup \{ e \} \} \} \) is in the union that defines \( \Gamma' \) and this case follows by induction as well.

So if \( \vdash \{ c \} \Gamma_1 \) and \( \vdash \{ c_1; c_2 \} \Gamma_2 \) this means \( \vdash \{ c_1; c_2 \} \Gamma_2; \Gamma_1 \). As \( a_p \in \Gamma_2(a_p) \), we have \( \Gamma_1(a_p) \subseteq \Gamma(a_p) \) which is what we had to show.

Similar for property 2.

Lemma 16 (Non-Branching Reduction). For all configurations \( c \) and stores \( \sigma, \rho \) such that \( \vdash \{ c \} \Gamma \) with \( \langle c, \sigma \rangle \xrightarrow{\alpha} \langle c', \sigma' \rangle \) and \( \langle c, \rho \rangle \xrightarrow{\beta} \langle c', \rho' \rangle \), i.e. to the same command \( c' \), where \( \alpha, \beta \) is either an output or \( c \). Then \( \sigma = \Gamma(a_p) \rho \) implies that \( \vdash \{ c' \} \Gamma' \) and \( \sigma' = \Gamma'(a_p) \rho' \).

Proof. By induction on the small step evaluation.

- \( \langle \text{skip}; c, \sigma \rangle \xrightarrow{\Delta} \langle c, \sigma \rangle \) and \( \langle \text{skip}; c, \rho \rangle \xrightarrow{\Delta} \langle c, \rho \rangle \)
  Typing gives us \( \Gamma = \Gamma_2; \Gamma_\text{id} \) and \( \Gamma' = \Gamma_2 \). As \( \Gamma_2; \Gamma_\text{id} = \Gamma_2 \) we have that \( \Gamma = \Gamma' \), \( \sigma = \sigma' \) and \( \rho = \rho' \); and \( \sigma' = \Gamma'(a_p) \rho' \) holds trivially.
- \( \langle c_1; c_2, \sigma \rangle \xrightarrow{\alpha} \langle c_1', c_2, \sigma' \rangle \) with \( \langle c_1, \sigma \rangle \xrightarrow{\alpha} \langle c_1', \sigma' \rangle \) and \( \langle c_1; c_2, \rho \rangle \xrightarrow{\alpha} \langle c_1', c_2', \rho' \rangle \) with \( \langle c_1, \rho \rangle \xrightarrow{\alpha} \langle c_1', \rho' \rangle \)
  By induction there exists a \( \Gamma'_1 \) such that \( \vdash \{ c_1' \} \Gamma'_1 \) and \( \sigma' = \Gamma'(a_p) \rho' \). \( \Gamma = \Gamma_2; \Gamma_1 \) and \( \Gamma' = \Gamma_2; \Gamma'_1 \). When \( \sigma = \Gamma_2; \Gamma_1(\rho) \) then by definition of \( \vdash \cdot \) \( \gamma \in \Gamma_2(a_p), \sigma = \Gamma_1(\gamma) \). By induction we also have that \( \forall \gamma \in \Gamma_2(a_p), \sigma' = \Gamma_1(\gamma) \rho' \). Thus \( \sigma' = \Gamma_2; \Gamma_1(\rho) \rho' \).
- \( \langle x := e, \sigma \rangle \xrightarrow{\Delta} \langle \text{skip}, \sigma | x \mapsto v \rangle \) with \( \sigma(e) = v \) and \( \langle x := e, \rho \rangle \xrightarrow{\Delta} \langle \text{skip}, \rho | x \mapsto w \rangle \) with \( \rho(e) = w \)
  As \( \Gamma' = \Gamma_\text{id} \), \( \sigma' = \Gamma_\text{id}(a_p) \rho' \) holds for any \( \sigma', \rho' \).
– \{\text{out on } a @ p, \sigma \} \longrightarrow_{(a,v,p)} \{\text{skip, } \sigma\} \text{ with } \sigma(e) = v \text{ and }
\{\text{out on } a @ p, \rho \} \longrightarrow_{(a,v,p)} \{\text{skip, } \rho\} \text{ with } \rho(e) = w

Similar as for assignment.

– \{\text{if } e c_1 c_2, \sigma \} \longrightarrow \{c_1, \sigma\} \text{ with } \sigma(e) \neq 0 \text{ and }
\{\text{if } e c_1 c_2, \rho \} \longrightarrow \{c_2, \rho\}

As \sigma' = \sigma \text{ and } \rho' = \rho \text{ we need to show that } \Gamma_1(a_p) \subseteq \Gamma(a_p) \text{ which is clear from }
the typing rule.

– \{\text{if } e c_1 c_2, \sigma \} \longrightarrow \{c_2, \sigma\} \text{ with } \sigma(e) = 0 \text{ and }
\{\text{if } e c_1 c_2, \rho \} \longrightarrow \{c_2, \rho\}

As \sigma' = \sigma \text{ and } \rho' = \rho \text{ we need to show that } \Gamma_2(a_p) \subseteq \Gamma(a_p) \text{ which is clear from }
the typing rule.

– \{\text{while } e c, \sigma \} \longrightarrow \{\text{if } (\text{while } e c) \text{ skip, } \sigma\} \text{ and }
\{\text{while } e c, \rho \} \longrightarrow \{\text{if } (\text{while } e c) \text{ skip, } \rho\}

As \sigma' = \sigma \text{ and } \rho' = \rho \text{ it follows from } \Gamma'(a_p) \subseteq \Gamma(a_p) \text{ by Lemma 11}.

**Lemma 17 (Branching Reduction).** For all configurations \(c\) and stores \(\sigma, \rho\) such that
\(\vdash \{c\} \Gamma\) with \(\langle c, \sigma \rangle \longrightarrow_{(c', \sigma')} \langle c', \sigma'\rangle\text{ and }\langle c, \rho \rangle \longrightarrow_{(c', \rho')} \langle c', \rho'\rangle\text{, where } c' \neq c\text{. For all } a_p\text{ such that }
\langle c', \sigma' \rangle \longrightarrow_{(a,v,p)} \langle c', \sigma'\rangle\text{ it holds that if } \sigma = \Gamma(a_p) \rho \text{ then either:}

– There exists a joining command \(c_j\) with \(t_1 \leq t\), such that \(\langle c', \sigma' \rangle \longrightarrow_{a} \langle c_j, \sigma_j \rangle\text{ and }
\langle c', \rho' \rangle \longrightarrow_{a} \langle c_j, \rho_j \rangle\text{ with } |t_1| = |t'_1|\text{ and }\vdash \{c_j\} \Gamma_j\text{ with } \sigma_j = \Gamma_j(a_p) \rho_j\text{. I.e. both }
exections join again at equal command \(c_j\) after an equal number of outputs with equivalent dependencies for \(a_p\), or

– For all \(t'\) such that \(\langle c, \rho \rangle \longrightarrow_{a} |t'|\text{, }|t| \geq |t'|\).

**Proof.** By induction on the small step evaluation \(\langle c, \sigma \rangle \longrightarrow \langle c', \sigma'\rangle\). There are only three evaluations
that may result in a different command for different stores, the others hold trivially.

– \{\text{if } e c_1 c_2, \sigma \} \longrightarrow \{c_1, \sigma\} \text{ with } \sigma(e) \neq 0

\begin{itemize}
  \item If \(pc \in \Gamma(a_p)\) then the typing rules give that \(\sigma(e) = \rho(e)\) and both executions take
the same branch; hence \(c'_\sigma = c'_\rho\) so we contradict our assumption.
  \item If \(pc \notin \Gamma(a_p)\) then by Lemma 12 \(\langle c, \sigma \rangle\) should not produce an output on \(a_p\),
which contradicts our assumption.
\end{itemize}

– \{\text{if } e c_1 c_2, \sigma \} \longrightarrow \{c_2, \sigma\} \text{ with } \sigma(e) = 0

Similar as for \(\sigma(e) \neq 0\).

– \{c_1; c_2, \sigma \} \longrightarrow \{c_1; c_2, \sigma'\} \text{ with } \langle c_1, \sigma \rangle \longrightarrow \langle c_1', \sigma'\rangle

\begin{itemize}
  \item \(c_1\) produced \(a,v,p\), that is \(\langle c_1, \sigma \rangle \longrightarrow_{(a,v,p)} \langle \text{skip, } \sigma_j \rangle\text{, By property 1 of Lemma 15 }
\Gamma_1(a_p) \subseteq \Gamma(a_p)\), therefore \(\sigma = \Gamma(a_p) \rho\) implies \(\sigma = \Gamma_1(a_p) \rho\) and the case
follows by induction.
  \item \(c_2\) produces \(a,v,p\). That is, \(c_1\) evaluates completely \(\langle c_1, \sigma \rangle \longrightarrow_{a} \langle \text{skip, } \sigma_j \rangle\text{ and thus }
\langle c_1; c_2, \sigma \rangle \longrightarrow \langle c_2, \sigma' \rangle\text{, }\Gamma_2(a_p) \subseteq \Gamma(a_p)\text{ by property 2 of Lemma 14.}\)
By \(\langle c_1; c_2, \sigma \rangle \longrightarrow \langle c_1; c_2, \sigma'\rangle\) as \(c_2\) produces an output on \(a_p\), it must be that \(a \in \Gamma_2(a_p)\text{. As } \Gamma = \Gamma_2; \Gamma_1\text{ with }
\vdash \{c_1\} \Gamma_1, \vdash \{c_2\} \Gamma_2\text{ and } \sigma = \Gamma(a_p) \rho\text{ this means that }\sigma = \Gamma_1(a_p) \rho\text{. By Lemma 14}
property 1 we get that for all \(t_1\) such that \(\langle c_1, \rho \rangle \longrightarrow_{a} \langle \text{skip, } \rho_j \rangle\text{ then } |t_1| \geq |t'_1|\text{ and }
if \(\langle c_1, \rho \rangle \longrightarrow_{a} \langle \text{skip, } \rho_j \rangle\text{ then } |t_1| = |t'_1|\text{. If } c_1\text{ diverges this means that the}
second case of Lemma 17 holds and we are done. Otherwise we also have that 
\( \langle c_1, \rho \rangle \xrightarrow{\Sigma} \langle \text{skip}, \rho_3 \rangle \) and thus \( \langle c_1; c_2, \rho \rangle \xrightarrow{\Sigma} \langle c_2, \rho_3 \rangle \). Again by \( \Gamma = \Gamma_2; \Gamma_1 \) and \( \sigma = \Gamma(a_p) \rho \) we have that \( \forall x \in \Gamma_2(l, p), \sigma = \Gamma_1(x) \rho \). By Lemma 13 it follows that \( \sigma_j = \Gamma_2(a_p) \rho_j \), which is what we had the show (with \( c_j = c_2 \)).