Novel Thermal Management Strategy for Improved Inverter Reliability in Electric Vehicles

Elena Trancho 1,*, Edorta Ibarra 2, Pablo Prieto 1, Antoni Arias 3, and Adrian Lis 4 and Ajay Poonjal Pai 4

1 Industry and Transport Unit, Tecnalia Research and Innovation, 48160 Derio, Spain; pablo.prieto@tecnalia.com
2 Department of Electronic Technology, University of the Basque Country (UPV/EHU), 48013 Bilbao, Spain; edorta.ibarra@ehu.eus
3 Departament d’Enginyeria Electrònica, Institut d’Organització i Control, Universitat Politècnica de Catalunya (UPC), 08028 Barcelona, Spain; antoni.arias@upc.edu
4 Infineon Technologies AG, Am Campeon 1-15, 85579 Neubiberg, Germany; Adrian.Lis@infineon.com (A.L.); AjayPoonjal.Pai@infineon.com (A.P.P.)
* Correspondence: elena.trancho@tecnalia.com

Featured Application: Battery powered electric vehicles.

Abstract: Requirements for electric vehicle (EV) propulsion systems—i.e., power density, switching frequency and cost—are becoming more stringent, while high reliability also needs to be ensured to maximize a vehicle’s life-cycle. Thus, the incorporation of a thermal management strategy is convenient, as most power inverter failure mechanisms are related to excessive semiconductor junction temperatures. This paper proposes a novel thermal management strategy which smartly varies the switching frequency to keep the semiconductors’ junction temperatures low enough and consequently extend the EV life-cycle. Thanks to the proposal, the drivetrain can operate safely at maximum attainable performance limits. The proposal is validated through simulation in an advanced digital platform, considering a 75-kW in-wheel Interior Permanent Magnet Synchronous Machine (IPMSM) drive fed by an automotive Silicon Carbide (SiC) power converter.

Keywords: electric vehicle; thermal management; reliability; SiC

1. Introduction

Transport is one of the main sources of greenhouse gas emissions of anthropogenic origin. For example, road transport accounts for 27% of total emissions in Europe, being the greatest contributor among all sectors [1]. There is almost a consensus on the idea that a shift to electro-mobility will be mandatory to meet the Paris Agreement goals, as is confirmed by the meta-analysis conducted in [2]. In this context, a comprehensive evaluation of CO₂ emissions generated by battery powered electric vehicles (EV) vs. internal combustion engine (gasoline and diesel) vehicles has been conducted in [3]. A life-cycle assessment (LCA) was carried out considering not only emissions during vehicle operation, but also the ones generated from fuel extraction, refining, power generation, raw material extraction, vehicle manufacturing, regional particularities (electrical mix, driving distances, etc.), maintenance and end-of-life phase. The authors in [3] conclude that, with some particular exception, the longer the EV’s driven distance, the more the CO₂ emissions would be reduced (note that battery manufacturing significantly penalizes EVs in terms of emissions), performing research focused on improving reliability and maximizing the life-cycle of EVs extremely important. In particular, EVs in
Europe would outperform gasoline and diesel vehicles regarding emissions from a range of 76,545 km and 109,415 km, respectively.

However, for the general adoption of battery powered EVs by end users, features need to be comparable to the ones of internal combustion engine based vehicles. Thus, ambitious technological targets have been set for both electric machines (power density of 5.7 kW/L, overall efficiency of 93% and cost of 4.7 $/kW) and power electronics (power density of 13.4 kW/L, overall efficiency of 97% and cost of 3.3 $/kW) [4]. With regard to high power density and efficiency requirements, surface mounted or interior permanent magnet synchronous machines (PMSM) are commonly adopted [5–8]. Current trends on PMSM technology focus on multiphase configurations [9,10], high speed electric machines (HSEM) [11–13], and on in-wheel direct drive solutions [14,15]. In this context, wide band-gap semiconductor technologies are being investigated for their introduction in PMSM drive power inverters [16,17] due to the following reasons:

(i) Both HSEM and in-wheel multi-pole direct drive solutions lead to high electrical stator frequencies. These trends exhibit regulation issues to be addressed [13,18,19], which can be overcome by introducing power electronics technologies that can operate at high switching frequencies with reasonable power losses [20].

(ii) Cost reduction trends consider sharing the high temperature cooling loop of the electric machine (at 105 °C) with power electronics [21,22], or also using air cooling technologies that adopt convenient power converter placement to take advantage of circulating air during EV motion [23]. Thus, the utilization of power semiconductor technologies that can withstand higher junction temperatures is required [20].

As a consequence of such stringent operation conditions, reliability of power converters can be jeopardized. According to [24], approximately 60% of power semiconductor breakages are a consequence of thermal issues. Moreover, an increment of 10 °C in the operation mean temperature of a power semiconductor can double its failure-ratio [25]; consequently, special care of inverter thermal management must be taken. Such failures are a consequence of the mismatches between the coefficients of thermal expansion of the materials that constitute power modules, which suffer from significant thermo-mechanical fatigue during thermal or power cycling [26].

In such a context, operation point-dependant predefined current and/or switching frequency derating schemes are commonly used in the traction industry to avoid excessively high junction temperatures. However, in many cases such derating values can be conservative, and the operation limits of the drive can be reduced unnecessarily [27]. Thus, the incorporation of active thermal management solutions is being investigated to maximize vehicle performance. Active control of power losses can be applied in different layers (drivers, modulation and control) of an electric drive [28]. Control approaches aiming at the active modification of the PWM strategy (changing between different space vector sequences, continuous and discontinuous) are presented in [29]. Simple switching frequency hysteresis regulators can also be implemented [29]. Although hysteresis regulators are mainly used by the industry due to their simplicity, their unique usage possesses some drawbacks, as they do not cancel steady state errors. Thus, more advanced approaches can be found in the literature. For example, the authors in [27,30] propose a control algorithm for traction drives that considers electro-thermal constraints and real-time temperature estimation to impose current (torque) and switching frequency limits. By incorporating not only hysteresis but also discrete regulators, such limits are modified to precisely track a given temperature constraint. Similarly, an active thermal management algorithm is proposed in [31] based on a model predictive controller.

In this work, a novel online Temperature Constraint Tracking (TCT) based switching frequency regulator is proposed to improve the reliability of automotive power converters in the whole EV operation range: (i) at low speeds and standstill, where high acceleration and regenerative braking is required under urban traffic; (ii) at high speeds, where controllability needs to be ensured by determining a minimum switching frequency value. This novel solution focuses on simplicity and
low computational burden, as automotive microcontrollers have limited processing capabilities and the incorporation of an increasing number of software functionalities is demanded by the industry. The proposal is successfully incorporated in the control architecture of a 75 kW in-wheel direct drive IPMSM fed by an automotive grade Silicon Carbide (SiC) power converter, and is validated in an OPAL-RT RT-Lab OP4510 platform by means of simulation under realistic driving conditions.

2. Torque Regulation of the IPMSM

Figure 1 shows the general diagram of the implemented IPMSM torque regulation strategy including an optimum set-point generation block, a field oriented current control loop (with the required vector transformations), and the proposed TCT thermal management strategy, this last one incorporating a power loss estimator, a highest virtual junction temperature estimator and a TCT algorithm.

From the reference torque $T_{em}^*$, the current set-point generation block determines the $i_d^*$ and $i_q^*$ stator current references from two 2-D Look-Up Tables (LUTs). (From data obtained by Finite Element Analysis (FEM), current trajectories are determined offline and stored in the LUTs for Maximum Torque Per Ampere (MTPA), Field Weakening (FW) and Maximum Torque Per Volt (MTPV) regions. DC-link voltage dependency has been eliminated from the LUTs using the speed normalization ($\omega_{\text{norm}}$) concept proposed in [32], requiring one less dimension in the LUTs.) Unlike in conventional LUT-based current set-point generation approaches [33], the controller incorporates an additional Voltage Constraint Tracking feedback loop (VCT, Figure 1), ensuring that maximum stator voltage modulus of $V_{\text{DC}}/\sqrt{3}$ is never exceeded during field weakening even if significant electrical machine parameter deviations exist [34].

Once the reference currents are determined, the IPMSM torque is indirectly regulated by the current control loop. This loop follows the well-known field oriented control (FOC) approach in the synchronous reference frame ($dq$). However, although PI-based regulators are generally used, a second order Sliding Mode Control (SMC) with super-twisting has been implemented, as such an approach is more robust under machine non-linearities that are common in automotive IPMSMs [34,35].

The $dq$ reference voltages to be synthesized by PWM have been obtained by [34]:

\begin{align}
    v_d^* &= v_{d,ST} + v_{d,eq}, \quad (1) \\
    v_q^* &= v_{q,ST} + v_{q,eq}, \quad (2)
\end{align}
where \( v_{d,eq} \) (3) and \( v_{q,eq} \) (4) are the equivalent control signals, while \( v_{d,ST} \) and \( v_{q,ST} \) are computed by applying the Super-Twisting Algorithm (STA) in (5) and (6).

\[
v_{d,eq} = L_d \left[ c_d e_d - \frac{-R_s i_d + w_s \Psi_d(i_d, i_q)}{L_d(i_d, i_q)} \right], \tag{3}
\]

\[
v_{q,eq} = L_q \left[ c_q e_q - \frac{-R_s i_q + w_s \Psi_d(i_d, i_q)}{L_q(i_d, i_q)} \right], \tag{4}
\]

\[
v_{d,ST} = L_d \left[ \lambda_d |s_{i_d}|^{1/2} \text{sgn}(s_{i_d}) + \Omega_d \int \text{sgn}(s_{i_d}) dt \right], \tag{5}
\]

\[
v_{q,ST} = L_q \left[ \lambda_q |s_{i_q}|^{1/2} \text{sgn}(s_{i_q}) + \Omega_q \int \text{sgn}(s_{i_q}) dt \right], \tag{6}
\]

where \( R_s \) is the stator resistance, \( L_d(i_d, i_q), L_q(i_d, i_q) \), \( \Psi_d(i_d, i_q) \) and \( \Psi_q(i_d, i_q) \) are the dependent stator inductances and fluxes, \( \omega_s \) is the electrical speed, \( e_d = (i_d^* - i_d) \) and \( e_q = (i_q^* - i_q) \) are the error signals, \( c_d, c_q, \lambda_d, \lambda_q, \Omega_d \) and \( \Omega_q \) are the positive gains for tuning, \( \text{sgn}(s_{i_d}) = s_{i_d}/|s_{i_d}| \), \( \text{sgn}(s_{i_q}) = s_{i_q}/|s_{i_q}| \), where \( s_{i_d} \) and \( s_{i_q} \) are the sliding surfaces:

\[
s_{i_d} = e_d + c_d \int e_d dt, \tag{7}
\]

\[
s_{i_q} = e_q + c_d \int e_q dt. \tag{8}
\]

### 3. Proposed Thermal Management Strategy

The thermal management strategy (Figure 1) is responsible for regulating the switching frequency \( F_{sw, TCT} \) of the PWM to keep semiconductors’ junction temperatures below excessive limits. As a first step, the highest virtual junction temperature \( T_{vj} \) between all of the power semiconductors must be determined by this block prior to sending it to the TCT algorithm. This procedure is detailed in the next section.

#### 3.1. Power Loss and Highest Virtual Junction Temperature Estimation

In general, two possible approaches can be considered to estimate the highest \( T_{vj} \) among all switches:

1. If reliable estimation of semiconductor \( T_{vj} \) through temperature measurement sensors (thermistors, NTC or PTC) soldered on the Direct Bonded Copper (DBC) substrate of the power module is possible [36], the highest of all the estimated temperatures is provided to the TCT algorithm for its regulation. (As temperature sensors are placed at a given distance to the semiconductor die, it provides a lower temperature than \( T_{vj} \). To obtain accurate results, such a measurement point can be treated as a node within a simplified Foster thermal network to compensate the temperature underestimation (which is not straightforward) or, for simplicity, a security margin can be considered instead [36].)

2. If the previous is not possible, temperature should be estimated by using accurate thermal and power loss models in conjunction with an external temperature sensor (measuring heatsink or coolant temperature) [36]. This approach has been proven successful in [27,30], as estimated and measured temperatures matched very well. In such works, power losses of all semiconductors were estimated online.

This work follows the latter approach for the temperature estimation. However, as stated in the introduction of the document, low computational burden is a key feature demanded by the automotive industry. Thus, it is proposed to calculate the highest single semiconductor average power losses (worst case) per electrical period offline for all the torque/speed map of the machine and store these results in a 4-D LUT (Figure 1), considering \( T_{vj}, F_{sw}, T_{em} \) and \( \omega_m \) as inputs. (Although at high speeds
the junction temperatures are balanced, unbalances occur at very low speeds that need to be considered for obtaining accurate estimations.

For the offline power loss estimation, an accurate electric drive model has been implemented in Matlab/Simulink incorporating a per-device power loss model. In this work, a novel automotive grade SiC power module FS03MR12A6MA1B [37] of Infineon Technologies has been used to validate the proposal. This module offers state-of-the-art Trench SiC MOSFET technology with very low switching and conduction losses, which is necessary to reach high efficiency in traction inverters. It is packaged in a stray-inductance optimized module, which is also necessary for reaching high switching speeds as the ones considered for this application ($f_{sw} = 25$ kHz). Figure 2 shows the most significant power loss related parameters considered from the datasheet.

![Figure 2. MOSFET and body diode curves for the offline determination of power losses of the FS03MR12A6MA1B Silicon Carbide (SiC) power module. Source: “Target Datasheet of FS03MR12A6MA1B: Version 1.1”.

Regarding MOSFETs, the average conduction ($P_{cond,MOS}$) and switching ($P_{sw,MOS}$) losses during a modulation period $T_{sw}$ are calculated as:
\[ P_{\text{cond,MOS}} = \frac{1}{T_{\text{sw}}} \int_0^{T_{\text{sw}}} V_{\text{DS}}(i_D, T_{\text{vj,MOS}})i_D(t) dt, \quad (9) \]

\[ P_{\text{sw,MOS}} = \frac{1}{T_{\text{sw}}} \left[ \frac{V_{\text{DC}}}{V_{\text{DC(test)}}} \right]^{K_V} \left[ E_{\text{ON}}(i_D, T_{\text{vj,MOS}}) + E_{\text{OFF}}(i_D, T_{\text{vj,MOS}}) \right], \quad (10) \]

where \( V_{\text{DS}}(i_D, T_{\text{vj,MOS}}) \) is the drain-to-source voltage drop (Figure 2a), \( i_D(t) \) is the drain current, \( T_{\text{vj,MOS}} \) is the virtual junction temperature of the MOSFET, \( E_{\text{ON}}(i_D, T_{\text{vj,MOS}}) \) and \( E_{\text{OFF}}(i_D, T_{\text{vj,MOS}}) \) are the energy losses produced during turn-on and turn-off (Figure 2b), \( V_{\text{DC}} \) is the DC-link voltage value, and \( V_{\text{DC(test)}} \) is the blocking voltage from which \( E_{\text{ON}}(i_D, T_{\text{vj,MOS}}) \) and \( E_{\text{OFF}}(i_D, T_{\text{vj,MOS}}) \) were calculated by the manufacturer [38]. For the sake of simplicity, the compensation factor \( K_V \approx 1 \).

Similarly, the average conduction losses of the body diode during a modulation period are calculated as [38]:

\[ P_{\text{cond,D}} = \frac{1}{T_{\text{sw}}} \int_0^{T_{\text{sw}}} V_{\text{SD}}(i_{SD}, T_{\text{vj,D}})i_{SD}(t) dt, \quad (11) \]

where \( V_{\text{SD}}(i_{SD}, T_{\text{vj,D}}) \) is the body diode forward voltage (Figure 2c), \( i_{SD}(t) \) is the current circulating through the body diode, and \( T_{\text{vj,D}} \) is the diode virtual junction temperature. Note that in SiC MOSFETs the reverse recovery energy loss \( E_{\text{RR}} \) is negligible; thus, diode switching losses \( P_{\text{sw,D}} \) have not been considered. Additionally, as both MOSFET and body diode share the same die, it has been considered that \( T_{\text{vj,MOS}} \approx T_{\text{vj,D}} \approx T_{\text{vj}} \).

When executing the thermal management strategy online (Figure 1), highest average power losses of a power device (\( P_{\text{device},h} = P_{\text{cond,MOS}} + P_{\text{sw,MOS}} + P_{\text{cond,D}} \)) are estimated at each modulation period from the precalculated 4-D LUT, according to the operation point of the power inverter. Then, fluid temperature \( T_{\text{fluid}} \) is measured, and \( T_{\text{vj}} \) is calculated from the equivalent Foster network (Figure 3) whose thermal resistances \( R_i \) and capacitances \( C_i \) have been obtained from the datasheet (Figure 2d). Note that the \( R_{th} \) nomenclature used follows the AQG 324 Automotive Qualification Guideline. A detailed explanation regarding \( R_i \) and \( C_i \) estimations can be found in [39].

![Figure 3. Equivalent Foster thermal network.](image)

The transient thermal impedance \( Z_{th,JF} \) of the circuit of Figure 3 can be expressed in the Laplace domain as follows:

\[ Z_{th,JF} = \sum_{i=1}^{n} \frac{R_i}{1 + \tau_i s}, \quad (12) \]

where \( n \) is the number of the RC equivalent circuits and \( \tau_i = R_i C_i \). For the digital implementation of the foster network, such continuous transfer function is transformed into the \( z \) domain, with a sample-time of \( T_{\text{sw}} \), and \( T_{\text{vj}} \) is numerically obtained.

### 3.2. Temperature Constraint Tracking Algorithm

Finally, the estimated highest \( T_{\text{vj}} \) value is sent to the TCT algorithm, whose block diagram is shown in Figure 4. This regulator aims not to exceed a given temperature limit \( T_{j,max} \) by computing the difference between \( T_{\text{vj}} \) and \( T_{j,max} \):

\[ \Delta T = T_{\text{vj}} - T_{j,max}. \quad (13) \]
The switching frequency of the PWM modulator, determined by the TCT algorithm for the $k$-th instant is:

$$F_{sw,TCT}^*(k) = F_{sw}^* - \delta F_{sw}(k),$$

(14)

where $F_{sw}^*$ is the predefined PWM frequency of the inverter and $\delta F_{sw}$ is a correction term that reduces the switching frequency when $T_{vj}$ exceeds $T_{j,max}$:

$$\delta F_{sw}(k) = \delta F_{sw}(k - 1) + \alpha \Delta T,$$

(15)

where $\alpha$ is a positive tunable parameter that sets the dynamics of the TCT algorithm.

Finally, the correction term $\delta F_{sw}$ is saturated as follows:

$$\delta F_{sw}(k)_{sat} = \begin{cases} 0 & \text{if } \delta F_{sw}(k) \leq 0, \\ F_{sw}^* - F_{sw,min}^*(k) & \text{if } \delta F_{sw}(k) > 0. \end{cases}$$

(16)

Saturation in (16) has two purposes. On the one hand, the predefined switching frequency $F_{sw}^*$ cannot be exceeded; thus, $\delta F_{sw}$ is saturated to zero when the compensation term is positive. On the other, the minimum switching frequency that ensures electric machine controllability ($F_{sw,min}^*(k)$) for the operation conditions of the $k$-th instant is established as follows:

$$F_{sw,min}^*(k) = f_e(k)S_p = \frac{\omega_m(k)P}{2\pi}S_p,$$

(17)

where $f_e(k)$ is the electrical frequency of the IPMSM in the $k$-th instant, $P$ is the pole-pair number of the machine, $\omega_m(k)$ is the mechanical speed of the machine, and $S_p$ is the number of samples per period required to ensure system controllability. In this work, $S_p$ has been set to eight, while the minimum value that $F_{sw,min}$ can acquire has been saturated to 2 kHz in order not to exceed a given torque ripple during near standstill operation. (Despite some recent research works [13,40,41] try to minimize the $S_p$ to as low as 2 in order to fulfil the well-known Nyquist frequency (which must be half of the sampling one in order to guarantee stability); it is known that the conservative value should not be lower than 8.)

4. Validation of the Proposed Thermal Management Algorithm

4.1. Simulation Platform Description

The proposed thermal management algorithm has been validated in an advanced simulation platform. An RT-Lab OP4510 real-time simulator (OPAL-RT, Montreal, QC Canada) with four computational nodes (Intel Xeon E3 v5 CPU, 8MB cache, 3.5GHz, 16G B RAM, 128 GB SSD) has been used to accelerate simulations and evaluate the performance of the proposal under realistic driving conditions. On the one hand, a rear-wheel drive SUV Full Electric vehicular model has been implemented, whose mathematical model is summarized in Appendix A. The vehicle powertrain has two in-wheel electric machines fed by two three-phase power inverters constituted by the SiC semiconductors presented in Section 3.1, whose most significant parameters are shown in Table 1. In this application, a nominal switching frequency of 25 kHz has been selected to take advantage of
the SiC technology capabilities. This way a smooth operation is provided when operating at very high electrical frequencies that result due to the multi-pole nature of the machine. Note also that, for cost saving, both the electric machine and the power inverter share the same high temperature liquid cooling loop. Table 2 shows the nominal parameters of each 75 kW IPMSM, while the TCT control settings are provided in Table 3. Note that, in the following section, a single in-wheel drive is simulated.

### Table 1. Most relevant parameters of the simulated power electronics system.

| Parameter                                      | Symbol | Value | Units |
|------------------------------------------------|--------|-------|-------|
| Power semiconductor maximum drain-source voltage | $V_{DSS}$ | 1200  | V     |
| Power semiconductor nominal drain current      | $I_{D,nom}$ | 400   | A     |
| Nominal switching frequency                    | $f_{sw}$ | 25    | kHz   |
| Nominal battery (DC-link) voltage              | $V_{DC}$ | 300   | V     |
| Cooling fluid temperature                      | $T_{fluid}$ | 105  | °C    |

### Table 2. Most relevant parameters of the automotive Interior Permanent Magnet Synchronous Machine (IPMSM).

| Parameter                                      | Symbol | Value | Units |
|------------------------------------------------|--------|-------|-------|
| Maximum power                                  | $P_{max}$ | 75    | kW    |
| Nominal power                                  | $P_{nom}$ | 50    | kW    |
| Maximum mechanical speed                       | $\omega_{max}$ | 1500  | rpm   |
| Stator maximum current                         | $I_{max}$ | 400   | $A_{rms}$ |

### Table 3. Regulation parameters of the Temperature Constraint Tracking (TCT) controller.

| Parameter                                      | Symbol | Value | Units |
|------------------------------------------------|--------|-------|-------|
| Maximum junction temperature constraint        | $T_{j,max}$ | 120  | °C    |
| TCT tunable parameter                          | $\alpha$ | 1    | -     |

In the following section, thermal management validation tests under low speed operation (close to standstill operation) and during a World Harmonized Light-duty Vehicle Test Procedure (WLTP) [42] driving cycle are provided.

#### 4.2. Low Speed and High Torque Operation Results

EV drives usually operate at low speeds with high torque, for example starting uphill, or also when driving in urban driving conditions with a dense traffic, intersections and traffic lights. These operation conditions are critical, as at very low speeds power losses are concentrated in a few power semiconductors, which implies high junction temperatures. Thus, a high torque step demand (from 100 Nm to 300 Nm) has been simulated while driving at $\omega_m = 1$ rpm (Figure 5a).

When no TCT algorithm is applied, the high power losses on the semiconductors produced by the high torque step (Figure 5b, from t = 0.4 s) result on a high junction temperature (Figure 5c). As a consequence, the power converter overheats, significantly reducing its life-cycle if such operation conditions are constantly repeated. However, such temperature can be constrained by using the proposed thermal management algorithm (Figure 6). Figure 6b shows that power losses are reduced when the TCT algorithm reduces the switching frequency from 25 kHz to approximately 10 kHz, maintaining the temperature around the maximum allowed one, which in this work has been set at 120 °C (Figure 6c). Note that the switching frequency regulation dynamics can be set by adjusting such a parameter. In this context, Figure 7 exemplifies the temperature response of the system obtained for
various values of $\alpha$. The higher the value of $\alpha$, the faster the settling time and the lower the overshoot. However, a trade-off between temperature settling time and current regulation robustness must be considered. In this particular application, the TCT regulation parameter $\alpha$ has been set to 1.

**Figure 5.** Simulation results obtained at very low speed and under a high torque step without using the proposed TCT algorithm.

**Figure 6.** Simulation results obtained at very low speed and under a high torque step when using the TCT algorithm.

It is important to remark that a small temperature overshoot (Figure 6c) has been produced due to the high torque step. However, as torque reference is always ramped in EVs to ensure driving comfort
and vehicle’s mechanical integrity, the TCT algorithm has enough time to react and ensure that the power semiconductor junction temperature does not exceed a given temperature constraint.

Figure 7. Thermal regulation dynamics vs. TCT parameter $\alpha$.

4.3. Validation under Standardized WLTP Driving Cycle

Finally, the proposed TCT algorithm has been evaluated through a WLTP driving cycle. Figure 8 shows the results obtained when the thermal management strategy is not active. In this scenario and as a consequence of the high power losses (Figure 8c), $T_{j,max}$ is exceeded in various intervals of the WLTP cycle (Figure 8d). However, when using the proposed thermal management approach (Figure 9), the junction temperature does not exceed the limit value thanks to the regulation of the switching frequency (Figure 9d). It is important to remark that when the junction temperature remains under the maximum limit, $\delta F_{sw}$ is saturated to zero, and the nominal switching frequency is restored to its default value of 25 kHz. Additionally, as shown in Figure 9a, the thermal management strategy does not interfere in the normal operation of the drive, extending its life-cycle and helping to achieve the $CO_2$ emission reduction goals. Although a slightly higher torque ripple is present when the TCT algorithm reduces the switching frequency, its effects are filtered by the EV’s large inertia, making such a ripple negligible for the passengers’ comfort.

Figure 8. Simulation results of the SiC based IPMSM drive under standardized World Harmonized Light-duty Vehicle Test Procedure (WLTP) driving cycle without TCT regulation.
Figure 9. Simulation results of the SiC based IPMSM drive under standardized WLTP driving cycle when using TCT regulation.

5. Conclusions

Maximizing the life-cycle of modern EVs and their constituting elements is a relevant research topic. Due to the increasingly demanding requirements of the automotive industry, EV traction inverters must operate under high power semiconductor junction temperatures and stringent thermal cycles. Under these extreme working conditions it is difficult to ensure the power converter reliability while not excessively reducing the performance of the drive. In this context, a novel online thermal management algorithm based on a TCT switching frequency regulator that maintains the semiconductors’ junction temperature below excessive temperatures has been proposed in this paper. The algorithm has been designed to be easily fitted in automotive microcontrollers in order to meet the always exigent computational requirements of the automotive industry.

A realistic 75 kW in-wheel direct drive IPMSM fed by an automotive grade SiC power converter has been accurately modelled and used to validate the proposal through numerous and precise simulations, which also include standardized and therefore representative driving cycles. From the results obtained, it can be concluded that the introduction of the proposed TCT algorithm improves the thermal behaviour of the drive power converter without jeopardizing the system controllability and performance. As a final remark, it is worth mentioning that for some applications where a much lower temperature constraint or more severe load conditions are imposed, only regulating the switching
frequency could not be sufficient to track the temperature limit. In such cases, the proposed thermal management strategy could be extended by incorporating a torque limiting regulator.

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Appendix A. Mathematical Model of the Vehicle

In this appendix, the mathematical expressions of the backward model used for vehicular simulation are presented. In an in-wheel direct drive, the mechanical speed \( \omega_m \) and torque of the drive \( T_{em} \) equal the ones of the wheel. For a given EV linear speed of \( v_{dc} \), the wheel angular speed and torque can be determined by:

\[
\omega_m = \omega_{wheel} = \frac{v_{dc}}{r_{wheel}},
\]

\[
T_{em} = T_{wheel} = r_{wheel}(F_{Roll} + F_{Aero} + F_{Inertia}),
\]

where \( r_{wheel} \) is the wheel radius, and \( F_{Roll}, F_{Aero}, \) and \( F_{Inertia} \) are the rolling resistance, aerodynamic resistance, and inertia forces, respectively. These last three can be defined as:

\[
F_{Roll} = \mu g M_{car},
\]

\[
F_{Aero} = \frac{\rho v_{dc}^2 C_d A_f}{2},
\]

\[
F_{Inertia} = (1 + M_{rot}) a_{car},
\]

where \( M_{car} \) is the total vehicle mass, \( a_g \) is the gravity acceleration, \( \mu \) is the rolling friction coefficient, \( \rho \) is the air density, \( C_d \) is the drag coefficient, \( A_f \) is the vehicle cross section; \( M_{rot} \) is the equivalent mass of the rotating parts of the car in (%), and, \( a_{car} \) is the car acceleration defined as \( dv/dt \). The grade force was not considered in this simplified vehicle model.

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