Safety Verification of Asynchronous Pushdown Systems with Shaped Stacks

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Abstract. In this paper, we study the program-point reachability problem of concurrent pushdown systems that communicate via unbounded and unordered message buffers. Our goal is to relax the common restriction that messages can only be retrieved by a pushdown process when its stack is empty. We use the notion of partially commutative context-free grammars to describe a new class of asynchronously communicating pushdown systems with a mild shape constraint on the stacks for which the program-point coverability problem remains decidable. Stacks that fit the shape constraint may reach arbitrary heights; further a process may execute any communication action (be it process creation, message send or retrieval) whether or not its stack is empty. This class extends previous computational models studied in the context of asynchronous programs, and enables the safety verification of a large class of message passing programs.

Keywords: Pushdown systems, asynchronous message passing, verification

1 Introduction

The safety verification of concurrent and distributed systems, such as client-server environments, peer-to-peer networks and the myriad web-based applications, is an important topic of research. We consider asynchronously communicating pushdown systems (ACPS), a model of computation for such systems suitable for the algorithmic analysis of the reachability problem. Each process of the model is a pushdown system; processes may be spawned dynamically and they communicate asynchronously via a number of unbounded message buffers which may be ordered or unordered. In order to obtain a decision procedure for reachability, some models restrict the retrieval (or, dually, the sending) of messages or the scheduling of tasks, allowing it to take place only when the call stack is empty.

Can these restrictions on call stacks be relaxed? Unfortunately\(^1\) some form of constraint on the call stacks in relation to the communication actions is unavoidable. Inspired by the work on asynchronous procedure calls [30, 22, 17], we consider processes that communicate asynchronously via a fixed number of unbounded and unordered message buffers which we call channels. Because channels are unordered, processes cannot observe the precise sequencing of such concurrency actions as message send and process creation; however, the sequencing of other actions, notably blocking actions such as message retrieval which requires synchronisation, is observable. If the behaviour of a

\(^1\) Any analysis that is both context-sensitive and synchronisation-sensitive is undecidable [29].
process is given by its action sequences, then we may postulate that certain actions commute with each other (over sequential composition) while others do not. To formalise these assumptions, we make use of partially commutative context-free grammars (PC-CFG) \[7\], introduced recently by Czerwinski et al. as a study in process algebra. A PCCFG is just a context-free grammar equipped with an irreflexive symmetric relation, called independence, over an alphabet \(\Sigma\) of terminal symbols, which precisely captures the symbols that commute with each other. In our model, a process is described by a PCCFG that generates the set of its action sequences; terminal symbols represent concurrency and communication actions, while the non-terminal symbols represent procedure calls; and there is an induced notion of commutative procedure calls. With a view to deciding reachability, a key innovation of our work is to summarise the effects of the commutative procedure calls on the call stack. Rather than keeping track of the contents of the stack, we precompute the actions of those procedure calls that produce only commutative side-effects, and store them in caches on the call stack. The non-commutative procedure calls, which are left on the stack in situ, act as separators for the caches of commutative actions. As soon as the top non-commutative non-terminal on the stack is popped, which may be triggered by a concurrency action, the cache just below it is unlocked, and all the cached concurrency actions are then despatched at once.

In order to obtain a decision procedure for (a form of reachability called) coverability, we place a natural constraint on the shape of call stacks: at all times, no more than an a priori fixed number of non-commutative non-terminals may reside in the stack. Note that because the constraint does not apply to commutative non-terminals, call stacks can grow to arbitrary heights. Thanks to the shape constraint, we can prove that the coverability problem is decidable by an encoding into well-structured transition systems. To our knowledge, this class extends previous computational models studied in the context of asynchronous programs. Though our shape constraint is semantic, we give a simple sufficient condition which is expressed syntactically, thus enabling the safety verification of a large class of message-passing programs.

Example 1. In Figures 1 and 2 we give an example program written in a version of Erlang that employs channels (as opposed to Actor-style mailboxes), implementing a simple replicated workers pattern. It consists of a distributor process that initially spawns a number of workers, sets up a single shared resource, and distributes one task per worker over a one-to-many channel. Each worker runs a task-processing loop. Upon reception of a task, the worker recursively decomposes it, which involves communicating with the shared resource at each step. Note that the communication of each worker with the resource is protected by a lock. For the worker, the decomposition has two possible outcomes: (i) the task is partially solved, generating one subtask and an intermediate result or (ii) the task is broken down into one subtask and one new distributable task. In case (i) the worker recursively solves the subtask and combines the result with the intermediate result. In case (ii) the worker recursively solves the subtask and subsequently dispatches the newly generated distributable task before returning. When a worker has finished processing a task, it relays the result to the server and awaits a new task to process. We have left the implementation of the functions decompose_task and combine
main() → setup_network(),
    redistribute () .

setup_network() →
    spawn(worker),
    case (∗) of
      true → setup_network();
      false →
        spawn(res_start(init)),
        toResource ! isReady,
        receive toDistributor :
          ready → ()
    end;
    end, toWorkers ! task.

redistribute () →
    receive toDistributor :
      redist (Task) → toWorkers ! Task;
      result (Result) → print (Result);
    end, redistribute () .

redistribute () →
    receive toDistributor :
      redist (Task) → toWorkers ! Task;
      result (Result) → print (Result);
    end, redistribute () .

% Resource
res_start (S) =
    fun() → toDistributor ! ready,
    resource(S)
end.

resource(S) →
    receive toResource:
      lock_req →
      toWorkers ! locked,
      resource_locked(S)
end.

resource_locked(S) →
    receive toResource:
      unlock_req → resource(S);
      getState →
      toWorkers ! state(S),
      resource_locked(S);
    update(X) → resource_locked(X)
end.

Fig. 1. A resource and a task distributor.

open; for the purpose of this example we only assume that they do not perform any concurrency actions, but they may be recursive functions.

Note that the call stacks of both the distributor and the workers may reach arbitrary heights, and communication actions may be performed by a process at any stage of the computation, regardless of stack height. For example the worker sends and receives messages at every decomposition, and each recursive call increases the height of the call stack.

An interesting verification question for this example program is whether the locking mechanism for the shared resource guarantees exclusive access to the shared resource for each worker process in its critical section.

A Computational Model. To verify programs such as the above we need a computational model that allows us to model recursive procedure calls, message passing concurrency actions and process creation. Once the obvious abstractions are applied to make the data and message space finite, we arrive at a network of pushdown systems (equivalently context-free grammars) which can communicate asynchronously over a finite number of channels with unbounded capacity. Since we are interested in a class of such systems with decidable verification problems we assume that channels are unordered (FIFO queues with finite control are already Turing powerful [5]).

Outline. The rest of the paper is organised as follows. In Section 2 we present our model of asynchronous partially commutative pushdown systems (APCPS), its (standard) semantics and a verification problem. In Section 3 we investigate an alternative semantics for APCPS, a corresponding verification problem, and relate it to the verification problem of Section 2. In Section 4 we introduce the class of APCPS with shaped stacks and show that the verification problems are decidable for this class. In Section 5 we discuss related work and then conclude. Owing to space constraints we have relegated proofs to the appendix.
2 Asynchronous Communicating Pushdown Systems

In this section we introduce our model of concurrency, asynchronous partially commutative pushdown systems. Processes are modelled by a variant of context-free grammars, which distinguish commutative and non-commutative concurrency actions. Communication between processes is asynchronous, via a fixed number of unbounded and unordered message buffers, which we call channels.

Preliminaries. An independence relation $I$ over a set $U$ is a symmetric irreflexive relation over $U$. It induces a congruence relation $\simeq_I$ on $U^*$ defined as the least equivalence relation $R$ containing $I$ and satisfying: $(\mu, \mu') \in R \Rightarrow \forall \nu_0, \nu_1 \in U^* : (\nu_0 \mu \nu_1, \nu_0 \mu' \nu_1) \in R$.

Let $I$ be an independence relation over $U$. An element $a \in U$ is non-commutative (with respect to $I$) just if $\forall b \in U : (a, b) \notin I$ i.e. $a$ does not commute with any other element. An element $b$ is commutative (with respect to $I$) just if for each $c \in U$, if $c$
is not non-commutative then \((c, b) \in I\); intuitively it means that \(b\) commutes with all elements of \(U\) except those that are non-commutative. We call an independence relation \(I\) unambiguous if just every element of \(U\) is either commutative or non-commutative.

**Definition 1.** Let \(\Sigma\) be an alphabet of terminal symbols and \(I \subseteq \Sigma \times \Sigma\) an independence relation over \(\Sigma\). A partially commutative context-free grammar (PCCFG) is a quintuple \(G = (\Sigma, I, N, R, S)\) where \(S \in N\) is a distinguished start symbol, and \(R\) is a set of rewrite rules of the following types:\(^2\) let \(A \in N\) (i) \(A \rightarrow a\) where \(a \in \Sigma \cup \{\epsilon\}\), (ii) \(A \rightarrow a B\) where \(a \in \Sigma, B \in N\), (iii) \(A \rightarrow B C\) where \(B, C \in N\). We refer to each \(\rho \in R\) as a \(G\)-rule.

The (leftmost) derivation relation \(\rightarrow_{\text{seq}}\) is a binary relation over \((\Sigma \cup N)^*/\sim_I\) defined as \(X \alpha \rightarrow_{\text{seq}} \beta \alpha\) if \(X \rightarrow \beta\) is a \(G\)-rule. Note the derivation relation is defined over the quotient by \(\sim_I\), so the words generated are congruence classes induced by \(\sim_I\). As usual we denote the \(n\)-step relation as \(\rightarrow_{\text{seq}}^n\) and reflexive, transitive closure as \(\rightarrow_{\text{seq}}^*\).

We further define a \(k\)-index derivation to be a derivation in which every term contains at most \(k\) occurrences of non-terminals. Recent work \([14, 12]\) has shown that for every commutative context-free grammar \(G\) there exists \(k \geq 1\) such that the entire language of \(G\) can be generated by derivations of index \(k\).

PCCFG was introduced by Czerwinski et al. as a study in process algebra. They investigated \([7]\) the decidability of bisimulation for a class of processes described by PCCFG where the commutativity of the sequential composition is constrained by an independence relation on non-terminals. We propose to use words generated by PCCFGs to represent the sequence of concurrency actions of processes.

### 2.1 Asynchronous Partially Commutative Pushdown Systems

Our model of computation, asynchronous partially commutative pushdown systems, are in essence PCCFGs equipped with an independence relation over an alphabet \(\Sigma\) of terminal symbols, which represent the concurrency actions and program point labels. First some notation. Let \(\text{Chan}\) be a finite set of channel names ranged over by \(c\), \(\text{Msg}\) be a finite message alphabet ranged over by \(m\), and \(\mathcal{L}\) be a finite set of program point labels ranged over by \(l, l', l_1\), etc. Further let \(N\) be a finite set of non-terminal symbols. We derive an alphabet \(\Sigma\) of terminal symbols

\[
\Sigma := \mathcal{L} \cup \{c!m, c?m \mid c \in \text{Chan}, m \in \text{Msg}\} \cup \{\nu X \mid X \in N\}. \tag{1}
\]

An action of the form \(c!m\) denotes the sending of the message \(m\) to channel \(c\), \(c?m\) denotes the retrieval of message \(m\) from channel \(c\), and \(\nu X\) denotes the spawning of a new process that begins execution from \(X\). We will use \(a, a', b\), etc. to range over \(\Sigma\). Our computational model will emit program point labels in its computation, allowing us to pose questions of reachability. We will now define the computational power of our processes in terms of PCCFGs.

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\(^2\) Identifying rules of type (ii), which is a special case of type (iii), allows us to distinguish tail-recursive and non-tail recursive calls, which will be handled differently in the sequel, beginning with Definition 4.
The words that are generated by a process qua PCCFG represent its action sequences. Because channels are unordered, processes will not be able to observe the precise sequencing of concurrency actions such as message send and process creation; however the sequencing of other actions such as message retrieval is observable. Using the language of partially commutative context-free grammar, we can make this sensitivity to sequencing precise by an independence relation on actions.

**An Independence Relation for the Concurrency Actions.** Let $\Xi \subseteq \Sigma$, we define the independence relation over $\Sigma$ generated by $\Xi$ as

$$\text{IndRel}_\Sigma(\Xi) := \{(a, a'), (a', a) \mid a, a' \in \Xi, a \neq a'\}$$

Now let $\Sigma^\flat := \mathcal{L} \cup \{c ! m \mid c \in \text{Chan}, m \in \text{Msg}\} \cup \{\nu X \mid X \in \mathcal{N}\}$ be the subset of $\Sigma$ consisting of the program point labels and the send and spawn actions. It is straightforward to see that $\text{IndRel}_\Sigma(\Sigma^\flat)$ is, by construction, an unambiguous independence relation over $\Sigma$. Thus $\text{IndRel}_\Sigma(\Sigma^\flat)$ allows us to commute all concurrency actions except receive. Further we allow program point labels to commute. This is harmless, since our goal is to analyse (a form of) control-state reachability, i.e. the question whether a particular label can be reached, as opposed to questions that require sequential reasoning such as whether label $l_1$ will be reached before $l_2$ is reached.

We can now lift the independence relation to the non-terminals of a PCCFG $G$. Let $I$ be the least subset of $(\mathcal{N} \cup \Sigma)^2$ such that (i) $\text{IndRel}_\Sigma(\Sigma^\flat) \subseteq I$, and (ii) for all $a, b \in \Sigma \cup \mathcal{N}$ and $A \in \mathcal{N}$, if $\forall a \in \text{RHS}(A) : (a, b) \in I$ then $\{(a, b), (b, A)\} \subseteq I$, where $\text{RHS}(A) := \{a \in \mathcal{N} \cup \Sigma \mid A \rightarrow \alpha \in G, a \text{ occurs in } \alpha\}$. We note that $I$, which is well-defined, is an unambiguous independence relation over $\mathcal{N} \cup \Sigma$. Thus we can partition both $\Sigma$ and $\mathcal{N}$ into $\Sigma^{\text{com}}$ and $\mathcal{N}^{\text{com}}$, the commutative actions and non-terminals respectively, and $\Sigma^{\text{com}}$ and $\mathcal{N}^{\text{com}}$ their non-commutative counterparts respectively.

We can now define our model of computation.

**Definition 2.** Assume $\mathcal{L}$, Chan, Msg and $\mathcal{N}$ as introduced earlier, and the derived alphabet $\Sigma$ of terminals as defined in (1). An asynchronous partially commutative pushdown system (APCPS) is just a PCCFG $G = (\Sigma, I, \mathcal{N}, \mathcal{R}, S)$. Henceforth we fix $\mathcal{L}$, Chan, Msg and $\mathcal{N}$, and the derived (1) alphabet $\Sigma$ of terminals.

### 2.2 Standard Semantics

The operational semantics is given as a transition system. A configuration of the system is a pair, consisting of a parallel composition of processes and a set of channels. We represent the state of a single process as an element of $\text{Control} := (\Sigma \cup \mathcal{N})^*/ \sim_\text{seq}$. The derivation relation of PCCFGs, $\rightarrow_\text{seq}$, defines how processes make sequential transitions. Processes interact concurrently by message passing via a fixed set of unbounded and unordered channels.

**Definition 3 (Standard Concurrent Semantics).** The configurations are elements of $\mathcal{M}[\text{Control}] \times (\text{Chan} \rightarrow \mathcal{M}[\text{Msg}])$. For simplicity, we write a configuration (say) $([\alpha, \beta], \{c_1 \mapsto [m_a, m_b, m_c], c_2 \mapsto []\})$ as $\alpha \parallel \beta \triangleleft [m_a, m_b, m_c]^c, [\ ]^c$. We abbreviate a set of processes running in parallel as $\Pi$ and a set of channels by $\Gamma$ with names.
in Chan. The operational semantics for APCPS, a binary relation $\rightarrow_{\text{con}}$ over configurations, is then defined by induction over the rule:

$$\frac{\alpha \rightarrow_{\text{seq}} \alpha'}{\alpha \parallel \Pi \triangleleft \Gamma \rightarrow_{\text{con}} \alpha' \parallel \Pi \triangleleft \Gamma}$$

and the following axioms: let $m \in \text{Msg}, c \in \text{Chan}, l \in L$ and $X \in N$

1. \((c ? m) \alpha \parallel \Pi \triangleleft (\lbrack m \rbrack \oplus q)^c, \Gamma \rightarrow_{\text{con}} \alpha \parallel \Pi \triangleleft q^c, \Gamma\)
2. \((c ! m) \alpha \parallel \Pi \triangleleft q^c, \Gamma \rightarrow_{\text{con}} \alpha \parallel \Pi \triangleleft (\lbrack m \rbrack \oplus q)^c, \Gamma\)
3. \(l \alpha \parallel \Pi \triangleleft \Gamma \rightarrow_{\text{con}} \alpha \parallel \Pi \triangleleft \Gamma\)
4. \((\nu X) \alpha \parallel \Pi \triangleleft \Gamma \rightarrow_{\text{con}} \alpha \parallel X \parallel \Pi \triangleleft \Gamma\)

The start configuration is $S \triangleleft \emptyset$. We define a partial order on configurations: $\Pi \triangleleft \Gamma \leq \Pi' \triangleleft \Gamma'$ just if $\Pi \leq_{M} \Pi'$ and for every $c \in \text{Chan}, \Gamma(c) \leq_{M} \Gamma'(c)$.

### 2.3 Program-Point Coverability

In the sequential setting of (ordinary) pushdown systems, the control-state reachability problem is of central interest. In our notation, it asks, given a control-state $A$, if it is possible to reach a process-configuration $A \alpha$ where $A$ is the control-state and $\alpha$ is some call stack. It should be clear that an equivalent problem is to ask whether $l \alpha$ is reachable, where $l$ is a program-point label. We prefer a formulation that uses program-point labels because it simplifies our argument (and is equi-expressive).

In the concurrent setting, we wish to know whether, given an APCPS and program-point labels $l_1, \ldots, l_n$, there exist call stacks $\alpha_1, \ldots, \alpha_n$ and channel contents $\Gamma$ such that the configuration $l_1 \alpha_1 \parallel \cdots \parallel l_n \alpha_n \triangleleft \Gamma$ is $\rightarrow_{\text{con}}$-reachable, possibly in parallel with some other processes. Note that this question allows us to express not just control-state reachability queries but also mutual exclusion properties. We state the problem of program-point coverability more formally as follows.

**Verification Problem 1** (Program-Point Coverability). Given an APCPS $G$ and program point labels $l_1, \ldots, l_n$, a tuple $(G; l_1, \ldots, l_n)$ is a yes-instance of the program-point coverability problem just if there exist a configuration $\Pi \triangleleft \Gamma$ and $\alpha_1, \ldots, \alpha_n \in (\Sigma \cup N)^*/\sim_I$ such that $\Pi \triangleleft \Gamma$ is $\rightarrow_{\text{con}}$-reachable and $l_1 \alpha_1 \parallel \cdots \parallel l_n \alpha_n \triangleleft \emptyset \leq \Pi \triangleleft \Gamma$.

The program-point coverability problem allows us to characterise “bad-configurations” $c_{\text{bad}}$ in terms of program-point labels. We regard a configuration $c$ that covers $c_{\text{bad}}$, in the sense that ($c_{\text{bad}} \leq c$), also as “bad”. Using program-point coverability, we can express whether any such configuration is reachable.

**Example 2.** Consider the program in Figures 1 and 2 and call it $P$. The problem of whether each worker has exclusive access to the shared resource in its critical section is expressible as a program-point coverability problem. A bad configuration is one in which two worker processes are executing the line marked by ?label("critical "). We can thus see that $(P; ?label("critical "), ?label("critical "))$ is an instance of the program-point coverability problem; a no answer implies mutual exclusion, a yes answer tells us that two worker processes can be simultaneously within their critical section.
The program-point coverability problem is undecidable for unconstrained APCPS. In fact APCPS is Turing powerful: it is straightforward to simulate a system with two synchronising pushdown systems.

3 An Alternative Semantics for APCPS

In this section we present an alternative semantics for APCPS which captures enough information to solve the program-point coverability problem. The key idea is to summarise the effects of the commutative non-terminals. In the alternative semantics, rather than keeping track of the contents of the call stack, we precompute the actions of those procedure calls that produce only commutative side-effects, i.e. sends, spawns and program point labels, and store them in caches on the call stack. The non-commutative procedure calls, which are left on the call stack, then act as separators for the caches of commutative side-effects. As soon as the top non-commutative non-terminal on the stack is popped, which may be triggered by a concurrency action, the cache just below it is unlocked. The cached actions are made effective instantaneously. This is enough to ensure a precise correspondence between the program-point coverability problem for APCPS and a corresponding coverability problem for our alternative semantics.

An Alternative Semantics. First we introduce a representation of the states of a process. Let $k \in \mathbb{N} \cup \{\infty\}$.

\[
\begin{align*}
\text{TermCache} & := M[\Sigma^{\text{com}}] \\
\text{MixedCache} & := M[\Sigma^{\text{com}} \cup N^{\text{com}}] \\
\text{NonTermCache} & := M[N^{\text{com}}] \\
\text{Cache} & := \text{TermCache} \uplus \text{MixedCache} \\
\text{CallStack}^{\leq k} & := (N^{\text{com}} \cdot \text{Cache})^{\leq k} \\
\text{DelayedControl} & := \text{TermCache} \uplus \text{MixedCache} \uplus \text{NonTermCache} \\
\text{NormalControl} & := (N \cdot \text{Cache}) \uplus (\Sigma \cdot N \cdot \text{Cache}) \uplus (\Sigma \cdot \text{Cache}) \\
\text{ControlState} & := \text{NormalControl} \uplus \text{DelayedControl} \\
\gamma, \delta & \in \text{Control}^{\leq k} := \text{ControlState} \cdot \text{CallStack}^{\leq k} \\
\text{Queue} & := M[\text{Msg}] \\
\text{Queues} & := \text{Chan} \rightarrow \text{Queue} \\
\text{Config}^{\leq k} & := M[\text{Control}^{\leq k}] \times \text{Queues}
\end{align*}
\]

Note that we assume the equality $\epsilon = \emptyset$ to simplify notation. We write $\text{Control}^{M} := \text{Control}^{\leq \infty}$ and $\text{CallStack}^{M} := \text{CallStack}^{\leq \infty}$.

Definition 4 (Alternative Sequential Semantics). Let $\mathcal{G}$ be a PCCFG. We define a transition relation $\rightarrow_{\text{seq}}$ on $\text{Control}^{M}$ by induction over the following rules:

If $A \rightarrow B C$ is a $\mathcal{G}$-rule, $C$ commutative and $C \rightarrow_{\text{seq}}^{\ast} w \in (N^{\text{com}} \cup \Sigma^{\text{com}})^{*}$ then

\[ A M \gamma \rightarrow_{\text{seq}} B (M(w) \oplus M) \gamma \quad (7) \]

3 Defining $\text{Cache}$ as a disjoint union enables a definition by cases according to the type of cache, thus rendering $\rightarrow_{\text{cov}}$ monotone with respect to an ordering.
If $A \rightarrow B C$ is a $G$-rule and $C$ non-commutative then
\[ AM \gamma \rightarrow_{\text{seq}} BC M \gamma \quad (8) \]
If $A \rightarrow a B$ is a $G$-rule and $a \in \Sigma$ and $B \in N$ then
\[ AM \gamma \rightarrow_{\text{seq}} a BM \gamma \quad (9) \]
If $A \rightarrow a$ is a $G$-rule where $a \in \Sigma \cup \{e\}$ then
\[ AM \gamma \rightarrow_{\text{seq}} a M \gamma \quad (10) \]

where $\gamma \in \text{CallStack}^M$, $M \in \text{Cache}$, and $A$, $B$ and $C$ range over non-terminals.

From the alternative sequential semantics, we derive a corresponding alternative concurrent semantics, using the following notation: for $M \in M[\Sigma^{\text{com}}]$ and $w \in (\Sigma^{\text{com}})^*$

\[ \Gamma \oplus \Gamma' := \{ c \mapsto \Gamma(c) \oplus \Gamma'(c) \mid c \in \text{Chan} \} \]
\[ \Gamma(M) := \{ c \mapsto \sum_{c!m \in M} M(c!m) \mid c \in \text{Chan} \} \quad \Gamma(w) := \Gamma(M(w)) \]
\[ \Pi(M) := \{ X \mapsto M(\nu X) \mid X \in N \} \quad \Pi(w) := \Pi(M(w)) \]

**Definition 5 (Alternative Concurrent Semantics).** We define a binary relation $\rightarrow_{\text{con}}$ over $M[\text{Control}^M] \times (\text{Chan} \rightarrow M[\text{Msg}])$ by induction over the following rules:

If $\gamma \in \text{NormalControl} \cdot \text{CallStack}^M$, $\gamma \rightarrow_{\text{seq}} \gamma'$ then
\[ \gamma \parallel \Pi \prec \Gamma \rightarrow_{\text{con}} \gamma' \parallel \Pi \prec \Gamma \quad (11) \]
If $(c ? m) \gamma \in \text{NormalControl} \cdot \text{CallStack}^M$, $m \in \text{Msg}$ then
\[ (c ? m) \gamma \parallel \Pi \prec ([m] \oplus q)^c, \Gamma \rightarrow_{\text{con}} \gamma \parallel \Pi \prec q^c, \Gamma \quad (12) \]
If $X \in N$, $(\nu X) \gamma \in \text{NormalControl} \cdot \text{CallStack}^M$ then
\[ (\nu X) \gamma \parallel \Pi \prec \Gamma \rightarrow_{\text{con}} \gamma \parallel X \parallel \Pi \prec \Gamma \quad (13) \]
If $(c ! m) \gamma \in \text{NormalControl} \cdot \text{CallStack}^M$, $m \in \text{Msg}$ then
\[ (c ! m) \gamma \parallel \Pi \prec q^c, \Gamma \rightarrow_{\text{con}} \gamma \parallel \Pi \prec ([m] \oplus q)^c, \Gamma \quad (14) \]
If $l \gamma \in \text{NormalControl} \cdot \text{CallStack}^M$, $l \in L$ then
\[ l \gamma \parallel \Pi \prec \Gamma \rightarrow_{\text{con}} \gamma \parallel \Pi \prec \Gamma \quad (15) \]
If $MX \gamma \in \text{DelayedControl} \cdot \text{CallStack}^M$, $M \in \text{TermCache}$, $\Gamma'' = \Gamma \oplus \Gamma(M)$, $\Pi' = \Pi \oplus \Pi(M)$ then
\[ MX \gamma \parallel \Pi \prec \Gamma \rightarrow_{\text{con}} X \gamma \parallel \Pi' \prec \Gamma' \quad (16) \]
If $M \gamma \in \text{DelayedControl} \cdot \text{CallStack}^M$, $M \in \text{MixedCache}$, $\Gamma'' = \Gamma \oplus \Gamma(M)$, $\Pi' = \Pi \oplus \Pi(M)$ and $M' = M \mid (N^{\text{com}} \cup L)$ then
\[ M \gamma \parallel \Pi \prec \Gamma \rightarrow_{\text{con}} M' \gamma \parallel \Pi' \prec \Gamma' \quad (17) \]

The alternative semantics precomputes the actions of commutative non-terminals on the call stacks. This is achieved by rule (7) in the alternative sequential semantics. The rules (16) and (17) are the concurrent counterparts; they ensure that the precomputed actions are rendered effective at the appropriate moment. Rule (16) is applicable when the
precomputed cache $M$ contains exclusively commutative actions; such a cache denotes a sequence of commutative non-terminals whose computation terminates and generates concurrency actions. Rule (17), on the other hand, handles the case where the cache $M$ contains non-terminals. An interpretation of such a cache is a partial computation of a sequence of commutative non-terminals. In this case rule (17) dispatches all commutative actions and then blocks. It is necessary to consider this case since not all non-terminals have terminating computations. Thus rule (7) may non-deterministically decide to abandon the pre-computation of actions.

We give a variant of the program-point coverability problem tailored to the alternative semantics and show its equivalence with the program-point coverability problem.

**Verification Problem 2** (Alternative Program-Point Coverability). Given an APCPS $G$ and a set of program point labels $l_1, \ldots, l_n$, a tuple $(P; l_1, \ldots, l_n)$ is a yes-instance of the alternative program-point coverability problem just if there exist a $\rightarrow_{\text{con}}$-reachable configuration $\Pi \triangleleft \Gamma$ such that for every $i \in \{1, \ldots, n\}$ there exists $\lambda_i \gamma_i \in \Pi$ such that either $\lambda_i = l_i$, or $\lambda_i = M_i$ and $l_i \in M_i$?

In the appendix we show that the standard semantics weakly simulates the alternative semantics for APCPS (Proposition 4). Thus for every configuration reachable in the alternative semantics there is a corresponding configuration reachable in the standard semantics. Owing to the nature of precomputations and caches, it is more difficult to relate runs of the standard semantics to those of the alternative semantics. However, in the appendix, we show that for every run in the standard semantics reaching a configuration, there exists a run in the alternative semantics reaching a corresponding configuration (Proposition 2).

**Theorem 1** (Reduction of Program-Point Coverability). A tuple $(P; l_1, \ldots, l_n)$ is a yes-instance of the program-point coverability problem if, and only if, $(P; l_1, \ldots, l_n)$ is a yes-instance of the alternative program-point coverability problem.

### 4 APCPS with Shaped Stacks

In this section we present a natural restriction on the shape of the call stacks of APCPS processes. This shape restriction says that, at all times, at most an a priori fixed number of non-commutative non-terminals may reside in the call stack. Because the restriction does not apply to commutative non-terminals, call stacks can grow to arbitrary heights. We show that the alternative semantics for such shape-constrained APCPS gives rise to a well-structured transition system, thus allowing us to show the decidability of the alternative program-point coverability problem.

**Definition 6.** Define $\text{Reach}_{\rightarrow_{\text{con}}} := \{ \Pi \triangleleft \Gamma \mid [S] \triangleleft \emptyset \rightarrow^*_{\text{con}} \Pi \triangleleft \Gamma \}$. Let $k \in \mathbb{N}$, we say an APCPS $G$ has $k$-shaped stacks just if $\text{Reach}_{\rightarrow_{\text{con}}} \subseteq \text{Config} \leq k$. An APCPS $G$ has shaped stacks just if $G$ has $k$-shaped stacks for some $k \in \mathbb{N}$.

It follows from the definition that, in the alternative semantics, processes of an APCPS with $k$-shaped stacks have the form: $\gamma X_1 M_1 X_2 M_2 \cdots X_j M_j$ where $\gamma \in$
ControlState, \( X_i \in N^{\text{com}} \) and \( j \leq k \). Relating this to the standard semantics, processes of an APCPS with \( k \)-shaped stacks are always of the form \( \alpha \cdot X_1 \beta_1 \cdot X_2 \beta_2 \cdots \cdot X_j \beta_j \) where \( \alpha \in (N \cup (\Sigma \cdot N) \cup (\Sigma \cup \{\varepsilon\})) \cdot N^{\text{com}*} \) and \( \beta_i \in N^{\text{com}*} \). It is this shape that lends itself to the name APCPS. Even though the shaped stacks constraint is semantic, we can give a syntactic sufficient condition: (the simple proof is omitted.)

**Proposition 1.** Let \( G \) be an APCPS. If there is a well-founded partial order \( \geq_{\text{shape}} \) such that for every \( A \in N \) and \( B \in \text{RHS}(A) \cap N^* \): (i) \( A \geq_{\text{shape}} B \), and (ii) \( \exists C \in N^{\text{com}} : A \rightarrow B C \) is a \( G \)-rule \( \Rightarrow A >_{\text{shape}} B \), then \( G \) has shaped stacks.

**Example 3.** Proposition 1 tells us that the program in Figures 1 and 2 can be modelled by an APCPS with shaped stacks. Non-tail recursive calls are potentially problematic. In our example the recursive call to setup_network() in the definition of setup_network() is non-tail recursive, but only places a send action on the call stack, thus causing no harm. The only other non-tail recursive calls occur in do_task: the call to decompose_task poses no threat since decompose_task does not invoke do_task again. The two recursive calls to do_task either place procedure calls with send or no concurrent actions on the stack.

### 4.1 APCPS with Shaped Stacks and Well-Structured Transition Systems

We will now show the decidability of the alternative program-point coverability problem for APCPS with shaped stacks. First we recall the definition of well-structured transition systems [15]. Let \( \leq \) be an ordering over a set \( U \); we say \( \leq \) is a well-quasi-order (wqo) just if for all infinite sequences \( u_1, u_2, \ldots \) there exists \( i, j \) such that \( u_i \leq u_j \).

A well-structured transition system (WSTS) is a quadruple \((S, \rightarrow, \leq, s_0)\) such that \( s_0 \in S \), \( \leq \) is a wqo over \( S \) and \( \rightarrow \subseteq S \times S \) is monotone with respect to \( \leq \), i.e. if \( s \rightarrow s' \) and \( s \leq t \) then there exists \( t' \) such that \( t \rightarrow t' \).

WSTS are an expressive class of infinite state systems that enjoy good model checking properties. A decision problem for WSTS of particular interest to verification is the coverability problem i.e. given a state \( s \) it is the case that \( s_0 \rightarrow^* s' \) and \( s \leq s' \). For \( U \subseteq S \) define the sets \( \text{Pred}(U) := \{ s \mid s \rightarrow u, u \in U \} \) and \( \uparrow U := \{ u' \mid u \leq u', u \in U \} \).

For WSTS the coverability problem is decidable [15] provided that for any given \( S \) the set \( \uparrow \text{Pred}(\uparrow \{ s \}) \) is effectively computable. Wqos can be composed in various ways which makes decision results for WSTS applicable to a wide variety of infinite state models. In the following we recall a few results on the composition of wqos.

(WQO-a) If \( (A_i, \leq_i) \) are wqo sets for \( i = 1, \ldots, k \) then \( (A_1 \times \cdots \times A_k, \leq_1 \times \cdots \times \leq_k) \) is a wqo set. (Dickson’s Lemma)

(WQO-b) If \( A \) is a finite set then \( (A, =) \) is a wqo set.

(WQO-c) If \( (A, \leq) \) is a wqo then \( (M[A], \leq_{M[A]}) \) is a wqo set where \( M_1 \leq_{M[A]} M_2 \) just if for all \( a \in A \) there exists an \( a' \geq a \) such that \( M_1(a) \leq M_2(a') \) [33].

(WQO-d) If \( (A, \leq_A) \) and \( (B, \leq_B) \) are wqo sets, then \( (A \cdot B, \leq_A \cdot \leq_B) \) is a wqo set, where \( \gamma \cdot \gamma' \leq_A \cdot \leq_B \delta \cdot \delta' \) just if \( \gamma \leq_A \delta \) and \( \gamma' \leq_B \delta' \).

(WQO-e) If \( (A, \leq_A) \) and \( (B, \leq_B) \) are wqo set, then \( (A \not\neq B, \leq_A \not\neq \leq_B) \) is a wqo set, where \( a \leq_A \not\neq b \) just if \( a, b \in A \) and \( a \leq_A b \) or \( a, b \in B \) and \( a \leq_B b \).
4.2 A Well-Quasi-Order for the Alternative Semantics

Fix a $k$. Our goal is to construct a well-quasi-order for $\text{Config}^{\leq k}$ as a first step to showing the alternative semantics gives rise to a WSTS for APCPS with shaped stacks.

We order the multi-sets $\text{TermCache}$, $\text{NonTermCache}$, $\text{MixedCache}$ and $\text{Queue}$ with the multi-set inclusion $\leq_M$ which is a well-quasi-order. Since $\text{Chan}$ is a finite set and $\text{Queues} = \text{Chan} \rightarrow M[\text{Msg}] \cong M[\text{Msg}]^{\text{Chan}}$ we obtain a well-quasi-order for $\text{Chan} \rightarrow M[\text{Msg}]$ using a generalisation of Dickson’s lemma. We then compose the wqo of $\text{TermCache}$ and $\text{MixedCache}$ to obtain a wqo $\leq_{\text{Cache}} := \leq_{\text{TermCache}} \sqcup \leq_{\text{MixedCache}}$ for $\text{Cache}$. For each $j \in \{1 \ldots k\}$ we define

$$X_1 M_1 X_2 M_2 \cdots X_j M_j \leq X_1 M'_1 X_2 M'_2 \cdots X_j M'_j \quad \text{iff} \quad \forall i : M_i \leq_{\text{Cache}} M'_i$$

which gives a well-quasi-order for $\text{CallStack}^{\leq k}$. We obtain a wqo for $\text{DelayedControl}$ by composing the wqos of $\text{TermCache}$, $\text{NonTermCache}$ and $\text{MixedCache}$:

$$\leq_{\text{DelayedControl}} := \leq_{\text{TermCache}} \cup \leq_{\text{NonTermCache}} \cup \leq_{\text{MixedCache}}.$$

Since $\Sigma$ and $\mathcal{N}$ are finite sets, $(\Sigma, =_{\Sigma})$ and $(\mathcal{N}, =_{\mathcal{N}})$ are wqo sets, and so, we can compose a wqo for $\text{NormalControl}$:

$$\leq_{\text{NormalControl}} := (\Sigma \cdot =_{\text{Cache}}) \cup (\Sigma \cdot =_{\mathcal{N}} \cdot =_{\text{Cache}}) \cup (\mathcal{N} \cdot =_{\text{Cache}}).$$

Similarly we can construct wqos for $\text{ControlState}$ and $\text{Control}^{\leq k}$ by composition:

$$\leq_{\text{ControlState}} := \leq_{\text{NormalControl}} \cup \leq_{\text{DelayedControl}}$$

$$\leq_{\text{Control}^{\leq k}} := \leq_{\text{ControlState}} \cdot \leq_{\text{CallStack}^{\leq k}}.$$

As a last step we use (WQO-c) to construct a wqo for $M \left[ \text{Control}^{\leq k} \right]$ which then allows us to define a wqo for $\text{Config}^{\leq k}$ by $\leq_{\text{Config}^{\leq k}} := \leq_M[\text{Control}^{\leq k}] \times \leq_{\text{Queues}}$.

To prove the decidability of the coverability problem for APCPS with shaped stacks, it remains to show that $\rightarrow_{\text{conv}}$ is monotonic and $\uparrow \text{Pred}(\uparrow \{\gamma\})$ is computable.

**Lemma 1 (Monotonicity).** The transition relation $\rightarrow_{\text{conv}}$ is monotone with respect to the well-order $\leq_{\text{Config}^{\leq k}}$.

**Corollary 1.** The transition system $\left(\text{Config}^{\leq k}, \rightarrow_{\text{conv}}, \leq_{\text{Config}^{\leq k}}\right)$ is a well-structured transition system.

To see that $\uparrow \text{Pred}(\uparrow \{\gamma\})$ is computable is mostly trivial; only predecessors generated by rule (7) are not immediately obvious. Given $M' \in \text{Cache}$ we observe that it is enough to be able to compute the set $P_{M'} := \uparrow \{(C, M) \mid C \in \mathcal{N}_{\text{com}}, C \rightarrow^* w, M'' = M \oplus M(w), M' \leq_M M''\}$. Now $C \rightarrow^* w$ is a computation of a commutative context-free grammar (CCFGs) for which an encoding into Petri nets has been shown by Ganty and Majumdar [17]. Their encoding builds on work by Esparza [11] modelling CCFG in Petri nets. Their translation leverages a recent result [14]: every word of a CCFG has a bounded-index derivation i.e. every term of the derivation...
uses no more than an *a priori* fixed number of occurrences of non-terminals. A budget counter constrains the Petri net encoding of a CCFG to respect boundedness of index; termination of a CCFG computation can be detected by a transition that is only enabled when the full budget is available. This result allows us to compute the set $P_{M'}$ using a backwards coverability algorithm for Petri nets.

**Theorem 2.** The alternative program-point coverability problem, and hence the program-point coverability problem, for APCPS with $k$-shaped stacks are decidable for every $k \geq 0$.

## 5 Related Work and Discussion

**Partially Commutative Context-Free Grammars (PCCFG).** Czerwinski et al. introduced PCCFG as a study in process algebra [7]. They proved that bisimulation is NP-complete for a class of processes extending BPA and BPP [11] where the sequential composition of certain processes is commutative. Bisimulation is defined on the traces of such processes, although there is no synchronisation between processes. In [8] the problem of word reachability for partially commutative context-free languages was shown to be NP-complete.

**Asynchronous Procedure Calls.** Petri net models for finite state machines that communicate asynchronously via unordered message buffers were first investigated by Mukund et al. [27, 28]. In an influential paper [30] in 2006, Sen and Viswanathan showed that safety verification is decidable for first-order programs with atomic asynchronous methods. Building on this, Jhala and Majumdar [22] constructed a VAS that models such asynchronous programs on-the-fly. Liveness properties, such as fair termination and starvation, of asynchronous programs were extensively studied by Ganty et al. in [18, 17]. In our more general APCPS framework, we may view the asynchronous programs considered by Ganty and Majumdar in [17] as APCPS running a single “scheduler” process. Task bags can be modelled as channels in our setting and the posting of a task can be modelled by sending a message; the scheduling of a procedure call can be simulated as a receive of a non-deterministically selected channel which unlocks a commutative procedure call defined by rules of types (i) and (ii) and rules of type (iii) where $C \in N^{\text{com}}$, in the sense of Definition 2. It is thus easy to see that APCPS with shaped stacks subsume programs with asynchronous procedure calls. In light of the fact that their safety verification is ExpSpace-complete we can infer that the program-point coverability problem for APCPS with shaped stacks is ExpSpace-hard.

Various extensions of Sen and Viswanathan’s model [6] and applications to real-world asynchronous task scheduling systems [19] have been investigated. From the standpoint of message-passing concurrency, a key restriction of many of the models considered is that messages may only be retrieved by a communicating pushdown process when its stack is empty. The aim of this paper is to relax this restriction while retaining decidability of safety verification.
Communicating Pushdown Systems. The literature on communicating pushdown systems is vast. Numerous classes with decidable verification problems have been discovered. Heußner et al. [21] studied a restriction on pushdown processes that communicate asynchronously via FIFO channels: a process may send a message only when its stack is empty, while message retrieval is unconstrained. Several other communicating pushdown systems have been explored: parallel flow graph systems [13], visibly pushdown automata that communicate over FIFO-queues [1], pushdown systems communicating over locks [23], and recursive programs with hierarchical communication [4, 2].

Verification techniques that over-approximate correctness properties of concurrent pushdown systems have been studied [16, 20]. Under-approximation techniques typically impose constraints, such as bounding the number of context switches [32, 25], bounding the number of times a process can switch from a send-mode to receive-mode [3], or allowing symbols pushed onto the stack to be popped only within a bounded number of context switches [31]. Another line of work focuses on pushdown systems that communicate synchronously over channels, restricting model checking to synchronisation traces that fall within a restricted regular language [12]; this approach has been developed into an effective CEGAR method [26].

Future Directions and Conclusion. We have introduced a new class of asynchronously communicating pushdown systems, APCPS, and shown that the program-point cov erability problem is decidable and EXPSPACE-hard for the subclass of APCPS with shaped stacks. We plan to investigate the precise complexity of the program-point coverability problem, construct an implementation and integrate it into SOTER [9, 10], a safety verifier for Erlang programs, to study APCPS empirically.

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A  Proof of Theorem 1

A.1  Direction: ⇐

We lift define a function $M(\cdot)$ over sequences $\mathcal{N}^\text{com}^* (\mathcal{N}^\text{com}^\text{com}^*)^*$ in the following way:

$$M(C_1 \cdots C_n) = \left\{ \bigoplus_{i=1}^{n} M(w_i) \mid C_i \to_{\text{seq}}^* w_i, w_i \in (\Sigma \cup \mathcal{N})^*/\sim_l \right\}$$

$$M(C_1 \cdots C_n Z\alpha) = M(C_1 \cdots C_n) \cdot Z \cdot M(\alpha)$$

$$M(X\alpha) = X \cdot M(\alpha)$$

$$M(\alpha) = a \cdot M(\alpha)$$

Let $U, V \subseteq \text{Control}^M$, we define $U \to_{\text{seq'}} V$ just if for all $\gamma' \in V$ there exists a $\gamma \in U$ such that $\gamma \to_{\text{seq'}} \gamma'$.

**Lemma 2.** If $\alpha \to_{\text{seq}} \beta$ such that $\alpha \in \mathcal{N}^*$ then $M(\alpha) \to_{\text{seq'}} M(\beta)$.

**Proof.** Since $\alpha \to_{\text{seq}} \beta$ we have $\alpha = X\alpha_0$ and $\beta = \alpha_1 \alpha_0$ such that $X \to \alpha_1$. And so $M(\alpha) = XM(\alpha_0)$. We will proceed by case analysis on $X \to \alpha_1$.

- $X \rightarrow a, a \in \Sigma \cup \{\epsilon\}$.
  Take $a \delta \in a \cdot M(\alpha_0) = M(a\alpha_0) = M(\beta)$. Then $X\delta \in M(\alpha)$ and $X\delta \to_{\text{seq'}} a\delta$.
  Hence $M(\alpha) \rightarrow_{\text{seq'}} M(\beta)$.

- $X \rightarrow aA, a \in \Sigma$.
  Take $aA\delta \in a \cdot A \cdot M(\alpha_0) = M(aA\alpha_0) = M(\beta)$, then since $X\delta \in M(\alpha)$ and $X\delta \to_{\text{seq'}} aA\delta$. Hence $M(\alpha) \rightarrow_{\text{seq'}} M(\beta)$.

- $X \rightarrow AB, B \in \mathcal{N}^\text{com}$.
  Suppose $AB\delta \in A \cdot B \cdot M(\alpha_0) = M(\alpha_1 \alpha_0) = M(\beta)$, then $X\delta \in M(\alpha)$ and $X\delta \to_{\text{seq'}} AB\delta$. Hence $M(\alpha) \rightarrow_{\text{seq'}} M(\beta)$.

- $X \rightarrow AB, B \in \mathcal{N}^\text{com}$.
  Suppose $AM'\delta \in M(AB\alpha_0) = M(\alpha_1 \alpha_0) = M(\beta)$. Then clearly $M' = M(w) \oplus M$ such that $B \to_{\text{seq'}}^* w$ and $M\delta \in M(\alpha_0)$. Now then $XM\delta \in M(\alpha)$ and $XM\delta \to_{\text{seq'}} A(M(w) \oplus M)\delta$. Thus $M(\alpha) \rightarrow_{\text{seq'}} M(\beta)$.

\[\square\]

For $U, V \subseteq M[\text{Control}^M]$ define

$$U \parallel V := \{ \Pi_0 \parallel \Pi_1 \mid \Pi_0 \in U, \Pi_1 \in V \}$$

Further we define

$$M(\Pi \parallel \Pi') := M(\Pi) \parallel M(\Pi')$$

We say that for $U, V \subseteq M[\text{Control}^M]$ $U \ll \Gamma \to_{\text{con}} V \ll \Gamma'$, just if for all $\Pi' \in V$ there exists $\Pi \in U$ such that $\Pi \ll \Gamma \to_{\text{con}} \Pi' \ll \Gamma'$. Note this means that if $M(\alpha) \rightarrow_{\text{seq'}} M(\beta)$ then clearly $M(\alpha \parallel \Pi) \ll \Gamma \to_{\text{con}} M(\beta \parallel \Pi) \ll \Gamma$ for all $\Pi, \Gamma$. 
Lemma 3.  1. If \( a \in \Sigma^\text{com} \) and \( a\alpha' \parallel \Pi \parallel \Gamma \rightarrow \text{con} \alpha\alpha' \parallel \Pi \parallel \Pi(a) \parallel \Gamma \parallel \Gamma(a) \) where \( \alpha \in (\mathcal{N} \cup \{\epsilon\})\mathcal{N}^\text{com}* \), \( \alpha' \in (\mathcal{N}^\text{com}\mathcal{N}^\text{com})* \), then
\[
\mathcal{M}(a\alpha' \parallel \Pi) \parallel \Gamma \rightarrow \text{con'} \mathcal{M}(\alpha\alpha' \parallel \Pi \parallel \Pi(a)) \parallel \Gamma \parallel \Gamma(a).
\]
2. If \( (c \div m)\alpha\alpha' \parallel \Pi \parallel \Gamma \rightarrow \text{con} \alpha\alpha' \parallel \Pi \parallel \Pi(c \div m) \rightarrow \text{con} \alpha\alpha' \parallel \Pi \parallel \Pi^\prime \) where \( \alpha \in (\mathcal{N} \cup \{\epsilon\})\mathcal{N}^\text{com}*, \) \( \alpha' \in (\mathcal{N}^\text{com}\mathcal{N}^\text{com})* \), then
\[
\mathcal{M}((c \div m)\alpha\alpha' \parallel \Pi) \parallel \Gamma \parallel \Gamma(c \div m) \rightarrow \text{con'} \mathcal{M}(\alpha\alpha' \parallel \Pi \parallel \Pi^\prime) \rightarrow \Gamma.
\]

Claim 1. We show that \( \mathcal{M}(a\alpha' \parallel \Pi) \parallel \Gamma \rightarrow \text{con'} \mathcal{M}(\alpha\alpha' \parallel \Pi \parallel \Pi^\prime) \rightarrow \Gamma' \) by case analysis on \( a \):

- \( a = c \div m \)
  Then \( \Pi \parallel \Pi(a) = \Pi \)
  Take \( \gamma \parallel \pi \in \mathcal{M}(\alpha\alpha' \parallel \Pi) \) then \( (c \div m)\gamma \parallel \pi \in \mathcal{M}((c \div m)\alpha\alpha' \parallel \Pi) \). Using rule 14 we see that
  \[
  \gamma \parallel \pi \rightarrow \text{con'} \gamma \parallel \pi \rightarrow \Gamma \parallel \Gamma(c \div m).
  \]
  Hence we conclude \( \mathcal{M}((c \div m)\alpha\alpha' \parallel \Pi \parallel \Pi^\prime) \rightarrow \Gamma \parallel \Gamma(c \div m) \).

- \( a = \nu X \)
  Then \( \Pi \parallel \Pi(\nu X) = \Pi \parallel X, \Gamma = \Gamma \)
  Take \( \gamma \parallel X \parallel \pi \in \mathcal{M}(\alpha\alpha' \parallel \Pi \parallel \Pi^\prime) = \mathcal{M}(\alpha\alpha' \parallel \Pi \parallel \Pi(\nu X)) \) then \( (\nu X)\gamma \parallel \pi \in \mathcal{M}((\nu X)\alpha\alpha' \parallel \Pi \parallel \Pi^\prime) \). Using rule 13 we see that
  \[
  (\nu X)\gamma \parallel \pi \rightarrow \text{con'} \gamma \parallel X \parallel \pi \rightarrow \Gamma.
  \]
  Hence we conclude \( \mathcal{M}((\nu X)\alpha\alpha' \parallel \Pi \parallel \Pi^\prime) \rightarrow \Gamma \rightarrow \text{con'} \mathcal{M}(\alpha\alpha' \parallel \Pi \parallel \Pi(\nu X)) \rightarrow \Gamma \).

- \( a = l \)
  Then \( \Pi \parallel \Pi(l) = \Pi, \Gamma \parallel \Gamma(l) = \Gamma \). Take \( \gamma \parallel \pi \in \mathcal{M}(\alpha\alpha' \parallel \Pi \parallel \Pi) \) then \( l\gamma \parallel \pi \in \mathcal{M}(l\alpha\alpha' \parallel \Pi \parallel \Pi) \). Using rule 15 we see that
  \[
  l\gamma \parallel \pi \rightarrow \text{con'} \gamma \parallel \pi \rightarrow \Gamma.
  \]
  Hence we conclude
  \( \mathcal{M}(l\alpha\alpha' \parallel \Pi \parallel \Pi) \rightarrow \Gamma \rightarrow \text{con'} \mathcal{M}(\alpha\alpha' \parallel \Pi \parallel \Pi) \rightarrow \Gamma \).

Claim 2. Take \( \gamma \parallel \pi \in \mathcal{M}(\alpha\alpha' \parallel \Pi \parallel \Pi) = \mathcal{M}(\alpha\alpha' \parallel \Pi \parallel \Pi) \) then \( (c \div m)\gamma \parallel \pi \in \mathcal{M}((c \div m)\alpha\alpha' \parallel \Pi) \)

Then using rule 12 we see that
\[
(c \div m)\gamma \parallel \pi \rightarrow \Gamma \parallel \Gamma(c \div m) \rightarrow \text{con'} \gamma \parallel \pi \rightarrow \Gamma.
\]
Hence we conclude \( \mathcal{M}((c \div m)\alpha\alpha' \parallel \Pi \parallel \Pi) \rightarrow \Gamma \rightarrow \text{con'} \mathcal{M}(\alpha\alpha' \parallel \Pi \parallel \Pi) \rightarrow \Gamma \).

\( \square \)
Lemma 4. 
If \( a_1 \cdots a_n \in \Sigma_{\text{com}}^* \), \( \alpha_i \in \mathcal{N}_{\text{com}}^* \) and \( \alpha' \in (\mathcal{N}_{\text{com}} \mathcal{N}_{\text{com}})^* \),

\[
\alpha_1 \alpha' \parallel \Pi(e) \triangleleft \Gamma(e) \rightarrow^*_{\text{con}} \alpha_2 \alpha' \parallel \Pi(e) \triangleleft \Gamma(e) \\
\rightarrow^*_{\text{con}} \alpha_2 \alpha' \parallel \Pi(a_1) \triangleleft \Gamma(a_1) \\
\rightarrow^*_{\text{con}} \cdots \rightarrow^*_{\text{con}} \\
a_\alpha \alpha_{n+1} \alpha' \parallel \Pi(a_1 \cdots a_{n-1}) \triangleleft \Gamma(a_1 \cdots a_{n-1}) \\
\rightarrow^*_{\text{con}} \alpha_{n+1} \alpha' \parallel \Pi(a_1 \cdots a_n) \triangleleft \Gamma(a_1 \cdots a_n)
\]

then
\[
M(\alpha_1 \alpha' \parallel \Pi(e)) \triangleleft \Gamma(e) \rightarrow^*_{\text{con}} M(\alpha_{n+1} \alpha' \parallel \Pi(a_1 \cdots a_n)) \triangleleft \Gamma(a_1 \cdots a_n).
\]

Proof. We prove the claim by induction on \( n \). For \( n = 0 \) the claim is vacuously true.

For \( n = k + 1 \), assuming the claim holds for \( k \) it is enough to show that if

\[
\alpha_{k+1} \alpha' \parallel \Pi(a_1 \cdots a_k) \triangleleft \Gamma(a_1 \cdots a_k) \rightarrow^*_{\text{con}} \alpha_{k+1} \alpha_{k+2} \alpha' \parallel \Pi(a_1 \cdots a_k) \triangleleft \Gamma(a_1 \cdots a_k)
\]

then

\[
M(\alpha_{k+1} \alpha' \parallel \Pi(a_1 \cdots a_k)) \triangleleft \Gamma(a_1 \cdots a_k) \rightarrow^*_{\text{con}} M(\alpha_{k+2} \alpha' \parallel \Pi(a_1 \cdots a_k+1)) \triangleleft \Gamma(a_1 \cdots a_k+1)
\]

which we obtain by repeatedly applying Lemma 2 and then Lemma 3.

Lemma 5. If \( \alpha \alpha' \parallel \Pi \triangleleft \Gamma \rightarrow^*_{\text{con}} \alpha \alpha' \parallel \Pi' \triangleleft \Gamma' \rightarrow^*_{\text{con}} \alpha' \parallel \Pi'' \triangleleft \Gamma'' \) where \( a \in \Sigma, \alpha \in \mathcal{N}_{\text{com}}^*, \alpha' \in (\mathcal{N}_{\text{com}} \mathcal{N}_{\text{com}})^*, \alpha \rightarrow^*_{\text{seq}} w \in \Sigma^* \mathcal{N} \mathcal{N} \mathcal{N}, \Pi'' = \Pi' + \Pi(w), \Gamma'' = \Gamma' + \Gamma(w) \)

then

\[
M(\alpha \alpha' \parallel \Pi) \triangleleft \Gamma \rightarrow^*_{\text{con}} M(\alpha \alpha' \parallel \Pi') \triangleleft \Gamma' \rightarrow^*_{\text{con}} M(\alpha' \parallel \Pi'') \triangleleft \Gamma''.
\]

Proof. For \( M(\alpha \alpha' \parallel \Pi) \triangleleft \Gamma \rightarrow^*_{\text{con}} M(\alpha \alpha' \parallel \Pi') \triangleleft \Gamma' \) we appeal to Lemma 3.

Now since \( \alpha \rightarrow^*_{\text{seq}} w \) we have \( \alpha \alpha' \parallel \Pi' \triangleleft \Gamma' \rightarrow^*_{\text{con}} \alpha' \parallel \Pi' + \Pi(w) \triangleleft \Gamma' + \Gamma(w) \).

Let \( M = M(w) \) and take \( \gamma \parallel \pi' + \Pi(M) \triangleleft \Gamma' + \Gamma(M) \in M(\alpha' \parallel \Pi' + \Pi(w)) \triangleleft \Gamma' + \Gamma(w) \)

Then note \( M \gamma \parallel \pi' \in M(\alpha \alpha' \parallel \Pi') \) and using rule 16

\[
M \gamma \parallel \pi' \triangleleft \Gamma' \rightarrow^*_{\text{con}} \gamma \parallel \pi' + \Pi(M) \triangleleft \Gamma' + \Gamma(M).
\]

Lemma 6. If \( \alpha \alpha' \parallel \Pi \triangleleft \Gamma \rightarrow^*_{\text{con}} \alpha \alpha' \parallel \Pi' \triangleleft \Gamma' \rightarrow^*_{\text{con}} w \alpha' \parallel \Pi' \triangleleft \Gamma' \rightarrow^*_{\text{con}} w' \alpha' \parallel \Pi'' \triangleleft \Gamma'' \) where \( a \in \Sigma, \alpha \in \mathcal{N}_{\text{com}}^*, \alpha' \in (\mathcal{N}_{\text{com}} \mathcal{N}_{\text{com}})^*, \alpha \rightarrow^*_{\text{seq}} w \in (\Sigma \mathcal{N})^* \mathcal{N} \mathcal{N} \mathcal{N}, w' \in \mathcal{N}^* \mathcal{N} \mathcal{N}, \Pi'' = \Pi' + \Pi(w), \Gamma'' = \Gamma' + \Gamma(w) \) then

\[
M(\alpha \alpha' \parallel \Pi) \triangleleft \Gamma \rightarrow^*_{\text{con}} M(\alpha \alpha' \parallel \Pi') \triangleleft \Gamma' \rightarrow^*_{\text{con}} M(w') \cdot M(\alpha') \parallel M(\Pi'') \triangleleft \Gamma''.
\]
Proof. For \( \mathcal{M}[\alpha \alpha^\prime \parallel \Pi] \prec \Gamma \rightarrow \) we appeal to Lemma 3. Now since \( \alpha \rightarrow^* \omega \) we have \( \alpha \alpha^\prime \parallel \Pi^\prime \prec \Gamma^\prime \rightarrow \omega^\prime \alpha^\prime \parallel \Pi^\prime \oplus \Pi \prec \Gamma^\prime \oplus \Gamma \).

Let \( M' = M(\omega') \) then

\[
M' \gamma \parallel \pi' \oplus \Pi(M) \prec \Gamma' \oplus \Gamma(M) \in \mathcal{M}(\omega') \cdot \mathcal{M}[\alpha \alpha^\prime \parallel \Pi^\prime] \prec \Gamma' \oplus \Gamma \] and \( M \gamma \parallel \pi' \in \mathcal{M}[\alpha \alpha^\prime \parallel \Pi^\prime] \) such that \( M = M(\omega) \). Using rule 17

\[
M \gamma \parallel \pi' \prec \Gamma' \rightarrow \) \( M' \gamma \parallel \pi' \oplus \Pi(M) \prec \Gamma' \oplus \Gamma(M) \).
\]

\[ \square \]

Lemma 7. Let \( X \in \mathcal{N}, \alpha \in (\mathcal{N}^{\omega \alpha \alpha^\prime}^\ast, w \in \Sigma^\omega \text{ and } \beta, \alpha^\prime \in \mathcal{N}^{\omega \alpha \alpha^\prime}^\ast \)

1. If \( \alpha \alpha^\prime \parallel \Pi(\omega) \prec \Gamma(\omega) \text{ then } \mathcal{M}[\alpha \alpha^\prime \parallel \Pi(\omega)] \prec \Gamma(\omega) \text{ and clearly also } \mathcal{M}[\alpha \alpha^\prime \parallel \Pi(\omega)] \prec \Gamma(\omega) \).

2. If \( \alpha \alpha^\prime \parallel \Pi(\omega) \prec \Gamma(\omega) \text{ then } \mathcal{M}[\alpha \alpha^\prime \parallel \Pi(\omega)] \prec \Gamma(\omega) \text{ and clearly also } \mathcal{M}[\alpha \alpha^\prime \parallel \Pi(\omega)] \prec \Gamma(\omega) \).

\[ \square \]

Lemma 8. Let \( X \in \mathcal{N}^{\omega \alpha \alpha^\prime}, \beta, \beta^\prime \in \mathcal{N}^{\omega \alpha \alpha^\prime} \text{ and } \alpha, \alpha^\prime \in (\mathcal{N}^{\omega \alpha \alpha^\prime}^\ast, w \in \Sigma^\omega \text{ and } \beta, \alpha^\prime \in \mathcal{N}^{\omega \alpha \alpha^\prime}^\ast \).

1. If \( \alpha \alpha^\prime \parallel \Pi(\omega) \prec \Gamma(\omega) \text{ then } \mathcal{M}[\alpha \alpha^\prime \parallel \Pi(\omega)] \prec \Gamma(\omega) \text{ and clearly also } \mathcal{M}[\alpha \alpha^\prime \parallel \Pi(\omega)] \prec \Gamma(\omega) \).

2. If \( \alpha \alpha^\prime \parallel \Pi(\omega) \prec \Gamma(\omega) \text{ then } \mathcal{M}[\alpha \alpha^\prime \parallel \Pi(\omega)] \prec \Gamma(\omega) \text{ and clearly also } \mathcal{M}[\alpha \alpha^\prime \parallel \Pi(\omega)] \prec \Gamma(\omega) \).

Claim 1. Then \( X \beta \alpha \parallel \Gamma(\omega) \rightarrow \) such that \( X \rightarrow^* X' \alpha \alpha^\prime \parallel \Pi(\omega) \prec \Gamma(\omega) \).

\[
\mathcal{M}[X \beta \alpha \parallel \Pi(\omega)] \prec \Gamma(\omega)
\]

Then the proof of Lemma 7 Claim 1 applies to give the result.

\[ \square \]

Claim 2. Then

\[
X \beta \alpha \parallel \Gamma(\omega) \rightarrow \) such that \( X' \rightarrow^* X' \beta \alpha^\prime \parallel \Pi(\omega) \prec \Gamma(\omega) \) such that \( X'' \rightarrow^* X'' \beta \alpha^\prime \parallel \Pi(\omega) \prec \Gamma(\omega) \) such that \( X' \rightarrow^* X' \beta \alpha^\prime \parallel \Pi(\omega) \prec \Gamma(\omega) \) such that \( X'' \rightarrow^* X'' \beta \alpha^\prime \parallel \Pi(\omega) \prec \Gamma(\omega) \).

\[ \square \]
so by Lemma 4 and Lemma 2.
\[ M(X \beta \alpha) \prec \Gamma(\epsilon) \rightarrow^{* \text{con}} \alpha \parallel \Pi'(w) \prec \Gamma(w). \]

For \( \alpha_1, \ldots, \alpha_m \in \mathcal{N}^{\text{com}} \) and \( Z_1, \ldots, Z_{m-1} \in \mathcal{N}^{\text{com}} \) define:
\[ \overline{M}(\alpha_1 Z_1 \cdots \alpha_{m-1} Z_{m-1} \alpha_m) := \overline{M}(\alpha_1) Z_1 \cdots \alpha_{m-1} \overline{M}(\alpha_m) \]
\[ \overline{M}(\Pi \parallel \Pi') := \overline{M}(\Pi) \parallel \overline{M}(\Pi') \]

Proposition 2. If \( S \prec \Gamma(\epsilon) \rightarrow^{* \text{con}} \Pi' \prec \Gamma' \) then \( \overline{M}(S) \prec \Gamma(\epsilon) \rightarrow^{* \text{con}} \overline{M}(\Pi') \prec \Gamma' \)

Proof. Let \( \Pi^f \in \mathcal{M}[\text{Control}] \) and define the set \( P_{\Pi^f} = \{ \alpha \mid \exists \Pi^f \alpha \parallel \Pi = \Pi^f \} \).
Further define the set of configurations \( P := \{ \Pi \prec \Gamma \mid \forall \alpha \in \Pi, \alpha \in \mathcal{N}^{\text{com}} \} \)
\[ \mathcal{N}^{\text{com}}(\mathcal{N}^{\text{com}})^{*} \cup \mathcal{N} \cup P_{\Pi^f} \].

Now suppose that for some \( \Pi, \Pi' \) and \( \Gamma, \Gamma' \)
\[ \Pi \prec \Gamma := \Pi_0 \prec \Pi_1 \prec \Pi_2 \prec \cdots \prec \Pi_n \prec \Gamma_n =: \Pi' \prec \Gamma' \]
such that \( \Pi_i \prec \Gamma_i \in P \) for \( i = 0, \ldots, n \). Without loss of generality we can assume that
for \( i = 0, \ldots, n, \Pi_i = \Pi^{f_i} \parallel \Pi^{f_i} \) such that for all \( \alpha \in \Pi^{f_i} \) we have \( \alpha \in P_{\Pi^f} \)
and \( \alpha \) is not involved in any transitions in \( \Pi_i \prec \Gamma_i \rightarrow^{* \text{con}} \Pi_n \prec \Gamma_n \). Note that we are not
loosing generality, since a reduction \( \alpha \parallel \Pi \rightarrow^{* \text{con}} \alpha \parallel \Pi' \) can either be pre-empted
or goes through a process state in \( \mathcal{N}^{\text{com}}(\mathcal{N}^{\text{com}})^{*} \). Note this also means
that \( \Pi^{f_{i+1}} = \Pi^{f_i} \parallel \Pi^{f_{i+1}} \). We further assume w.l.o.g that for each \( i \) it is the case that
\( \Pi^{f_i} = \alpha \parallel \Pi^{f_i} \) and \( \Pi^{f_{i+1}} = \alpha' \parallel \Pi^{f_{i+1}} \parallel \Pi(w) \) and \( \Gamma_{k+1} \parallel \Gamma(w') = \Gamma_k \parallel \Gamma(w) \)
for some \( w \in \mathcal{N}^{\text{com}} \) and \( w' \in \{ \epsilon \} \). During each \( \Pi^{f_i} \prec \Gamma_i \rightarrow^{* \text{con}} \Pi^{f_{i+1}} \prec \Gamma_{i+1} \)
only one process makes progress (note this can be achieved by delaying receptions
and performing sends and spawns as early as possible) and none of the intermediate steps
are configurations of \( P \).

We will prove by induction on \( n \):
\[ \overline{M}(\Pi_0^n) \parallel \overline{H}_0^n \prec \Gamma_0^n \rightarrow^{* \text{con}} \overline{M}(\Pi_1^n) \parallel \overline{H}_1^n \prec \Gamma_1^n \rightarrow^{* \text{con}} \cdots \rightarrow^{* \text{con}} \overline{M}(\Pi_n^n) \parallel \overline{H}_n^n \prec \Gamma_n^n \]
where for all \( i = 0, \ldots, n \) and \( \alpha \in \Pi^{f_i} \) we have either \( \Pi^{f_i}(\alpha) = \overline{H}_i(\overline{M}(\alpha)) \) or
\( \Pi^{f_i}(\alpha) = \overline{H}_i(\overline{M}(\alpha_0) \cdot \overline{M}(\alpha_1)), \alpha = \alpha_0 \alpha_1 \).

- \( n = 0 \).
- The claim holds trivially.
- \( n = k + 1 \), assuming the claim holds for \( k \).

To prove the inductive claim we need to show that from \( \Pi_k = \alpha \parallel \Pi_k^{f_k} \), \( \Pi_{k+1} = \alpha' \parallel \Pi_{k+1}^{f_k} \parallel \Pi(w) \) and \( \Gamma_{k+1} \parallel \Gamma(w') = \Gamma_k \parallel \Gamma(w) \) where \( \Pi_k \prec \Gamma_k \rightarrow^{* \text{con}} \Pi_{k+1} \prec \Gamma_{k+1} \),
we can infer \( \overline{M}(\Pi_k^n) \prec \Gamma_k \rightarrow^{* \text{con}} \overline{M}(\Pi_{k+1}^{f_k}) \prec \Gamma_{k+1} \). We will do so by a case analysis
on the shape of \( \alpha \) and \( \alpha' \).

* \( \alpha, \alpha' \in (\mathcal{N}^{\text{com}}(\mathcal{N}^{\text{com}})^{*}) \)
  Then \( \alpha = X \alpha_0 \alpha_1, X \in \mathcal{N}^{\text{com}} \), \( \alpha_0 \in \mathcal{N}^{\text{com}} \), \( \alpha_1 \in (\mathcal{N}^{\text{com}}(\mathcal{N}^{\text{com}})^{*}) \) and \( \alpha' = \alpha_0' \alpha_1 \) where \( \alpha_0' \in \epsilon \cup (\mathcal{N}^{\text{com}}(\mathcal{N}^{\text{com}})^{*}) \), i.e. either we increase the call-stack
or we pop one non-commutative non-terminal off the call-stack. Otherwise
we would end up either in an intermediate configuration in \( P \) or in a different case.
* Case $\alpha_0 = \epsilon$.

Then $X\alpha_0\alpha_1 \parallel \Pi_1 < \Gamma_k \rightarrow^*_\com X'\alpha_2\alpha_0\alpha_1 \parallel \Pi_4 < \Gamma_k$ such that $X' \in N^{\com}$, $\alpha_2 \in N^{\com^*}$ and $X'\alpha_2\alpha_0\alpha_1 \parallel \Pi_1' < \Gamma_k$ $\rightarrow^*_\com X'\alpha_2\alpha_0\alpha_1 \parallel \Pi_4' < \Gamma_k$ $\rightarrow^*_\com \alpha_1 \parallel \Pi_1' \oplus \Pi(w) < \Gamma_k \oplus \Gamma(w)$, where $w \in \Sigma^{\com^*}$ such that $\Pi_{k+1} = \alpha' \parallel \Pi_1' \oplus \Pi(w)$ and $\Gamma_{k+1} = \Gamma_k \oplus \Gamma(w)$. Lemma 3 then allows us to conclude that $\mathcal{M}(X\alpha_0\alpha_1 \parallel \Pi_1') < \Gamma_k \rightarrow^*_\com \mathcal{M}(X'\alpha_2\alpha_0\alpha_1 \parallel \Pi_4') < \Gamma_k$ and Lemma 7.1 gives us $\mathcal{M}(X'\alpha_2\alpha_0\alpha_1 \parallel \Pi_4') < \Gamma_k \rightarrow^*_\com \mathcal{M}(\alpha_1 \parallel \Pi_1' \oplus \Pi(w)) < \Gamma_k \oplus \Gamma(w) = \Pi_{k+1} < \Gamma_{k+1}$.

* Case $\alpha_0' \neq \epsilon$.

Follows directly from Lemma 8.1

- $\alpha \in \Sigma^{com}N^{com^*}(N^{com}N^{com^*})^*$ and $\alpha' \in (N^{com}N^{com^*})^*$

Follows from Lemma 5.

- $\alpha \in (N^{com}N^{com^*})^*$ and $\alpha' \in \Sigma^{com}N^{com^*}(N^{com}N^{com^*})^*$

Follows from Lemma 8.2

- $\alpha \in \Sigma$ and $\alpha' \in (N^{com}N^{com^*})^*$

We can assume that $\alpha \in N^{com}$ since otherwise a case above already applies. By the definition of $N^{com}$ we can thus infer that $\alpha' = \epsilon$ since otherwise $\alpha$ would not be commutative. Thus Lemma 7.1 applies.

- $\alpha \in \Sigma$ and $\alpha' \in \Sigma^{com}N^{com^*}(N^{com}N^{com^*})^*$

There is nothing to prove for this case as, similarly to the case above, either $\alpha \in N^{com}$ and so a case above applies or $\alpha \in N^{com}$ but then $\alpha' \notin \Sigma^{com}N^{com^*}(N^{com}N^{com^*})^*$ which is impossible; so the former must be the case.

- $\alpha \in (N^{com}N^{com^*})^*$ and $\alpha' \in P_{\Pi_j}$

If $\alpha' \in (N^{com}N^{com^*})^* \cup \Sigma^{com}N^{com^*}(N^{com}N^{com^*})^*$ the above cases apply. Otherwise it must be the case that $\alpha' \in \Sigma^{com^*}(N^{com}N^{com^*})^* \cup \Sigma^{com}N^{com^*}(N^{com}N^{com^*})^*$.

- $\alpha \in \Sigma^{com^*}(N^{com}N^{com^*})^*$

So it must be the case that $\alpha = X\alpha_0\alpha_1$. $X \notin N^{com}$, $\alpha_0 \notin N^{com^*}$, $\alpha_1 \in (N^{com}N^{com^*})^*$ and $\alpha' = \alpha_0'\alpha_1'\alpha_1$ where $\alpha_1' \in (N^{com}N^{com^*})^*$, $\alpha_0' \notin \Sigma$.

- $\alpha \notin \Sigma^{com^*}$

Lemma 7.2 applies to give

$\mathcal{M}(X\alpha_0\alpha_1 \parallel \Pi_k') < \Gamma_k \rightarrow^*_\com \mathcal{M}(\alpha_0') \cdot \mathcal{M}(\alpha_1) \parallel \mathcal{M}(\Pi_k') \parallel \mathcal{M}(\Pi(w)) < \Gamma_k \oplus \Gamma(w)$

- $\alpha' \in \Sigma^{com}N^{com^*}(N^{com}N^{com^*})^*$

Follows from Lemma 4

- $\alpha \in \Sigma^{com}N^{com^*}(N^{com}N^{com^*})^*$ and $\alpha' \in P_{\Pi_j}$

Unless $\alpha' \in \Sigma^{com^*}(N^{com}N^{com^*})^* \cup \Sigma^{com}N^{com^*}(N^{com}N^{com^*})^*$ this case is covered by a case above. The remaining follows from Lemma 6.

- $\alpha \in \Sigma$ and $\alpha' \in P_{\Pi_j}$

Unless $\alpha' \in \Sigma^{com^*}(N^{com}N^{com^*})^* \cup \Sigma^{com}N^{com^*}(N^{com}N^{com^*})^*$ this case is covered by a case above. The remaining follows from Lemma 2 and Lemma 4

This concludes the proof of the inductive step.

Now we apply the above for the case that $\Pi_0 \triangleleft \Gamma_0 = S \triangleleft \Gamma(\epsilon)$ and $\Pi_f := \Pi'$. We can then see that $\mathcal{M}(S) < \Gamma(\epsilon) \rightarrow^*_\com \Pi_f \triangleleft \Gamma'.$

Then since for all $\alpha \in N^{com^*}(N^{com}N^{com^*})^*$ it is the case that $\mathcal{M}(\alpha) \in \mathcal{M}(\alpha)$ and further for $\alpha_0 \in N^{com^*}$, $\alpha_1(N^{com}N^{com^*})^* \mathcal{M}(\alpha_0\alpha_1) \in \mathcal{M}(\alpha_0) \cdot \mathcal{M}(\alpha_1)$ we can
deduce from the definition of \( \rightarrow_{\text{con}} \) on sets of configurations that
\[
\overline{\mathbb{M}}(S) \rightarrow_{\text{con}}^{*} \overline{\mathbb{M}}(II') \prec \Gamma'
\]
which concludes the proof. \( \square \)

**A.2 Direction: \( \Rightarrow \)**

\[
[M] = \left\{ \begin{array}{c}
C_i \mapsto \sum_{(C_i, w) \in M'} M'(C_i, w) \\
M = \bigoplus_{(C_i, w) \in M'} \mathbb{M}(w), \\
M' \in \mathbb{M}\{(C, w) \mid C \rightarrow_{\text{seq}}^{*} w, C \in \mathcal{N}\}
\end{array} \right\}
\]

\[
[M] = \{ \alpha \in \text{Control} \mid \mathbb{M}(\alpha) = [M] \}
\]

\[\parallel \gamma_1 \cdots \gamma_n \parallel = [\gamma_1] \cdots [\gamma_n] \text{ where } \gamma_i \in \mathcal{N} \cup \text{Cache} \cup \Sigma\]

Define for \( V, W \subseteq \mathbb{M}[\text{Control}] \)
\[
V \parallel W = \{ \Pi \parallel \Pi' \mid \Pi \in V, \Pi' \in W \}
\]

and \( \Pi, \Pi' \in \mathbb{M}[\text{Control}^M] \)
\[
[\Pi \parallel \Pi'] = [\Pi] \parallel [\Pi']
\]

**Definition 7 (Simulation Relation).** Let \( R \subseteq S \times S' \) where \((S, \rightarrow_S), (S', \rightarrow_{S'})\) are transition systems. We say \( R \) is a *weak \((S, S')\)-simulation just if
\[
(q, p) \in R \text{ and } q \rightarrow_S \gamma' \Rightarrow p \rightarrow_{S'} \gamma' \text{ and } (q', p') \in R.
\]

Let \( \preceq_S := \{ (\gamma, \alpha) \mid \alpha \in [\gamma] \} \subseteq \text{Control}^M \times \text{Control} \) and \( \preceq_C := \{ (\Pi_1 \prec \Gamma, \Pi_2 \prec \Gamma) \mid \Pi_2 \in [\Pi_1] \} \).

**Proposition 3 (Sequential Simulation).** \( \preceq_S \) is a weak simulation relation.

**Proof.** Let \( \gamma \in \text{Control}^M \) and \( \alpha \in \text{Control} \) such that \( \alpha \in [\gamma] \) and \( \gamma \rightarrow_{\text{seq}} \gamma' \).

Since \( \gamma \rightarrow_{\text{seq}} \gamma' \) we know that \( \gamma = X \alpha \gamma_0 \) and \( \gamma' = \gamma_1 \gamma_0 \). Hence by definition of \( [\gamma] \) it must be that \( \alpha = X \beta \alpha_0 \) such that \( \beta \in [M] \) and \( \alpha_0 \in [\gamma_0] \).

We will prove that there exists a \( \alpha' \succ_S \gamma' \) such that \( \alpha \rightarrow_{\text{seq}} \alpha' \) by case analysis on the type of rule used for \( \gamma \rightarrow_{\text{seq}} \gamma' \).

**Claim 1 and 2:** \( X \rightarrow a \in \Sigma \cup \{ \epsilon \}. (\text{trivial}) \)

Clearly \( X \beta \alpha_0 \rightarrow_{\text{seq}} a \beta \alpha_0 \), and \( \gamma_1 = aM \). Clearly \( a \beta \alpha_0 \Rightarrow aM \) and so \( \alpha' \succ_S \gamma' \).

**Claim 3:** \( X \rightarrow aA \in G. (\text{trivial}) \)

Then \( X \beta \alpha_0 \rightarrow_{\text{seq}} aA \beta \alpha_0 \) and \( \gamma_1 = aAM \). Clearly \( aA \beta \alpha_0 \Rightarrow aM \gamma_0 \Rightarrow [\gamma'] \) and so \( \gamma' \preceq_S \alpha' \).
Claim 4: \( X \rightarrow AB \in G, B \in \Lambda^{\text{com}} \rightarrow_{\text{seq}} w. \) **(non-trivial)**

Then \( X \beta \alpha_0 \rightarrow_{\text{seq}} AB \beta \alpha_0 \). To prove \( AB \beta \alpha_0 =: \alpha' \in [A(M(w) \oplus M) \gamma_0] = [\gamma] \)

we need to show that \( B \beta \in [M(w) \oplus M] \). Since \( \beta \in [M] \) we know that \( M(\beta) = [M] \). It remains to prove \( M(B) \oplus M(\beta) \in [M(w) \oplus M] \). Since \( M(\beta) \in [M] \) there exists \( M' \in \mathbb{M}[\{(C, w) | C \rightarrow_{\text{seq}}^{\ast} w, C \in N\}] \) such that

\[
M(\beta) = \left\{ C_i \mapsto \sum_{(C_i, w) \in M'} M'(C_i, w) \right\}
\]

and \( M = \bigoplus_{(C_i, w) \in M'} M'(C_i, w) \). Then writing \( M'_B := M' \oplus M((B, w)) \)

it is the case that \( M'_B \in \mathbb{M}[\{(C, w) | C \rightarrow_{\text{seq}}^{\ast} w, C \in N\}] \) and

\[
M \oplus M(w) = \left( \bigoplus_{(C_i, w') \in M'} M'(C_i, w') \right) + M(w)
\]

Thus we can conclude that

\[
f_B := \left\{ C_i \mapsto \sum_{(C_i, w') \in M'_B} M'_B(C_i, w') \right\} \in [M(w) \oplus M]
\]

and since \( M'_B(B, w) = M'(B, w) + 1 \) and \( M'_B(B', w') = M'(B', w') \) if either \( B \neq B' \) or \( w \neq w' \), it is the case that \( f_B(B) = M(\beta)(B) + 1 \) and \( f_B(C) = M(\gamma)(C) \) if \( C \neq B \). Hence \( f_B = M(B) \oplus M(\beta) \) and so \( M(B) \oplus M(\beta) \in [M(w) \oplus M] \) which implies \( \alpha' \triangleright_{S} \gamma' \) and concludes the proof of this case.

Claim 5: \( X \rightarrow AB \in G, B \in N^{\text{com}}. \) **(trivial)**

Then \( X \beta \alpha_0 \rightarrow_{\text{seq}} AB \beta \alpha_0 \) and \( \gamma_1 = ABM. \) Clearly \( AB \beta \alpha_0 = \alpha' \in [ABM \gamma_0] = [\gamma] \) and so \( \alpha' \triangleright_{S} \gamma' \).

The claim holds for all cases which concludes the proof. \( \square \)

**Proposition 4 (Concurrent Simulation).** \( \preceq_C \) is a weak simulation relation.

**Proof.** Let \( \Pi_1 \in \mathbb{M}[\text{Control}^{|M|}], \Pi_2 \in \mathbb{M}[\text{Control}] \) and \( \Gamma, \Gamma' \in \text{Chan} \rightarrow \mathbb{M}[\text{Msg}] \) such that \( \Pi_1 \triangleright \Gamma \preceq_C \Pi_1' \triangleright \Gamma' \) and suppose that \( \Pi_1 \triangleright \Gamma \rightarrow_{\text{con}} \Pi_1' \triangleright \Gamma' \).

We will prove that there exists \( \Pi'_2 \) such that \( \Pi_2 \triangleright \Gamma' \preceq_C \Pi_2' \triangleright \Gamma' \) and \( \Pi_2 \triangleright \Gamma \rightarrow_{\text{con}} \Pi'_2 \triangleright \Gamma' \) by case analysis on the rule used for \( \Pi_1 \triangleright \Gamma \rightarrow_{\text{con}} \Pi_1' \triangleright \Gamma' \).

- **Rule 11**
  - Follows immediately by Proposition 3.

- **Rule 12**
  - Then \( \Pi_1 = (c ? m) \gamma \parallel \Pi_1^0 \) and \( \Gamma = \Gamma' \parallel (\Gamma(c \mid m) \gamma) \) and \( \Pi_1^0 = \gamma \parallel \Pi_1^0 \). Hence \( \Pi_2 = (c ? m) \alpha \parallel \Pi_2^0 \in [\Pi_1^1] \) with \( \alpha \in [\gamma] \) and so \( \alpha \parallel \Pi_2^0 \in [\Pi_1^1] \). And using rule 3 \( (c ? m) \alpha \parallel \Pi_2^0 \triangleright \Gamma' \parallel (\Gamma(c \mid m) \gamma) \rightarrow_{\text{con}} \alpha \parallel \Pi_2^0 \triangleright \Gamma' \).
Rule 13

Then \( \Pi_1 = (\nu X) \gamma \parallel \Pi_0^1, \Pi_1' = \gamma \parallel X \parallel \Pi_0^1 \) and \( \Gamma' = \Gamma \). Hence \( \Pi_2 = (\nu X) \alpha \parallel \Pi_0^2 \in [\Pi_1] \) with \( \alpha \in [\gamma] \) and so \( \alpha \parallel X \parallel \Pi_0^2 \in [\Pi_1'] \). By rule 6 \((\nu X) \alpha \parallel \Pi_0^2 < \Gamma \rightarrowcon \alpha \parallel X \parallel \Pi_0^2 < \Gamma \).

Rule 14

Then \( \Pi_1 = (c_j ! m) \gamma \parallel \Pi_0^1, \Pi_1' = \gamma \parallel \Pi_0^1 \) and \( \Gamma' = \Gamma \oplus \Gamma(c ! m) \). Then \( \Pi_2 = (c_j ! m) \alpha \parallel \Pi_0^2 \in [\Pi_1] \) with \( \alpha \in [\gamma] \) and so \( \alpha \parallel \Pi_0^2 \in [\Pi_1'] \). By rule 14 we can see \((c_j ! m) \alpha \parallel \Pi_0^2 < \Gamma \rightarrowcon \alpha \parallel \Pi_0^2 < \Gamma(c ! m) \).

Rule 15

Then \( \Pi_1 = l \gamma \parallel \Pi_0^1, \Pi_1' = \gamma \parallel \Pi_0^1 \) and \( \Gamma' = \Gamma \). Then \( \Pi_2 = l \alpha \parallel \Pi_0^2 \in [\Pi_1] \) with \( \alpha \in [\gamma] \) and so \( \alpha \parallel \Pi_0^2 \in [\Pi_1'] \). By rule 5 \( l \alpha \parallel \Pi_0^2 < \Gamma \rightarrowcon \alpha \parallel \Pi_0^2 < \Gamma \).

Rule 16

Then \( \Pi_1 = M X \gamma \parallel \Pi_0^1 \) such that \( M \in \text{TermCache}, \Gamma' = \Gamma \oplus \Gamma(M) \), \( X \in \mathcal{N}^\text{com} \) and \( \Pi_1' = X \gamma \parallel \Pi_0^1 \parallel \Pi(M) \). Also \( \Pi_2 = \beta X \alpha \parallel \Pi_0^2 \in [\Pi_1] \) and \( \beta X \alpha \in [M X \gamma] \).

Hence \( X \alpha \in [X \gamma] \) and \( \beta \in [M] \) and hence \( \mathcal{M}(\beta) = [M] \). Thus \( \beta \rightarrowseq w, w \in \mathcal{X}^\text{com} \) such that \( \mathcal{M}(w) = M \). Hence using rules 4, 6 and 2 repeatedly we can see that \( \beta X \alpha \parallel \Pi_0^2 < \Gamma \rightarrowcon X \alpha \parallel \Pi_0^2 \parallel \Pi(M) < \Gamma \oplus \Gamma(w) = X \alpha \parallel \Pi_0^2 \parallel \Pi(M) < \Gamma \oplus \Gamma(M) \).

Rule 17

Then \( \Pi_1 = M X \gamma \parallel \Pi_0^1 \) and \( \Pi_1' = M' X \gamma \parallel \Pi_0^1 \parallel \Pi(M) \) such that \( m \in \text{MixedCache}, M' \in \text{NonTermCache}, \Gamma' = \Gamma \oplus \Gamma(M) \) and \( X \in \mathcal{N}^\text{com} \).

Also \( \Pi_2 = \beta X \alpha \parallel \Pi_0^2 \in [\Pi_1] \) and \( \beta X \alpha \in [M X \gamma] \). Thus \( \beta \in [M] \) and hence \( \beta \rightarrowseq w, w \in \mathcal{X}^\text{com} \) such that \( \mathcal{M}(w) = M \). Then \( w \simeq_I w_0 w_1 \) such that \( w_0 \in \mathcal{X}^\text{com} \) and \( w_1 \in \mathcal{N}^\text{com} \) and \( M' = \mathcal{M}(w_1) \).

Hence \( w_1 X \alpha \in [M' X \gamma] \) and thus \( w_1 X \alpha \parallel \Pi_0^2 \parallel \Pi(M) < \Gamma \oplus \Gamma(M) \) := \( \Pi_2' \in [\Pi_1'] \).

Using rules 4, 6 and 2 repeatedly we can see that \( \beta X \alpha \parallel \Pi_0^2 < \Gamma \rightarrowcon w_1 X \alpha \parallel \Pi_0^2 \parallel \Pi(M) < \Gamma \oplus \Gamma(M) = \Pi_2' \).

Hence the claim holds in all cases and thus we can conclude that \( \lll_{\text{ACPS}} \) is a weak simulation relation.

\[\square\]

Corollary 2. Given an ACPS P if \( S \ll \Gamma(e) \rightarrowcon \Pi \ll \Gamma \) then \( S \ll \Gamma(e) \rightarrowcon \Pi' \ll \Gamma \) such that \( \Pi \ll \Gamma \lll P' < \Gamma \).

Proof. Follows trivially by induction from Proposition 4. \[\square\]

Theorem 1 (Reduction of Program-Point Coverability). \((P; l_1, \ldots, l_n)\) is a yes-instance of Program-Point Coverability problem iff \((P; l_1, \ldots, l_n)\) is a yes-instance of Alternative Program-Point Coverability problem.

Proof. We will first prove the \( \Rightarrow \) direction. Let \((P; l_1, \ldots, l_n)\) be a yes-instance of the Program-Point Coverability problem then a configuration \( l_1 \alpha_1 \parallel \cdots \parallel l_n \alpha_n \parallel \Pi < \Gamma \) for some \( \alpha_1, \ldots, \alpha_n \in (\Sigma^* \cup \mathcal{N})^{+} / \sim_I \) is \( \rightarrowcon \) reachable. By Proposition 2 \( \overline{\mathcal{M}}(l_1 \alpha_1 \parallel \cdots \parallel l_n \alpha_n \parallel \Pi) < \Gamma \) is reachable for \( \rightarrowcon \) and thus \((P; l_1, \ldots, l_n)\) is a yes-instance of the Alternative Program-Point Coverability problem.
For the \( \preceq \)-direction let \( (P; l_1, \ldots, l_n) \) be a yes-instance of the Alternative Program-Point Coverability problem. Then a configuration \( \gamma_1 \parallel \cdots \parallel \gamma_n \parallel \Pi \prec \Gamma \) is \( \rightarrow_{\text{con'}} \) reachable and for \( i = 1, \ldots, n \) either \( \gamma_0 = l_i \gamma_i' \) or \( \gamma_i = M_i \gamma_i' \) such that \( l_i \in M_i \). By Proposition 4 we can conclude that \( \alpha_1 \parallel \cdots \parallel \alpha_n \parallel \Pi' \prec \Gamma' \) is \( \rightarrow_{\text{con'}} \) reachable such that \( \gamma_1 \parallel \cdots \parallel \gamma_n \parallel \Pi \prec \Gamma \). That means for \( i = 1, \ldots, n \) either \( \alpha_i = l_i \alpha_i' \) or \( \alpha_i = \beta_i \alpha_i' \) such that \( \beta_i \in N^{\text{com}} \) and \( \beta_i \rightarrow_{\text{seq}} w_i \) such that \( M_i(w_i) = M_i \) and \( w_i \in \Sigma^{\text{com}} \). It follows, by using \( \simeq_1 \) where necessary and choosing rewrite rules to expose \( l_i \), that \( \beta_i \rightarrow_{\text{seq}} w_i l_i \beta_i' \) where \( w_i \in \Sigma^{\text{com}} \) and \( \beta_i \in N^{\text{com}} \).

Hence \( \alpha_1 \parallel \cdots \parallel \alpha_n \parallel \Pi' \prec \Gamma \rightarrow_{\text{seq}}^* \alpha_n'' \parallel \cdots \parallel \alpha_1'' \parallel \Pi' \prec \Gamma \oplus \Gamma(w_0, \ldots, w_n) \) where either \( \alpha_i'' = \alpha_i = l_i \alpha_i' \) and \( w_i' = \epsilon \) or \( \alpha_i'' = l_i \beta_i' \). Thus we can conclude that \( (P; l_1, \ldots, l_n) \) is a yes-instance for the Program-Point Coverability problem. \( \square \)

B Proof of Lemma 1

Lemma 9 (Sequential Monotonicity). The transition relation \( \rightarrow_{\text{seq}} \) is monotone with respect to \( \leq_{\text{Control}} \).

Proof. Suppose \( \gamma, \gamma', \delta \in \text{Control} \) such that \( \gamma \leq \delta \) and \( \gamma \rightarrow_{\text{seq}} \gamma' \). We will show that there \( \exists \delta' \) such that \( \delta \rightarrow_{\text{seq}} \delta' \) and \( \gamma \leq \delta' \). We conclude from the definition of \( \leq_{\text{Control}} \) on \( \text{Control} \) and the fact that \( \gamma \rightarrow_{\text{seq}} \gamma' \) that \( \gamma = X_1 M_1 X_2 M_2 \cdots X_j M_j \) and \( \delta = X'_1 M'_1 X'_2 M'_2 \cdots X'_j M'_j \) with \( M_i \leq_{\text{Cache}} M'_i \) for \( 1 \leq i \leq j \leq k \). Our proof will be by case analysis on \( \gamma \rightarrow_{\text{seq}} \gamma' \).

\[ - \gamma \rightarrow_{\text{seq}} \gamma' \text{ using Rule 7.} \]

Thus there is a \( X_1 \rightarrow BC \) rule and \( C \rightarrow_{\text{seq}} w \) and \( \gamma' = B(M(w) \oplus M_1) X_2 M_2 \cdots X_j M_j \). Hence \( \delta \rightarrow_{\text{seq}} B(M(w) \oplus M_1) X_2 M_2 \cdots X_j M_j =: \delta' \). Clearly \( M(w) \oplus M_1 \leq_{\text{Cache}} (M(w) \oplus M'_1) \) and thus \( \gamma' \leq_{\text{Control}} \delta' \).

\[ - \gamma \rightarrow_{\text{seq}} \gamma' \text{ using Rule 8.} \]

Thus there is a \( X_1 \rightarrow BC \), \( C \in N^{\text{com}} \) and \( \gamma' = B C M_1 X_2 M_2 \cdots X_j M_j \). Further since \( \gamma' \in \text{Control} \) it is the case that \( j < k \). Hence \( \delta 
rightarrow_{\text{seq}} B C M'_1 X'_2 M'_2 \cdots X'_j M'_j =: \delta' \), \( \delta \in \text{Control} \) since \( j < k \) and obviously \( \gamma' \leq_{\text{Control}} \delta' \).

\[ - \gamma \rightarrow_{\text{seq}} \gamma' \text{ using Rule 9.} \]

Thus there is a \( X_1 \rightarrow a B \), \( a \in \Sigma \cup \{ \epsilon \} \) and \( \gamma' = a B M_1 X_2 M_2 \cdots X_j M_j \). Hence \( \delta 
rightarrow_{\text{seq}} a B M'_1 X'_2 M'_2 \cdots X'_j M'_j =: \delta' \) and obviously \( \gamma' \leq_{\text{Control}} \delta' \).

\[ - \gamma \rightarrow_{\text{seq}} \gamma' \text{ using Rule 10.} \]

Thus there is a \( X_1 \rightarrow a \), \( a \in \Sigma \cup \{ \epsilon \} \) and \( \gamma' = a M_1 X_2 M_2 \cdots X_j M_j \). Hence \( \delta 
rightarrow_{\text{seq}} a M'_1 X'_2 M'_2 \cdots X'_j M'_j =: \delta' \) and obviously \( \gamma' \leq_{\text{Control}} \delta' \).

\[ \square \]

Lemma 10 (Monotonicity). The transition relation \( \rightarrow_{\text{con'}} \) is monotone with respect to \( \leq_{\text{Config}} \).

Proof. Suppose \( \Pi_1 \preceq \Gamma_1, \Pi'_1 \preceq \Gamma'_1, \Pi_2 \preceq \Gamma_2 \) and \( \Pi_1 \preceq \Gamma_1 \rightarrow_{\text{con'}} \Pi_1' \preceq \Gamma_1' \). We will show that there \( \exists \Pi_2' \) such that \( \Pi_2' \preceq \Gamma_2 \rightarrow_{\text{con'}} \Pi_2 \preceq \Gamma_2' \). Since \( \Pi_1 \preceq \Gamma_1 \preceq \Pi_2 \preceq \Gamma_2 \) and
$\Pi_1 \triangleleft \Gamma \rightarrow_{\text{con}} \Pi_1' \triangleleft \Gamma'$, we can infer the components of the configuration involved in the latter transition. That means $\Pi_1 = \gamma \parallel \Pi_1^0$ and $\Pi_2 = \delta \parallel \Pi_2^0$ such that $\Pi_1^0 \preceq [\text{Control}^{\leq k}] \Pi_2^0$, $\Gamma_1 \subseteq \text{Queue}$, $\Gamma_2, \gamma, \delta \in \text{Control}^{\leq k}$ and $\gamma \preceq \text{Control}^{\leq k} \delta$. Our proof will be by case analysis on $\Pi_1 \triangleleft \Gamma \rightarrow_{\text{con}} \Pi_1' \triangleleft \Gamma'$.

- $\Pi_1 \triangleleft \Gamma_1 \rightarrow_{\text{con}} \Pi_1' \triangleleft \Gamma_1'$ using 11.
  This follows immediately by Lemma 9.

- $\Pi_1 \triangleleft \Gamma_1 \rightarrow_{\text{con}} \Pi_1' \triangleleft \Gamma_1'$ using 12.
  Thus we can conclude (i) $\gamma = e \right_p m \gamma'$, (ii) $\Pi_1' = \gamma' \parallel \Pi_1^0$, (iii) $\Gamma_1 = \Gamma_1' \oplus \Gamma(e \left_p m)$. Further since $\Pi_1 \triangleleft \Gamma_1 \leq_{\text{Config}} \Pi_2 \triangleleft \Gamma_2$ we infer (iv) $\delta = e \right_p m \delta'$ with $\gamma' \preceq_{\text{Control}^{\leq k}} \delta'$. Then we have $\Pi_2 \triangleleft \Gamma_2 \rightarrow_{\text{con}} \delta' \parallel \Pi_2^0 \triangleleft \Gamma_2'$.
  Writing $\Pi_2 := \delta' \parallel \Pi_2^0 \parallel X$ it remains to show $\Pi_1' \triangleleft \Gamma_1' \leq_{\text{Config}} \Pi_1'' \triangleleft \Gamma_1''$.
  Writing (a) $\gamma' \leq_{\text{Control}^{\leq k}} \delta'$, (b) $\Pi_1^0 \leq \Pi_1^0 [\text{Control}^{\leq k}] \Pi_2^0$ by assumption and (c) since $\Gamma_1 \leq \text{Queue} \Gamma_2$ and clearly $\Gamma_1' \leq \text{Queue} \Gamma_2'$.
  Hence we conclude $\Pi_1' \triangleleft \Gamma_1' \leq_{\text{Config}} \Pi_1'' \triangleleft \Gamma_1''$.

- $\Pi_1 \triangleleft \Gamma_1 \rightarrow_{\text{con}} \Pi_1' \triangleleft \Gamma_1'$ using 13.
  Thus we can conclude (i) $\gamma = \nu X \gamma'$, (ii) $\Pi_1' = \gamma' \parallel \Pi_1^0 \parallel X$, (iii) $\Gamma_1' = \Gamma_1$. Further since $\Pi_1 \triangleleft \Gamma_1 \leq_{\text{Config}} \Pi_2 \triangleleft \Gamma_2$ we infer (iv) $\delta = \nu X \delta'$ with $\gamma' \preceq_{\text{Control}^{\leq k}} \delta'$. Then we have $\Pi_2 \triangleleft \Gamma_2 \rightarrow_{\text{con}} \delta' \parallel \Pi_2^0 \parallel X \triangleleft \Gamma_2$.
  Writing $\Pi_2 := \delta' \parallel \Pi_2^0 \parallel X$ it remains to show $\Pi_1' \triangleleft \Gamma_1 \leq_{\text{Config}} \Pi_1'' \triangleleft \Gamma_1''$.
  Now (a) $\gamma' \leq \Pi_1^0 [\text{Control}^{\leq k}] \delta'$, (b) $\Pi_1^0 \leq \Pi_1^0 [\text{Control}^{\leq k}] \Pi_2^0$ by assumption and (c) clearly $X \leq \Pi_1^0 [\text{Control}^{\leq k}] X$.
  Hence we conclude $\Pi_1' \triangleleft \Gamma_1' \leq_{\text{Config}} \Pi_1'' \triangleleft \Gamma_1''$.

- $\Pi_1 \triangleleft \Gamma_1 \rightarrow_{\text{con}} \Pi_1' \triangleleft \Gamma_1'$ using 14.
  Thus we can conclude (i) $\gamma = e \right_p m \gamma'$, (ii) $\Pi_1' = \gamma' \parallel \Pi_1^0$, (iii) $\Gamma_1' = \Gamma_1 \oplus \Gamma(e \left_p m)$. Further since $\Pi_1 \triangleleft \Gamma_1 \leq_{\text{Config}} \Pi_2 \triangleleft \Gamma_2$ we infer (iv) $\delta = e \right_p m \delta'$ with $\gamma' \preceq_{\text{Control}^{\leq k}} \delta'$. Then we have $\Pi_2 \triangleleft \Gamma_2 \rightarrow_{\text{con}} \delta' \parallel \Pi_2^0 \parallel \Gamma(e \left_p m) \triangleleft \Gamma_2'$.
  Writing $\Pi_2 := \delta' \parallel \Pi_2^0$ and $\Pi_2' := \Pi_2 \oplus \Gamma(e \left_p m)$ it remains to show $\Pi_1' \triangleleft \Gamma_1' \leq_{\text{Config}} \Pi_1'' \triangleleft \Gamma_1''$.
  Now (a) $\gamma' \leq \Pi_1^0 [\text{Control}^{\leq k}] \delta'$, (b) $\Pi_1^0 \leq \Pi_1^0 [\text{Control}^{\leq k}] \Pi_2^0$ by assumption and (c) since $\Gamma_1 \leq \text{Queue} \Gamma_2$, $\Pi_2 \oplus \Gamma(e \left_p m) \leq \text{Queue} \Gamma_2 \oplus \Gamma(e \left_p m)$.
  Hence we conclude $\Pi_1' \triangleleft \Gamma_1' \leq_{\text{Config}} \Pi_1'' \triangleleft \Gamma_1''$.

- $\Pi_1 \triangleleft \Gamma_1 \rightarrow_{\text{con}} \Pi_1' \triangleleft \Gamma_1'$ using 15.
  Thus we can conclude (i) $\gamma = l \gamma'$, (ii) $\Pi_1' = \gamma' \parallel \Pi_1^0$, (iii) $\Gamma_1' = \Gamma_1$. Further since $\Pi_1 \triangleleft \Gamma_1 \leq_{\text{Config}} \Pi_2 \triangleleft \Gamma_2$ we infer (iv) $\delta = l \delta'$ with $\gamma' \preceq_{\text{Control}^{\leq k}} \delta'$. Then we have $\Pi_2 \triangleleft \Gamma_2 \rightarrow_{\text{con}} \delta' \parallel \Pi_2^0 \triangleleft \Gamma_2$.
  Writing $\Pi_2 := \delta' \parallel \Pi_2^0$ it is trivial to see $\Pi_1' \triangleleft \Gamma_1 \leq_{\text{Config}} \Pi_1'' \triangleleft \Gamma_1''$.

- $\Pi_1 \triangleleft \Gamma_1 \rightarrow_{\text{con}} \Pi_1' \triangleleft \Gamma_1'$ using 16.
  Thus we can conclude (i) $\gamma = M_1 X \gamma_1$, (ii) $\Pi_1' = \gamma' \parallel \Pi_1^0 \parallel \Pi(M_1)$, (iii) $\gamma' = X \gamma_1$ and (iv) $\Gamma_1' = \Gamma_1 \oplus \Gamma(M_1)$. Further since $\Pi_1 \triangleleft \Gamma_1 \leq_{\text{Config}} \Pi_2 \triangleleft \Gamma_2$ we infer (v) $\delta = M_2 X \delta_1$ with $M_1 \leq \text{Cache} M_2$ and $X \gamma_1 \leq \Pi_1^0 [\text{Control}^{\leq k}] X \delta_1$. Let $\delta' := X \delta_1$ then clearly $\gamma' \preceq_{\text{Control}^{\leq k}} \delta'$ and we have $\Pi_2 \triangleleft \Gamma_2 \rightarrow_{\text{con}} \delta' \parallel \Pi_2^0 \parallel \Pi(M_2) \triangleleft \Gamma_2 \oplus \Gamma(M_2)$. 
Writing $\Pi'_x := \delta' \parallel \Pi ^ 0_x \parallel \Pi(M_2)$ and $\Gamma'_x := \Gamma_2 \oplus \Gamma(M_2)$ we will now show $\Pi'_1 \triangleleft \Gamma'_1 \triangleleft \text{Config} \Pi'_2 \triangleleft \Gamma'_2$.

Now (a) $\gamma' \leq_{\text{Config}} \delta'$, (b) $\Pi'_1 \leq_{\text{Config}} \Pi_0$ by assumption, (c) since $M_1 \leq_{\text{Cache}} M_2$ and since $\Pi(\cdot)$ is clearly monotonic $\Pi(M_1) \leq_{\text{Config}} \Pi(M_2)$ and (d) lastly since $\Gamma(\cdot)$ is monotonic we can conclude $\Pi(M_1) \leq_{\text{Queues}} \Pi(M_2)$.

Hence we conclude $\Pi'_1 \triangleleft \Gamma'_1 \triangleleft \text{Config} \Pi'_2 \triangleleft \Gamma'_2$.

- $\Pi_1 \triangleleft \Gamma_1 \rightarrow_{\text{con'}} \Pi'_1 \triangleleft \Gamma'_1$ using 17.

Thus we can conclude (i) $\gamma = M_1 X_1 \gamma_1$ with $M_1 \in \text{MixedCache}$, (ii) $\Pi'_1 = \gamma' \parallel \Pi_0 \parallel \Pi(M_1)$, (iii) $\gamma' = M'_1 X_1 \gamma_1$ with $M'_1 \in \text{NonTermCache}$ and $M'_1 = M_1 \upharpoonright (\mathcal{N} \cup \mathcal{L})$, (iv) $\Gamma'_1 = \Gamma_1 \oplus \Gamma(M_1)$. Further since $\Pi_1 \triangleleft \Gamma_1 \triangleleft \text{Config} \Pi_2 \triangleleft \Gamma_2$ we infer (v) $\delta = M_2 X_2 \delta_1$ with $M_2 \leq_{\text{Cache}} M_2$ and $X_1 \gamma_1 \leq_{\text{Cache}} \mathcal{X} \delta_1$.

Let $M'_2 := M_2 \upharpoonright (\mathcal{N} \cup \mathcal{L})$ and $\delta' := M'_2 X_2 \delta_1$ then we have $\Pi_2 \triangleleft \Gamma_2$.

Writing $\Pi'_x := \delta' \parallel \Pi ^ 0_x \parallel \Pi(M_2)$ and $\Gamma'_x := \Gamma_2 \oplus \Gamma(M_2)$ we will now show $\Pi'_1 \triangleleft \Gamma'_1 \triangleleft \text{Config} \Pi'_2 \triangleleft \Gamma'_2$.

Now since $\cdot \parallel \cdot$ is monotonic in the first argument and $M_1 \leq_{\text{Cache}} M_2$ we conclude $M'_1 \leq_{\text{Cache}} M_2$ and thus (a) $\gamma' \leq_{\text{Config}} \delta'$, (b) $\Pi'_1 \leq_{\text{Config}} \Pi_0$ by assumption, (c) and since $\Pi(\cdot)$ is monotonic $\Pi(M_1) \leq_{\text{Config}} \Pi(M_2)$ and (d) lastly since $\Gamma(\cdot)$ is monotonic we can conclude $\Pi(M_1) \leq_{\text{Queues}} \Pi(M_2)$.

Hence we conclude $\Pi'_1 \triangleleft \Gamma'_1 \triangleleft \text{Config} \Pi'_2 \triangleleft \Gamma'_2$.

\begin{corollary}
The transition system $(\mathbb{M}[\text{Control} \leq k] \times (\text{Chan} \to \mathbb{M}[\text{Msq}]), \rightarrow_{\text{con'}}, \leq)$ is a well-structured transition system.
\end{corollary}

\begin{proof}
Follows immediately from Lemma 10.
\end{proof}

\begin{theorem}
The Program-Point Coverability problem for unbounded spawning $k$-ACPS is decidable.
\end{theorem}

\begin{proof}
By Theorem 1 it suffices to show that the Alternative Program-Point Coverability problem is decidable, which follows from Corollary 3 and the fact that the set

$$U := \{ \hat{\Gamma}_1 X_1^1 \cdots X_{j_1}^1 \parallel \cdots \parallel \hat{\Gamma}_n X_n^1 \cdots X_{j_n}^n \triangleleft \Gamma(\cdot) \mid X_i^j \in \mathcal{N}_{\text{con}}, \hat{\Gamma}_i = I_i \text{ or } \lfloor l \rfloor \text{ and } 0 \leq j \leq k \}$$

is upward-closed and $(P; l_1, \ldots, l_n)$ is a yes-instance for the Alternative Program-Point Coverability problem iff an element of $U$ is $\rightarrow_{\text{con'}}$-reachable.
\end{proof}
## C Notation

| Symbol | Description                                      |
|--------|--------------------------------------------------|
| $M$    | multiset                                         |
| $\mu, \nu$ | general sequence                              |
| $m$    | message                                          |
| $c$    | channel                                          |
| $A, B, C, X, Y, Z$ | non-terminal i.e. element of $\mathcal{N}$ |
| $l$    | label                                            |
| $a$    | terminal i.e. element of $\Sigma$               |
| $w$    | word over $\Sigma$                              |
| $\alpha, \beta$ | word over $(\Sigma \cup \mathcal{N})$ |
| $\gamma, \delta$ | word in $\text{Control}^M$ |
| $i, j, n, k$ | integer                                         |
| $\Gamma$ | channel                                         |
| $\Pi$  | set of processes                                 |
| $U, V, W$ | set                                             |
| $I$    | Independence relation                            |
| $D$    | dependence relation                               |
| $R$    | relation                                         |
| $u, v$ | general element                                  |
| $\rightarrow_{\text{seq}}$ | standard sequential semantics                      |
| $\rightarrow_{\text{con}}$ | standard concurrent semantics                        |
| $\rightarrow_{\text{seq'}}$ | alternative sequential semantics                   |
| $\rightarrow_{\text{con'}}$ | alternative concurrent semantics                    |