Separating Controller Design from Closed-Loop Design:
A New Perspective on System-Level Controller Synthesis

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Abstract—We show that given a desired closed-loop response for a system, there exists an affine subspace of controllers that achieve this response. By leveraging the existence of this subspace, we are able to separate controller design from closed-loop design by first synthesizing the desired closed-loop response and then synthesizing a controller that achieves the desired response. This is a useful extension to the recently introduced System Level Synthesis framework, in which the controller and closed-loop response are jointly synthesized and we cannot enforce controller-specific constraints without subjecting the closed-loop map to the same constraints.

We demonstrate the importance of separating controller design from closed-loop design with an example in which communication delay and locality constraints cause standard SLS to be infeasible. Using our new two-step procedure, we are able to synthesize a controller that obeys the constraints only while incurring a 3% increase in LQR cost compared to the optimal LQR controller.

I. INTRODUCTION

Large-scale distributed cyberphysical systems (e.g. power grids, intelligent transportation systems) are composed of numerous local controllers that exchange local information via some communication network. The information that each local controller is able to obtain is limited by properties of the communication network, e.g. delay. It is a challenge to scalably synthesize optimal local controllers subject to the limitations of the communication network [1]–[6].

A New Perspective on System-Level Controller Synthesis: Separating Controller Design from Closed-Loop Design

The recently developed System Level Synthesis (SLS) framework addresses this challenge by shifting the optimization from the space of available controllers to the space of achievable system closed-loop maps [7]. In doing so, it allows the problem to be decomposed into sub-problems to be solved in parallel, resulting in a synthesis procedure with $O(1)$ complexity [8].

In the original SLS framework, the closed-loop maps themselves are used to implement the controller, and thus any constraints applied to the controller are directly enforced on the closed-loop response as well. However, the aforementioned communication limitations motivate constraints on controllers, not closed-loop maps; by applying these constraints on the closed-loop response, we unnecessarily limit the space over which we can search for solutions.

Standard SLS is infeasible under excessive communication constraints. [9] addresses this by searching over approximate closed-loop maps instead of exact closed-loop maps; constraints are imposed on the approximate closed-loop maps. We propose an alternative two-step procedure, as follows:

1) Synthesize the desired closed-loop response, subject to closed-loop constraints. This can be done using SLS or any other linear synthesis method (Proposition [1]).
2) Synthesize the controller, subject to controller constraints.

To fully separate closed-loop map constraints from controller constraints, we require a controller that is implemented using transfer matrices other than the closed-loop maps. We define the space of such matrices in Theorem [2] and give conditions for their existence in Lemma [3].

The main contribution of this paper is to introduce the controller synthesis step of the design procedure and demonstrate its importance. We show that our proposed two-step synthesis allows us to design low-cost, distributed controllers that were unavailable to us in the previous framework. Additionally, the controller synthesis problem can be decomposed into parallelizable sub-problems, much like the original SLS problem.

II. PRELIMINARIES

A. Notation

We use italicized lower-case letters (e.g. $x_t$) to denote vectors in the time domain. We use italicized upper-case letters (e.g. $A$) to denote constant matrices. We use superscripts to denote individual matrix elements (e.g. $A^{ij}$).

We use boldface lower and upper case letters (e.g. $x$, $\Phi_x$, $R_c$) to denote signals and transfer matrices in the frequency domain. We use $R_c(k)$ to denote the $k$th spectral component of $R_c$, i.e. $R_c(z) = \sum_{k=0}^{\infty} R_c(k) z^{-k}$.

In this paper, we will restrict ourselves to strictly proper finite-impulse-response (FIR) transfer matrices, i.e. $R_c(z) = \sum_{k=1}^{T} R_c(k) z^{-k}, T \in \mathbb{Z}_+$.

B. System setup

We use the same setup as in (2.1) of [7]:

$$x_{t+1} = Ax_t + Bu_t + w_t \quad (1)$$

where $x, w \in \mathbb{R}^n$ and $u \in \mathbb{R}^m$. In this paper we focus on the time-invariant case (i.e. $A, B$ have no time-dependence) with state feedback.

$\Phi_x$ and $\Phi_u$ are the closed-loop maps from $w$ to $x$ and $u$, with FIR time horizon $T$:

$$\begin{bmatrix} x \\ u \end{bmatrix} = \begin{bmatrix} \Phi_x \\ \Phi_u \end{bmatrix} w \quad (2)$$
C. Controller implementation

Fig. 1 shows the controller implementation. \( R_c \) and \( M_c \) are the implementation matrices, with order (i.e. FIR time horizon) \( T_c \).

The controller includes two internal signals; \( \hat{x} \) and \( \hat{\delta} \). The equations describing the controller are

\[
\begin{align*}
\hat{\delta}_t &= x_t - \sum_{k=2}^{T_c} R_c(k) \hat{\delta}_{t-k+1} \quad (3a) \\
u_t &= \sum_{k=1}^{T_c} M_c(k) \hat{\delta}_{t-k+1} \quad (3b)
\end{align*}
\]

where (3a) assumes that \( R_c(1) \) is the identity. For a more detailed derivation, refer to [10]. The corresponding frequency-domain equations are

\[
\begin{align*}
\hat{\delta} &= x + (I - zR_c) \hat{\delta} \quad (4a) \\
x &= zR_c \hat{\delta} \quad (4b) \\
u &= zM_c \hat{\delta} \quad (4c)
\end{align*}
\]

Proposition 1. Any linear controller (i.e. \( u = Kx \)) can be implemented using the controller structure defined in Fig. 1.

Proof. We can construct closed-loop maps \( \Phi_x \) and \( \Phi_u \) directly from \( K \), as shown in (4.4) of [7]:

\[
\begin{align*}
\Phi_x &= (zI - A - BK)^{-1} \quad (5a) \\
\Phi_u &= K(zI - A - BK)^{-1} \quad (5b)
\end{align*}
\]

We can then set \( R_c = \Phi_x \) and \( M_c = \Phi_u \) in (4), which gives back the original controller \( u = Kx \). \( \square \)

III. IMPLEMENTATION MATRICES

A. Controllers and closed-loop maps

Theorem 1. Let \((\Phi_x, \Phi_u)\) be stable closed-loop maps. The only linear controller \( K \) (i.e. \( u = Kx \)) that achieves these closed-loop maps is \( K = \Phi_u \Phi_x^{-1} \).

Proof. By Theorem 4.1 in [7], \( K = \Phi_u \Phi_x^{-1} \) achieves the closed-loop maps. We show uniqueness by contradiction. Assume there is another linear controller \( K_1, K_1 \neq K \), that also achieves the desired closed-loop maps. Since both \( K \) and \( K_1 \) achieve \((\Phi_x, \Phi_u)\),

\[
\begin{align*}
\Phi_x &= (zI - A - BK_1)^{-1} = (zI - A - BK)^{-1} \quad (6a) \\
\Phi_u &= K_1(zI - A - BK_1)^{-1} = K(zI - A - BK)^{-1} \quad (6b)
\end{align*}
\]

Substituting (6a) into (6b) gives

\[
K_1 \Phi_x = K \Phi_x
\]

(7)

Since \( \Phi_x \) is invertible, this implies that \( K_1 = K \). Contradiction! \( \square \)

Theorem 1 along with the definitions from [5], show a one-to-one mapping between \((\Phi_x, \Phi_u)\) and \( K \). However, the linear controller \( K \) can be implemented in a variety of ways. For example, we could directly implement \( u = Kx \); we could also implement a linear controller using the structure shown in Figure 1. In the original SLS framework, the latter is used to avoid direct matrix inversion of \( \Phi_x \).

B. Implementing closed-loop maps

For the controller structure defined in Fig. 1 let the controller implemented by \((R_c, M_c)\) achieve closed-loop maps \((\hat{\Phi}_x, \hat{\Phi}_u)\). We define the following terminology:

Definition 1. \((R_c, M_c)\) are the implementation transfer matrices for the closed-loop maps \((\Phi_x, \Phi_u)\). We will refer to them as implementation matrices.

Definition 2. We call \((\hat{\Phi}_x, \hat{\Phi}_u)\) the implemented closed-loop maps of the controller \((R_c, M_c)\).

The implemented closed-loop maps are found by combining (3) and (1) as done in [10]:

\[
\begin{bmatrix}
\hat{\Phi}_x \\
\hat{\Phi}_u
\end{bmatrix} =
\begin{bmatrix}
R_c \\
M_c
\end{bmatrix} \Delta_c^{-1}
\]

(8)

Where \( \Delta_c \) is a helper variable defined as

\[
\Delta_c = [zI - A - B] \begin{bmatrix} R_c \\ M_c \end{bmatrix}
\]

(9)

Note that \( \Delta_c \) can also be written as \( I + \Delta \). This is the same formulation used by (4.22) in [7], modulo notational differences (we use \( R_c \) and \( M_c \) instead of \( \Phi_x, \Phi_u \)). \( \Delta_c \) is invertible since its leading spectral element, \( I \), is invertible.

Our analysis largely focuses on closed-loop maps \((\Phi_x, \Phi_u)\) instead of the controller \( K \). However, due to the one-to-one mapping between controller and closed-loop maps, we can also view \((R_c, M_c)\) as implementation matrices for the controller \( K = \Phi_u \Phi_x^{-1} \).

Theorem 2. For \( R_c(1) = I, (R_c, M_c) \) are implementation matrices for \((\Phi_x, \Phi_u)\) if and only if they satisfy

\[
\begin{bmatrix}
R_c \\
M_c
\end{bmatrix} =
\begin{bmatrix}
\Phi_x \\
\Phi_u
\end{bmatrix} \begin{bmatrix} zI - A - B \end{bmatrix} \begin{bmatrix} R_c \\ M_c \end{bmatrix}
\]

(10)

Proof. Necessity. If \((R_c, M_c)\) are implementation matrices for \((\Phi_x, \Phi_u)\), then we require

\[
\begin{bmatrix}
\hat{\Phi}_x \\
\hat{\Phi}_u
\end{bmatrix} =
\begin{bmatrix}
\Phi_x \\
\Phi_u
\end{bmatrix}
\]

(11)

Substituting (5) into (11) and multiplying by \( \Delta_c \), then writing \( \Delta_c \) in terms of \( (A, B, R_c, M_c) \), gives (10).

Sufficiency. If \((R_c, M_c)\) satisfy (10), we can substitute (10) into (5) to conclude that \((\hat{\Phi}_x, \hat{\Phi}_u) = (\Phi_x, \Phi_u)\), i.e. \((R_c, M_c)\) are implementation matrices for \((\Phi_x, \Phi_u)\). \( \square \)
This constraint describes an affine subspace of implementation matrices for \((\Phi_x, \Phi_u)\).

**Corollary 2.1.** If \((R_c, M_c)\) are implementation matrices for \((\Phi_x, \Phi_u)\), then the first spectral components of \(\Phi_u\) and \(M_c\) are equal, i.e. \(M_e(1) = \Phi_u(1)\).

This equivalence arises directly from writing \([\text{10}]\) in terms of its spectral elements.

**Corollary 2.2.** For \(T_c \geq T\), \((\Phi_x, \Phi_u)\) are implementation matrices for themselves.

\((\Phi_x, \Phi_u)\) are used as implementation matrices in \([7]\).

**Corollary 2.3.** If \((R_c, M_c)\) are implementation matrices for \((\Phi_x, \Phi_u)\), then \(K = \Phi_u \Phi_x^{-1} = M_e R_c^{-1}\)

**C. Existence of solutions**

To better understand the dimension of the space of implementation matrices, we rearrange the constraint \([\text{10}]\) so that the variables \((R_c, M_c)\) appear on only one side of the constraint.

Rewrite \(\Delta_e\) in block-matrix form:

\[
\begin{pmatrix}
\Delta_e(0) \\
\Delta_e(1) \\
\vdots \\
\Delta_e(T_c)
\end{pmatrix} =
\begin{bmatrix}
I & 0 \\
-A & I & -B \\
& \ddots & \ddots & \ddots \\
& & -A & -B
\end{bmatrix}
\begin{bmatrix}
R_c(1) \\
\vdots \\
R_c(T_c) \\
0
\end{bmatrix}
= \Phi_x(1) \cdots \Phi_x(T_c)
\]

Rewrite the right hand side of \([\text{10}]\) in block-matrix form:

\[
\begin{bmatrix}
R_c(1) \\
\vdots \\
R_c(T_c) \\
0
\end{bmatrix} =
\begin{bmatrix}
\Phi_x(1) \\
\vdots \\
\Phi_x(T_c)
\end{bmatrix}
\begin{pmatrix}
\Delta_e(0) \\
\Delta_e(1) \\
\vdots \\
\Delta_e(T_c)
\end{pmatrix}
\]

We show only the formulation for \(R_c\); the formulation for \(M_e\) is identical but with \(\Phi_u\) and \(M_c\) instead of \(\Phi_x\) and \(R_c\).

Using the block-matrix formulations, we can rearrange \([\text{10}]\) into a constraint of the form

\[
Fv = G
\]

where \(F\) and \(G\) are matrices that do not depend on \(R_c\) and \(M_c\). The total number of constraints is \((T_c + T)(m+n)\).

**Lemma 2.1.** The implementation constraints (as defined in \([\text{10}]\)) are feasible if and only if \(\text{rank}(F) = \text{rank}(F(G))\). If feasible, the solution space has dimension \(\text{dim}(\text{null}(F)) \times n\), where \(n\) is the number of states in the system.

*Proof.* This result is a direct application of the Rouché-Capelli theorem to the linear system defined in \([\text{14}]\).

Corollary 2.2 states that \([\text{10}]\) has at least one solution for \(T_c \geq T\). When \(T_c < T\), we can check the rank of \(F \text{ and } [F|G]\) and calculate the dimension of the solution space if it exists.

**IV. STABILITY**

**A. Internal dynamics**

The system is internally stable if the dynamics of \(\hat{\delta}\), the internal signal, are stable. By substituting \([\text{3}]\) into \([\text{1}]\) and rearranging, we can obtain internal dynamics of the form

\[
\begin{bmatrix}
\delta_{t-T_c+1} \\
\vdots \\
\delta_{t-1} \\
\delta_t
\end{bmatrix}
= A_z
\]

**B. Stability check**

We can verify internal stability *a posteriori* by checking that \(A_z\) is stable. Alternatively, a sufficient condition for internal stability is \(\|\Delta\| < 1\) \([7]\).

The stability of \(A_z\) can be checked in a distributed manner. First, a helpful proposition:

**Proposition 2.** Let \(\|\cdot\|\) be an induced matrix norm. For \(A \in \mathbb{R}^{n \times n}\), if \(\exists m > 0 \text{ s.t. } \|A^m\| < 1\), then \(A\) is stable.

*Proof.* Let \(\rho = \|A^m\|^{1/m}, \rho \in [0, 1)\). Using norm submultiplicativity and some algebra, we can show that \(\forall t > m, \|A^t\| \leq C \rho^t\) where \(C\) is some constant. Using this upper bound and induced norm properties, we can show that \(\forall x_0 \in \mathbb{R}^n, \lim_{t \to \infty} \|A^t x_0\| = 0\). This is the definition of stability in the discrete time setting.

Let each processor store \(A_z\) and some columns of \(A^k_{z(\cdot)}\), denoted \(A^k_{z(i)}\). Overall, every column of \(A^k_{z(\cdot)}\) is stored on some processor. The stability check procedure is as follows, starting with \(k = 1\):

1. Calculate \(A_{z(i)}^k\) by multiplying \(A_z\) and \(A_{z(i)}^{k-1}\)
2. Check the induced 1-to-1 norm of \(A_{z(i)}^k\)
3. Consensus on whether a termination condition has been met. If no termination condition is met, increment \(k\) and return to Step 1

The clear termination condition is \(\|A^k_{z(\cdot)}\| < 1\); then, \(A_z\) is certified to be stable by Proposition 2. We suggest two additional termination conditions:
with one key difference. For an objective as the objective, this termination condition corresponds to finding an unacceptably large transient condition

\[ k > k_{\text{max}}, \text{ where } k_{\text{max}} \text{ is some predetermined maximum number of iterations} \]

Both conditions would indicate that the stability check failed to certify stability. Since we select a column-wise separable norm, the entire procedure can be distributed. The complexity per iteration scales quadratically with \( n \), under the conservative assumption that each node has at least one processor. For the system in Section VII, this procedure certifies stability in 7 iterations for the low-order controller and 32 iterations for the full-order controller.

V. APPROXIMATE IMPLEMENTATIONS

The solution space defined by (10), although it exists for \( T_c \geq T \), often yields solutions that are unstable. Further, Corollary 2.1 gives a fundamental limit on the sparsity of \( M_c \). If \( \Phi_u(1) \) is dense, we cannot find implementation matrices that support any type of sparsity (e.g. communication delay, locality). These necessitate relaxations of (10).

For a relaxed implementation, we want the implemented closed-loop maps \( (\Phi_x, \Phi_u) \) to be as close to the optimal closed-loop maps \( (\Phi_x, \Phi_u) \) as possible while maintaining internal stability, i.e.

\[
\min_{R_c, M_c} \| \begin{bmatrix} R_c \\ M_c \end{bmatrix} (I + \Delta) - \begin{bmatrix} \Phi_x \\ \Phi_u \end{bmatrix} \|_2 \\
\text{s.t. } (I + \Delta)^{-1} \text{ stable, } \begin{bmatrix} R_c \\ M_c \end{bmatrix} \in S
\]

where \( S \) includes sparsity and FIR constraints, and \( I + \Delta = \Delta_c \). This optimization problem is clearly nonconvex. Factoring the objective function as

\[
\| (\begin{bmatrix} R_c \\ M_c \end{bmatrix} - \begin{bmatrix} \Phi_x \\ \Phi_u \end{bmatrix} (I + \Delta)) (I + \Delta)^{-1} \|
\]

and using similar submultiplicativity, small-gain, and power series arguments as Section 4.5.1 of [7], we can upper bound the optimization problem (16) with this quasi-convex problem:

\[
\min_{\gamma \in [0,1]} \frac{1}{1 - \gamma} \min_{R_c, M_c, \Delta} \| \begin{bmatrix} R_c \\ M_c \end{bmatrix} - \begin{bmatrix} \Phi_x \\ \Phi_u \end{bmatrix} (I + \Delta) \|
\text{s.t. } \begin{bmatrix} zI - A & -B \end{bmatrix} \begin{bmatrix} R_c \\ M_c \end{bmatrix} = (I + \Delta), \| \Delta \| \leq \gamma, \begin{bmatrix} R_c \\ M_c \end{bmatrix} \in S
\]

This is similar to the virtualized SLS method [9] [7], with one key difference. For an objective \( g(\Phi_x, \Phi_u) \), the virtualized SLS method uses \( g(R_c, M_c) \) as the objective, while our two-step method uses

\[
\| \begin{bmatrix} R_c \\ M_c \end{bmatrix} - \begin{bmatrix} \Phi_x \\ \Phi_u \end{bmatrix} (I + \Delta) \|
\]

as the objective. This is the equation error for (10), and is a heuristic for the closed-loop difference.

The nested optimization problem defined by (18) is time-consuming to solve; it can also be mathematically infeasible if the sparsity constraints \( S \) are too strict. We instead solve (20), which is much quicker and uses a regularizer on \( \Delta \) to promote stability. We suggest starting with a small \( \lambda \), solving (20), checking for stability using the distributed method presented in Section IV-B and increasing \( \lambda \) if the stability check is failed. Alternatively, we can enforce \( \| \Delta \| < 1 \).

\[
\min_{R_c, M_c, \Delta} \| \begin{bmatrix} R_c \\ M_c \end{bmatrix} - \begin{bmatrix} \Phi_x \\ \Phi_u \end{bmatrix} (I + \Delta) \| + \lambda \| \Delta \|
\text{s.t. } \begin{bmatrix} zI - A & -B \end{bmatrix} \begin{bmatrix} R_c \\ M_c \end{bmatrix} = (I + \Delta), \begin{bmatrix} R_c \\ M_c \end{bmatrix} \in S
\]

We can also include additional objectives in (20), e.g. \( \| \Delta \| \) to promote sparsity.

The optimization problem (20) is column-wise separable if we choose a column-wise separable norm for the objective (e.g. \( H_2 \) norm). Like the original SLS problem, it can be decomposed into subproblems to be solved in parallel.

VI. CLOSED-LOOP CONSTRAINTS VS. CONTROLLER CONSTRAINTS

In this section, we discuss the physical interpretation of separately applying locality and delay constraints to the closed-loop and to the controller, and when such constraints are appropriate. This separation is not possible in standard SLS, since the closed-loop maps themselves are used as implementation matrices for the controller.

First, a result on how applying controller constraints on the closed-loop maps can be overly restrictive:

**Lemma 2.2.** Let \( K \) be the controller corresponding to the closed-loop maps \( (\Phi_x, \Phi_u) \). Then, the operator \( \Phi_u \) lies in the range of the operator \( K \).

**Proof.** By Theorem 1 we have that \( K\Phi_x = \Phi_u \). \( \square \)

**Lemma 2.2** shows that sparsity constraints (e.g. locality, delay) on \( K \) will translate to sparsity constraints on \( \Phi_u \), but not \( \Phi_x \). Directly applying these constraints on \( \Phi_x \) may be too restrictive. Note that although it is also true that \( KR_c = M_c \), both \( M_c \) and \( R_c \) must obey sparsity constraints as they are directly used in the implementation.

A. Locality

Let \( L(i) \) denote the locality of node \( i \). Generally, \( L(i) \) consists of the \( l \) closest neighbours of node \( i \) in the network. Locality constraints restrict spectral components of \( R_c \) and \( M_c \) (or \( \Phi_x \) and \( \Phi_u \)) to have nonzero support only over the allowed localities; i.e.

\[
R_c(k)_{i,j} = 0 \quad \forall j \notin L(i) \\
BM_c(k)_{i,j} = 0 \quad \forall j \notin L(i)
\]

where \( B \) is the actuation matrix of the system.

For a system with nodes arranged in a chain configuration and \( L(i) \) equal to the \( l \) closest neighbours of node \( i \), these
constraints result in banded diagonal $R_c(k)$ and $M_c(k)$ with a band width of $2l + 1 \forall k$.

When we apply locality constraints on the implementation matrices as per (21), we enforce that node $i$ will only communicate with nodes in $L(i)$ for all time. When we apply locality constraints on the closed-loop maps (i.e. replace $R_c$ and $M_c$ in (21) with $\Phi_x$ and $\Phi_u$), we limit how far a disturbance at a node spreads before it is contained. While both are useful, controller locality tends to be a hard constraint that arises from physical limitations in the communication network, while closed-loop locality is a soft constraint that can be relaxed.

B. Delay

Let $d(i,j)$ denote the delay from node $j$ to node $i$. In general, $d(i,j)$ is proportional to the distance between nodes $i$ and $j$. Delay constraints are like time-varying locality constraints with an expanding locality, where $L(i)$ at time $k$ contains all nodes $j$ for which $k \geq d(i,j)$. Delay constraints are enforced as follows:

$$R_c(k)^{i,j} = 0 \quad \forall k < d(i,j)$$
$$BM_c(k)^{i,j} = 0 \quad \forall k < d(i,j)$$

(22)

where $B$ is the actuation matrix of the system.

For a system in a chain configuration and $d(i,j)$ proportional to inter-nodal distance, these constraints result in banded diagonal $R_c(k)$ and $M_c(k)$, with wider bands for higher values of $k$.

When we apply delay constraints on the implementation matrices as per (22), we are ensuring that controllers do not require information that cannot be communicated to them in time. For example, node $i$ cannot use any information about node $j$ that is more recent than $t - d(i,j)$. When we apply delay constraints on the closed-loop maps (i.e. replace $R_c$ and $M_c$ in (22) with $\Phi_x$ and $\Phi_u$), we limit how fast a disturbance at node $j$ propagates to the state and input at node $i$. As with locality, the controller delay constraint tends to be a hard constraint arising from physical communication limitations. Unlike in the locality case, the closed-loop delay constraint serves no clear purpose; by separating the controller design from the closed-loop design, we avoid imposing this unnecessary constraint on the closed-loop map.

C. Delay and locality as optimization objectives

We can augment the objective in (20) with the following terms to encourage tolerance for communication delay:

$$\sum_{k=1}^{T_c} \sum_{i=1}^{n} \sum_{j=1}^{n} e^{-d(i,j)k} \left( \|R_c(k)^{i,j}\| + \|BM_c(k)^{i,j}\| \right)$$

(23)

where $d(i,j)$ is the distance between nodes $i$ and $j$ in the network.

We can encourage tolerance for communication locality by using similar terms (note the removal of $k$ from the exponential weight):

$$\sum_{k=1}^{T_c} \sum_{i=1}^{n} \sum_{j=1}^{n} e^{-d(i,j)} \left( \|R_c(k)^{i,j}\| + \|BM_c(k)^{i,j}\| \right)$$

(24)

Again taking the chain configuration as an example, these terms encourage banded-diagonal $R_c(k)$ and $M_c(k)$ with higher penalties on elements farther away from the diagonal. Elements that survive despite heavy penalty represent edges in the network that require fast communication in order to best preserve the desired closed-loop map.

VII. EXAMPLES

All subsequent analysis was done on MATLAB using the cvx toolbox with SDPT3 on the low precision setting. The optimization was done on a laptop with an Intel i7 processor and 8GB of RAM.

The system we work with is a 10-node chain with the following tridiagonal $A$ matrix:

$$A = \begin{bmatrix} 0.6 & 0.4 & 0 & \ldots \\ 0.4 & 0.2 & \ddots & \vdots \\ \vdots & \ddots & \ddots & 0.2 & 0.4 \\ \vdots & \vdots & \ddots & 0.4 & 0.6 \end{bmatrix}$$

(25)

The system has three actuators, located at nodes 3, 6, and 10. The system is marginally stable, with a spectral radius of 1. General observations below extend to larger chains with similarly sparse actuation.

A. Low-norm centralized controllers

We first synthesize a desired closed-loop map via SLS, with no communication or locality constraints. We use an FIR horizon of $T = 20$ and an LQR objective. We then synthesize unconstrained controllers using (20) with an additional $L_1$ regularization term on $(R_c, M_c)$. We synthesize controllers with order ranging from $T_c = 2$ to $T_c = 25$.

Fig. 2 shows the differences between the desired closed loop maps $(\Phi_x, \Phi_u)$ and the implemented closed-loop maps $(\Phi_x, \Phi_u)$, normalized by $\|\Phi_x\|$ and $\|\Phi_u\|$, respectively. As expected, the closed loop differences decrease with increasing $T_c$. Interestingly, we are able to approximate the system relatively well even for $T_c \ll T$; at $T_c = 2$, we are less than 10% away from the optimal closed-loop map.

Fig. 2 also shows the spectral radii of $A_z$. The spectral radius of the original controller is far lower than that of the new controllers, suggesting a possible tradeoff between controller norm and internal stability margins. All implementations are internally stable, and spectral radius remains relatively constant over $T_c$. 
Lastly, Fig. 2 shows the $L_1$ norms of the implementation matrices. All new controllers have significantly lower norm than the original controller, and $L_1$ norm remains almost constant over $T_c$.

### B. Localized LQR controller

In this example, separating closed-loop synthesis from controller synthesis yields much better results than the original synthesis procedure, in which controller and closed-loop synthesis are coupled.

The objective of this example is to synthesize a controller with an LQR objective and FIR horizon of $T = 20$. An SLS formulation of LQR can be found in [11]. The following constraints must be obeyed: the controller at each node is only allowed to use information from its two neighbouring nodes, and communication speed is restricted to be the same speed as propagation speed.

Directly applying the constraints to the closed-loop map renders the standard SLS problem infeasible (“Constrained CL map” in Table I), the algorithm cannot find a controller that meets the constraints. We use the virtual localization technique introduced in [9] to synthesize a controller that meets these constraints (“Virtually local” in Table I), while relaxing the constraints on the closed-loop map.

We then apply our proposed two-step procedure. First, we synthesize the desired closed-loop maps ($\Phi_x$, $\Phi_u$) via SLS without communication and locality constraints. We use these closed-loop maps to implement a centralized controller for comparison purposes (“FIR centralized” in Table I). We then synthesize a controller subject to the communication and locality constraints (“Two-step” in Table I), using (20) with $L_1$ regularization. We synthesize one low-order controller with order $T_c = 2$, and one full-order controller with $T_c = T$.

For all controllers, we evaluate the LQR cost, spectral radius of the internal dynamics, and $L_1$ norm of the implementation matrices. The LQR cost is normalized by the optimal infinite horizon LQR cost. Results are shown in Table I.

#### TABLE I

| Controller                  | LQR cost | Spectral radius | $L_1$ norm |
|-----------------------------|----------|-----------------|------------|
| FIR centralized             | 1.001    | 0.214           | 9.688      |
| Constrained CL map          |          |                 |            |
| Virtually local             | 1.294    | 0.847           | 9.704      |
| Two-step, $T_c = T$         | 1.033    | 0.876           | 1.495      |
| Two-step, $T_c = 2$         | 1.034    | 0.851           | 1.426      |

In this example, both the full-order and low-order controller (“Two-step”) give an LQR cost increase of about 3% over the optimal infinite-horizon controller. In contrast, the virtually local controller incurs a cost increase of nearly 30%.

All synthesized controllers are internally stable, with spectral radius less than one. The centralized controller has lower spectral radius than the constrained controllers, which have comparable spectral radii. Additionally, both of our controllers are able to attain an $L_1$ norm that is very close to the $L_1$ norm achieved in the previous example, despite much more severe constraints. Overall, our proposed two-step synthesis procedure generates a controller that performs better than the controller generated by existing techniques, without sacrificing internal stability margins.

Interestingly, the low-order controller performs almost as well as the full-order controller, with only 0.1% performance degradation. This suggests that in this case, highly delayed information (which correspond to higher order terms of the implementation matrices) are not very useful to the controller.

#### VIII. CONCLUSIONS AND FUTURE WORK

By separating controller synthesis from closed-loop synthesis, we are able to apply constraints to the controller without unnecessarily limiting the closed-loop map. As demonstrated above, our proposed two-step procedure offers benefits over the original single step procedure. This procedure offers a new perspective on system-level controller design, and an alternative approach for regimes in which standard SLS is infeasible. In future work, we would like to better understand how our method relates to the existing work on virtually localized SLS, and which types of problems each method is better suited to. Additionally, we would like to extend this work to the output feedback case.

Synthesis methods mentioned in this paper can be found in the SLS-MATLAB toolbox at [https://github.com/ls-caltech/sls-code](https://github.com/ls-caltech/sls-code).

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