A Novel Nonsingular Terminal Sliding Mode Control-Based Double Interval Type-2 Fuzzy Systems: Real-Time Implementation

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Abstract: Extensive use of wind turbine (WT) systems brings remarkable challenges to the stability and safety of the power systems. Due to the difficulty and complexity of modeling such large plants, the model-independent strategies are preferred for the control of the WT plants which eliminates the need to model identification. This current work proposes a novel model-independent control methodology in the rotor side converter (RSC) part to ameliorate low voltage ride through (LVRT) ability especially for the doubly-fed induction generator (DFIG) WT. A novel model-independent nonsingular terminal sliding mode control (MINTSMC) was developed based on the principle of the ultra-local pattern. In the suggested controller, the MINTSMC scheme was designed to stabilize the RSC of the DFIG, and a sliding-mode supervisor was adopted to determine the unknown dynamics of the proposed system. An auxiliary dual input interval type 2 fuzzy logic control (DIT2-FLC) was established in a model-independent control structure to remove the estimation error of the sliding mode observer. Real-time examinations have been carried out using a Real-Time Model in Loop (RT-MiL) for validating the applicability of the proposed model-independent control in a real-time platform. To evaluate the usefulness and supremacy of the MINTSMC based DIT2-FLC, the real-time outcomes are compared with outcomes of RSC regulated conventional PI controller and MINTSMC controller.

Keywords: wind turbine (WT); rotor side converter (RSC); low voltage ride through (LVRT); model-independent nonsingular terminal sliding mode control (MINTSMC); dual input interval type 2 fuzzy logic control (DIT2-FLC)

1. Introduction

As the world is experiencing a diminishing trend in terms of fossil fuels and their bad effects on the environment through emitting harmful gases into the atmosphere, wind energy has become the fastest-growing renewable energy source that has an eco-friendly power generation [1–5]. For this reason and also due to the advanced technological equipment and facilities, the number of wind turbine installations along with their size has increased for converting the wind’s kinetic energy into electrical energy. Moreover, wind farms are replacing conventional power generating techniques that worked with fossil fuels as their energy resource. Therefore, the existing grid codes are not valid anymore and they should be modified to remain stable and act the same as synchronous generators during voltage dips [6–8]. One of the most important factors in grid codes for ensuring the
stability of voltage is the low-voltage ride through (LVRT). LVRT specifies the ability of wind farms to stay in a grid-connected mode during the occurrence of a voltage sag/dip. The LVRT compares the voltage terminal with its minimum characteristics, thus if the voltage is below these characteristics the generator will trip, otherwise, the generator must remain connected and support the grid. Therefore, the functionality of the LVRT will increase the reliability of the systems during fault occurrences [9–11].

Voltage dips and fault occurrence present wind turbines system, especially the DFIG, with many difficulties of being connected to the power grid that overwhelms the LVRT’s ability during voltage dips because the stator flux of stator does not track abrupt alteration at its voltage, as well as the DC part in the flux of stator reveals [12–14]. While the electrical rotor keeps rotating and also because of the impact of speed, it is more likely that an overvoltage and overcurrent event will occur. Moreover, voltage dips can cause oscillations in the electrical currents, active power, and also reactive power of the machine. Besides, the condition is much worse for the asymmetrical faults, such that they produce a negative sequence component with a very high slip in the stator voltage and create more damages. In these situations, the rotor side converter (RSC) is commonly blocked by crowbar circuits to protect it from the overcurrent and overvoltage durations, whereby crowbar circuits do so by putting the rotor windings in short circuit and bypassing the RSC, and as a result, the connectivity of the system to the power grid and its LVRT will devastate [15]. Thus, several works have designed or improved the control algorithms of the RSC to ride it through fault occurrences instead of activating and using crowbar circuits to increase their LVRT factor. In [16], a novel fault current limiter with help of reactive power and inductance emulating control is developed to improve the LVRT factor, while other works used R-SFCL independent from the control parts. Besides, the authors of [10] designed a new reactive power control strategy by combining the rotor side, as well as grid side converter (GSC), for improving stability and LVRT factor of the system.

Sliding mode control (SMC) has been a robust method to control nonlinear models that have uncertainty in their system model and parameters, and thus, need to arrive at a high performance under external disturbances [17]. Moreover, it comprises of double steps, sliding surface and off surface dynamics, it first defines the sliding surface, and then tries to keep the state of the system near to or on that surface to satisfy the desired goals and performance [18]. Today, to improve its robustness and precision and also for confronting its drawbacks such as finite-time stability and rapid response, some methods like terminal SMC (TSM) [19], the nonsingular TSM (NTSM) [20,21], fast TSM (FTSM) [22], and nonsingular fast TSM (NFTSM) [23] have been developed. NTSM, FTS, and NFTSM have been developed to increase time convergence and elimination of singularity problems, and TSM adds the nonlinear element in sliding variables that have been developed for limited period convergence for state variables, having a more precise and faster tracking and improving the transient performance.

The interval type 2 fuzzy logic control (IT2-FLC) is an extension of classic (Type-1) FLCs and has been a remarkable breakthrough in the field of computational intelligence [24–28]. Particularly, the IT2-FLCs have been developed in different areas of engineering such as robotics [29,30], networked systems [31], coaxial tri-rotor aircraft [32], load frequency control [26], and ship power systems [33]. The IT2-FLCs provide a high level of uncertainty suppression, as well as handling the non-linear dynamics than its T1-FLCs and traditional counterparts. These advantages are reached owing to an extra degree of independence prepared using the footprint of uncertainty (FOU), which is realized by their T2 fuzzy sets (T2-FSs). However, the efficiency and robustness of the IT2-FLCs depend on the FOU coefficients that impose a great challenge in its implementation in sophisticated practical systems. To solve the shortcomings related to classic IT2-FLCs, single input IT2-FLCs (SIT2-FLCs) [5,34–36] and double input IT2-FLCs (DIT2-FLCs) [37–39] have been characterized based on control surfaces. Several research works show that by adjusting only the FOU coefficients, a specific degree of robustness can be offered in a straightforward scheme.
In this paper, an innovative model-independent control methodology was developed for the regulation of RSC to boost the LVRT performance in the DFIG WT plant, without accurate knowledge of the system dynamics. The main idea is to implement a MINTSMC based DIT2-FLC controller for the DFIG system to enhance the stability of the system by appropriate control of the RSC.

The main purposes of the current article could be assessed as

A MINTSMC controller with SM observer was adopted for the internal loop control of RSC in a DFIG WT test system. An auxiliary DIT2-FLC controller was established to improve the DFIG performance and eliminate the SM observer estimation error. The comprehensive real-time model-in-the-loop (RT-MiL) examinations were made to validate the applicability of the suggested MINTSMC based DIT2-FLC controller in a real-time testbed.

This work is arranged as follows. The mathematical modeling for the DFIG WT plant is formulated in Section 2. In Section 3, the MINTSMC controller along with the auxiliary DIT2-FLC controller is provided. Section 4 describes real-time outcomes of the MiL platform to affirm the theoretical assertions. Finally, the conclusion of the current work is presented in Section 5.

2. Modeling of DFIG WT Test-System

The DFIG WT schematic diagram is demonstrated in Figure 1, connecting to the power grid using a power transformer. The DFIG WT system includes a control system, a back-to-back PWM converters drive train, induction generator, wind turbine, and blade, while its control structure comprises two significant levels, the DFIG and WT control. First, the WT control is responsible for creating the desired magnitude of the DFIG rotor speed using calculated wind pace and an optimal power-speed feature curve that adjusts wind turbine mechanical power through the pitch angle.

**Figure 1.** Schematic diagram of the DFIG WT structure.

2.1. Modeling of the Generator System

Using the d-q reference frame rotating at synchronous speed, the equations of stator voltage, as well as rotor circuit drove are as follows [40,41].

\[
\begin{align*}
    u_{ds} &= R_s i_{ds} - \omega_s q_i_{qs} + \frac{1}{\omega_b} \frac{d q_i_{qs}}{dt} \\
    u_{qs} &= R_s i_{qs} - \omega_s q_i_{ds} + \frac{1}{\omega_b} \frac{d q_i_{ds}}{dt} \\
    u_{dr} &= R_r i_{dr} - (\omega_s - \omega_r) q_i_{qr} + \frac{1}{\omega_b} \frac{d q_i_{qr}}{dt} \\
    u_{qr} &= R_r i_{qr} - (\omega_s - \omega_r) q_i_{dr} + \frac{1}{\omega_b} \frac{d q_i_{dr}}{dt}
\end{align*}
\]  

(1)
where \( u_{ds} \) and \( u_{qs} \) represent the electrical stator voltage of the \( d \) and \( q \) axis of the wind turbine, respectively. \( u_{dr} \) and \( u_{qr} \) denote the electrical rotor voltage of the \( d \) and \( q \) axis of the wind turbine, respectively. \( \omega_s \) is the electrical synchronous speed of a wind turbine generator. \( \omega_r \) is the electrical speed of the rotor, and \( \omega_b \) is the base speed of the wind turbine. Likewise, \( R_s \) and \( R_r \) are defined as the electrical resistance of stator and rotor of the generator, respectively. \( \phi_{qs} \), \( \phi_{ds} \), \( \phi_{qr} \) and \( \phi_{dr} \) are the electrical fluxes of the generator for the stator and rotor [11].

### 2.2. Modeling of the Drive Train System

The model of the two-mass drive train is expressed using these equations

\[
\frac{d\omega_r}{dt} = \frac{1}{2H_g} (T_{sh} - T_e - B\omega_r) \tag{2}
\]

\[
\frac{d\theta_t}{dt} = \omega_p (\omega_t - \omega_r) \tag{3}
\]

\[
\frac{d\omega_t}{dt} = \frac{1}{2H_t} (T_m - T_{sh}) \tag{4}
\]

where \( \omega_t \) defines as the speed of the wind turbine, \( H_g \) and \( H_t \) are respectively the generator and turbine inertia constants. \( B \) defines as generator friction component. \( \theta_t \) defines as the angle of shaft in terms of a radian, and input torques of the wind turbine, \( T_e \), \( T_{sh} \), and \( T_m \) that are the electromagnetic torque, the shaft torque, and the mechanical torque, respectively, and they formulate as:

\[
T_e = L_m (i_{qs}i_{dr} - i_{ds}i_{dr}) \tag{5}
\]

\[
T_{sh} = K_{sh}\theta_t + D_{sh}\omega_p (\omega_t - \omega_r) \tag{6}
\]

\[
T_m = \frac{0.5\rho\pi R^2 C_P(\lambda, \beta)V_w^3}{\omega_t} \tag{7}
\]

where \( \rho \), \( R \), \( \beta \), \( V_w \), and \( C_p \) are the air density, turbine radius, angle of the pitch, wind speed, and power coefficient, in which the power coefficient formulates as

\[
C_p = 0.22 \left( \frac{116}{\lambda_i} - 0.4\beta - 5 \right) e^{-12.5/\lambda_i} \tag{8}
\]

\[
\lambda_i = \frac{1}{\left( \lambda + 0.08\beta \right) - 0.035 \left( \beta^3 + 1 \right)} \tag{9}
\]

where \( \lambda = \omega_t R / V_w \) defines as blade tip speed ratio, and \( C_p(\lambda, \beta) \) arrives at its maximum, \( C_p^{\text{max}} \) for optimum tip speed ratio, \( \lambda_{\text{opt}} \).

The wind turbine power regulation shown in Figure 2 is accomplished by adjusting the generator/turbine speed along the optimal attribute curve of the power speed that correlates with the highest wind received energy [42]. The DFIG runs in variable speed mode while wind velocity exists between the lower limit and nominal value, where pitch angle \( \beta \) and blade tip speed ratio \( \lambda \) are held constant to their optimal value. If the wind velocity is greater compared to the standard value, then pitch control can regulate the overrated turbine power output and reduce the generator’s speed. It can be seen that the rotor speed of the electrical rotor relation in its controller solidifies for minimum magnitude where wind velocity is higher compared to the cut-in value. It is also smaller than the lower limit to support the generator slip at less than 0.3.
2.3. Rotor Side Converter (RSC) Control

The structure control for RSC when it is operating normally is depicted in Figure 3a. The induction generator is regulated for decoupling rotor excitation current and electromag- netic torque, using a synchronously rotating reference frame named the stator-flux oriented reference frame. Meanwhile, the \(d\)-axis component is oriented with the vector position of the stator-flux. To regulate the outer control loops mean reactive power and speed of the rotor, and inner control loops mean current of the rotor, conventional proportional integral (PI) controllers are employed [42,43]. The reference reactive power and wind turbine speed are the input variables that are compared with their actual components and then pass from PI controllers to create reference currents of the rotor in the DQ axis. In the same procedure, these variables were compared with their real components to create the reference voltages of the rotor in the DQ axis. However, for creating the \(u_{qr}\) component, MINTSMC was employed to improve the overall performance of the system, which will be explained in detail. After that, these variables need to be decoupled for sending to PWM.

In addition, an over-current occurring in the rotor and stator circuits during short-term low voltage faults can be quenched by reducing the DFIG WT system’s imbalanced flowing energy. The presented LVRT control procedure that is shown in Figure 3b would be activated by the minimum controlled variable, containing current of rotor and stator, the voltage of DC-link and grid section, surpasses their safety value, respectively, owing to disturbances. In this technique, for transforming the received energy from the wind into kinetic energy in WT inertia, the rotor side controller can enhance the speed of the rotor by reducing generator torque around zero within disturbance. After fault clearing, the elevated kinetic energy is converted to the WT system.

2.4. Grid Side Converter (GSC) Control

The control structure of the GSC in DFIG operation mode is shown in Figure 4, where \(u_a\) and \(i_p\) are the DC link voltage and inductor current vector of the GSC. Besides, the control procedure works in a grid voltage reference frame that is an asynchronously rotating reference frame with its axis centered towards vector control of grid voltage. Thereby it acquires autonomous control of the active power, as well as reactive power parameters, between the power grid and GSC.
2.4. Grid Side Converter (GSC) Control

The control structure of the GSC in DFIG operation mode is shown in Figure 4, where \( u_a \) and \( i_L \) are the DC link voltage and inductor current vector of the GSC. Besides, the control procedure works in a grid voltage reference frame that is an asynchronously rotating reference frame with its axis centered towards vector control of grid voltage. Thereby it acquires autonomous control of the active power, as well as reactive power parameters, between the power grid and GSC.

\[
\begin{align*}
    \omega_v & = \frac{L_s}{L_m \psi_s} \frac{dL_s}{d\theta} + \alpha \frac{i_{dr}}{L_s} \\
    \omega_{slip} & = \frac{L_m \psi_s}{L_s} + \alpha \frac{i_{dr}}{L_s} \\
    \omega_{slip} & = \frac{L_m \psi_s}{L_s} + \alpha \frac{i_{dr}}{L_s}
\end{align*}
\]

Figure 3. The control structure of the RSC in (a) normal operation, and (b) during a grid fault.

Figure 4. Control structure for the GSC.

3. Design of Model-Independent NTSMC Based DIT2-FLC

3.1. Model-Independent NTSMC Technique

Definition 1. The nonlinear system dynamics that includes un-model dynamics and disturbances, by considering the model-independent control techniques \cite{44,45} and the approximate ULM form can be expressed as

\[
y^{(\lambda)}(t) = \varepsilon + \alpha u_v(t)
\]

where \( y(t) \) is the output, \( \nu \) is the derivative order, \( \varepsilon \) includes un-model dynamics along with disturbances, that is estimated based on the input signal \( u(t) \) and output \( y(t) \), \( \alpha \in \mathbb{R} \) is an assumed constant.

Definition 2. New state variables, \( x_1 \) and \( x_2 \) are introduced for determination of the sliding surface as:
\[
\begin{align*}
\left\{ \begin{array}{l}
x_1 = \int_0^t e_1(t) \, dt \\
x_2 = e_1(t)
\end{array} \right. \quad (11)
\end{align*}
\]

Then, the state-space equations derive as Equation (10) based on Equation (11):
\[
\begin{align*}
\left\{ \begin{array}{l}
\dot{x}_1 = x_2 \\
\dot{x}_2 = \dot{x}^* - \alpha u_v - \varepsilon
\end{array} \right. 
\end{align*} \quad (12)
\]

Using the SMC framework, and proposed definitions, a second-order NTSM is presented in Equation (13) with the designed parameter \( \gamma \), and the \( p \) and \( q \) are odd defined as \( 1 < p/q < 2 \).
\[
s_1 = x_1 + \gamma x_2^{p/q} \quad (13)
\]

Which taking the derivative of Equation (13) results in
\[
\dot{s}_1 = \dot{x}_1 + \gamma \frac{p}{q} x_2^{p/q-1} x_2 = x_2 + \gamma \frac{p}{q} x_2^{p/q-1} (\dot{x}^* - \alpha u_v - \varepsilon) \quad (14)
\]

**Theorem 1** [46]. The error in the specified state-space (12) converges to zero in finite time, \( e \to 0 \), if the NTSMC manifold is considered as (13) and control law is formulated as follows
\[
u_v = \frac{1}{\alpha} \left[ -\hat{\varepsilon} + \hat{x}^* + \frac{1}{\gamma} \frac{q}{p} x_2^{2-p/q} + \eta_1 \text{sgn}(s_1) + \eta_2 s_1 \right] \quad (15)
\]

where, \( \hat{\varepsilon} \) is the estimated value for \( \varepsilon \), \( \eta_1 \) and \( \eta_2 \) are the designed parameters, and \( \eta_1 > \|\hat{\varepsilon} - \varepsilon\| + \mu (\mu > 0) \).

**Proof.** By considering the Lyapunov function as:
\[
V_1 = \frac{1}{2} s_1^T s_1 \quad (16)
\]

And substituting (14) into its derivative, one can obtain:
\[
\dot{V}_1 = s_1^T \dot{s}_1 \\
= s_1^T \left( \gamma \frac{p}{q} x_2^{p/q-1} (\hat{\varepsilon} - \varepsilon - \eta_1 \text{sgn}(s_1) - \eta_2 s_1) \right) \\
= s_1^T \gamma \frac{p}{q} x_2^{p/q-1} (\hat{\varepsilon} - \eta_1 \text{sgn}(s_1) - \eta_2 s_1) \quad (17)
\]

where the estimation error \( \hat{\varepsilon} = \tilde{F} - F \) is bounded because
\[
1 < p/q < 2, \quad 0 < p/q - 1 < 1 \quad (18)
\]

And exists \( x_2^{p/q-1} > 0 \) for \( p \) and \( q \) are odd constants.

when \( \eta_1 > \varepsilon + \mu (\mu > 0) \), the Equation (18) will be converted into:
\[
\dot{V}_1 \leq \gamma \frac{p}{q} x_2^{p/q-1} \left( -\mu ||s_1|| - \eta_2 ||s_1||^2 \right) \leq 0 \quad (19)
\]

Thus, the stability of the controller will be satisfied by the demand of the Lyapunov theory. \( \square \)
3.2. Design of SM Observer

For dealing with un-model dynamics, input disturbances, and due to the superior characteristics of sliding-mode (SM) observers, for example, their simplicity, flexibility, and robust control framework, an observer-based SM was adopted in this work. The overall control structure of NTSMC along with the SM observer can be seen in Figure 5. Using the SM observer, the unknown parts of the block converter, \( \Phi \), which can be seen in Equation (8) are estimated as \( \hat{\Phi} \) using the following ultra-local model formulation [46]:

\[
\hat{x} = \sigma \text{sgn}(x - \hat{x}) + \Lambda u
\]

where \( \hat{x} \) denotes the observed value of \( x \); \( \sigma \text{sgn}(x - \hat{x}) \) represents the term relating to sliding control, also \( \sigma \) is defined as the designed coefficient and \( \text{sgn}(\bullet) \) symbolizes a sign function.

![Figure 5. Illustration of the MINTSMC controller with an SM observer.](image)

Then, by considering the sliding surface of SM as \( e_2 = x - \hat{x} \) and subtracting Equation (10) from Equation (20), the error dynamic derives as

\[
\dot{e}_2 = \Phi - \sigma \text{sgn}(x - \hat{x})
\]

**Theorem 2** [46]. For (13), choosing the SM manifold as \( s = e \) and \( \sigma \) appropriately results in the asymptotical convergence of the error to zero.

**Proof.** If the Lyapunov function be chosen as

\[
V = \frac{1}{2} s^2
\]

By differentiating Equation (22) concerning time, the following inequality derives

\[
\dot{V} = ss = e_2\dot{e}_2 = e_2(\Phi - \sigma \text{sgn}(e_2)) \leq \|e_2\|(\|\Phi\| - \sigma)
\]

Now, supposing that \( \sigma \) satisfies the condition of \( \|\Phi\| + \eta < \sigma \), where \( \eta > 0 \), according to Equation (16), one can obtain

\[
\dot{V} \leq -\eta\|e_2\|
\]

This implies the asymptotical stability of the observer. □

3.3. Dual Input Interval Type 2 FLC

3.3.1. General Structure of Dual Input IT2-FL

In Figure 6, the overall diagram of the PI type DIT2-FPI based on the RSC control of the DFIG WT is demonstrated. First, \( e \) and \( de/dt \) are normalized for the discourse universe of membership functions (MFs), i.e., \([-1,+1]\) by embedding the scaling factors (SFs) \( G_p \).
and $G_i$ in the structured controller. Then, they are altered to $\lambda_1$ and $\lambda_2$, respectively, and also control the output parameter $O_{DIT2}$ is created based on the IT2 controller output ($\varphi_o$).

$$O_{DIT2} = G_p \varphi_o + G_i \int \varphi_o dt$$  \hspace{1cm} (25)

In which the $G_p$ and $G_i$ parameters are factors of the baseline PI structure.

![DIT2-FPI Diagram](image)

**Figure 6.** Diagram of DIT2-FPI controller.

3.3.2. DIT2-FL Design Strategy

This section presents the footprint on uncertainty (FOU) and the mathematical concepts of IT2-FL [35,47], which have a similar structure to its type-1 (T1) counterpart. A significant distinction somehow defines which fuzzy sets of rule-based models are based on the IT2 fuzzy set (IT2-FS). As a result, their constituent information assesses for distinguishing the contrasts between two T1-FS and IT2-FS, while the type 2 fuzzy set, referred as $\tilde{A}$, is explained as:

$$\tilde{A} = \{(\lambda, u, 1) \mid \forall \lambda \in \Lambda, \forall u \in U_\lambda \subseteq [0, 1]\}$$  \hspace{1cm} (26)

Then, the unification of every primary MF (FOU) is demonstrated as:

$$FOU (\tilde{A}) = \bigcup_{\forall \lambda \in \Lambda} U_\lambda$$  \hspace{1cm} (27)

FOU is distinguished in IT2-FLs, by duo type-1 MFs, while $\pi_{\tilde{A}}(\lambda)$ and $\mu_{\tilde{A}}(\lambda)$ represents its upper and lower bounds $\forall \lambda \in \Lambda$, respectively, as follows

$$\pi_{\tilde{A}}(\lambda) = FOU (\tilde{A}) \quad \forall \lambda \in \Lambda$$  \hspace{1cm} (28)

$$\mu_{\tilde{A}}(\lambda) = FOU (\tilde{A}) \quad \forall \lambda \in \Lambda$$  \hspace{1cm} (29)

Normally, the structural rule of DIT2-FL by considering the $N$ rule is defined as:

$$R_n : \text{if } \lambda_1 = \tilde{A}_{1,n} \text{ and } \lambda_2 = \tilde{A}_{2,n}, \text{ THEN } \varphi_o \text{ is } C_n$$  \hspace{1cm} (30)

where the overall firing for the $n$th rule $F_n(\lambda) = \left[f_n(\lambda), \overline{f}_n(\lambda)\right]$ is:

$$\begin{align*}
    f_n(\lambda) &= \mu_{\tilde{A}_{1j}}(\lambda_1) \ast \pi_{\tilde{A}_{1j}}(\lambda_2) \\
    \overline{f}_n(\lambda) &= \pi_{\tilde{A}_{1j}}(\lambda_1) \ast \mu_{\tilde{A}_{1j}}(\lambda_2)
\end{align*}$$  \hspace{1cm} (31)

Based on the rules of Equation (32), a DIT2-FL takes the crisp inputs $\lambda = [\lambda_1 \lambda_2]^T$, and returns its corresponding crisp output value, $\varphi_o$. In this work, for simplification of the analytical derivation and reducing the complexity of the controller design, MFs were characterized using the IT2-FSs triangular as depicted in Figure 7a [37]. The symmetrical MFs are adopted as illustrated in Figure 7b, respectively. As a result, $m_j$ is defined as:

$$m_{j,1} = m_{j,2} = m_{j,3} = \delta_j$$  \hspace{1cm} (32)
Using centroid defuzzification for type reduction of the IT2-FLC results in the defuzzified crisp output as:

\[
\varphi^{IT2}(\lambda) = \left( \varphi^{IT2}_r + \varphi^{IT2}_l \right) / 2
\]

In which \( \varphi^{IT2}_l \) and \( \varphi^{IT2}_r \) signify endpoints of the type of reduced set which is demonstrated in Reference [36], and using iterative algorithms is common for obtaining the switching points (L, R) in these equations.

\[
\begin{align*}
\varphi^{IT2}_l &= \frac{\sum_{n=1}^{L} f_n(\lambda) C_n + \sum_{n=L+1}^{N} f_n(\lambda) C_n}{\sum_{n=1}^{N} f_n(\lambda) C_n} \\
\varphi^{IT2}_r &= \frac{\sum_{n=1}^{R} f_n(\lambda) C_n + \sum_{n=R+1}^{N} f_n(\lambda) C_n}{\sum_{n=1}^{N} f_n(\lambda) C_n}
\end{align*}
\]

Table 1 shows all of the consequent MFs that can comprehend by a parameterized function \( \lambda \) according to three rules named negative (N), big (B), and zero (Z). Thus, triple region modes can construct, i.e., \([L = 3, R = 3]\), \([L = 3, R = 6]\), and \([L = 6, R = 6]\), in which a region (\( \Gamma_1, \Gamma_2, \Gamma_3 \)) is explained at the \([\lambda_1 \lambda_2]\) plane for each mode, deploying the two separator counters \( \beta_{12} \) and \( \beta_{23} \), as provided in Table 2. Moreover, \( \beta_{12} \) and \( \beta_{23} \) split \( \Gamma_1 \) from \( \Gamma_2 \) and \( \Gamma_2 \) from \( \Gamma_3 \), respectively. The areas of \( \Gamma_1, \Gamma_2 \) and \( \Gamma_3 \) are devoted to fuzzy mapping (FM), \( \varphi^{IT2}_{12}(\lambda) \), \( \varphi^{IT2}_{23}(\lambda) \) and \( \varphi^{IT2}_{3}(\lambda) \), similarly. Based on Equation (22), \( \varphi^{IT2}(\lambda) \) is separated as [39]:

\[
\varphi^{IT2}(\lambda) = \begin{cases} \\
\varphi^{IT2}_{12}(\lambda) = \left( \varphi^{IT2}_{L=0}(\lambda) + \varphi^{IT2}_{R=0}(\lambda) \right) / 2, & \lambda \in \Gamma_1 \\
\varphi^{IT2}_{23}(\lambda) = \left( \varphi^{IT2}_{L=0}(\lambda) + \varphi^{IT2}_{R=6}(\lambda) \right) / 2, & \lambda \in \Gamma_2 \\
\varphi^{IT2}_{3}(\lambda) = \left( \varphi^{IT2}_{L=6}(\lambda) + \varphi^{IT2}_{R=0}(\lambda) \right) / 2, & \lambda \in \Gamma_3
\end{cases}
\]
Table 1. Rule base of the DIT2-FLC.

| $\lambda_2$ | $\lambda_1$ | N | Z | P |
| --- | --- | --- | --- | --- |
| N | N | N | Z |  |
| Z | N | Z | P |  |
| P | Z | P | P |  |

Using Equation (34), the left and right of endpoints of $\varphi^{IT2}(\lambda)$ are obtained as:

\[
\varphi^{IT2}_{L=3}(\lambda) = \frac{\lambda_1(\lambda_2 + 1) - \lambda_1\lambda_2 + \lambda_2(\lambda_1 + 1)}{\delta_1\delta_2(\lambda_1 + 1)(\lambda_2 + 1) - \lambda_1 - \lambda_2(\lambda_1 + 1)} 
\]

\[
\varphi^{IT2}_{L=6}(\lambda) = \frac{\delta_1\delta_2\lambda_2 - \delta_1\delta_2\lambda_1 + (\lambda_2 - 1)}{(\lambda_1 - 1)(\lambda_2 - 1) + \delta_1\delta_2(\delta_1 + \delta_2 - \delta_2\delta_1)} 
\]

\[
\varphi^{IT2}_{R=3}(\lambda) = \frac{\delta_1\delta_2\lambda_1 + \delta_1\delta_2\lambda_2(\lambda_1 + 1)}{(\lambda_1 + 1)(\lambda_2 + 1) - \delta_1\delta_2(\lambda_1\lambda_2 + \lambda_1 + \lambda_2)} 
\]

\[
\varphi^{IT2}_{R=6}(\lambda) = \frac{\lambda_1(1 - \lambda_2) + \lambda_1\lambda_2 - \lambda_2(1 - \lambda_1)}{\delta_1\delta_2(\lambda_2 - 1)(\lambda_2 - 1) - \lambda_1 - \lambda_2(\lambda_1 - 1)} 
\]

Considering the above equations, $\varphi^{IT2}_{1}(\lambda)$, $\varphi^{IT2}_{2}(\lambda)$ and $\varphi^{IT2}_{3}(\lambda)$ could be achieved from Equation (35).

By determining the divider counters:

\[
\beta_{12} = \left\{ \lambda \in [-1, 1]^2 \mid \varphi^{IT2}_{R=3}(\lambda) = \varphi^{IT2}_{R=6}(\lambda) \right\} 
\]

\[
\beta_{23} = \left\{ \lambda \in [-1, 1]^2 \mid \varphi^{IT2}_{L=3}(\lambda) = \varphi^{IT2}_{L=6}(\lambda) \right\} 
\]

By determining the divider counters:

\[
\beta_{12}(\lambda) = \left\{ \frac{-\delta_1\delta_2\lambda_1}{\lambda_1 - \delta_1\delta_2\lambda_1 + \lambda_1}, \quad \lambda_1 < 0 \right\} 
\]

\[
\beta_{12}(\lambda) = \left\{ \frac{-\delta_1\delta_2\lambda_1}{\lambda_1 + \delta_1\delta_2\lambda_1 - \lambda_1 + \lambda_1 + \lambda_1 + \lambda_1}, \quad \lambda_1 \geq 0 \right\} 
\]

3.4. The Strategy of MINTSMC Based DIT2-FPI Scheme

To ameliorate the LVRT ability of the DFIG WT system, a novel MITSMC based DIT2-FLC controller was developed for the rotor side converter. For stabilization of system outcomes of the DFIG WT system, the performance of the traditional methodologies was restricted in the following aspects:

(i) The PI type of controllers can stabilize the system responses in the normal condition while they are ineffective when the system is subjected to uncertainties and faults.

(ii) The model-based strategies (e.g., MPC and backstepping scheme) need accurate mathematical modeling of the system to restricts their applicability in the complex DFIG systems.
In this application, rather than relying on accurate controlled plant dynamics knowledge, the MITSMC scheme based on the ULM control scheme was proposed. In the control structure, an SM supervisor is created to assess system uncertainties and eliminate their effects on the feedback controller. Moreover, an auxiliary controller-based DI2-FLC was established to reduce the error of the SM observers and improve system efficiency. According to the said descriptions, the overall structure of the MITSMC based DI2-FLC controller is illustrated in Figure 8. Thus, the final law control of the suggested model-independent controller is defined as:

\[
u_c = \frac{1}{\alpha} \left[ -\dot{e} + x^* + \frac{1}{\gamma} p \, x_2 \right] + \eta_1 \text{sgn}(s_1) + \eta_2 s_1 + \Omega_{\text{DI2}} = \frac{1}{\alpha} \left[ -\dot{e} + x^* + \frac{1}{\gamma} p \, x_2 \right] + \eta_1 \text{sgn}(s_1) + \eta_2 s_1 + G_P \varphi_0 + G_i \int \varphi_0 \, dt \quad (44)
\]

Figure 8. Structure of the proposed MINTSMC scheme-based DI2-FPI controller.

4. Results and Discussion

In this section, the DFIG WT test system developed in Section II (Figure 1) was considered to evaluate the applicability of the suggested model-independent controller. For this purpose, the MITSMC based DI2-FLC was established for the internal loop of the RSC while the external loop of RSC, as well as the internal/external loops of GSC, were controlled by a conventional PI controller. The relevant variables for the DFIG test system, established on available data about the literature and industries, are provided in Table 3. To evaluate the efficiency of the suggested MITSMC based DI2-FLC controller, the internal loop control of RSC was also designed by the PI controller and the MITSMC controller.

Table 3. Technical parameters of the DFIG WT test system.

| Parameter                  | Value | Unit |
|----------------------------|-------|------|
| DC link Capacitor          | 10    | mF   |
| Rated power                | 250   | kW   |
| Rated voltage              | 575   | V(rms) |
| Rated frequency            | 50    | Hz   |
| Rated current              | 185   | A(rms) |
| Number of poles            | 4     |      |
| Stator resistor            | 20    | Ωm   |
| Stator leakage inductor    | 0.2   | mH   |
| Rotor resistor             | 20    | Ωm   |
| Rotor leakage inductor     | 0.2   | mH   |
| Magnetizing inductor       | 4.2   | mH   |
| Inertia                    | 0.685 | s     |

To confirm the applicability of the MITSMC based DI2-FLC from a real-time perspective, for the real-time analysis shown in Figure 9, a model in the loop (MiL) testbed with a dSPACE 1202 board is adopted. The AO was adopted in the Simulink environment and in a real-time platform. The solver for this platform was realized based on Runge Kutta’s procedure where the frequency sample time was 10 kHz.
Scenario I:

The wind speed was 15 m/s and the drop voltage occurred in the power grid where its amplitude was half of the grid voltage. The DC-link capacitor voltage and magnitude rotor current of the DFIG system are compared with the PI controller, MINTSMC controller, MINTSMC controller-based DIT2-FLC in Figures 10 and 11, respectively. From Figures 10 and 11, it is revealed that with the application of MINTSMC controller-based DIT2-FLC, the smallest overshoot amplitude, faster-settled behavior, and better dynamical outcomes are achieved compared to the others. Moreover, for ease of analyzing the performance and functionality of the proposed method, the grid voltage and current, along with active/reactive power and generator speed of the DFIG system are presented, as depicted in Figures 12–14 to confirm the stabilization ability of the suggested designed RSC controller. It can be seen from the output results that active power and reactive power reached almost 0.9 MW, 0.1 MVar, after passing the transient state, respectively. Moreover, the generator speed oscillates around 1.2 p.u.

![Figure 9. Overall scheme of the real-time MiL platform.](image_url)

**Figure 9.** Overall scheme of the real-time MiL platform.

![Figure 10. The voltage of the DC link capacitor in the DFIG system for scenario I via the PI controller, MINTSMC controller, MINTSMC controller-based DIT2-FLC.](image_url)

**Figure 10.** The voltage of the DC link capacitor in the DFIG system for scenario I via the PI controller, MINTSMC controller, MINTSMC controller-based DIT2-FLC.

![Figure 11. Magnitude rotor current of DFIG system under scenario I via the PI controller, MINTSMC controller, MINTSMC controller-based DIT2-FLC.](image_url)

**Figure 11.** Magnitude rotor current of DFIG system under scenario I via the PI controller, MINTSMC controller, MINTSMC controller-based DIT2-FLC.

### Technical parameters of the DFIG WT test system

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| Inertia                 | 0.685 | s    |
| Stator resistor         | 20    | Ω    |
| Rotor resistor          | 20    | Ω    |
| Magnetizing inductor    | 4.2   | mH   |
| Stator leakage inductor | 0.2   | mH   |
| Rotor leakage inductor  | 0.2   | mH   |
Figure 10. The voltage of the DC link capacitor in the DFIG system for scenario I via the PI controller, MINTSMC controller, MINTSMC controller-based DIT2-FLC.

Figure 11. Magnitude rotor current of DFIG system under scenario I via the PI controller, MINTSMC controller, MINTSMC controller-based DIT2-FLC.

Figure 12. Grid voltage of the DFIG system under scenario I.

Figure 13. Grid current of the DFIG system under scenario I.

Figure 14. The DFIG responses according to the scenario I (a) measured active power of DFIG WT, (b) measured reactive power of DFIG WT, (c) Generator speed of DFIG system.

Scenario II:
In this scenario, the wind speed was 15 m/s and drop voltage occurred in the power grid where its amplitude was 20% of the grid voltage. Figures 15 and 16 illustrate, respectively, the DC link capacitor voltage, and magnitude rotor current of the DFIG plant for the RSC designed by the conventional PI controller, MINTSMC controller, and MINTSMC controller-based DIT2-FLC. Output results vividly reveal that, based on the recommended RSC controller, the transient behavior for capacitor DC link voltage is ameliorated compared to other methods. It is evident that with the application of the PI controller and MINTSMC, the WT system experienced an overshoot of 1205 V and 1180 V for the voltage of the DC link capacitor, respectively. However, a lower voltage deviation of the DC link capacitor was reached by employing the suggested scheme with the overshoot of 1164 V.

Figures 17 and 18 demonstrate grid voltage and current correspondingly. Moreover, Figure 19 depicts the active power, reactive power, and generator speed of the DFIG plant.
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In this scenario, the wind speed was 15 m/s and drop voltage occurred in the power grid where its amplitude was 20% of the grid voltage. Figures 15 and 16 illustrate, respectively, the DC link capacitor voltage, and magnitude rotor current of the DFIG plant for the RSC designed by the conventional PI controller, MINTSMC controller, and MINTSMC controller-based DIT2-FLC. Output results vividly reveal that, based on the recommended RSC controller, the transient behavior for capacitor DC link voltage is ameliorated compared to other methods. It is evident that with the application of the PI controller and MINTSMC, the WT system experienced an overshoot of 1205 V and 1180 V for the voltage of the DC link capacitor, respectively. However, a lower voltage deviation of the DC link capacitor was reached by employing the suggested scheme with the overshoot of 1164 V. Figures 17 and 18 demonstrate grid voltage and current correspondingly. Moreover, Figure 19 depicts the active power, reactive power, and generator speed of the DFIG plant respectively. It can be extracted that the active power parameter was reached at approximately 0.9 MW, meanwhile, the final magnitude of reactive power parameter was achieved about 0.1 MVar.

![Capacitor DC-link voltage of the DFIG system via the PI controller, MINTSMC controller, MINTSMC controller-based DIT2-FLC.](image1)

![Magnitude rotor current of the DFIG system via the PI controller, MINTSMC controller, MINTSMC controller-based DIT2-FLC.](image2)

![Grid voltage of the DFIG system under scenario II.](image3)
respectively. It can be extracted that the active power parameter was reached at approximately 0.9 MW, meanwhile, the final magnitude of reactive power parameter was achieved about 0.1 MVar.

Figure 15. Capacitor DC-link voltage of the DFIG system via the PI controller, MINTSMC controller, MINTSMC controller-based DIT2-FLC.

Figure 16. Magnitude rotor current of the DFIG system via the PI controller, MINTSMC controller, MINTSMC controller-based DIT2-FLC.

Figure 17. Grid voltage of the DFIG system under scenario II.

Figure 18. Grid current of the DFIG system under scenario II.

Figure 19. The DFIG responses according to scenario II (a) measured active power of the DFIG WT, (b) measured reactive power of the DFIG WT, (c) generator speed of the DFIG system.

Scenario III:
In this case, the wind speed was 11m/s and the drop voltage happened in the power grid where its amplitude was half of the grid voltage. The DC-link capacitor voltage and magnitude rotor current under this scenario for the various RSC controllers are depicted in Figures 20 and 21, respectively. From the outcomes of Figures 20 and 21, it is revealed that superior control behavior regarding overshoot/undershoot and settling time is obtained by the suggested RSC controller, similar to previous scenarios. Figures 22 and 23, demonstrate the grid voltage and current correspondingly. Moreover, the active and reactive power and generator speed of the DFIG plant is depicted in Figure 24. It can be extracted in which the active power parameter is reached at about 0.75 MW. Meanwhile, the final value of reactive power is achieved at approximately 0.1 MVar.
Scenario III:

In this case, the wind speed was 11m/s and the drop voltage happened in the power grid where its amplitude was half of the grid voltage. The DC-link capacitor voltage and magnitude rotor current under this scenario for the various RSC controllers are depicted in Figures 20 and 21, respectively. From the outcomes of Figures 20 and 21, it is revealed that superior control behavior regarding overshoot/undershoot and settling time is obtained by the suggested RSC controller, similar to previous scenarios. Figures 22 and 23, demonstrate the grid voltage and current correspondingly. Moreover, the active and reactive power and generator speed of the DFIG plant is depicted in Figure 24. It can be extracted in which the active power parameter is reached at about 0.75 MW. Meanwhile, the final value of reactive power is achieved at approximately 0.1 MVar.

![Figure 20. DC link capacitor voltage of the DFIG system under scenario III via the PI controller, MINTSMC controller, MINTSMC controller-based DIT2-FLC.](image1)

![Figure 21. Magnitude rotor current of the DFIG system under scenario III via the PI controller, MINTSMC controller, MINTSMC controller-based DIT2-FLC.](image2)

![Figure 22. Grid voltage of the DFIG system under scenario III.](image3)
Figure 20. DC link capacitor voltage of the DFIG system under scenario III via the PI controller, MINTSMC controller, MINTSMC controller-based DIT2-FLC.

Figure 21. Magnitude rotor current of the DFIG system under scenario III via the PI controller, MINTSMC controller, MINTSMC controller-based DIT2-FLC.

Figure 22. Grid voltage of the DFIG system under scenario III.

Figure 23. Grid current of the DFIG system under scenario III.

Figure 24. The DFIG responses according to scenario III (a) active power of the DFIG system, (b) reactive power of the DFIG system, (c) generator speed of the DFIG system.

5. Conclusions

This paper develops the novel MINTSMC based DIT2-FPI controller to boost the LVRT ability of the DFIG WT plant in a model-independent framework. In particular, the MINTSMC controller with an SM observer is adopted for the internal control of RSC while an axillary controller-based DIT2-FPI controller is established to remove the estimation error of the SM observer. The real-time simulation is made based on the MiL platform to assess the efficiency of the proposed model-independent design in a systematic scheme. The comparative analysis is provided to confirm the better behavior of the proposed controller than modern approaches like the PI controller and MINTSMC controller. The MiL results of the DFIG WT system demonstrated that with the application of the suggested MINTSMC based DIT2-FPI controller, the system outcomes, including grid voltage and grid current, can be effectively stabilized. In addition, a lower level of fluctuations in the terms of capacitor DC-link voltage, as well as magnitude rotor current, could be reached.
5. Conclusions

This paper develops the novel MINTSMC based DIT2-FPI controller to boost the LVRT ability of the DFIG WT plant in a model-independent framework. In particular, the MINTSMC controller with an SM observer is adopted for the internal control of RSC while an auxiliary controller-based DIT2-FPI controller is established to remove the estimation error of the SM observer. The real-time simulation is made based on the MiL platform to assess the efficiency of the proposed mode-independent design in a systematic scheme. The comparative analysis is provided to confirm the better behavior of the proposed controller than modern approaches like the PI controller and MINTSMC controller. The MiL results of the DFIG WT system demonstrated that with the application of the suggested MINTSMC based DIT2-FPI controller, the system outcomes, including grid voltage and grid current, can be effectively stabilized. In addition, a lower level of fluctuations in the terms of capacitor DC-link voltage, as well as magnitude rotor current, could be reached by the MINTSMC based DIT2-FPI controller in comparison with other considered RSC controllers. As future work, the impact of inertia on the performance of the WT system can be investigated. Since the lower system inertia will lead to larger and quicker fluctuations in the system responses, the impact of inertia should be considered in the control stage to ensure system stability.

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