Fabrication and measurements of hybrid Nb/Al Josephson junctions and flux qubits with $\pi$-shifters

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Received 29 May 2014, revised 27 October 2014
Accepted for publication 19 November 2014
Published 13 January 2015

Abstract
We describe fabrication and testing of composite flux qubits combining Nb- and Al-based superconducting circuit technology. This hybrid approach to making qubits allows for employing $\pi$-phase shifters fabricated using well-established Nb-based technology of superconductor–ferromagnet–superconductor Josephson junctions. The important feature here is to obtain high interface transparency between Nb and Al layers without degrading sub-micron shadow mask. We achieve this by in situ Ar etching using e-beam gun. Shadow-evaporated Al/AlO$_x$/Al Josephson junctions with Nb bias pads show the expected current–voltage characteristics with reproducible critical currents. Using this technique, we fabricated composite Nb/Al flux qubits with Nb/CuNi/Nb $\pi$-shifters and measured their magnetic field response. The observed offset between the field responses of the qubits with and without $\pi$-junction is attributed to the $\pi$ phase shift. The reported approach can be used for implementing a variety of hybrid Nb/Al superconducting quantum circuits.

Keywords: superconducting qubits, Josephson junctions, flux qubits, $\pi$-junctions, superconductor-ferromagnet junctions

(Some figures may appear in colour only in the online journal)

1. Introduction

One of several successfully implemented superconducting quantum circuits is a flux qubit [1, 2], which consists of a superconducting ring interrupted by three or four Josephson tunnel junctions. Though any kind of superconductor can be, in principle, taken to make a qubit, the longest coherence times for flux and other types of qubits are achieved by using shadow-evaporated aluminum as the superconducting material and naturally grown aluminum oxide on top of it as the tunnel barrier. Shadow two-angle evaporation of aluminum using a suspended electron-beam resist mask was established over thirty years ago [3, 4] and is presently the most reliable and widely used process for making sub-micron Josephson junctions. In recent years, great progress has been achieved in applying this junction manufacturing technique for superconducting quantum circuits. Two-angle evaporation process has been successfully used for a variety of
superconducting qubit types [5, 6] (charge, flux, transmon, fluxonium, etc.) and appears to be most suitable for obtaining well-defined sub-micron Al/AIO\textsubscript{x}/Al Josephson junctions with reliable characteristics and low density of microscopic two-level defects in the oxide tunnel barrier [7, 8]. While niobium serves as the base material for most of conventional superconducting circuits employing Nb/AIO\textsubscript{x}/Nb Josephson junctions, quantum coherence times of Nb-based qubits [9–11] are significantly shorter than those of their Al-based counterparts. Aluminum superconducting flux qubits can be made very compact, while well-controlled sizes of Josephson junctions defined by two-angle evaporation make it possible engineering qubit potential with precisely defined parameters [12].

The magnetic bias needed to drive the flux qubit to its working point is a source of significant noise, leading to dephasing. The flux qubit has the most favorable operation point with minimal dephasing at the value of magnetic flux threading its loop of about $\Phi_0/2$, where $\Phi_0$ is the magnetic flux quantum. In order to reduce the effects of external magnetic noise, it was proposed to avoid magnetic biasing by using the so-called $\pi$-junction in the qubit loop [13, 14]. The most reliable and well-established process of implementing $\pi$-junctions relies on Nb-based technology of superconductor–ferromagnet–superconductor (SFS) Josephson junctions [15]. The conventional fabrication process of $\pi$-junctions is based on the depositing of an SFS Nb/CuNi/Nb trilayer, forming the junction, followed by depositing the upper Nb wiring.

A $\pi$-junction in the superconducting loop having large enough critical current acts as a phase battery which biases the loop in the way that the phase shift on the junction is $\pi$ [16]. The effect of such a phase shifter is equivalent to applying flux of $\Phi_0/2$ through the loop [17]. SFS phase shifters have already been successfully implemented in Nb-based phase qubit circuits [18], but they haven’t yet been used in flux qubits. The need to combine separately made Nb and Al layers is caused by non-existing so far Al-based $\pi$-junction SFS technology, which would otherwise make it possible avoiding Nb completely. A complication arising along this development is to combine two completely separated and not easily compatible technological steps, first one for making relatively large SFS junctions based on Nb, followed by another process of manufacturing more fragile sub-micron Al junctions needed for highly-coherent flux qubits. Aluminum two-angle evaporation is performed in a separate setup, and pre-fabricated Nb structure is exposed to the air under which the natural oxide NbO\textsubscript{x} is formed on Nb surface. This complication makes the implementation of the SFS $\pi$-junction in the Al flux qubit loop challenging and requires removing the NbO\textsubscript{x} completely before the deposition of Al part of the flux qubit.

This paper is organized as follows. Section 2 describes the combined Nb/Al technology for preparation of $\pi$-qubits. In section 3, we present current–voltage measurements of Al/AIO\textsubscript{x}/Al Josephson junctions with electrodes deposited on Nb pads. The developed technology allows for obtaining high quality Al/AIO\textsubscript{x}/Al Josephson junctions without residual resistance at Nb/Al interface. In section 4, we present measurements of flux qubits with Nb/CuNi/Nb $\pi$-junctions.

2. Fabrication

As the starting point, we describe our fabrication process of Al/AIO\textsubscript{x}/Al Josephson junctions with the Nb contact pads. Schematically, the process is shown on figure 1. Prior applying this process, Nb pads are fabricated in a separate vacuum chamber. The main difficulty for achieving a good superconducting contact between Nb and Al here is caused by a layer of non-superconducting NbO\textsubscript{x}, formed on the surface of Nb due to exposure to the air between the two processes.

The Nb contact pad layer was deposited by dc magnetron sputtering and its patterning was done with the help of conventional optical lithography. After that the sample was covered by a double-layer resist and exposed in an electron-beam lithography machine to define the desired structure for the following double-angle evaporation of Al. Upon transferring the sample to Al deposition chamber, the surface layer of NbO\textsubscript{x} was etched away in situ using the directed Ar beam in order to create a clean Nb surface before deposition of Al. The specific data for interface transparency after Ar-cleaning of Nb-surface could be found in [15], for example.

Several measures for the etching procedure were taken aiming at preventing the resist pattern from melting. We pre-cooled the sample for 1 h in a main chamber of evaporation machine at a high vacuum of $10^{-9}$ mbar at the temperature of about $T \approx -120$ °C. The layer of NbO\textsubscript{x} was etched away in the load-lock by the directed Ar beam in four periods of 30 s each, interrupted by 1 min pauses. After that, the aluminum Josephson junctions were deposited using the standard double-angle shadow evaporation and oxidation [3, 4]. We found out that the Ar beam current density of $20 \mu$A cm\textsuperscript{-2} was sufficient to etch down the NbO\textsubscript{x} layer and establish the superconducting contact between Al and Nb. The whole fabrication process is shown in figure 1.

We fabricated a series of Al/AIO\textsubscript{x}/Al Josephson junctions having the dimensions $0.2 \times 1.0 \mu$m\textsuperscript{2}. Furthermore, the flux qubits interrupted by three Josephson junctions of this kind were fabricated. The dimensions of Josephson junctions in...
flux qubits were 0.2 × 0.5 μm² for the two junctions and 0.2 × 0.335 μm² for the smaller α-junction aiming at \( \alpha = 0.67 \) [1, 2].

3. Characterization of Al/AlOₓ/Al Josephson junctions

Fabricated samples were measured in vacuum attached to a sample holder of a He-3 cryostat and cooled down to a temperature of 300 mK. The current–voltage characteristics were measured using a four-point configuration in the current-bias mode. Figure 2 shows typical IV-curve for one of the test Al/AlOₓ/Al junctions having the dimensions 0.2 × 1.0 μm². One can see a clear supercurrent branch as high as 2.5 μA. Switching current values of 2 ± 0.54 μA and re-trapping currents of around 1 ± 0.25 μA were measured for junctions made on several chips. According to the process described above, all the contact pads of our junctions are made of niobium so that each lead contained Nb/Al interface. The current–voltage characteristics of Al/AlOₓ/Al Josephson junctions showed no evidence of any residual resistance due to Nb/Al interfaces. So thus the quality of the fabricated Nb/AlOₓ/Al/Nb hybrid structures is high enough for implementation in quantum circuits.

From the topology of the junctions we estimated the junction capacitance \( C \approx 4.43 \pm 0.92 \) fF. The pronounced ‘back-bending’ of the re-trapping current branch visible in figure 2 is rather typical for vacuum-based transport measurements of small aluminum junctions in this range of the critical current density and can be explained by non-equilibrium effects due to the junction self-heating. Same phenomenon is also responsible for about 20% reduction in the measured value of the gap voltage \( V_g \) [19].

Figure 2. Typical current–voltage characteristics of a hybrid Nb/Al/AlOₓ/Al/Nb Josephson junctions having the dimensions 0.2 × 1.0 μm². The re-trapping current ‘back-bending’ is explained by the self-heating.

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4. Flux qubit measurements

SFS π-junction fabrication technology is described in detail in [20]. The fabrication process employed in this study differs from previous works. Here, all layers were deposited in the beginning of the process in a single vacuum cycle and then patterned using photo-lithography, reactive and argon ion etching, followed by magnetron sputtering and thermal evaporation. We have studied the dependence of critical current density on the CuNi-layer thickness using this technology [21] and discovered that it results in critical currents one order of magnitude larger than the technology used earlier [15].

For measurements of the flux qubits we used the conventional dispersive readout setup discussed in detail elsewhere [12]. Two flux qubits were placed near the shorted end of the λ/4 resonator, one with the SFS π-junction with 12 nm layer of Cu₀.₄₇Ni₀.₅₃ [15], and another one without it. The opposite open end of the resonator was capacitively coupled to an on-chip coplanar waveguide. The micrograph, shown in figure 3, illustrates the sample.

The magnetic flux through the qubit loops was applied by using an external magnetic bias coil. Measurements were performed in a dilution cryostat at the base temperature of 25 mK. We swept electrical current through the bias coil, thus changing the flux through the qubit loops. Simultaneously, the resonator was probed at its fundamental λ/4 mode frequency \( \omega_0 \) with a microwave signal transmitted and detected via a vector network analyzer. We measured the amplitude and phase responses of the probe signal amplified with a low-noise cryogenic amplifier at a fixed frequency \( \omega_0/(2\pi) = 10.218 \) GHz.

In the employed dispersive readout scheme, the resonator acquires a dispersive shift due to the coupling to the qubit...
is an effective coupling of the resonator to the qubit, for the qubit with 〈Φ⟩ 〈ω 〈 coupling and detuning of the qubits from the resonator. The difference in amplitudes of the signals is attributed to the different amplitude corresponds to the

where

\[ \Delta \omega = \pm \frac{g}{\omega_q - \omega_1}. \]  

(1)

occurs when the frequency of the transition between the ground and excited state of one of either qubit \( \omega_{q1} \) or \( \omega_{q2} \) approaches the resonator frequency \( \omega_r \), which occurs at the magnetic flux values close to \( \Phi_0/2 \pm n \Phi_0 \) for the qubit without \( \pi \)-junction and at \( \pm n \Phi_0 \) for the qubit with \( \pi \)-junction, where \( n \) is an integer.

Next, we need to sort out two families of periodic peaks in figure 4, one of them corresponding to 0-qubit and another to \( \pi \)-qubit. This procedure is not straightforward because of non-ideal magnetic shielding. Indeed, one can see from figure 4 that there is no peak exactly at zero magnetic field, indicating the presence of residual magnetic field in the setup. In figure 5, we have plotted the positions of peaks as a function of magnetic flux, for data in a broader flux range, taken in the same measurement as figure 4. We assumed that a period for each peak family is one flux quantum, the residual flux of less than one flux quantum and the nearest-to-zero peak as corresponding to \( \pi \)-qubit. One can see that both peak families could be approximated by linear dependencies, each having its own slope, and the intersection between two families takes place at zero net magnetic field. The difference in slopes is due to different mutual inductances between qubits and external coil. An intersection point has to correspond to zero magnetic flux, at which the \( \pi \)-qubit should display a peak in the dispersive signal. Under this assumption, the residual magnetic field is equal to approximately 2 μT and the residual magnetic flux is less than one flux quantum. One can easily identify two periods of oscillations in figure 4. We suppose that the smaller period of the \( \pi \)-junction qubit oscillations in the applied magnetic field can be associated with an additional Josephson inductance of its loop induced by the \( \pi \)-junction. The smaller peak amplitude of the \( \pi \)-junction qubit response can be related to a larger detuning of its gap frequency from the resonator frequency, as well as to the additional inductance mentioned above. The energy gap of a flux qubit is extremely hard to control due to

\[ \Delta \omega = \pm \frac{g}{\omega_q - \omega_1}. \]  

(1)
its very sensitive dependence on the relation between critical currents for three aluminum junctions.

It is important to discuss the uniqueness of $\pi$-qubit response identification. Actual values of magnetic flux in figure 5 are assigned with possible offset by an integer number of flux quanta. However, this circumstance doesn’t alter our definition of 0- and $\pi$-qubit responses. Indeed, an offset in definition of zero-flux peak shifts all points of both peak families in figure 5 along the horizontal axis by an integer value. In this case, the crosspoint flux value will change but it will remain to be integer-valued and the same peak will correspond to the crosspoint. While our definition of zero magnetic flux is just an assumption, the identification of $\pi$-qubit peaks base on the above described arguments seems unambiguous.

5. Conclusion

In conclusion, we developed a fabrication process which allows to create a superconducting interface between Nb and Al thin films produced in different technological processes. The developed procedure features pre-cooling and Ar etching procedure of oxidized Nb surface in situ, before deposition of Al/AIO/Al Josephson junctions using a standard double-angle shadow evaporation. This process can also be implemented for more complex Nb/Al qubit circuits. Hybrid Al/AIO/Al flux qubits containing Nb/Cu$_{0.47}$Ni$_{0.53}$/Nb $\pi$-shifters were fabricated using the developed approach. We observed the field response of two flux qubits (one with and another without $\pi$-shifter) coupled to the same $\lambda/4$ resonator. The magnetic field shift between two periodic qubit oscillation patterns measured at mK temperatures indicates the expected $\pi$-junction phase bias in one of the flux qubit loops. The use of the $\pi$-shaper makes it possible to avoid magnetic biasing, normally needed for reaching the most favorable flux qubit operating point, and thus to reduce unavoidable variations of magnetic bias between different qubits on chip.

Acknowledgments

This work was supported in part by the Russian Quantum Federation under contract no. 11.G34.31.0062 and no. 2014-025 (in the framework of Increase Competitiveness Program of NUST MISiS), the Programs of the Russian Academy of Sciences, the Deutsche Forschungsgemeinschaft (DFG) and the State of Baden-Württemberg through the DFG Center for Functional Nanostructures (CFN).

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