A Review on Photonic Crystal Logic Gates

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Abstract—We review the most important techniques to build logic devices in photonic crystals. In particular, we evaluated several papers that include different approaches such as self-collimation effects, multi-mode interference, waveguide intersection path and nonlinear effects. We then exploited the advantages and weakness of each method. With the resulting information, we addressed the main challenges and possible applications for each scheme. With this review we expect to improve the development of PhC integrated logic devices and circuits.

Index Terms—Photonic Crystal, Logic Gates, Integrated Photonics Technology, Photonic Circuits and Systems.

I. INTRODUCTION

It is well known that logic gates are in the third abstraction level of computer logic systems and play a key role to the development of communication and computer systems. Nowadays, they are built connecting transistors based on CMOS technology. Digital electronics is reaching its physical limit of miniaturization, and the incoming future computers with high speed and low power consumption may not be possible with this technology. Thus, it is important to find alternatives to handle this drawback. One possibility is to change to photonics instead of electronics.

Photonic Crystals (PhC) emerged as good candidates to realize fully optical processing information and computing [1–3]. Their ability to control electromagnetic waves, compactness, low power consumption, and exceptional confinement of light, has opened a large variety of applications for telecommunications, signal processing, biophotonics, and computing [4–8].

PhC logic gates have been explored during the last decade. In this way, several approaches have been applied to build logic devices such as: self-collimation, multi-mode interference, waveguide interference path, semiconductor optical amplifiers, and nonlinear effects. Each technique presents advantages and disadvantages that make them suitable for particular applications.

Thus, this literature review pretends to explore the most common approaches to build photonic crystal logic gates, as well as address possible applications and exposes the main challenges of these technology. This manuscript is organized as follows: Section II gives the fundamental theory to better understand this review. In section III, we make a survey on the most common techniques used to design PhC logic devices. In section IV, we summarize and compile the review and discuss the main features of each approach. Finally, Section V gathers the main conclusions.

II. FUNDAMENTAL THEORY

This section gives a theoretical review of what photonic crystals are and their fundamental properties. It also covers the kinds of defects that can be introduced in them, the interference and nonlinear effects, and the simulation methods.

A. Photonic Crystals

Photonic crystals (PhC) are devices that have awaken a great interest since their proposal in 1987, and even more after their experimental demonstration in 1991 [1–3]. Fundamentally, PhC are a class of optical structures formed by a periodic modulation of the dielectric function distributed in a specific geometric lattice.

Due to this periodicity, electromagnetic waves are affected by the coherent scattering phenomena when they are launched into the PhC. The scattered waves cause the propagation or reflection of particular frequencies inside the structure, as a result of the constructive and destructive interferences between them. The allowed and forbidden frequencies are associated to the photonic bands (PB) and photonic band gaps (PBG), respectively. Thus, when the electromagnetic wave incident on the crystal has frequency within the PBG, it is completely reflected. Otherwise, if the frequency is in the allowed region (PB), it will propagate inside the PhC [9].

As can be noted, this fact means that PhC operate as optical semiconductor devices and allow us to design insulators or conductors of electromagnetic waves by engineering their geometrical parameters. Photonic crystals can be divided geometrically into three broad categories, namely one-dimensional (1D), two-dimensional (2D), and three-dimensional (3D) structures.

In 1D PhCs, the periodic modulation of the dielectric function occurs in one direction, while in the other two directions the medium is uniform. A very low number of possible variations can be accomplished, in this case: only the dielectric materials, layer’s thicknesses, and the number of layers within the period can be varied [9, 10].

In a 2D photonic crystal structure, the periodicity of the dielectric function occurs in two directions, while in the third the medium is uniform. Consequently, a large variety of configurations can be achieved by varying the dielectric materials, lattice geometry (square, hexagonal), lattice constant a, and shape of the dielectrics (square, ellipses, triangle). Nevertheless, for technological reason two types of configurations are commonly used: square and hexagonal lattices, illustrated in Figure 1. For both cases, good examples are a silicon substrate with etched holes or a system of dielectric rods in air [9].

The three-dimensional photonic crystals have dielectric modulations along all three directions. With that, the number
of possible configurations is much larger than in case of 1D or 2D PhC. However, these 3D structures are more difficult to fabricate, although they are relatively common in nature. The most known naturally formed 3D PhC is the valuable stone opal [9].

At this point, as in solid state physics, the concept of band diagrams (also called band structure or dispersion relation) is useful to understand the properties of the structure. A band diagram is a plot of the allowed frequencies as a function of the wave vector, usually along a highly symmetric direction in the reciprocal space (the first Brillouin Zone). Its physical meaning is the connection between the characteristics of the photonic crystal and its properties of propagation (PB) and reflection (PBG) [9, 10]. It can be obtained solving the eigenvalue problem of the master Maxwell equation:

\[ \hat{\Theta} H(r) = \left( \frac{\omega}{c} \right)^2 H(r), \]  

where \( H(r) \) is the magnetic field, \( r \) is the (cartesian) position vector, \( \omega \) is a given frequency, \( c \) the speed of light, and \( \Theta \) is the hermitian Maxwell operator, defined as:

\[ \hat{\Theta} = \nabla \times \left( \frac{1}{\varepsilon(r)} \nabla \times H(r) \right). \]

Here, \( \varepsilon(r) \) is the dielectric function and it is the square of the refractive index \( n \). In general \( n = \sqrt{\varepsilon \mu / \varepsilon_0 \mu_0} \). However, for most dielectric materials of interest, the relative magnetic permeability \( \mu(r) = \mu / \mu_0 \) is very close to unity making \( n \) approximately \( \sqrt{\varepsilon(r)} \) [10].

In summary, photonic crystals are promising devices considering their properties, which allow the manipulation of photons, like semiconductors allow the control of electrons. Indeed, scientists are now turning to light instead of electrons as the information carriers and have begun imagining photonic integrated circuits which resemble microscopic networks at micrometer length scales. Light has several advantages over the electron, such as it can travel in a dielectric material at much greater speeds, can also carry a larger amount of information per second, the bandwidth of dielectric materials is significantly larger than that of metals, and light particles (or photons) are not as strongly interacting as electrons, which helps reduce energy losses [11].

A.1 Photonic Crystal Slab

Control the light in all three dimensions is the goal of the technological and scientific community. To do that, three-dimensional photonic crystals are the ideal platform to confine and guide the light in the three dimensions. However, 3D PhC are particularly difficult to realize because large-area and defect-free implementations are quite challenging even for state-of-art fabrication techniques [12–14]. Two-dimensional photonic crystals embedded into planar dielectric waveguides, known as two-dimensional PhC slabs, have emerged in the last decade as the best candidates for efficiently confining and guiding the electromagnetic fields at optical frequencies in three dimensions. Currently, lithography and etching processes at sub-micron scales are in a very mature and advanced stage, allowing the fabrication of large-area photonic crystal slabs with high-nanometric precision [15].

![Photonic Crystal Slab](image)

In PhC slabs, the vertical confinement mechanism is given by total internal reflection (TIR), and in-plane the propagation is controlled by the photonic pattern via distributed Bragg reflection (DBR), as shown in Figure 2. Photonic band gaps in these systems are thus conditioned by both TIR and DBR mechanisms.

The electromagnetic modes in a PhC slab can be divided into two orthogonal polarizations, known as transverse-electric (TE) and transverse-magnetic (TM). The first has field components \( (E_x, E_y, H_z) \), while the second presents the following components \( (H_x, H_y, E_z) \), considering \( xy \) as the plane where the photonic pattern is present.

B. Photonic Crystal Defects

In particular, two types of defects can be introduced in two-dimensional photonic crystals: point defects and line defects, known as cavities and waveguides, respectively.

B.1 Photonic Crystal Cavity

A photonic crystal cavity is a perturbation of the system that breaks the periodicity of its dielectric function. This can be made removing one or more dielectric features, a dielectric rod or a hole in the structure, or replacing them with other features, whose size, shape or dielectric constant are different from the original.

In effect, if the designed defect has the proper size, shape and dielectric constant to support an electromagnetic mode within the photonic band gap, the light cannot escape from it, and it is trapped into the cavity forming an optical resonator, as illustrated in Figure 3. The two most important characteristics of interest in PhC cavities are: the mode volume and the quality factor (\( Q \)).

The mode volume is a measure of how tightly the mode is localized into the cavity. It can be determined with the dielectric function and the field function of the electromagnetic mode profile [16].

![Photonic Crystal Cavity](image)
The quality factor $Q$ is a dimensionless quantity that relates the outgoing power and the electromagnetic energy localized in the cavity. Also, it can be viewed as a measure of the number of optical periods that elapse before the energy decays, i.e., the lifetime of the electromagnetic modes in the cavity [10]. The quality factor of a PhC cavity can be computed using the following relation:

$$\frac{1}{Q} = \frac{P}{\omega_0 U}, \quad (3)$$

where $P$ is the outgoing power, $\omega_0$ is the resonant frequency and $U$ is the electromagnetic energy localized in the cavity [10]. A high $Q$ implies a longer lifetime of the electromagnetic waves inside the defect.

High quality factors have been achieved in two-dimensional photonic crystals using the L3 cavity. It is created by removing three holes or rods in the same line of the structure, as shown in Figure 3. Values of $Q$ reaching the order of $10^5$ have been reported in the literature for L3 cavities [17].

Such kind of cavity has important applications for low threshold lasers, high finesse filters, sensors, cavity quantum electrodynamics as well as filtering, switching, and integrated optical processing when integrated with waveguides [18–22].

**B.2 Photonic Crystal Waveguide**

Another fundamental type of perturbation in a photonic crystal structure is a waveguide. Essentially, a waveguide in a two-dimensional photonic crystal device is a line defect created by removing or changing the properties of one entire dielectric row or column, as shown in Figure 4.

The key difference between cavities and waveguides is that, for the former electromagnetic modes can be localized whenever its frequency is in the photonic band gap, while for the latter the behavior of the mode relies on its frequency and wave vector. Thus, a guided mode needs only the combination of wave vector and frequency allowed in the structure; it is not necessary that frequency alone is disallowed. With this condition, the conservation of the wave vector along the defect allows the propagation of the electromagnetic mode from one side to another, as opposed to being just a region where it is localized [10].

Taking advantage of this, waveguides are used to guide the electromagnetic waves with very high efficiency and optical devices working in the low losses and low energy-consumption regime for telecommunication purposes can be achieved.

**B.3 Photonic Crystal Coupled Systems**

A coupled system is one in which the operation of each component is described according to the behavior of the other elements that compose the system.

Waveguides and cavities can form a coupled system in a photonic crystal in order to build efficient frequency filter devices. There are two possible configurations to couple waveguide and cavities in a PhC structure: side-coupled and directly-coupled.

In a waveguide-cavity directly coupled system, the cavity is placed in line with the waveguide, as illustrated in Figure 5. Thus, light from the input waveguide couples into the cavity, and the cavity in turn couples into the output waveguide allowing the transmission of frequencies near to the resonant frequency of the cavity. This system acts as a narrow-band filter device [10, 23].

The transmission spectrum ($T$) of this system can be estimated using the following function:

$$T(\omega) = \frac{\delta}{(\omega - \omega_0)^2 + \delta}, \quad (4)$$

where $\omega_0$ is the resonant frequency of the cavity and $\delta$ is a quantity related to the quality factor as: $1/Q^2$. On the other hand, in a waveguide-cavity side coupled system, shown in Figure 6, the cavity is placed in a line up or down to the waveguide. Thus, the cavity couples with the input and output waveguides reflecting the frequencies around the resonant frequency of the cavity [10, 23]. For this kind of configuration, the transmission spectrum can be computed.
with the equation:

\[ T(\omega) = \frac{(\omega - \omega_0)^2}{(\omega - \omega_0)^2 + \delta}. \]  

(5)

These coupled systems have been used successfully to enhance nonlinear effects, frequency filters, absorptive devices and sensors [24].

C. Interference Effect Based on MMI

Generally, Multi-Mode Interference (MMI) devices are composed of the access waveguides, the multi-mode region, and the output waveguides. Its operation principle is the self-imaging property. In this, guided modes are excited and interfere constructively or destructively at the MMI region when an input field is launched at the access waveguides. Then, the excited field at the MMI region transmutes periodically along the propagation direction to the output waveguides. Usually, access and output waveguides are single-mode to ensure a high-performance MMI device. On the other hand, the MMI region is a waveguide that supports a large number of modes and where the MMI occurs [25].

D. Interference Effect Based on Self-Collimation

Self-Collimation (SC) or self-guiding of light in a PhC is a process in which a narrow beam of electromagnetic wave can propagate without any significant broadening or change in the beam profile, and without relying on a bandgap or engineered defects, such as waveguides. This phenomena occurs due to the complex spatial dispersion properties of planar photonic crystal generating anomalous refraction of light. To achieve the SC in a PhC, the allowed wavevectors in the structure and their corresponding frequencies must be as flat as possible to ensure the perpendicularity of the group velocity of light. Thus, launching beams signals with appropriate phases into a SC PhC, the reflected and transmitted can generate constructive or destructive interference [26].

E. Interference Effect Based on Waveguide Intersection Paths

As described earlier, PhC waveguides are used to guide electromagnetic modes from one side to another. PhC bend waveguides based on square and triangular lattices have been extensively studied and also experimentally demonstrated. Two rules of thumbs can be applied to adequately design the intersection path between two PhC waveguides. In general, a path difference of zero implies that constructive interference occurs between the two input signals. In contrast, a destructive interference will occur if the path difference between the waveguides are one lattice constant [27, 28].

F. Kerr Effect in Photonic Crystals

The optical Kerr effect is a kind of phenomena in that a nonlinear material changes its refractive index under high intensity radiation passing over them. This change of the refractive index can be calculated through the following equation:

\[ \Delta n = n_0 + n_2 I, \]  

(6)

where \( n_0 \) is the linear refractive index, \( n_2 \) is the Kerr coefficient and \( I \) is the intensity of the electromagnetic wave. Such variations of the refractive index can cause essential changes of device fundamental characteristics [29].

In this way, the best approach to observe the optical Kerr effect in photonic crystals is through a coupled system with a nonlinear cavity. In this kind of system, the introduced nonlinearity induce a change of the refractive index inside the cavity which follows the relation of the Equation 6. Besides, it is observed that the cavity resonant frequency shifts proportional to \( \Delta n \) due to the subsequent growth of the electromagnetic energy inside the resonator [24].

Then, after applying a rigorous perturbation theory arguments, the relation between the output and input power in a directly-coupled system can be expressed by the Lorentzian [24]:

\[ \frac{P_{out}}{P_{in}} = \frac{1}{1 + (P_{out}/P_0 - \Delta)^2} \]  

(7)

where \( P_{in} \) and \( P_{out} \) are the input and output powers, \( P_0 \) is the characteristic power of the cavity, and \( \Delta \) relates the resonant frequency \( \omega_0 \) with the width of the resonant(\( \Gamma \)), \( \Delta = \omega - \omega_0/\Gamma \). For a side waveguide-cavity coupled system, this relation can be expressed as:

\[ \frac{P_{out}}{P_{in}} = \frac{(P_{out}/P_0 - \Delta)^2}{1 + (P_{out}/P_0 - \Delta)^2} \]  

(8)

In general, if the applied input on the waveguide has the proper amount of intensity to put in the cavity on the nonlinear regime, then the resonant frequency of the system shifts due to the change in the refractive index of the nonlinear cavity. This variation gives the possibility to design a new class of optical devices such as optical information storage elements, bistables, transistors, and logical elements [22,30–37].

G. Simulation Methods

G.1 Finite Difference Time Domain Method

The finite difference time domain method (FDTD) is a numerical technique used to solve Maxwell’s equations. It can be implemented without using linear algebra which makes it appropriate for rigorous simulations of large scale problems, and structures with non-uniform dielectric distribution and complicated geometries. It is accurate, and sources of numerical error are well understood. Also, FDTD calculates naturally the nonlinear response of an electromagnetic system as it is a time-domain technique [38, 39].

To solve Maxwell’s equations, FDTD divides space and time into a grid (usually uniform) of discrete points and approximates their derivatives \((\nabla \times \times \partial / \partial t)\) by finite differences. The propagation in time, in particular, uses a leap-frog scheme where the \( E \) fields at time \( t \) are computed from the \( E \) fields at time \( t - \Delta t \) along with the \( H \) fields at time \( t - \Delta t / 2 \), and vice versa for \( H \) at \( t + \Delta t / 2 \). In this way, the \( E \) and \( H \) field patterns are marched through time, offset by half of a time step \( \Delta t \) [38–40].

Nowadays, FDTD is one of the most advanced methods for computation the field distribution and to study other behaviors such as nonlinearities in photonic crystals. It had
also been applied to a large variety of electromagnetic problems [38, 39]. There are some commercial and open source software packages that implements the FDTD method, such as MEEP, OptiFDTD, Lumerical FDTD Solution Tool and RSof.

G.2 Plane Wave Expansion Method

The Plane Wave Expansion (PWE) method is a frequency-domain eigensolver which solves the eigenproblem for the frequencies of a periodic system such as photonic crystals.

The discretization of the master Equation 1 through planewave expansion yields a finite generalized eigenproblem \( Ax = \omega^2 B x \), where \( A \) and \( B \) are \( N \times N \) matrices, \( x \) the eigenvector and \( \omega \) the eigenvalues. Here, the eigenvalues represent the frequencies while the eigenvectors contain amplitude coefficients of the component plane waves [41–43]. In particular, for a one dimensional PhC structure, the plane wave using Fourier series takes the form:

\[
 u_k(x) = \sum_{n=-\infty}^{\infty} c_{n}(k)e^{i \frac{2\pi n}{a} x},
\]

where \( u_k(x) \) is a periodic function \( u_k(x) = u_k(x+a) \), where \( a \) is the lattice constant. \( c_{n}(k) = \frac{1}{a} \int_{0}^{a} dx e^{-i \frac{2\pi n}{a} x} u_k(x) \) are the complex Fourier series coefficients.

In general, PWE method may be the most popular approach for calculating band diagrams of periodic structures like photonic crystals. It is a simple method to implement and enables rigorous analysis of 3D structures [10]. A free software tool called MPB (MIT Photonic Band-gap) is available for this purpose.

III. PHOTONIC CRYSTAL LOGIC GATES

This section presents a literature review of several methods used to perform logic devices in photonic crystals.

A. LG Based on Self-Collimation (SC)

An incident light beam is partially reflected and partially transmitted when incident a PhC structure with a self-collimated region. Thus, a phase change occurs in the reflected beam with respect to the transmitted beam. This phase difference depends on the radius of the dielectrics of SC PhC region. If another light beam with appropriate phase is launched, the reflected and transmitted beams may interfere constructively or destructively. Thus, logic gates based on SC PhC can be achieve by modifying the radii of the dielectrics and introducing different phase shifts between the incident beams at the input faces. Several works using this approach demonstrated a complete set of all-optical logic gates [44–47]. Figure 7 shows the schematic structure of the photonic crystal self-collimated device proposed by Zhang et al [44]. Adapted from [44].

B. LG Based on MMI (Multi-Mode Interference)

To accomplish logic gates based on MMI, the input logic values need to be determined only by the phase of the input signals for each gate. This can be achieved by using Binary Phase Shift Keyed (BPSK) signals, whereas the output logic value is determined by the amplitude of the output signal regardless of its phase. With the proper signal phases of the input signals, they interact together in the MMI area to either generate a signal, which corresponds to logic 1, or eliminate the signal generation, which corresponds to logic 0 at the output port. Then, the logic gates can be realized by the proper parameters selection in addition to the choice of the phases of the input signals, as demonstrated by Ishizaka et al., Liu et al., and Tang et al. [48–51].

Figure 8 shows the schematic representation of the PhC MMI device proposed by Liu et al [49]. It is formed by two input ports (A and B) and two output ports (X and Y). Thus, to achieve the logical functions, two kinds of BPSK signals are injected into each input port. In the case of XNOR gate, logic 1 for the input port A is expressed as signal phase 0, while logic 0 is expressed as signal phase \( \pi \). Whereas the input port B expresses logic 1 as signal phase \( -\pi/2 \), while logic 0 is expressed as signal phase \( \pi/2 \). Similarly, the XOR, OR and NAND gates can be realized by selecting the proper input signal’s phase for each logic.
C. LG Based on Waveguide Interference Paths

This is a simple, useful, and effective approach to project logic gates in photonic crystals. In this scheme, the high transmission state (logic 1) is guaranteed if a constructive interference occurs between the inputs waveguides. This is accomplished by designing an intersection point with a path difference of 0, which generate a phase difference of $2k\pi$ between input signals. In contrast, if the designed path difference is one lattice constant, it generates a phase difference of $(2k+1)\pi$, and destructive interference occurs between the inputs signals achieving the low transmission state (logic 0).

Using these approach, the OR, XOR, NOT, XNOR, and NAND gates have been realized theoretically by Fu et al. [27]. The structures were projected on a photonic crystal composed of a triangular lattice of cylindrical silicon rods, with a dielectric constant of 11.56, embedded in a background medium of air (refractive index 1). The lattice constant and the diameter of the silicon rods were 875 nm and 495 nm, respectively. The OR logic gate is formed by two intersecting waveguides (inputs) at $10.5 \times 10^3$ nm to the cross point between them, forming an angle of 120 and a phase difference of 0, as illustrated in Figure 9a. Thus, if a single beam is injected into one of the inputs, the signal light can propagate to the output through the waveguide, and a logical value of 1 can be obtained in the output. When two beams are injected in both input ports simultaneously, constructive interference occurs, and high power is observed in the output. Obviously, when no single beam is injected into any input port, no light comes to the output, corresponding to the logical value of zero.

![Fig. 9](image9.png)

Fig. 9: Schematic representation of photonic crystal (a) OR and (b) XOR gates proposed by Fu et al. [27]. Adapted from [27].

For the XOR gate, the designed structure consists of two waveguides with one lattice constant of path difference to the cross point between them, as shown in Figure 9b. At this point, when the inputs are excited simultaneously with a continuous wave source, a phase difference of $\pi$ generates a destructive interference, and the output signal is approximately zero (0.67%). On the other hand, if only one input is excited, the output signal is greater than 75%. Thus, considering transmissions greater than 70% and lower than 1% as logic values 1 and 0, respectively, the XOR logic gate was carried out.

The XNOR and NAND gates structures were based on the XOR device, but introducing a control signal in order to generate a high transmission output when no light is injected into the input ports, i.e for the logic input case (0,0)=1. Thus, for these devices, transmission values greater than 85% and lower than 10% have been reported for logic values 1 and 0, respectively. The logic devices proposed can operate at the telecommunication window of 1550 nm and in the low power regime. Also, the author reported the intensity contrast ratio between the output signals for the logic states of 1 and 0 as high as 20 dB.

Using the same parameters, Caballero et al. addressed the Majority and the Feynman gates [52]. The former allows the creation of simple and optimized computational circuits and the latter is a reversible logic device projected to design circuits in the thermodynamic limit of computation. For the Majority gate, illustrated in Figure 10a, the authors reported that transmission values at the output greater than 85% and lower than 35% can be interpreted as logic 1 and 0, respectively. For the Feynman gate, shown in Figure 10b, the equivalent transmission for the logic 1 was found as high as 40% while transmissions values lower than 10% correspond to the logic 0.

![Fig. 10](image10.png)

Fig. 10: Schematic representation of photonic crystal (a) Majority and (b) Feynman gates proposed by Caballero et al. [52]. Adapted from [52].

Employing the control interference effect in a two-dimensional photonic crystal composed of a square lattice of cylindrical silicon rods, with a dielectric constant of 11.56, embedded in a background medium of air, D’Souza demonstrated the operation of the OR, XOR, NOT, and AND logic
The parameters of the based structure were 650 nm for the lattice constant and 230 nm for the radii of the rods. The devices are formed by a square ring resonator waveguide with three other linear waveguides which are connected to each other by the ring resonator, as illustrated in Figure 11. The principle is that the signal injected into the input waveguides split into two through the ring resonator. One travels in clockwise and the other in the counterclockwise direction. If a constructive interference occurs, a larger output energy is obtained at the output and the OR, and AND gates were accomplished. Engineering the ring resonator to produce destructive interference, the XOR and NOT logic gates were also demonstrated. The advantage of these devices is that they can operate at different wavelengths around the 1550 nm window. In addition, a contrast ratio higher than 35 dB was reported by the authors.

A methodology to evaluate the effect of structural disorder on photonic crystals logic gates were applied to these devices in order to figure out their robustness and fault tolerance [53]. The study was based on the evaluation of two metrics: the error rate (ER) and the mean absolute deviation of transmission of the error cases (MATEC). ER is the probability that a fabricated photonic crystal logic gate does not accomplish its logic function correctly, and MATEC measures the imperfection degree of the device through the transmission coefficient. The authors found that, for structures with a triangular lattice, regions in the corners and close to the output are more critical to ER and MATEC, respectively. For structures with a square lattice, the intersection regions are the most sensitive for both metrics.

Hussein et al. introduce new designs of all-optical OR, AND, XOR, NOT, NOR, NAND and XNOR logic gates based on the interference effect [54]. The designs were projected using 2D square lattice photonic crystal structures of germanium (Ge) rods, with a relative permittivity of 16, embedded in a background of air. The radius of the rods was set as 0.15a, where a is the lattice constant and its value was selected 580 nm to get a operational wavelength of 1550 nm. The interference effect to achieve the logical function is projected via an optical resonator induced for each structure, as shown in Figure 12. Optical bit rates ranging from 3.8 to 7.6 Tbps and contrast ratio from 5.036 dB to 12.15 dB were reported in this work.

Caballero et al. [55] accomplished an integrated and compact photonic crystal (PhC) device that can operate as a NAND or NOR logic gate, shown in Figure 13. It is designed in a PhC slab composed of GaAs/AlGaAs heterostructure and a 2D pattern of a triangular lattice of holes. By modifying target holes on the structure, the NAND or NOR gates can be achieved. The simulation results reported by authors show that the upper power limit to represent the logic 0 is 0.17Pin, where Pin is the input power. On the other hand, the lower power limit to represent the logic 1 is found to be 0.50Pin. The NAND and NOR logic gates present a response time of 5 ps, resulting in a clock rate of 200 GHz. They also operate within the C band of the telecommunication window (between 1530 and 1565 nm).

D. LG Based on Kerr Effect

To accomplish logic devices using the Kerr effect on PhC, a waveguide-cavity coupled system need to be introduced on the structure and the power of the inputs need to be properly selected.
Using this scheme, a high-contrast all-optical bistable switching was proposed by Yanik et al. [22]. The projected device is a 2D photonic crystal composed of a square lattice of dielectric rods ($\varepsilon = 12.25$) embedded in air, as illustrated in Figure 14. To achieve the optical bistability, a waveguide was side-coupled to a single-mode cavity with instantaneous Kerr nonlinearity. Consequently, the high transmission state is accomplished when the cavity is OFF resonance with the excitation and the field inside the cavity is low, making the decaying field amplitude from the cavity negligible. In contrast, when the field intensity inside the cavity is much higher, it pulls the cavity resonance frequency down to the excitation frequency of the incident field, and the low transmission state is achieved. To prove the correctness of the bistability, a coupled-mode theory was applied and presented excellent agreement with FDTD simulations.

In a following work performed by the same research group, the first all-optical transistor in photonic crystals was proposed [31]. The structure consists of two waveguides, input and control, arranged in a cross geometry. Additionally, a cavity with instantaneous Kerr nonlinearity was inserted in the waveguides intersection, as shown in Figure 15. With this configuration, the cross-talk between the waveguides is prohibited due to the modes of them are even to their axis; consequently, each waveguide couples to only the cavity mode with the same axis symmetry. FDTD simulations were performed to show the transistor operation. Thus, the ON state is reached at 25 ps when the input and control waveguides are excited with input powers about 200 mW/μm. Otherwise, if only the input waveguide is launched, the transistor is on the OFF state. A small footprint of a few micrometers squared and the requirement of only a few milliwatts of power at a 10 Gbit/s switching rate were considered the advantages of the proposed structure.

In the search for useful multipurpose devices, an all-optical half adder based on photonic crystals resonant cavities was proposed by Neisy et al. [56]. The designed structure is composed of a $31 \times 31$ square array of dielectric rods ($\varepsilon = 11.97$) immersed in air. Two inputs waveguides and two nonlinear resonant cavities, as can be observed in Figure 16, were introduced and optimized to get the correct behavior for the resonant frequencies. To prove the functionality as a half adder, a continuous wave with a wavelength of 1551 nm and an optical intensity of 10 mW/μm² is used as input source. Values greater than 70% and lower than 5% were reported to be considered as logic 1 and logic 0, respectively. Compactness and low delay rate of about 3 ps are highlighted as the main advantages of the proposed structure.

In the same direction, a photonic crystal 1-bit full adder was projected by Alipur-Banei et al. [57]. The structure was developed by cascading two optical 1-bit half-adders. The final structure consists of eight optical waveguides and two nonlinear resonant rings, created inside rod type two-dimensional photonic crystal with square lattice. The structure has X, Y and Z as input waveguides and, SUM and CARRY as output ports waveguides. The performance and functionality of the adder were validated by means of the FDTD method. The results shown that normalized transmission values greater than 60% and less than 5% were considered as logic 1 and logic 0, respectively. In addition, a delay time of 1.5 ps and the overall footprint of about 439 μm² were reported.

The energy dissipation produced by the information loss is currently a concern in the design and fabrication of logic circuits. R. Landauer showed that there is a great amount of energy dissipation inside large scale digital circuits because of information loss [59], which is an inevitable phenomenon in irreversible logic gates. However in reversible logic gates due to one by one mapping between the input ports and output ports, it is possible to obtain the state of input ports from

![Fig. 14: Schematic representation of the photonic crystal Switch proposed by Yanik et al [22]. Red ellipses represent nonlinear materials. Adapted from [22].](image)

![Fig. 15: Schematic representation of the photonic crystal Transistor proposed by Yanik et al [31]. Red ellipses represent nonlinear materials. Adapted from [31].](image)

![Fig. 16: Schematic representation of the photonic crystal half adder proposed by Neisy et al [56]. Red circles represent nonlinear materials. Adapted from [56].](image)
the output ports. As a result, no information loss is presented, so the energy dissipation will be reduced. Thus, all-optical reversible XOR and XNOR gates based on electromagnetic scattering phenomenon in nonlinear photonic crystal structures were reported [58]. The projected devices consist of two cross-connected waveguides acting as inputs and outputs along with some linear and nonlinear defect rods, illustrated in Figure 17. A 2DPhC with cubic lattice was used as the fundamental structure. The operation principle to get the XOR reversible logic is to map one of the inputs to one of the outputs, and compute in the second output the conventional XOR operation. For the XNOR, the inverse of one input is mapped to one of the outputs, and the conventional XNOR operation is performed in the second output. The results showed the correct operation of the logic gates for transmission values greater than 65% and lower than 2% considered as logic 1 and logic 0, respectively. The authors also reported a 10 ps of a maximum time delay to get the logic answer.

Recently, Caballero et al [60] reported the complementary photonic crystal integrated logic devices (CPCL). It consists of two logic devices named as Switch N and P, shown in Figure 18. The former is the equivalent of NMOS logic transistor and can operate as AND gate. The later can work as an inverter and as PMOS logic transistor. Simulation results provided by authors demonstrate a highly efficient clock rate, higher than 20 GHz, guaranteeing operation at both input and output with the same wavelength (around \( \lambda \), clock rate, higher than 20 GHz, guaranteeing operation at

PhC are good candidates to develop computational logic devices with low power consumption, high-efficiency and with high-speed data processing.

### IV. Discussion

In this section we discuss the advantages and disadvantages of the strategies to project PhC logic gates, together with a description of the most relevant achievements of this review.

PhC logic gates based on self-collimation present small area, faster response, wide operating frequency bandwidth, and low power operation. Indeed, their size can be reduced to the operating wavelength order. They are also independent of light intensity allowing operation in a low power regime. However, they lack a high contrast ratio, and their microfabrication is challenging to achieve since it is hard to control the dispersion relations to achieve the self-collimation effect. Besides, they need phase shifters to synchronize input signals, increasing the device size and limiting their adjacent connection. LG based on this approach can be suitable for hybrid system integration, signal processing, extreme environments sensors, and frequency shifters.

PhC logic gates based on MMI exhibit small size, faster response, large operating frequency bandwidth and low power operation. The compactness of the MMI region ensures their simple configuration, robustness and low loss. In addition, low power operation is accomplished because their light intensity independent. Nevertheless, they need to adopt different phase values to achieve the logic state representations. This fact brings inconvenience towards the development of integrated circuits because it increases their size and leads to hard fabrication processes. In general, this scheme can be used to project extreme environments sensors, frequency shifters, splitters, demultiplexers and signal processing.

In general, PhC logic gates based on waveguide interference paths have wide operating frequency bandwidth, low

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**Fig. 17**: Schematic representation of the photonic crystal reversible XOR and XNOR gates proposed by Hassangholizadeh-Kashtiban et al [58]. Red circles represent nonlinear materials. Adapted from [58].

**Fig. 18**: Schematic representation of XY view of the photonic crystal integrated switch N (a) and (b) switch P. In this, dark and light colors correspond to two different dielectric constant \( \varepsilon_1 \) and \( \varepsilon_2 \), respectively. With \( \varepsilon_1 > \varepsilon_2 \). Black and red circles represent optimized holes with radii 0.75\( r \) and 0.5\( r \), respectively. The displacements of the holes around the cavity are \( S1 = 0.33a \), \( S2 = 0.26a \), \( S3 = 0.12a \). Adapted from [60].
power operation, fast response, and reasonable contrast ratio. They are simple designs for two and three inputs logic gates showing high availability and fault tolerance. They can also operate within the entire bandgap of the PhC. Their input power independence also allows the operation in the low power regime with low losses and slight heat dissipation. In contrast, their size typically is in the order of few microns above the operating wavelength. Also, logic gates up to three inputs are challenging to achieve since it is difficult to synchronize the phase difference for the light input signals. This fact can also limit their microfabrication. In addition, the feedback propagation to the inactive inputs has limited their application for large scale circuits. In particular, these kinds of devices can be extensively used for applications in signal processing, routing, parallel computing, and hybrid system integration.

Nonlinear PhC logic gates can reach high contrast ratios and are input phase independent. LG based on this approach can have high power operation due to the required optical control pulse to generate the nonlinear effects. Also, the response time and the bandwidth are limited by the nonlinear material and the resonant frequency of the resonator, respectively. LG based on this approach are good candidates for integrated photonic devices, sensors, switching, and signal processing. They can also be applied towards the realization of PhC logic circuits.

Overall, the development of PhC logic gates has gain attention in recent years due to the importance of these systems to build the future of the fully photonic communication and computer systems. Here, we have evaluated 139 papers related to this subject retrieved from IEEE, IET, AIP, OSA, Elsevier, IOP, Springer. These are summarized in Figure 19. This fact is evident with the increasing number of papers published per years (see Figure 19a), compiled on 75% of journal articles and 25% proceedings and conference meetings. As expected, we found that the most used logic gates to build circuits appears at the top of the implementations, as illustrated in Figure 19b. On the other hand, Figure 19c shows that logic gates are implemented on a PhC lattice configuration of rods on air and holes in a substrate with 83% and 16%, respectively. It can explain the poor number of experimental works (3%) since the former still a challenging fabrication process. Finally, we found that employed materials are the most common in integrated photonic technology, such as Si, GaAs, SiO$_2$, AlGaAs, InP, Ag-Polymer, Lithium Niobate, Germanio and Chalcogenide glass, as illustrated in Figure 19d.

These achievements allow us to address and take advantage of PhC logic gates for a wide range of integrated photonics technology applications. However, the demonstration of PhC circuits still remains a big challenge rather than simple or compact isolated logic gates. It is also necessary to develop and improve fabrication processes to implement different PhC logic devices based on each technique. Another important point to consider is the standardization design for each method exploited here. Within the same approach, different dimensions, unequal transmission, or power values representing the same logic value can generate undesirable behaviors of devices.

| Logic Gate Method | Advantages | Weakness | Possible Applications |
|-------------------|------------|----------|-----------------------|
| **Self-Collimation**<br>Refs. [44–47] | Small area<br>Faster response<br>Wide operating frequency bandwidth<br>Low power operation | Low contrast ratio<br>Hard microfabrication<br>Input phase requirement | Hybrid system integration<br>Signal processing<br>Extreme environments sensors<br>Frequency shifters |
| **MMI**<br>Refs. [48–51] | Small size<br>Faster response<br>Large operating frequency bandwidth<br>Low power operation | Different phase for logic representation<br>Input phase requirement | Splitters<br>Demultiplexers<br>Signal processing<br>Extreme environments sensors<br>Frequency shifters |
| **Waveguide Interference Paths**<br>Refs. [27, 28, 52–55] | Wide operating frequency bandwidth<br>Low power operation<br>Fast response<br>Reasonable contrast ratio | Large size<br>Difficult to synchronize the phase difference | Signal processing<br>Routing<br>Parallel computing<br>Hybrid system integration |
| **Nonlinear Effects**<br>Refs. [22, 31, 56–58, 60] | High contrast ratio<br>Input phase independent | Narrow operating frequency bandwidth<br>High power consumption<br>Slow response | Sensors<br>Signal processing<br>Switching<br>Integrated photonics devices<br>Logic circuits |

Table 1: Summary of the advanced, weakness and possible applications for each PhC logic gates approach.
Fig. 19: Summary of the photonic crystal logic gates literature review. a) Articles published per year. b) Number of logic gates implemented. c) PhC lattice configurations and d) materials used to design logic gates.

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