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Design and modelling of all-optical NAND gate using metal-insulator-metal (MIM) waveguides based Mach-Zehnder Interferometers for high-speed information processing

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Abstract

All the basic logic gates play a major role in carrying out the mathematical computation. The drawbacks of conventional electronics are alleviated by all-optical integrated circuits with a great application of high-speed computing and information processing. In this paper, plasmonic metal-insulator-metal (MIM) waveguides have an excellent property of propagating the surface plasmons beyond the diffraction limit up to deep sub-wavelength scale. All-optical NAND gate design is optimized by using MIM plasmonic waveguide-based Mach-Zehnder Interferometers (MZIs) in the footprint of 36 µm × 8 µm that works at 1.55 µm operating wavelength. The better performance of the proposed device is achieved, such as the extinction ratio is 10.55 dB, insertion loss is obtained as 0.506 dB, and response time is 262 ps. The proposed design is verified by using the finite-difference time-domain (FDTD) technique and further analysis are carried out by mathematical computation and MATLAB simulation results.

Keywords: Plasmonic waveguide; MIM waveguide; Mach-Zehnder interferometer; all-optical logic gate, finite-difference time-domain (FDTD)
1. Introduction

In the earlier days, the enrichment of changing information is attained by semiconductor devices, though predictably used this semiconductor technology gets menial due to some restraints like heat dissipation and interconnect delays [1]. The telecommunication and network started research on photonics devices due to the increase of high deprivation of capacity and small size, in which the alteration of information is done through photons rather than electrons [2]. Due to miniaturization and with huge density devices, the diffraction of light becomes more susceptible. To overcome this problem, the restraining and regulating of light yonder diffraction of light can be contrived viable by using plasmonics, it attributes the combination of electrons and photonics. [3-4]. Due to some unique features, the propagation has been achieved by surface plasmon polaritons (SPPs) in plasmonics. The synergy of the electromagnetic waves and free electrons on the metals produce the surface plasmon polaritons which is a restrained wave that interface between the metal and dielectric [5]. In the last few years, the researchers lead to scrutiny of various plasmonic waveguides based on SPPs such as metal-insulator-metal (MIM), insulator-metal-insulator (IMI), dielectric-loaded surface plasmon polaritons (DLSPP) [6-10]. Among all these geometric waveguides, MIM waveguide has been preferred due to its their capability of exceptional properties like subwavelength confinement, very high grouping velocity and routing of light at nanoscale is also possible. By using semiconductor optical amplifier Mach-Zehnder interferometer (SOA-MZI) [11-13] and lithium-niobate (LiNbO3) [14-16] are already have been implemented for all the basic and universal logic gates. This work proposes the optimization of footprint in the NAND gate structure. As compared to the earlier design of the NAND gate using electro-optic effect, used four MZIs [17-19] to implement NAND gate but the proposed design is achieved by using only two MZIs to implement all-optical plasmonic NAND gate. Using these MIM waveguides, some designs are directional coupler (DC) and non-linear Mach-Zehnder interferometers (MZIs) have been proposed and serving as a house for optical circuit designs. The significant phenomenon of directional coupler is the coupling of optical signals is beating for even-odd modes. By combining two directional couplers, an MZI is proposed with two linear waveguides in which one linear (linear 2) waveguide is filled with non-linear Kerr material to accomplish the exchange of light and named as nonlinear plasmonic MZI or, metal-insulator-metal (MIM) waveguides based MZI. All-optical components have a
remarkable and vital role in modern integrated and optical circuits in like a manner; all logic gates play a major role in the electronic devices.

The outline of this paper presents a design of all-optical universal NAND gate using MEH-PPV [poly (2-methoxy-5-(28-ethylhexyloxy)-PPV)] as a nonlinear Kerr [20] material in its one arm of plasmonics MZI. The analysis of the proposed structure is carried out by simulation in MATLAB and results are verified using finite-difference time-domain (FDTD) [21]. Section 2 presents a description of nonlinear single MZI and its mathematical description and portraying the propagating of light through it. Section 3 comprises of design and simulation results of the NAND gate. Finally, Section 4 comprises the conclusion of work.

2. Design of metal-insulator-metal waveguides based Mach-Zehnder Interferometer

In this paper, metal-insulator-metal waveguides based MZI is designed by cascading of two directional couplers and two linear metal-insulator-metal (MIM) waveguides in which one linear (linear 2) waveguide is filled with nonlinear Kerr material to achieve switching of light as shown in Fig 1.

![Fig. 1 Schematic of nonlinear Mach-Zehnder interferometer](image)

The structure of MZI has a size within the footprints of (32 × 3.2) μm. This is the basic optical device used to a great extent for the development of logic gates. MEH-PPV [poly (2-methoxy-5-(28-ethylhexyloxy)-PPV)] is used as the nonlinear Kerr material which gives enormous Kerr nonlinearity with permittivity of $2e^{-18}$ m²/V² [22]. The relative linear permittivity ($\varepsilon_L$) of Kerr material is 2.7225 with refractive index (RI) and response time are $n=1.65$ and $\tau=2.0 \times 10^{-15}$s, respectively. The dispersion equation for three-layered MIM waveguide is given as [23]:

$$\frac{k_m}{\varepsilon_m} = -\frac{k_d}{\varepsilon_d} \tanh\left(\frac{r}{2} \frac{t_d}{\varepsilon_d}\right)$$

(1)
whereas $\varepsilon_d$ and $\varepsilon_m$ are the permittivity of dielectric and metal, respectively. $k_d$ and $k_m$ are the transverse wave number for dielectric and metal, respectively; where ‘d’ is the width of the linear insulator, which is taken as air. By using a Drude Lorentz model, the metal supporting SPPs are considered as silver [24]:

$$
\varepsilon_2(\omega) = 1 - \frac{\omega_p^2}{\omega(\omega + i\gamma)} + \sum_{n=1}^{5} f_{n\omega_p^2} \frac{\omega^2 - \omega_n^2}{\omega_n^2 - \omega^2 - i\omega\gamma n}
$$

(2)

$\omega_p = 140\times10^{14}$ rad/s and $\gamma = 0.13\times10^{14}$ rad/s are bulk plasma frequency of silver and damping constant, respectively. By changing the RI of Kerr material, the intensities of light vary, which is called the Kerr effect, and the phase of the signal varies, which leads to switching of the light signal from one port to another port by a change in power of input signal. When continuous wave (CW) of wavelength 1550 nm excites transverse electric (TE) mode with an input power of $E_{in} = 3e^9 \text{ W/m}$ (considered as logic ‘1’) is given at input port 1, by obeying the principle of self-phase modulation (SPM), output signal obtained at the first output port as shown in Fig. 2(b). When input power of $E_{in} = 0.7e^9 \text{ W/m}$ (considered as logic ‘0’) is given at input port 1, by obeying the principle of cross-phase modulation (XPM), output signal obtained at second output port as shown in Fig. 2(c).

If an input, $E_{in}$ applied at input port, the output at A and B can be written [25-26] as:

$$
A = \sqrt{1 - \alpha_1}(E_{in}) + j\sqrt{\alpha_1}(0)
$$

$$
B = j\sqrt{\alpha_1}(E_{in}) + \sqrt{1 - \alpha_1}(0)
$$

(3)

where $\alpha_1$ is the attenuation constant of first directional coupler shown in Fig. 2(a) near input ports Eq. 3 can be written as:

$$
\begin{bmatrix}
A \\
B
\end{bmatrix} = 
\begin{bmatrix}
\sqrt{1 - \alpha_1} & j\sqrt{\alpha_1} \\
j\sqrt{\alpha_1} & \sqrt{1 - \alpha_1}
\end{bmatrix} \begin{bmatrix} E_{in} \\
0
\end{bmatrix}
$$

(4)

Further propagation of signal is from first directional coupler off from points A and B through linear arms reaches at point C and D at output ports respectively, the input signal can be written as

$$
c = Ae^{-j\varphi_1}, \ D = Be^{-j\varphi_2}
$$

(5)

where $\varphi_2$ has a phase difference in second linear arm due to Kerr material and $\varphi_1$ has a value equal to zero since in the second linear arm there is no phase difference occurs as detailed in [27] as:

$$
\varphi_1 = \Delta \varphi = (2\pi/\lambda)(\vec{n}_X - \vec{n}_Y)L
$$

(6)

where $\lambda$ is the wavelength of signal and L is the length of the second linear arm of MZI and

$$
\begin{align*}
\vec{n}_X &= n_X + \Delta n_X \\
\vec{n}_Y &= n_Y + \Delta n_Y
\end{align*}
$$

(7)
where $\Delta x$ and $\Delta y$ are the linear different RIs in the second linear arms of the MZI, because of modal birefringence with low and high intensities of a signal, respectively. $\Delta n_X$ and $\Delta n_Y$ are the RIs of nonlinear parts because of the induced birefringence. If the signal is launched with low intensity, then

$$\Delta n_X = 2n_2|E_{in}|^2$$

where $E_{in}$ is the intensity of input signal. When high intensity signal is launched, then due to XPM, the RI becomes

$$\Delta n_Y = 2n_2 b|E_{in}|^2, b = \chi^{(3)}_{XXX}/\chi^{(3)}_{XYY}$$

where $n_2$ is the RI of the linear arm of the MZI containing Kerr material, $\chi^{(3)}_{XYY}$ and $\chi^{(3)}_{XXX}$ are the third-order susceptibility of the nonlinear Kerr material, $b = 1/3$ where the signal is purely electronic. Using Eqs. (4) and (7), phase shift becomes;

$$\phi_2 = \Delta \phi_L + \Delta \phi_{NL} = (2\pi L/\lambda)(\Delta n_L + n_2 b|E_{in}|^2)$$

where $\Delta n_L = n_X - n_Y$ and the Kerr coefficient is $n_2 = 2n_2 (1-b)$. For high signal intensity, $(\Delta \phi \neq 0)$ and the maximum, the transmission of signal through a first linear arm of MZI, with transmittivity written as

$$T_{out1} = \frac{1}{4} \left| 1 - e^{i \phi_2} \right|^2 = \sin^2 \left( \frac{\Delta \phi}{2} \right)$$

When low intensity of light signal is fed into the input, it arrives at the output port can be written as

$$T_{out2} = \cos^2 \left( \frac{\Delta \phi}{2} \right)$$

Eqs. (9) and (10) gives the output of single MZI, one can calculate the power when high and low-intensity signal is fed at its first input port, respectively. This exclusive switching property of MZI has been used to design the proposed device.

![Image](image-url)

**Fig. 2** Polarization of light through single MZI, at (a) high-intensity signal, (b) low-intensity signal

### 3. Design, mathematical formulation and simulation results of all-optical NAND gate

The miniature of all-optical NAND gate is designed using two nonlinear MZI within the footprint of $36 \mu m \times 8 \mu m$ as shown in Fig. 3. By using MIM, the proposed structure is
designed which supports the plasmonic modes. To get desired operation of universal gate, the optical signals are given at first and second input port of MZI1 and MZI2, respectively. The second and first output ports of MZI1 and MZI2 provides phase shift leads to switching of light. To obtain the optical power at the output port, the two signals combined to get the output of the NAND gate.

\[
\begin{align*}
    m_1 &= \cos^2 \left( \frac{\Delta \phi_{MZI1}}{2} \right) + \cos^2 \left( \frac{\Delta \phi_{MZI2}}{2} \right) & \text{for AB = 00} \\
    m_2 &= \cos^2 \left( \frac{\Delta \phi_{MZI1}}{2} \right) + \sin^2 \left( \frac{\Delta \phi_{MZI2}}{2} \right) & \text{for AB = 01} \\
    m_3 &= \sin^2 \left( \frac{\Delta \phi_{MZI1}}{2} \right) + \cos^2 \left( \frac{\Delta \phi_{MZI2}}{2} \right) & \text{for AB = 10} \\
    m_4 &= \sin^2 \left( \frac{\Delta \phi_{MZI1}}{2} \right) + \sin^2 \left( \frac{\Delta \phi_{MZI2}}{2} \right) & \text{for AB = 11}
\end{align*}
\] (13)

Hence the overall output of NAND gate is \( \text{OUTPUT}_{\text{NAND}} = m_1 + m_2 + m_3 \), because there is no output signal for the remaining combinations of input signals.

\[
\text{OUTPUT}_{\text{NAND}} = \cos^2 \left( \frac{\Delta \phi_{MZI1}}{2} \right) + \cos^2 \left( \frac{\Delta \phi_{MZI2}}{2} \right) + \cos^2 \left( \frac{\Delta \phi_{MZI1}}{2} \right) + \sin^2 \left( \frac{\Delta \phi_{MZI2}}{2} \right) + \\
\sin^2 \left( \frac{\Delta \phi_{MZI1}}{2} \right) + \cos^2 \left( \frac{\Delta \phi_{MZI2}}{2} \right)
\] (14)

The design of the all-optical NAND gate is verified using MATLAB and FDTD method. By cascading two MZIs, the layout of all-optical NAND gate as shown in Fig. 3. A continuous-wave (CW) in transverse electric (TE) a source of wavelength 1550 nm is fed with both input power of \( 0.7e^9 \) W/m and \( 3e^9 \) W/m are considered as low and high- intensity optical signals, respectively. The proposed design is within the mesh size of \( \Delta x = 0.07711 \) µm and \( \Delta z = 0.0772 \) µm. The perfect matched layer (PML) is used as the boundary conditions, with the reflection coefficient of \( e^{-12} \). The propagation of the optical signal through the proposed
design for all possible combinations of input signals is shown in Fig. 4. The timing diagram of the NAND gate is obtained through MATLAB simulation as shown in Fig. 5 is verified with the truth table as shown in Table. 1. The different possible input combinations to verify the logic of NAND gate have been applied and discussed as per the following case study. Some operating parameters of plasmonic NAND gate are shown in Table. 2.

Table. 1: Truth table of NAND gate

| Input A | Input B | Output (X) |
|---------|---------|------------|
| 0       | 0       | 1          |
| 0       | 1       | 1          |
| 1       | 0       | 1          |
| 1       | 1       | 0          |

Table. 2: Operating parameters of proposed all-optical NAND gate

| Input field transverse | Gaussian          |
|------------------------|-------------------|
| Wavelength (λ)         | 1.55 µm           |
| Polarization           | Transverse electric |
| No. of Mesh cells X    | 64                |
| No. of Mesh cells Z    | 414               |
| Boundary conditions    | Perfectly matched layers |
| Coupling length (Lc)   | 3.6µm             |

Case (a): A=0, B=0
In this case, A=0, B=0, means both signals are low-intensity signals fed from the first and second ports of MZI1 and MZI2. The outputs of these MZIs have arrived at the opposite port of the same MZI. The outputs are combined due to SPM and thereafter the output signal arrives at the output port (X) is considered as the output of this combination. Thus, the output of the NAND gate is “1” (as shown in Fig.4 (a)).

Case (b): A=0, B=1
At this point, A=0 means low- intensity signal is fed from first input port of MZI1, due to SPM, the output signal arrives at the cross port of the same MZI1. For B=1 means high-intensity signal is fed from second input port of MZI2, due to XPM, the output signal arrives at the first output port of the same MZI2. At the output port (X), a signal from the MZI1 is
considered as the output of this combination. Thus, the output of NAND gate is “1” (as shown in Fig. 4 (b)).

\[
\begin{array}{c}
\text{(A)} \rightarrow \quad \text{(b)} \\
0 \rightarrow \quad (X=1) \\
(B) \rightarrow \\
(0) \rightarrow \\
\end{array}
\]

\[
\begin{array}{c}
\text{(A)} \rightarrow \quad \text{(c)} \\
1 \rightarrow \quad (X=1) \\
(B) \rightarrow \\
(1) \rightarrow \\
\end{array}
\]

**Fig. 4** Output results for all possible combinations of an optical signal obtained from FDTD

**Case (c): A=1, B=0**

Under this case, A=1 means high-intensity signal is fed from the first input port of MZI1, due to XPM, the output signal arrives at the first output port of the same MZI1. For B=0 means low-intensity signal is fed from the second input port of the MZI2, due to SPM, the output signal arrives at the first output port the same MZI2. At the output port (X) signal from the MZI2 is considered as the output of this combination. Thus, the output of NAND gate is “1” (as shown in Fig.4 (c))

**Case (d): A=1, B=1**

For this case, A=1 means high-intensity signal is fed from first input port of MZI1, due to XPM, the output signal arrives at the first output port of the same MZI1. For B=1 means high-intensity signal is fed from the second input port of the MZI2, due to XPM, the output signal arrives at the second output port of the same MZI2. At the output port (X) no signal has arrived from both MZIs. Thus, the output of NAND gate is “0” (as shown in Fig.4 (d)).

From the above-discussed cases, we observed that the output of NAND gate is obtained only when both or either one of the inputs is fed with low intensity, which is exactly matched with the MATLAB timing diagrams shown in Fig. 5.
The response time is calculated for all the four input combinations of the proposed all-optical plasmonic NAND gate. The output power level requires some time to become stable. In the NAND gate for all the combinations, the response time almost equal. The response time of the proposed design all-optical NAND gate is in the range of 261.52 ps to 262.87 ps. The response time for all four combinations is shown in Fig. 6.

**Table. 3:** Performance study of proposed logic gate

| Sl. No. | Parameters       | Formulae                           | Value (dB) |
|---------|------------------|------------------------------------|------------|
| 1.      | Extinction ratio | \[10 \log_{10} \left( \frac{P_{ON}}{P_{OFF}} \right)\] | 10.55      |
|         |                  | \(P_{ON}\) = peak output power in ON state \(P_{OFF}\) = peak output power in OFF state |            |
The performance analysis of the all-optical plasmonic NAND logic gate is studied by calculating some parameters like extinction ratio (ER) and insertion loss (IL) are provided in Table. 3. The ER is calculated as 10.55 dB and IR is observed as 0.506 dB. The performance and some key design parameters of the proposed design is compared with the earlier designs are shown in Table. 4. The proposed all-optical plasmonic NAND gate is compared with existing plasmonic NAND gate, NAND gate designed by using semiconductor optical amplifier (SOA), and photonic crystal ring resonator. From Table. 4, we can easily determine that the proposed structure has less size as compared to previous design and the extinction ratio and insertion loss are also optimized.

Table 4: Performance and design parameter comparison of proposed all-optical plasmonic NAND gate with existing devices

| Sl. No. | Parameters | Semiconductor Optical Amplifier (SOA) NAND gate Ref. [29] | Existing photonic crystals NAND gate Ref. [30] | Existing plasmonic all-optical NAND gate Ref. [31] | Proposed plasmonic all-optical NAND gate |
|--------|------------|----------------------------------------------------------|-----------------------------------------------|------------------------------------------------|-----------------------------------------|
| 1.     | Dimensions | 43.75 µm × 37.625 µm                                      | 40 µm × 7.5 µm                                | 36 µm × 8 µm                                    |                                         |
| 2.     | Wavelength | 1.55 µm                                                  | 1.55 µm                                      | 1.55 µm                                        |                                         |
| 3.     | Polarization | TM                                                     | TE                                           | TE                                             |                                         |
| 4.     | Extinction ratio | 9.73 dB                                                | 20 dB                                        | 10.25 dB                                       | 10.55 dB                               |
| 5.     | Insertion loss | –                                                      | –                                            | 0.756 dB                                       | 0.506 dB                               |

4. Conclusion:
In this paper, metal-insulator-metal (MIM) plasmonic waveguides are used to design the Mach-Zehnder interferometer (MZI). An MZI switching property is mainly used to design an all-optical NAND gate. The footprint of the designed gate is 36 µm × 8 µm at an operating wavelength of 1.55 µm. Some parameters are calculated like extinction ratio (ER) and
insertion loss (IL) are 10.55 dB and 0.506 dB, respectively. The response time of the proposed all-optical NAND gate is in the range of 261.52 ps to 262.87 ps. As per Table 4, performance of the proposed gate is better than the earlier NAND gate. The study of the proposed design verification is done using mathematical computation, MATLAB simulation and carried out by FDTD results.

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Disclosure
The authors declare that there are no conflicts of interest related to this article.

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Figures

Figure 1
Schematic of nonlinear Mach-Zehnder interferometer

(a) High-intensity signal
(b) Low-intensity signal

Figure 2
Polarization of light through single MZI, at (a) high-intensity signal, (b) low-intensity signal
Figure 3
Schematic representation of all-optical NAND gate

(A) →
0 0
(B) →
(X=1)
(a)

(A) →
0 1
(B) →
(X=1)
(b)

(A) →
1 0
(B) →
(X=1)
(c)

(A) →
1 1
(B) →
(X=0)
(d)

Figure 4
Output results for all possible combinations of an optical signal obtained from FDTD

Figure 5
MATLAB simulation results for all-optical NAND gate
Figure 6

Normalized power versus time for all four combinations: a) “00”; b) “01”; c) “10”; d) “11”