Spectroscopic Sensing Method of Liquid Permittivity with On-Chip Capacitor

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Abstract

A liquid permittivity sensing method is proposed, which is based on measuring the $S_{21}$ of an on-chip capacitor submerged in a material under test (MUT). The real part of permittivity can be estimated from $\varepsilon' - |S_{21}|$ fitted relation in the frequency range of 100 MHz–5 GHz, which are pre-calibrated with four kinds of known materials. The on-chip capacitor is realized with a top metal in a complementary metal oxide semiconductor (CMOS) process, which has an inter-digitized structure featuring a small size of 150 $\mu$m $\times$ 195 $\mu$m. A simple circuit model of the capacitor is used to develop $\varepsilon' - |S_{21}|$ relation with a fitting parameter. It shows less than 6.4% root-mean-square (rms) error of $\varepsilon'$ for propanol (C$_3$H$_8$O) at 100 MHz–5 GHz.

Key Words: Calibration, Capacitive Sensor, Curve Fitting, Dielectric Spectroscopy, Inter-Digitized.
pendent capacitance.

A sensing capacitor, as a critical interface between the MUT and the spectroscopy measurement system can be implemented in either on-chip or off-chip [7–9, 11]. As mentioned earlier, an off-chip sensing capacitor suffers from large parasitics and its bulky size. The interconnection between an off-chip capacitor and a spectroscopy device requires a careful attention as well.

In this work, we propose a design and an implementation of an on-chip capacitor for dielectric constant extraction featuring a small sensor size and lower parasitic, and built a low-cost. Utilizing the top metal in the CMOS back-end, inter-digitized conductors are fabricated using 28-nm CMOS technology. The dielectric properties between inter-digitized conductors are governed by the off-chip liquid MUTs by opening the passivation layer. The permittivity of the MUT is extracted within the 100 MHz–5 GHz frequency range by measuring the forward voltage-gain ($S_{21}$). Permittivity as a function of $|S_{21}|$ at each frequency can be extracted by polynomial curve fitting. Additionally, we propose a weighted $|S_{21}|$ equation to improve a sensor accuracy. Through a proposed curve-fitting calibration method, less than 6.4% root-mean-square (rms) error is successfully achieved when propanol (C$_3$H$_8$O) is used as a material.

This article is organized as follows. Section II discusses a simple but intuitive circuit model for the sensing capacitor. Section III presents measurement results and polynomial curve fitting method for calibration. The conclusion of this article is in Section IV.

II. CIRCUIT MODEL OF SENSING CAPACITOR

Fig. 1 shows the illustration of an on-chip sensing capacitor whose dielectric property is governed by the MUT. The sensing capacitor is in an inter-digitized fashion implemented with the top metal of the CMOS back-end process (LB in the 28-nm Samsung CMOS process). The passivation layer has to be removed in order to contact the top metal directly to MUT.

A side section view of the on-chip sensing capacitor with a simple equivalent circuit model is depicted in Fig. 2. Capacitance and conductance denoted as $C_{sensing}$ and $G_{sensing}$ are due to the E-fields passing through the MUT by two inter-digitized top metal nodes, while the E-fields passing through the inter-metal-dielectric (IMD) create undesired parasitic capacitance and conductance modeled as $C_{par}$ and $G_{par}$, which are not related to the characteristics of the MUT. Additionally, capacitance due to the substrate, $C_{sub}$ is part of the network, which degrades the accuracy of the permittivity detection.

In the case of $C_{sensing}$ and $G_{sensing}$, their values are determined according to the permittivity of the MUT ($\epsilon_{MUT} = \epsilon_{MUT}' - j \cdot \epsilon_{MUT''}$). In contrast, $C_{par}$ and $G_{par}$ are fixed values governed by permittivity of the IMD ($\epsilon_{IMD} = \epsilon_{IMD}' - j \cdot \epsilon_{IMD''}$).

Excluding the effect of $C_{sub}$, the real-part of admittance between two inter-digitized electrodes ($Y_s$) comes from $G_{sensing}$ and $G_{par}$, while the imaginary part of $Y_s$ is composed of $C_{sensing}$ and $C_{par}$. $G_{sensing}$ and $G_{par}$ are circuit components due to the imaginary permittivity of MUT $\epsilon_{MUT''}$ and IMD $\epsilon_{IMD''}$, respectively, which corresponds to an energy loss. $C_{sensing}$ and $C_{par}$ come from the real permittivity of MUT $\epsilon_{MUT'}$ and IMD $\epsilon_{IMD'}$, respectively, representing energy storage [12]. Combined admittance between two electrodes can be expressed as follows.

$$Y_s = j \omega \cdot C_0 \cdot (\epsilon_{MUT'} - j \cdot \epsilon_{MUT''})$$
$$+ j \omega \cdot C_1 \cdot (\epsilon_{IMD'} - j \cdot \epsilon_{IMD''})$$
$$= j \omega \cdot (C_0 \cdot \epsilon_{MUT'} + C_1 \cdot \epsilon_{IMD'})$$
$$+ \omega \cdot (C_0 \cdot \epsilon_{MUT''} + C_1 \cdot \epsilon_{IMD''})$$
$$= j \omega \cdot f(\epsilon_{MUT'}) + \omega \cdot g(\epsilon_{MUT''})$$

where $C_0$ ($C_i$) are coefficients of effective capacitance due to MUT and IMD and $f(\cdot)$ and $g(\cdot)$ denote nonlinear mapping functions from the permittivity of MUT to admittance as a result.

To improve the sensitivity in measuring the permittivity of MUT, parasitic capacitances, $C_{par}$ and $C_{sub}$ in parallel with the
desired capacitance, \( C_{\text{sensing}} \), have to be minimized. \( C_{\text{sub}} \) can be made small by narrowing the spacing of the two nodes of the sensing capacitor. At the same time, narrow spacing between two electrodes reduces the self-resonant frequency (SRF) as a result of an increase in \( C_{\text{sensing}} \) and mutual inductances. The inductances caused by the geometry of the on-chip sensing capacitor have a direct trade-offs with the size of the sensing capacitor such as a spacing and the width of the top metal. Thus, in this work, the target frequency band and area of the chip are budgeted in advance, and the optimal sensing capacitor dimension is determined in consideration of the above mentioned trade-offs.

The top metal is composed of aluminum, and an oxide layer is created when the top metal is exposed to the MUT without any passivation (protection). The thick oxide film attenuates the electric field and reduces the sensitivity of the sensing capacitor. In the fabricated chip, the oxide film thickness ranges from 1 to 5 nm [13]. The electromagnetic simulation was performed and the sensitivity was not degraded even with 10 nm oxide film thickness. This thin oxide film, on the other hand, is beneficial by protecting the sensing capacitor from the damage and chemical reaction due to its high corrosion resistance and wear resistance.

Fig. 3 shows the schematic model of the sensing area with a two-port vector network analyzer (VNA). The forward voltage gain \( S_{21} \) of this network can be calculated by finding the ratio between the injected voltage source \( V_1 \) and the load voltage \( V_2 \) while the network is terminated with the port load impedance \( R_{\text{port}} \).

\[
S_{21}(j\omega) = \frac{V_2}{V_1} = \frac{Y_2}{Y_1 + \frac{j\omega C_{\text{sub}}}{R_{\text{port}} + j\omega C_{\text{sub}}}} = \frac{\omega g(\varepsilon_{\text{MUT}}') + j\omega R_{\text{port}} f(\varepsilon_{\text{MUT}}')}{\omega g(\varepsilon_{\text{MUT}}') + j\omega f(\varepsilon_{\text{MUT}}')} \quad (2)
\]

In both the numerator and the denominator, the resistance \( (\omega \cdot g(\varepsilon_{\text{MUT}}')) \) due to the imaginary part of permittivity is significantly smaller than those of the other components. For instance, long transmission lines, connected in series with a sensing capacitor, makes \( Y_2 \) more capacitive. Thus, we can simplify Eq. (2) into the following formula:

\[
S_{21}(j\omega) \approx \frac{j\omega R_{\text{port}} f(\varepsilon_{\text{MUT}}')}{1 + j\omega R_{\text{port}} f(\varepsilon_{\text{MUT}}')+C_{\text{sub}}} \quad (3)
\]

The dominant pole is calculated to be \( \omega_p = \frac{1}{R_{\text{port}} f(\varepsilon_{\text{MUT}}') + C_{\text{sub}}} \). Electromagnetic simulation with Advanced Design System (ADS) shows that \( \omega_p \) is higher than 6 GHz. Accordingly, in the expected measurement range between 0.1 and 5 GHz, the magnitude of \( S_{21} \) can be written as \( |S_{21}(j\omega)| = \omega \cdot R_{\text{port}} \cdot f(\varepsilon_{\text{MUT}}') \). This equation shows that magnitude of \( S_{21} \) indicates the real part of the permittivity of MUT(\( \varepsilon_{\text{MUT}} \)), while the non-linear mapping function, \( f(\cdot) \), needs to be decoded in a certain manner, as discussed in Section III.

### III. MEASUREMENT RESULTS AND CURVE FITTING CALIBRATION

For the measurement of scattering parameters of the fabricated sensor, PCB consisting of the mounted chip, interconnect transmission lines, and input/output ports are assembled as shown in Fig. 4. During the chip packaging process, the insulating epoxy was covered except for the fabricated capacitor area. With a plastic tube on top of the chip, the MUTs were injected through the micropipette (Eppendorf Research plus 312000038). Fig. 5 shows the real part of the permittivity \( \varepsilon' \) for methanol, ethanol, propanol, butanol and air over the frequency range of 0.1–5 GHz [14].
GHz [14]. The measured forward voltage gain ($S_{21}$) at room temperature is given in Fig. 6, which shows that the magnitude of $S_{21}$ is proportional to $\varepsilon'$ over the measured frequency range.

To extract $\varepsilon'$ of the MUT from the measured $S_{21}$ data, the curve-fitting calibration is performed first. Methanol, ethanol, butanol, and air are selected as reference materials, where the information on their real permittivity is provided in [14]. The real permittivity is then curve-fitted at each measured frequency by third order polynomial function with the measured $|S_{21}|$.

The detailed polynomial curve fitting procedure from the measured $|S_{21}|$ is elaborated below:

1. For each frequency, $f_i$, to be curve fitted, reference materials are injected into the sensing capacitor and its $|S_{21}|$ is measured through a VNA.
2. The $\varepsilon'$ of the reference materials are least-square fitted to third order polynomial function with respect to the measured $|S_{21}|$.

$$\varepsilon' = a_3|S_{21}|^3 + a_2|S_{21}|^2 + a_1|S_{21}| + a_0$$  \hspace{1cm} (4)

3. The fitting parameters $a_0$, $a_1$, $a_2$, and $a_3$ can be found and saved by using the least-square fitting method to fit the polynomial regression function. This procedure is repeated over the entire measurement frequency.

Once the curve-fitting procedure is finished, the unknown permittivity of MUT can be calculated by utilizing its measured $|S_{21}|$ along with the calculated fitting parameters.

Given the above procedure, the permittivity of any unknown MUT can be extracted. To further enhance the measurement accuracy, we propose reshaping (weighting) the parameter, given the measured $S_{21}$. Instead of utilizing raw $|S_{21}|$, we give the weights $\beta$ on the imaginary part of $S_{21}$ and $(1-\beta)$ on the real part of $S_{21}$. The weighting function can emphasize/deemphasize the relevant terms to minimize the extraction error. The revised magnitude of $S_{21}$ is given by

$$|S_{21}'| = \sqrt{(1-\beta) \cdot \text{Real}(S_{21})^2 + \beta \cdot \text{Imag}(S_{21})^2}, \quad 0 \leq \beta \leq 1$$  \hspace{1cm} (5)

Fig. 7 shows the curve-fitted polynomial function and the real permittivity of the reference materials at 100 MHz and 5 GHz frequency with the frequency-dependent parameter $\beta$. From the extracted curve-fitted polynomial function, the permittivity of propanol is found to have a good correlation with reference value as shown in Fig. 8. By replacing $|S_{21}|$ with the proposed equation, the rms permittivity error of measurement is lowered from 9.1% to 6.4% over the frequency range of 0.1–5 GHz.

The proposed spectroscopy sensor is compared to the reported research works as depicted in Table 1. Our work is realized in a compact footprint (0.03 mm$^2$) on chip, while the operating frequency range is the second largest.

More reference materials for calibration can be added to minimize the permittivity error (higher polynomial function). The off-chip and on-chip transmission line in series with the sensing capacitor complicate the mapping function and degrade the accuracy accordingly. Their length then needs to be minimized to further enhance the measurement accuracy.
Table 1. Comparison of the proposed spectroscopy sensor with previous works

| Study                  | Frequency (GHz) | Architecture                          | Size (mm²) |
|------------------------|-----------------|---------------------------------------|------------|
| Bakhshiani et al. [7]   | 0.009–2.4       | Off-chip center tapped microstrip line | N/A        |
| Chuma et al. [8]       | 2.4             | Off-chip split ring resonator          | N/A        |
| Helmy et al. [9]       | 7–9             | On-chip inter-digitized capacitor      | 0.06       |
| Chien et al. [11]      | 1–50            | On-chip transmission line              | 0.03       |
| This work              | 0.1–5           | On-chip inter-digitized capacitor      | 0.03       |

IV. CONCLUSION

An on-chip capacitive sensor for dielectric spectroscopy is implemented in this work. Based on the circuit model of the on-chip sensing capacitor, the permittivity extraction method is proposed and the measurement result verifies the successful detection of the real part of the permittivity in the frequency range of 0.1–5 GHz. The inter-digitized sensing capacitor can be optimized in its sensitivity and SRF in consideration of the fabrication trade-offs. To improve the permittivity detection accuracy, the measured forward voltage-gain is curve-fitted by the polynomial regression function. The revised forward voltage-gain parameter is suggested for the detection and the measured permittivity of the propanol shows 6.4% rms error compared with the theoretical value.

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