Electric-field-induced superconductivity in electrochemically etched ultrathin FeSe films on SrTiO$_3$ and MgO

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Among the recently discovered iron-based superconductors$^{1-3}$, ultrathin films of FeSe grown on SrTiO$_3$ substrates have uniquely evolved into a high-transition-temperature ($T_c$) superconducting material$^{4-10}$. The mechanisms for the high-$T_c$ superconductivity are under debate, with the superconducting gap mainly characterized with in situ analysis for FeSe films grown by molecular beam epitaxy. Here, we investigate the high-$T_c$ superconductivity in ultrathin FeSe using an alternative top-down electrochemical etching technique in a three-terminal transistor configuration. In addition to the high-temperature superconductivity in FeSe on SrTiO$_3$, the electrochemically etched ultrathin FeSe transistor on MgO also exhibits superconductivity around 40 K, implying that the application of an electric field effectively contributes to the high-$T_c$ superconductivity in ultrathin FeSe regardless of substrate material. Moreover, the observable critical thickness for the high-$T_c$ superconductivity is expanded up to 10 unit cells under an applied electric field and the insulator–superconductor transition is electrostatically controlled. The present demonstration implies that the modification of charge imbalance of holes and electrons by the electric-field effect plays a crucial role in inducing high-$T_c$ superconductivity in FeSe-based electric double-layer transistors.

Iron selenide (FeSe) is one of a family of iron-based superconductors ($T_c \sim 8$ K; refs 1–3), having a tetragonal PbO structure consisting of the layered atomic stacking of iron and selenium along the c-axis. In single or two unit-cell-thick FeSe films grown by state-of-the-art bottom-up molecular beam epitaxy (MBE; refs 4–13) on insulating SrTiO$_3$ (refs 12–14) or Nb-doped SrTiO$_3$ (refs 4–12,15), a large superconducting gap of about 20 meV has been detected by angle-resolved photoemission spectroscopy (ARPES; refs 4–10) and scanning tunnelling microscopy (STM; ref. 11). Compared to expected $T_c$ values as high as 65 K (ref. 6) or 80 K (ref. 11) from the large gap energy evaluated by ARPES or STM, the temperature dependence of resistivity yields a consensus of the onset $T_c$ value around 40 K (refs 11–14). However, a very recent report of superconductivity above 100 K detected by in situ electrical resistance measurements for FeSe films grown on Nb-doped SrTiO$_3$ would apparently increase the spread in $T_c$ (refs 15,16). The mechanisms for the enhancement of the superconducting gap energy in ultrathin FeSe are now considered to be strong electron–phonon coupling$^{10}$, variation of the electronic band structure at the M point and the Γ point$^4$, and a charge transfer effect from the substrates$^{6,13,17}$. Ex situ characterizations based on the temperature dependence of resistivity regarding the effect of an electric field, magnetic field, thickness dependence of $T_c$ and substrate material dependence are matters of interest for a deeper understanding of high-$T_c$ superconductivity in ultrathin FeSe studies.

In contrast to the in situ bottom-up MBE technique, classical electrochemical etching is one of the best means of carrying out a top-down approach to ultrathin FeSe films, owing to a high uniformity and fine tunability in thickness being possible through continuous monitoring of the electrical current originating from the electrochemical reaction. In our experiments, we employed this electrochemical etching approach in a three-terminal transistor configuration, the so-called electric double-layer (EDL) transistor (Fig. 1a)$^{18}$. The EDL transistors were fabricated from 13- and 18-nm-thick FeSe films grown on insulating SrTiO$_3$ and MgO substrates, respectively, by pulsed-laser deposition, as described in Methods. The films and Pt gate electrode are covered with the ionic liquid (IL) $N,N$-diphenyl-$N$-$N$-(2-methoxyethyl)ammonium bis(trifluoromethanesulphonyl)imide (DEME-TFSI), as schematically shown in Fig. 1a. The original concept of the EDL transistor involves an electrostatic charge carrier doping based on the giant capacitance originating from a very narrow (<1 nm) EDL width. Following this concept, various kinds of physical phenomena, such as superconductivity$^{14-23}$, ferromagnetism$^{24}$ and an insulator–metal transition in correlated electron systems$^{25}$, have been induced or controlled electrostatically. Another feature of EDL transistors is the electrochemical reaction$^{26-28}$. These two features appear separately, with some boundary that depends on the temperature of the IL and the applied gate voltage$^{26,27}$, as schematically shown by the dashed black line in Fig. 1b. In this study, we have applied an electrostatic doping effect with varying $V_G$ below the characteristic temperature (blue arrow in Fig. 1b) and electrochemical etching to peel FeSe layers while tuning the sample temperature under a constant applied $V_G$ (red arrow in Fig. 1b). For each etching sequence, the systematic dependence on thickness and the effect of the field on superconductivity in FeSe films can be investigated, all within a single EDL transistor.

The temperature dependence of the channel sheet resistance ($R_{sh}$–$T$ curve) and its superconducting properties were measured using a standard d.c. four-terminal method. In the initial thick films on SrTiO$_3$ and on MgO, onset superconducting transition temperatures $T_{on} = 8.2$ K and 4.8 K are clearly observed on lowering the temperature $T$, as shown in Fig. 1c (solid lines), which is consistent with the bulk properties of FeSe (ref. 3). No significant change in the $R_{sh}$–$T$ curves (broken lines) is observed with electrostatic doping using $V_G = 5$ V at 220 K for the initial thick condition, implying that

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Electrochemical etching and electrostatic doping in the electric double-layer transistor. a, Schematic of the EDL transistor composed of FeSe films on insulating SrTiO$_3$ and MgO substrates. The side gate voltage $V_G$ is applied through the ionic liquid. The gate leakage current $I_G$ is measured using a picammeter, which is a measure for the degree of etching. b, Schematic illustrating the operation of an EDL transistor as a function of $V_G$ and temperature $T$. The broken line indicates the boundary between electrostatic doping and electrochemical etching. c, $R_s$–$T$ curves of sample A on SrTiO$_3$ and sample M on MgO for the initial thickness condition when $V_G = 0$ V (solid lines) and 5 V (broken lines) are applied.

Figure 1 | Electrochemical etching and electrostatic doping in the electric double-layer transistor. a, Schematic of the EDL transistor composed of FeSe films on insulating SrTiO$_3$ and MgO substrates. The side gate voltage $V_G$ is applied through the ionic liquid. The gate leakage current $I_G$ is measured using a picammeter, which is a measure for the degree of etching. b, Schematic illustrating the operation of an EDL transistor as a function of $V_G$ and temperature $T$. The broken line indicates the boundary between electrostatic doping and electrochemical etching. c, $R_s$–$T$ curves of sample A on SrTiO$_3$ and sample M on MgO for the initial thickness condition when $V_G = 0$ V (solid lines) and 5 V (broken lines) are applied.

The high-$T_c$ superconductivity is not induced at the IL/FeSe interface (see Supplementary Information) at this temperature.

The high-$T_c$ superconductivity appears in FeSe films grown on both SrTiO$_3$ (sample A) and MgO (sample N) after a series of etching, as shown in Fig. 2a,b. During the etching sequence, with a constant $V_G = 5$ V at about 245 K, $R_s$–$T$ measurements were performed regularly (black arrow in Fig. 1b) showing that the onset $T_c$ in the normalized sheet resistance $R_s/R_{00k}$ is greatly increased from the bulk value up to $T_c^{\infty} = 43.3$ and 39.0 K for samples A and N, respectively (the corresponding raw data are presented in Supplementary Information). The estimated $T_c^{\infty}$ values from $R_s$–$T$ curves shown in Fig. 2a,b are summarized in Fig. 2c,d as a function of the average thickness, which is carefully estimated from the initial thickness and the etching rate in the electrochemical reaction from the gate leakage current (see Supplementary Information). There are four implications derived from these figures. The first implication is that two-step transitions are observed in the intermediate etching region. Accompanying the enhancement of metallic behaviour, $R_s$ drops first at around $T = 30$ K, with second steps at 8 K and 4 K in the case of FeSe on SrTiO$_3$ and MgO, respectively, giving a first signature of the appearance of high-$T_c$ superconducting region in the device. The systematic disappearance of the bulky low-$T_c$ region with decreasing thickness is evidence of our fine tuning of the thicknesses. Indeed, the zero-resistance temperature of about 30 K implies that the FeSe layers are uniformly etched down to approximately one/two unit cells in thickness in our primitive devices. The second implication is that the high-$T_c$ superconductivity emerges in the ultrathin FeSe film on MgO as a result of the electric-field effect in the EDL transistor. Such high-$T_c$ superconductivity in FeSe on MgO has not been reported in in situ MBE studies—probably owing to there being no charge transfer from MgO to FeSe. In reality, in Hall effect measurements on high-$T_c$ one/two-unit-cell-thick FeSe on MgO, a negative Hall coefficient is observed, indicating electron accumulation is realized. The third implication is that the values of $T_c^{\infty}$ for both samples on SrTiO$_3$ and MgO are close to the highest values that have been reported in capped single-unit-cell-thick FeSe films grown by MBE (refs 12–14). If the disorder at the IL/FeSe interface limits $T_c^{\infty}$ to about 40 K, as low as the capped FeSe, further improvements of the interface quality are required to enhance $T_c^{\infty}$ towards 100 K (refs 15,16). The final implication is that the high-$T_c$ superconductivity emerges under a relatively thicker condition of about ten unit cells, followed by a weak thickness dependence. At present, it is not clear whether the whole thick-film region contributes high-$T_c$ superconductivity or not. We will return to this later.

To further investigate the superconducting properties, we performed $R_s$–$T$ measurements under perpendicular magnetic fields $B$ for sample N on MgO. As shown in Fig. 2e, the tail of the resistive transition is broadened by applying $B$, indicating the two-dimensional nature of the FeSe film. Figure 2f shows the temperature dependence of the perpendicular upper critical field $B_{c2}(T)$ derived from the midpoint temperature of the resistance drop $T_c(B)$ in the $R_s$–$T$ curves in Fig. 2e. From the linear relation, which is consistent with the Ginzburg–Landau (GL) theory given as $B_{c2}(T) = (\phi_0/(2\pi F))((1 - (T/T_c(0))$), with $\phi_0$ and $\xi(0)$ being the flux quantum and the GL coherence length at zero temperature, respectively, we obtained $\xi(0) = 2.0$ nm, which is around half of the bulk value. We further compare the GL coherence length $\xi(0)$ to the expression of $0.74\xi_0$ in the clean limit based on the Bardeen, Cooper, and Schrieffer (BCS) theory with $\xi_0 = hv_F/\sqrt{\Delta(0)} = hv_F/\pi(1.76k_F T_c)$. Here, $h$, $v_F$, $\Delta(0)$ and $k_F$ are the reduced Plank constant, Fermi velocity, BCS superconducting gap and Boltzmann constant, respectively. Using a value of $v_F \sim 9.56 \times 10^5$ m s$^{-1}$ (ref. 5), we determined $0.74\xi_0$ to be $\sim 2.1$ nm. This value is comparable to $\xi(0) = 2.0$ nm obtained from the experiments, indicating the system is almost in the clean limit.

Returning to the thickness dependence of $T_c^{\infty}$ in Fig. 2c,d, we note that the first single-step superconducting transition occurs at thicknesses of 9.6 nm and 5.9 nm for samples A and N, respectively. The appearance of high-$T_c$ superconductivity in the rather thicker conditions prompted us to consider how the electric-field effect contributes. Here, we examined the effect of electrostatic doping on the superconductivity in both ultrathin and thicker conditions using different experimental schemes in the same device set-up as for sample A. First, samples B and C on SrTiO$_3$ and samples M and L on MgO were etched at $T = 245$ K to induce the superconductivity, as shown in Fig. 3a, the top panel of Fig. 3c, and in Fig. 3d (red line data) and the inset of Fig. 3d (red line data). The thicknesses of sample B on SrTiO$_3$ and sample L on MgO are tuned to approximately one/two unit cells in thickness (the red square in Fig. 2c and the purple triangle in Fig. 2d), whereas sample C on SrTiO$_3$ and sample M on MgO are etched to intermediate thicknesses of 9.4 nm (the blue triangle in Fig. 2c) and 3.7 nm (the green circle in Fig. 2d), by monitoring the leakage current under the assumption that the same etching rate holds as for sample A. After detection of the high-$T_c$ superconducting behaviour, we examined the electrostatic effect by removing and then reapplying $V_G$ at $T = 220$ K (along the blue arrow in Fig. 1b; note that no electrochemical etching occurs at this temperature, as mentioned above).
Figure 2 | Thickness tuning of FeSe on SrTiO$_3$ and MgO via electrochemical etching. a, b. Sheet resistance normalized to the value at $T = 100$ K, $R_s/R_s^{100K}$, as a function of temperature $T$ for continuing etching processes—that is, different film thickness (d)—for sample A on SrTiO$_3$ (a) and for sample N on MgO (b). (For the detailed information on the thickness, see Supplementary Information.) The onset superconducting transition temperature $T_{c1}$ is defined by the intersection of the two dashed linear extrapolation lines. c, d. $T_{c1}$ for samples A and N shown in a, b are collected as a function of averaged thickness for the electric-field–induced high-$T_c$ (E-induced; filled black circles) and the initial bulk-like low-$T_c$ (open black circles) conditions. A vertical broken line corresponds to the thickness for the first data showing a single-step transition in the $R_s$ vs $T$ curves. The data for samples B, C, M and L after etching and at $V_G = 5$ V are indicated by red square, blue triangle, green circle and purple triangle, respectively. Vertical error bars of 0.6 nm, corresponding to 1 u.c. of FeSe, are determined by the surface roughness of as-grown film measured by atomic force microscopy (see Supplementary Information). e, f. Perpendicular magnetic field dependence of $R_s$ vs $T$ curves of sample N after etching down to approximately one/two unit cells in thickness measured at $B = 0$ (red), 1 (orange), 3 (yellow), 5 (green), 7 (blue) and 9 (purple) T. Temperature dependence of the upper critical field $B_{c2}$ (T) derived from the midpoint of the resistance drop in f. The red line corresponds to a linear fitting line based on the GL theory.

For sample B, expectedly at a thickness of one/two unit cells, the initial insulating behaviour was not recovered on reducing $V_G$ from 5 to 3 and then to 0 V, as shown in Fig. 3b. This result is good evidence for the thickness having reached close to one/two unit cells, because the high-$T_c$ superconductivity under $V_G = 0$ V is consistent with the result of a previous report on FeSe/SrTiO$_3$ with charge transfer solely from the SrTiO$_3$ substrate. Interestingly, the increase of $T_{c1}$ from 43.5 to 46.3 K as a result of reduced $V_G$ implicates an over-doping effect from electrostatic doping. In contrast, in sample C, under the thicker 9.4 nm condition, the superconductivity vanishes and the $R_s$ vs $T$ curve returns to insulating behaviour when $V_G$ is removed (blue line in middle panel of Fig. 3c). The reappearance of insulating behaviour indicates uniform etching without a local ultrathin region providing a high-$T_c$ superconducting current path. Then, application of $V_G = 5$ V recovers the superconductivity (blue line in bottom panel of Fig. 3c), demonstrating reversible control of the insulator–superconductor transition by electrostatic means. We note that the possibility of intercalation can be excluded for two reasons: no significant variation of the $c$-axis length was observed after etching (see Supplementary Information) and reversible control of the insulator–superconductor transition was possible by gating. This series of results in sample C suggests that the observable thickness condition for high-$T_{c1}$ superconductivity is greatly expanded from one/two unit cells to more than ten unit cells by the application of an electric field. Furthermore, $T_{c1}$ values in samples M and L decrease on reducing the accumulated charge, as shown in Fig. 3d. Therefore, we suppose that the high-$T_c$ superconductivity in ultrathin FeSe on MgO is fully controllable by the application of an electric field and confirm that a thickness of one/two unit cells of FeSe itself is not superconducting on MgO, as distinct from on SrTiO$_3$ substrates.

Finally, we discuss possible mechanisms for the high-$T_c$ superconductivity in FeSe EDL transistors from the view point of the thickness dependence. The comparable values of $T_{c1}$ shown in Fig. 2c, d for SrTiO$_3$ and MgO suggest that the electric-field effect induces superconductivity at 40 K despite one possibly expecting different electron–phonon coupling between the FeSe film and the substrate. Concomitantly, the small thickness dependence of $T_{c1}$ below a thickness of ten unit cells indicates that an ultrathin condition, particularly for a single unit cell itself, probably does not play a crucial role in the increase of $T_c$ for the superconductivity of ultrathin FeSe. Nevertheless, the origin of the difference in the critical thickness for SrTiO$_3$ (9.6 nm) and MgO (5.9 nm) is still not clear at present, although it may be linked to the degree of charge transfer from the substrates to the FeSe. Naively, it is likely that the positive $V_G$ depletes holes in the valence band at the $\Gamma$ point and accumulates electrons in the conduction band at the M point through the IL/FeSe interface. While the interface between the IL and the thick FeSe does not work as the high-$T_c$ superconducting channel (see Fig. 1c). As a consequence, a similar modification of the electronic band structure at the $\Gamma$ and M points, as proved by ARPES for the single-unit-cell and three-unit-cell FeSe on SrTiO$_3$, exhibiting large superconducting gap energy$^{13-24}$, may be realized by applying an electric field below the critical thicknesses on SrTiO$_3$ and MgO. In other words, the electric-field effect in FeSe-EDLT expands the modified layer width yielding the high-$T_c$ superconductivity, which was one/two-unit-cell FeSe in the case of charge doping only from the SrTiO$_3$ substrate. Furthermore, we suppose that the whole sample contributes high-$T_c$ superconductivity when the modified layer width at IL/FeSe interface touches the interface of the FeSe substrates. This is what we observed in $R_s$ vs $T$ curve as the single-step transition. However, further investigation into the modification of electronic band structures in etched FeSe under the application of $V_G$ is required to verify our speculation.

By employing the EDL transistor configuration, high-$T_c$ superconductivity in ultrathin FeSe films has been investigated

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from several perspectives, such as thickness and substrate material dependence. The application of the electric-field effect to induce modification of the electronic band structure would be a major contribution towards $T_{\text{on}}$ values of 40 K below a thickness of ten unit cells regardless of substrate material. Our demonstration of etching that is controlled well by the temperature of the IL under an applied $V_G$ sheds light on new aspects of the EDL transistor as a useful approach in carrying out ultrathin-film studies and expands the opportunities for demonstrating ex situ the superconductivity of ultrathin FeSe grown on various insulating substrates.\(^\text{12,13,15,30}\)

**Methods**

Methods and any associated references are available in the online version of the paper.

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Author contributions
J.S., T.N. and A.T. conceived and designed this research. Y.I. and J.S. performed thin-film growth and electrical measurements. T.M. and T.N. prepared the electrical measurement system for the EDL transistor device configuration in a liquid helium vessel. Y.I. and J.S. analysed the data and all authors jointly discussed the results. J.S., T.N. and A.T. wrote the manuscript and all authors commented on the manuscript.

Additional information
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Competing financial interests
The authors declare no competing financial interests.
Methods
Sample fabrication. The FeSe films were grown at 300 °C on insulating SrTiO$_3$(001) and MgO(001) substrates by pulsed-laser deposition with an as-delivered FeSe target (Kojundo Chemical Laboratory), followed by in situ post annealing at 450 °C for 30 min. The growth rate with a laser repetition rate of 5 Hz is approximately 17 nm h$^{-1}$, the value of which is estimated by X-ray Laue fringes (see Supplementary Information). After cutting the film into a $2 \times 5$ mm$^2$ rectangular pieces, the channel and the four-terminal electrode (indium pads) were prepared to confine the current flow within the channel by scratching the film. Then, the electrodes were covered with a silicone sealant to prevent them from reacting chemically with the ionic liquid. A platinum plate for the side gate electrode was placed next to the FeSe channel on the same chip and indium pads were attached to the gate electrode in the same way (see Supplementary Information). Then, the FeSe channel and the gate electrode were covered by the ionic liquid, N,N-dethyl-N-methyl-N-(2-methoxyethyl)ammonium bis(trifluoromethanesulphonyl)imide (DEME-TFSI) to form an EDL transistor configuration, as shown in Fig. 1a. To suppress degradation of the sample in air, we carried out the EDL fabrication process as soon as possible. However, it usually took 6 h before starting the measurement and the sample surface was inevitably exposed to air for about 2–3 h. The thickness after etching is estimated from the initial thickness and the etching rate by summing the leakage current (see Supplementary Information). Surface roughness contributes a ±0.6 nm error to the thickness.

Electrical measurements. The sheet resistance $R_s$ is measured by the standard four-terminal method, where a constant current of typically 0.5 µA is applied and the longitudinal voltage drop is measured by a nanovoltmeter. The temperature is controlled by dipping the sample into the liquid helium vessel in electrical measurements for samples A–C. The sample temperature is monitored by means of a calibrated resistance thermometer (Cernox1050 resistor) inserted in the copper block where the sample is mounted. The $R_s$–$T$ curve and magnetotransport properties for samples M and N are measured using a Quantum Design physical property measurement system (PPMS). To control the doping level electrostatically through EDL tuning, we fix the temperature at $T = 220$ K while varying $V_G$. In contrast, the occurrence of the electrochemical reaction is controlled by tuning $T$ in the range from 240 to 250 K while keeping a constant $V_G = 5.0$ V.