COMPARATIVE ANALYSIS OF UPQC WITH VARIOUS HYSTERESIS CURRENT CONTROLLERS

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ABSTRACT

This paper deals the comparative analysis of a three-phase three wire Unified Power Quality Conditioner (UPQC) with various hysteresis current controllers. The UPQC is one among the custom power devices which is a integration of series and shunt active power filters (APF) connected by a common link capacitor. The construction of shunt and series APF is by using a three phase three wire Voltage source Inverter (VSI). In series active filter, synchronous reference frame technique is used to extract fundamental voltage; whereas in shunt active filters, instantaneous reactive power theory (IRPT) is used to extract fundamental current. Switching pulses for both filters generated by hysteresis current controller (HCC), multilevel vector based hysteresis current controller (MVHCC) and adaptive hysteresis current controller (AHCC). The performance of the various HCC based control algorithm of shunt active filter with series active filter is evaluated in a three-phase distribution system connected with non-linear and unbalanced load conditions. Fuzzy logic control is used for dc capacitance balancing. The control technique is executed by the use of MATLAB / Simulink-based simulations environment.

Key words: Unified power quality conditioner, Synchronous reference frame technique, Instantaneous reactive power theory, multilevel vector based hysteresis current controller, Adaptive Hysteresis current controller, current and voltage harmonics.

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1. INTRODUCTION

The wide use of power electronics components in industries leads to power line disturbances in power system which propagates power quality problem like voltage sag swell and harmonics. This harmonics degrades the quality of power at consumer end. In past few decades, many papers had been addressed these problems and proposed the compensating techniques for eliminating this problem [1],[2]. The harmonics makes many undesirable effects such as increased heating losses in electrical machines and transformers, poor power factor, malfunction of sensitive equipments, torque pulsation of motors[3]. Power quality problems are eliminated by custom power devices(CPD). The most promising CPD is UPQC, which is a combination of two VSI connected in back to back manner, shares the common Dc bus [4]. The function of series VSI is to compensate voltage quality problems and the shunt VSI mitigate the current related problems. Both series and shunt active filters works simultaneously so as to voltage delivered to the load regulated with low harmonic distortion [5]. Controllers are the heart of UPQC, many researches proposed different control technique for generating reference voltage and current, gate pulse generation using PWM controllers and capacitance balancing[6-9]. In this paper reference voltage generation needed for series active filter is synchronous reference frame(SRF) technique, similarly the reference current is generated by instantaneous reactive power theory(IRPT). For gate pulse generation many techniques are proposed like space vector modulation[10], dead beat control[11], hysteresis control[12], sliding mode control, and triangular current control method. Among all above methods hysteresis controller is compact and easily implemented without complex algorithm. So in this paper conventional hysteresis band control (CHCC), adaptive hysteresis controller (AHCC), vector based multilevel hysteresis current control(VBMHCC) [13],[14] are used. Finally a comparison is made between various HCC based UPQC. Maintaining constant DC voltage across capacitor is necessary in UPQC design. The dc link capacitor voltage will be changed during reactive power compensation [15]. In this paper fuzzy logic controller is used for capacitance voltage balancing. The control methods are evaluated and tested under non-linear and unbalanced load conditions using Matlab/Simulink software.

2. MATERIAL AND METHODS

The UPQC consists of two voltage source inverters Connected back to back with each other sharing a common dc link [14]. It is connected before the load to make the source and load voltages free from any voltage and current distortions and reactive current drawn from source is compensated in such a way that it is pure sinusoidal wave and in phase with supply voltage.
The aim of the series APF is harmonic isolation between consumer load and Supply; it has the capability of compensating flicker/voltage imbalance as well as voltage regulation [14]. The function of shunt APF is used to absorb current harmonics, compensate reactive power, negative-sequence of source current, and regulate the dc link voltage between both APFs and power factor correction [15]. The General UPQC block diagram is shown in Figure 1.

2.1. Reference Voltage Signal Generation of Series APF

In series APF the inverter injects a voltage in series with the supply which feeds through a series transformer. The injected voltage usually the harmonics with small amount of sinusoidal component which results to compensate reactive power losses which maintains D.C voltage as constant [16]. The control algorithm calculates the required amount of reference value to be injected by the series transformers. The harmonic voltage is calculated by comparing the fundamental voltage component with the load side line voltages [9, 10]. In equation (1), the three phase supply voltages $V_{abc}$ are transformed to d-q-0 co-ordinate.

\[
\begin{bmatrix}
V_{sd} \\
V_{sp} \\
V_{sc}
\end{bmatrix} = \begin{bmatrix}
\frac{1}{2} & \frac{1}{2} & \frac{1}{2} \\
sin(\omega t) & sin(\omega t - 2\frac{\pi}{3}) & sin(\omega t + 2\frac{\pi}{3}) \\
cos(\omega t) & cos(\omega t - 2\frac{\pi}{3}) & cos(\omega t + 2\frac{\pi}{3})
\end{bmatrix} \begin{bmatrix}
V_{so} \\
V_{si} \\
V_{sc}
\end{bmatrix}
\]

The d axis voltage ($V_{sd}$) contains both average and oscillating component $\bar{V}_{sd}$, $\tilde{V}_{sd}$ shown in (2). The average voltage $\bar{V}_{sd}$ is found by using second order LPF (low pass filter).

\[
V_{sd} = \overline{V_{sd}} + \tilde{V}_{sd}
\]

The load side reference voltages $V_{abc}$ are calculated as given in equation (3). The switching signals are derived by comparing reference voltages $V_{abc}$ and the load voltages $V_{abc}$ via PWM controller.

\[
\begin{bmatrix}
V_{sa} \\
V_{sb} \\
V_{sc}
\end{bmatrix} = \begin{bmatrix}
sin(\omega t) \\
sin(\omega t - 2\frac{\pi}{3}) \\
sin(\omega t + 2\frac{\pi}{3})
\end{bmatrix} 1
\]

The obtained three-phase load reference voltages are compared with load line voltages and errors are then processed by three different HCCs (vector base HCC, Conventional HCC, adaptive HCC), and gate pulse are generated. It is shown in Figure 2.
2.2. Reference Current Signal Generation of Shunt APF

The function of shunt APF is to compensate the current harmonics, reactive power generated by the nonlinear load. Here thyristor bridge rectifier is act as a non linear load. The instantaneous reactive power (p-q) theory is used to control of shunt APF in real time [16]. In this theory, the instantaneous three-phase currents and voltages are transformed to α-β-0 co-ordinates as shown in equation (4) and (5).

\[
\begin{bmatrix}
V_0 \\
V_a \\
V_b
\end{bmatrix} = \begin{bmatrix}
\frac{1}{\sqrt{2}} & 1 & 0 \\
\frac{1}{\sqrt{2}} & -\frac{1}{2} & -\frac{1}{2} \\
\frac{1}{\sqrt{2}} & -\frac{1}{2} & \frac{1}{2}
\end{bmatrix} \begin{bmatrix}
V_{sa} \\
V_{sb} \\
V_{sc}
\end{bmatrix}
\]  

(4)

\[
\begin{bmatrix}
i_0 \\
i_{\alpha} \\
i_{\beta}
\end{bmatrix} = \begin{bmatrix}
\frac{1}{\sqrt{2}} & 1 & 0 \\
\frac{1}{\sqrt{2}} & -\frac{1}{2} & -\frac{1}{2} \\
\frac{1}{\sqrt{2}} & -\frac{1}{2} & \frac{1}{2}
\end{bmatrix} \begin{bmatrix}
i_{sa} \\
i_{sb} \\
i_{sc}
\end{bmatrix}
\]  

(5)

The reference currents are calculated in order to compensate neutral, harmonic and reactive currents in the load. These reference source current signals are then compared with sensed three-phase source currents and the errors are processed by three different HCCs(vector base HCC, Conventional HCC, adaptive HCC), and gate pulse are generated. It is shown in Figure 3.

**References**

1. Electronic copy available at: https://ssrn.com/abstract=3658219
3. CONTROL STRATEGY

In this paper three different current control strategies are discussed such as vector based multilevel hysteresis current controller (VBMHCC), Conventional HCC and Adaptive Hysteresis current controller(AHCC).

3.1. Vector Based Multilevel HCC

When the actual inverter current or voltage crosses the tolerance region on one side, a new vector with opposite component on that side must be applied. This method gives a switching pattern constituted of six consecutive four-state optimal voltage vectors sets, which matches with six 60 degree sectors of a fundamental period [17]. At each instant digital outputs of the hysteresis comparators determines the discrete state of the inverter output voltage according to the table 1. The overall switching pattern is shown in Figure 4. The vector based hysteresis current controller is shown in Figure 5. The current error is always kept inside the square tolerance region [18]. The vector based hysteresis controller is used for both filters for producing switches pulses.

| \( D_a \) | 0 | 1 | 2 |
|-----------|---|---|---|
| \( D_b \) |   |   |   |
| 0         | V5| V4| V3|
| 1         | V5| V0| V3|
| 2         | V6| V0| V2|
| 3         | V6| V1| V2|

Table 1 inverter output voltage

Figure 3 Reference current generation block Diagram

Figure 4 Over all switching pattern
3.2. Conventional HCC

This method is done by comparing the measured current from load side with reference current, error is generated. According to the error switching pulses are produced. In this method the input reference should always with the specified band width. The switching logic is expressed as follows

If \( e_m > HB \) \( T_1 \) is On, \( T_2 \) is OFF.

\( e_m < -HB \) \( T_1 \) is OFF, \( T_2 \) is On. Where m indicates a, b, c phases. \( e_m = i_{\text{ref}} - i_{\text{measured}} \), \( HB \) represents the hysteresis band.

The performance of the HCC depends on the coupling inductance, dc voltage capacitance, hysteresis band. The conventional HCC diagram is shown in Figure 6.

\[ \frac{dt_{sa}}{dt} t_1 = \frac{1}{L} (v_{dc} - v_s), \frac{dt_{sa}}{dt} t_2 = -\frac{1}{L} (v_{dc} - v_s) \] (10)

the following equations can be written in the hysteresis-band curvature with respective switching intervals

\[ \frac{dt_{sa}}{dt} t_1 - \frac{dt_{sa}}{dt} t_2 = 2HB, \frac{dt_{sa}}{dt} t_2 - \frac{dt_{sa}}{dt} t_1 = -2HB, t_1 + t_2 = 1/f_c \] (11)

3.3. Adaptive HCC

When the current error \( e_m \) exceeds the upper limit of the adaptive-hysteresis band, the lower switch is turned ON. If the error current or voltage crosses the lower limit of the adaptive-hysteresis band, the upper switch is turned ON. Hence, the actual current is forced to track the reference current within the hysteresis-band [18]. The following equations can be written for the switching interval \( t_1 \) and \( t_2 \).

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From the above equation HB is calculated as,

\[ HB = \frac{0.25v_{dc}}{f_c L} \left[ 1 - \frac{l^2}{v_{dc}} \left( \frac{v_s}{L} + m \right) \right]^2 \quad (12) \]

Where \( L \) is the coupling inductance, \( v_s \) is the supply voltage, \( f_c \) is the modulation frequency, \( v_{dc} \) is the voltage across the capacitance, \( m = \frac{d i_a}{dt} \) is the slope of reference current signals. The hysteresis band \( HB \) can be modulated at different points of fundamental frequency to control the switching pulses of the inverter. The calculated hysteresis bandwidth \( HB \) is applied to the switching operation of HCC to produce gate control-switching pulses to drive the inverter. The adaptive HCC diagram is shown in Figure 7.

![Figure 7 Adaptive HCC.](image)

4. CAPACITANCE VOLTAGE BALANCING

The voltage across the capacitance is sensed and compared with reference value for capacitance balancing in order to minimize the energy loss. Then the error is passed to fuzzy controller for getting constant value. Fuzzy logic is characterized by seven fuzzy sets as input and output variable and 49 rules are formed, triangular membership function is used for the simplicity, mamdani min type is used as a implication, de-fuzzification using by the height method shown in Figure 8. The table 2 shows the fuzzy base rule.

![Figure 8 Fuzzy Logic Controller](image)

| change in error(Δe) | error(e) | NL  | NM  | NS  | Z   | PS  | PM  | PL  |
|---------------------|----------|-----|-----|-----|-----|-----|-----|-----|
| NL                  | NL       | NL  | NL  | NL  | NL  | NM  | NS  | Z   |
| NM                  | NL       | NL  | NL  | NL  | NM  | NS  | Z   | PS  |
| NS                  | NL       | NL  | NM  | NS  | Z   | PS  | PM  | PL  |
| Z                   | NL       | NM  | NS  | Z   | PS  | PM  | PL  | PL  |
| PS                  | NM       | NS  | Z   | PS  | PM  | PL  | PL  | PL  |
| PM                  | NS       | Z   | PS  | PM  | PL  | PL  | PL  | PL  |
| PL                  | Z        | PS  | PM  | PL  | PL  | PL  | PL  | PL  |
5. SIMULATION RESULTS

In this study, different HCC based control algorithms for the UPQC are examined simulation software. This system compensates the current harmonics produced by a thyristor bridge rectifier and eliminates the voltage harmonics under unbalance load condition. This 3P4W UPQC system consists of two voltage controlled inverters (shunt and series APFs) sharing the same dc bus in split-capacitor topology. A ripple RC filter is connected between output of series APF and the line. A 2100-μF of dc capacitors having 700-V dc is connected between two APFs in split capacitor topology. A three-phase and a single-phase thyristor bridge rectifier are used as nonlinear loads. The control algorithm for the UPQC is evaluated by Matlab / Simulink software. The system is investigated under unbalanced load conditions. The system parameters used are Line to line source voltage $V_{\text{rms}}$ is 380V; System frequency (f) is 50 Hz; Source inductance is $L_S$ is 0.7 m H; Series side Filter impedance of $R_{c1}$, $L_{c1}$, $C_{c1}$ is 90 $\Omega$, 0.8µH, 50µF; Shunt side Filter impedance of $R_{c2}$, $L_{c2}$, $C_{c2}$ is 10$\Omega$, 2.5mH, 8µF; three phase thyristor rectifier $R_L$, $L_L$ load: 20 $\Omega$, 10 m H respectively; Unbalanced three phase load resistances are $R_1 = 10 \Omega$, $R_2 = 50 \Omega$, $R_3 = 80 \Omega$ and Load inductance $L_L$ is 80 m H; DC side capacitance is 2100 $\mu$ F; Reference voltage ($V_{\text{DC, ref}}$) is 700V, switching frequency of series and shunt inverter is approximately 10 KHz.

5.1. UPQC OFF Condition

The Figure 9(a) indicates source voltage before compensation. The thyristor rectifier load current or source current before compensation is shown in Figure 9 (b). The Neutral current is shown in Figure 9 (c). The THD of the Source Voltage and Current before compensation is 27.6% and 28.5%.

![Figure 9 (a) Source voltage (UPQC OFF)](source)

![Figure 9(b) Source current (UPQC OFF)](source)

![Figure 9(c) Neutral current (UPQC OFF)](source)
5.2. UPQC ON Condition
The source voltage after compensation of different HCC is presented (VBMHCC, Conventional HCC, AHCC) in Figure 10 (a), this figure indicates the source voltage is sinusoidal. The source current after compensation of different HCC is presented (VBMHCC, Conventional HCC, AHCC) is presented in Figure 10 (b) this indicates the input supply current is sinusoidal. The APF is suppressing reactive/harmonic power and simultaneously improves power factor. The neutral current of UPQC(all HCC method) is presented in Figure 10 (c). The Fast Fourier Transform (FFT) is used to measure the order of harmonics in source voltage, source current. All this results shown in Figure 10 (d) and 10(e).

Figure 10 (a) Source voltage (UPQC ON)(VBM HCC, Conventional HCC, Adaptive HCC)

Figure 10 (b) Source Current (UPQC ON)(VBM HCC, Conventional HCC, Adaptive HCC)
6. CONCLUSION

This paper explains the UPQC compensation, that is reactive power along with voltage and current harmonics under unbalanced load conditions. In series and shunt Active filters, the reference voltages and currents are generated by SRF and IRPT method. In both filters conventional HCC, AHCC, VBMHCC based current controllers are used for gate pulse generation. The proposed UPQC is validated using extensive Matlab simulation. The important performance parameters are presented graphically and THD of source current is...
found to be 3.80 %, 4.16%, 3.96% under unbalanced load condition of UPQC with Vector based HCC current controller, conventional HCC based current controller, adaptive HCC and From simulation results, it is evident that the value of THD is low in Vector based HCC method when compared with conventional HCC, adaptive based UPQC. In both methods, the measured total harmonic distortion of the source current and voltage are low in compliance with IEEE 519 standards.

REFERENCES

[1] A.S. N. Anjali Devi, G. Madhavi, (2016) "Unified Power Quality Conditioner In Distribution System For Enhancing Power Quality", International Journal of Electrical Engineering & Technology (IJEET) Volume 7, Issue 6, Nov–Dec, pp.55–63

[2] Emanuel Alexander E Orr John A Cyganski David Gulachenski Edward M, (1993) "A survey of harmonic voltages and currents at the customer’s bus”. IEEE Transactions on Power Delivery;8(1):pp.411–21.

[3] A.Jeraldine Viji, T.Aruldoss Albert Victiore, (2014) "Enhanced PLL based SRF control method for UPQC with fault protection under unbalanced load condition”. International Journal of Electrical power and Energy System, issue 6, Vol:58 pp.No:319-328.

[4] R.A Walling, R.Saint, Dugan, R.C.J Burke and L.A.Kojovic, (2008) "Summary of Distributed Resources Impact on Power Delivery Systems.” IEEE Trans. Power Delivery 23(3):pp.1636-1644.

[5] W.M.Grady M.J.Samotyj A.H.Noyola. (1990) "Survey of Active Power Line Conditioning Methodologies." IEEE Trans on Power Delivery, 5(3):pp.1536-1542

[6] D. Graovac A. Katic A. Rufer, (2007) "Power Quality Problems Compensation with Universal Power Quality Conditioning System." IEEE Transaction on Power Delivery, 22(2):pp.968-976.

[7] H. Carneiro L. F. C. Monteiro J. L. Afonso, (2009) "Comparisons between Synchronizing Circuits to Control Algorithms for Single-Phase Active Converters," IEEE Annual Conference of Industrial Electronics, pp.3229-3234.

[8] P.H. Zope, A. Somkuwar, (2012) "Design and Simulation of single-phase Z-source inverter for utility interface, International Journal of Electrical Engineering and Technology. Vol.1,pp.127-143.

[9] Y. Tang, S. Xie, C. Zhang, (2011) "An Improved Z-Source Inverter, IEEE Transactions on Power Electronics", 26,pp.3865-3868.

[10] V. Saravanan, R. Ramanujam, M. Arumugam, (2015) "Novel improved three level Z-source inverter," Australian Journal of Electrical and Electronics Engineering.Vol.12,pp.133-140

[11] Zeng J Yu C Qi Q, (2004) "A novel hysteresis current control for active power filter with constant frequency, "Electric power system research; 68:75-82.

[12] M.A Aredes, (1995) "Combined series and shunt active power filter. "IEEE proc- Power Tech Conf :18–22.

[13] Y. Chen, X ZhaWang, (2000) "Unified power quality conditioner (UPQC): The theory, modeling and application. Power System Technology," Power Con Int. Conf proc-; 3:1329–1333.

[14] F.Z Peng, J.W McKeever, D.J Adams, (1998) "A power line conditioner using cascade multilevel inverters for distribution systems." IEEE Trans.Ind. Appl; 34(6): 1293–1298.
Comparative Analysis of UPQC with Various Hysteresis Current Controllers

[15] G.M Lee D.C Lee J.K Seok, (2004) "Control of series active power filter compensating for source voltage unbalance and current harmonics". IEEE Transaction on Industrial Electronics. 51(1):pp.132-139.

[16] Bose Bimal K, (1990) "An adaptive band current control technique of a voltage fed PWM inverter for machine drive system." IEEE Trans.Ind.Elec. 31(5):pp.402-408.

[17] Bellhaouchet N Rahmani, (2009)" Development of adaptive hysteresis band current control of PWM three phase AC chopper with constant switching frequency. "International journal of Elec. Power.Com.and Sys. 33(10):pp.583-598.

[18] Mansour Mohseni Syed M Islam, (2010) "A new vector based hysteresis current control scheme for three phase PWM voltage source Inverters." IEEE Trans.Power.Elec. 25(9):pp.2299-2309.