Multi-Access Distributed Computing

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Abstract—Coded distributed computing (CDC) is a new technique proposed with the purpose of decreasing the intense data exchange required for parallelizing distributed computing systems. Under the famous MapReduce paradigm, this coded approach has been shown to decrease this communication overhead by a factor that is linearly proportional to the overall computation load during the mapping phase. In this paper, we propose multi-access distributed computing (MADC) as a generalization of the original CDC model, where now mappers (nodes in charge of the map functions) and reducers (nodes in charge of the reduce functions) are distinct computing nodes that are connected through a multi-access network topology. Focusing on the MADC setting with combinatorial topology, which implies $\Lambda$ mappers and $K$ reducers such that there is a unique reducer connected to any $\alpha$ mappers, we propose a coded scheme and an information-theoretic converse, which jointly identify the optimal inter-reducer communication load, as a function of the computation load, to within a constant gap of 1.5. Additionally, a modified coded scheme and converse identify the optimal max-link communication load across all existing links to within a gap of 4.

Index Terms—Coded distributed computing, coded multicasting, communication load, information-theoretic converse, MapReduce, communication complexity, multi-access distributed computing (MADC).

I. INTRODUCTION

WITH the development of large-scale machine learning algorithms and applications relying heavily on large volumes of data, we are now experiencing an ever-growing need to distribute large computations across multiple computing nodes. Different computing frameworks, such as MapReduce [1] and Spark [2], have been proposed to address these needs, based on the aforementioned simple yet powerful concept of distributing large-scale algorithms — to be executed over a set of input data files — across multiple computing machines. Under the well-known MapReduce framework, the overall process is typically split in three distinct phases, starting with the map phase, the shuffle phase and then the reduce phase. During the map phase, each computing node is assigned a subset of the input data files, and proceeds to apply to each locally available file certain designated map functions. The outputs of such map functions, referred to as intermediate values (IVs), are then exchanged among the computing nodes during the shuffle phase, so that each computing node can retrieve any missing, required IVs it did not compute locally. Finally, during the reduce phase, each computing node computes one (or more) output functions depending on its assigned reduce functions, each of which takes as input the IVs computed for each input file.

A. Coded Distributed Computing

Several studies have shown that the aforementioned distributed map-shuffle-reduce approach comes with bottlenecks that may severely hinder the parallelization of computationally-intensive operations. While some works [3], [4] focused on the impact of straggler nodes, other works have pointed out that the total execution time of a distributed computing application is often dominated by the shuffling process. For instance, the work in [5], having explored the behavior of several algorithms on the Amazon EC2 cluster, revealed that the communication load in the shuffle phase was in fact the dominant bottleneck in computing the above tasks in a distributed manner. Similarly, the authors in [6] observed that, for the execution of a conventional TeraSort application, more than 95% of the overall execution time was spent for inter-node communication.

Motivated by this communication bottleneck in the shuffle phase, the authors in [6] introduced coded distributed computing (CDC) as a novel framework that can yield lower communication loads during data shuffling. This gain could be attributed to a careful and joint design of the map and the shuffle phases. Approaching the distributed computing problem from an information-theoretic perspective, the authors brought to light the interesting relationship between the computation load during the mapping phase, and the communication load of the shuffling step. In particular, the work in [6] revealed that if the computation load of the mapping phase is carefully increased by a factor $r$ — which means that each input file is mapped on average by $r$ carefully chosen computing nodes — then the communication load can be reduced by the same factor $r$ by employing coding techniques during the shuffle phase. Building on the coding-based results in cache networks [7], [8], the work in [6] characterized the exact information-theoretic tradeoff between this computation and communication loads under any map-shuffle-reduce scheme with uniform mapping capabilities and uniform assignment of reduce functions.
Since its original information-theoretic inception in [6], coded distributed computing has been explored with several variations. Such variations include heterogeneity aspects where, for example, each computing node may be assigned different numbers of files to be mapped and functions to be reduced. For such settings, novel schemes, based on hypercube and hypercuboid geometric structures, were developed in [9], [10], which managed not only to compensate for the heterogeneous nature of the considered scenarios, but to also exploit these asymmetries in order to require a smaller number of input files, compared to the initial scheme in [6]. Regarding this problem of requiring a large number of input files, it is worth also mentioning the work in [11], where the authors proposed a system model for distributed computing, where the required number of input files was lowered dramatically under an assumption of a multi-rank wireless network.

Some additional works explored the scenario where the computing servers communicate with each other through switch networks [12] or in the presence of a randomized connectivity [13], whereas some other works further investigated distributed computing over wireless channels [14], as well as explored the interesting scenario where each computing node might have limited storage and computational resources [15], [16], [17]. A comprehensive survey on CDC is nicely presented in [18].

B. Contributions

In this work, we propose the multi-access distributed computing (MADC) model, which can be considered as an extension of the original setting introduced in [6], and which entails mappers (map nodes) being connected to various reducers (reduce nodes), and where these mappers and reducers are now distinct entities. As is common, mappers are in charge of mapping subsets of the input files, whereas the reducers are in charge of collecting the IVs in order to compute the reduce functions. We will here focus on the so-called combinatorial topology which will define how the mappers are connected to the reducers. The choice of this topology stems from its analytical tractability that makes it interesting from an academic point of view, in addition to the fact that the same topology has also been recently studied in other settings outside of distributed computing [21], [22], [23]. Under such combinatorial topology, we consider \( \Lambda \) map nodes and \( K \geq \Lambda \) reduce nodes, where each map node maps a subset of the input files, where each reduce node is connected to \( \alpha \) map nodes, and where there is exactly one reducer for each subset of \( \alpha \) map nodes. Each reducer can retrieve intermediate values only from the mappers it is connected to, whereas these same reducers can exchange via an error-free shared-link broadcast channel the remaining required intermediate values. A simple schematic of the model is shown in Fig. 1 for the case \( \Lambda = 4 \), \( \alpha = 2 \) and \( K = 6 \).

As discussed before, the communication load in the shuffle phase can represent a significant bottleneck of distributed computing. As a consequence, our objective is to minimize the volume of data exchanged by the reducers over the common-bus link during the shuffle phase, as well as the communication load between the mappers and the reducers. We start our analysis by first neglecting the communication cost between mappers and reducers, and we propose — for the aforementioned MADC model with combinatorial topology — a coded scheme that allows for efficient communication over the broadcast communication channel. For such setting, we also provide an information-theoretic lower bound on the communication load, and we show this to be within a constant multiplicative gap of 1.5 from the achievable communication load guaranteed by the proposed coded scheme. We then proceed to also account for the download cost from mappers to reducers. For such setting, our goal is to minimize the maximal (normalized) number of bits across all links in the system. To this purpose, we introduce an additional mappers-to-reducers communication scheme and a novel converse bound which, together with the previous inter-reducer scheme, allow us to characterize the optimal max-link communication load within a constant multiplicative gap of 4. Finally, we show how the results developed to characterize both the communication load and the max-link communication load can be employed to characterize a weighted linear load metric \( L_{\text{max}} = w_1L + w_2J \) within a constant multiplicative factor of 2.

C. Paper Outline

The rest of the paper is organized as follows. First, the system model is presented in Section II. Next, Section III provides the main contributions of the paper. The general proofs of the achievable schemes and the converse bounds are presented from Section IV to Section VII. Finally, Section VIII concludes the paper. Some additional proofs are provided in the appendices.

D. Notation

We denote by \( \mathbb{N} \) the set of non-negative integers and by \( \mathbb{N}^+ \) the set of positive integers. For \( n \in \mathbb{N}^+ \), we define \( [n] := \{1, 2, \ldots, n\} \). If \( a, b \in \mathbb{N}^+ \) such that \( a < b \), then \( [a : b] := \{a, a+1, \ldots, b-1, b\} \). For sets we use calligraphic symbols, whereas for vectors we use bold symbols. Given a finite set \( \mathcal{A} \), we denote by \( |\mathcal{A}| \) its cardinality. For \( n, m \in \mathbb{N}^+ \), we define \( [n]_m := \{\mathcal{A} : \mathcal{A} \subseteq [n], |\mathcal{A}| = m\} \). We denote by \( \binom{p}{n} \) the binomial coefficient and we let \( \binom{n}{0} = 0 \) whenever \( n < 0, k < 0 \) or \( n < k \). For \( n, m \in \mathbb{N}^+ \), we denote by \( \mathbb{F}_{2^m}^n \) the n-dimensional vector space over the finite field with
cardinality $2^n$. For $n \in \mathbb{N}^+$, we denote by $S_n$ the group of all permutations of $[n]$.

II. System Model

The general distributed computing problem [6] consists of computing $Q$ output functions from $N$ input files with $Q, N \in \mathbb{N}^+$. Each file $w_n \in \mathbb{F}_2^F$ with $n \in [N]$ consists of $F$ bits for some $F \in \mathbb{N}^+$, and the $q$-th function is defined as

$$\phi_q : \mathbb{F}_2^N \rightarrow \mathbb{F}_2^B$$

for each $q \in [Q]$, i.e., each function maps all the $N$ input files into a stream $u_q = \phi_q(w_1, \ldots, w_N) \in \mathbb{F}_2^B$ of $B$ bits. The main assumption is that each function $\phi_q$ is decomposable and so can be written as

$$\phi_q(w_1, \ldots, w_N) = h_q(g_q,1(w_1), \ldots, g_q,N(w_N)), \forall q \in [Q]$$ (2)

where there is a map function $g_{q,n} : \mathbb{F}_2^N \rightarrow \mathbb{F}_2^B$ for each $n \in [N]$, which maps the input file $w_n$ into an intermediate value (IV) $v_{q,n} = g_{q,n}(w_n) \in \mathbb{F}_2^B$ of $T$ bits, and a reduce function $h_q : \mathbb{F}_2^B \rightarrow \mathbb{F}_2^B$, which maps all the IVs (one per input file) into the output value $u_q = h_q(v_{q,1}, \ldots, v_{q,N}) \in \mathbb{F}_2^B$ of $B$ bits.

In this paper, we assume to have machines that are devoted to computing map functions, and machines that are devoted to computing reduce functions. Thus, a node assigned map functions is not assigned reduce functions, and vice versa. In our setting, we consider $\Lambda$ mappers and $K = (\lambda)\Lambda$ reducers, where — in accordance with the combinatorial topology of choice — there is a unique reducer connected to each subset of $\alpha$ mappers. Denoting by $U \in [\Lambda]_{\alpha}$ the reducer connected to the $\alpha$ mappers in the set $U$, before the computation begins, each reducer $U \in [\Lambda]_{\alpha}$ is assigned a subset $W_U \subseteq [Q]$ of the output functions, where here $W_U$ contains the indices of the functions assigned to reducer $U$. For simplicity, we assume in our setting a symmetric and uniform task assignment, which implies $|W_U| = Q/K = \eta_2$ for some $\eta_2 \in \mathbb{N}^+$ and for each $U \in [\Lambda]_{\alpha}$, and $W_{U_1} \cap W_{U_2} = \emptyset$ for all $U_1, U_2 \in [\Lambda]_{\alpha}$ such that $U_1 \neq U_2$. Afterwards, the computation is performed across the set of mappers and reducers in a distributed manner following the map-shuffle-reduce paradigm.

During the map phase, a set of files $M_\Lambda \subseteq \{w_1, \ldots, w_N\}$ is assigned to the mapper $\lambda$ for each $\lambda \in [\Lambda]$. Each mapper $\lambda \in [\Lambda]$ computes the intermediate values $V_\lambda = \{v_{q,n} : q \in [Q], w_n \in M_\lambda\}$ for each $q \in [Q]$ of $T$ bits. Since reducer $U \in [\Lambda]_{\alpha}$ is connected to the mappers in $U$, it can access the intermediate values in the set $V_U = \{v_{q,n} : q \in [Q], w_n \in M_U\}$, where $M_U = \bigcup_{\lambda \in U} M_\lambda$ is simply the union set of files assigned to and mapped by the map nodes in $U$. When the communication cost between mappers and reducers is not neglected, we can define the download cost as follows.

Definition 1 (Download Cost). The download cost, denoted by $J$, is defined as the maximal normalized number of bits transmitted across the links from the mappers to the reducers, and is given by

$$J := \max_{\lambda \in [\Lambda]} \max_{U \in [\Lambda]_{\alpha}} \frac{R^U_{\lambda}}{QNT}$$ (3)

where $R^U_{\lambda}$ denotes the number of bits that are transmitted by mapper $\lambda \in [\Lambda]$ to reducer $U \in [\Lambda]_{\alpha}$ where $\lambda \in U$. Since $R^U_{\lambda}$ is the number of bits that are transmitted by mapper $\lambda \in [\Lambda]$ to reducer $U \in [\Lambda]_{\alpha}$ where $\lambda \in U$.

Remark 1. With emphasis on distributed computing scenarios where delay constraints are present, the nature of the download cost metric above allows us to bound the overall communication delay between mappers and reducers. While indeed the download cost as defined above allows for a certain parallelizing effect to remain unaccounted for, it remains a metric that properly captures a tradeoff between the computational effort at the mappers and a worst-case delivery time of data.

Assuming that each mapper computes all possible IVs from locally available files, we define the computation load as follows.

Definition 2 (Computation Load). The computation load, denoted by $r$, is defined as the total number of bits mapped across the $\Lambda$ map nodes and normalized by the total number of files $N$, and it takes the form

$$r := \frac{\sum_{\lambda \in [\Lambda]} |M_\lambda|}{N}. \quad (4)$$

During the shuffle phase, each reducer $U \in [\Lambda]_{\alpha}$ retrieves the IVs from the mappers in $U$ and creates a signal $X_U \in \mathbb{F}_{2^{|U|}}$ for some $|U| \in \mathbb{N}^+$ and for some encoding function $\psi_U : F_{2^{|U|}} \rightarrow \mathbb{F}_{2^{|U|}}$, where $X_U$ takes the form

$$X_U = \psi_U(V_U). \quad (5)$$

Then, the signal $X_U$ is multicast to all other reducers via the broadcast link which connects the reducers. Since such link is assumed to be error-free, each reducer receives all the multicast transmissions without errors. The amount of information exchanged during this phase is referred to as the communication load, which is formally defined in the following.

Definition 3 (Communication Load). The communication load, denoted by $L$, is defined as the total number of bits transmitted by the K reducers over the broadcast channel
during the shuffle phase, and — after normalization by the number of bits of all intermediate values — this load is given by

\[ L := \frac{\sum_{t \in [\Lambda]} \ell_t}{QNT} \tag{6} \]

Recalling that reducer \( U \in [\Lambda]_\alpha \) is assigned a subset of output functions whose indices are in \( \mathcal{W}_U \), each reducer \( U \in [\Lambda]_\alpha \) wishes to recover the IVs \( \{v_{q,n} : q \in \mathcal{W}_U, n \in [N]\} \) to correctly compute \( u_q \) for each \( q \in \mathcal{W}_U \). Thus, during the reduce phase, each reducer \( U \in [\Lambda]_\alpha \) reconstructs all the needed intermediate values for each \( q \in \mathcal{W}_U \) using the messages communicated in the shuffle phase and the intermediate values \( \mathcal{V}_U \) retrieved from the mappers in \( U \), i.e., each reducer \( U \in [\Lambda]_\alpha \) computes

\[
(v_{q,1}, \ldots, v_{q,N}) = \chi^q_U(X_S : S \in [\Lambda]_\alpha, \mathcal{V}_U) \tag{7}
\]

for each \( q \in \mathcal{W}_U \) and for some decoding function defined as \( \chi^q_U : \prod_{S \in [\Lambda]_\alpha} \mathbb{F}_{2^S} \times \prod_{q \in \mathcal{W}_U} \mathbb{F}_{2^q}^{[\Lambda]} \rightarrow \mathbb{F}_{2^N} \). In the end, each reducer \( U \in [\Lambda]_\alpha \) computes the output function \( u_q = h_q(v_{q,1}, \ldots, v_{q,N}) \) for each assigned \( q \in \mathcal{W}_U \).

When the download cost is neglected, our goal is to characterize the optimal tradeoff between computation and communication \( L^*(r) \). This optimal tradeoff is simply defined as

\[
L^*(r) := \inf \{ L : (r, L) \text{ is achievable} \} \tag{8}
\]

where the tuple \((r, L)\) is said to be achievable if there exists a map-shuffle-reduce procedure such that a communication load \( L \) can be guaranteed for a given computation load \( r \). On the other hand, when we indeed jointly consider both the inter-reducer communication cost and the mapper-to-reducer download cost, then our aim will be to characterize the optimal max-link communication load \( L^*_{\text{max-link}}(r) \), which is defined as

\[
L^*_{\text{max-link}}(r) := \inf \{ L_{\text{max-link}} : (r, L_{\text{max-link}}) \text{ is achievable} \} \tag{9}
\]

where \( L_{\text{max-link}} := \max(L,J) \) represents the maximum between the communication load and the download cost for a given computation load \( r \). In simple terms, \( L_{\text{max-link}} \) represents the maximal normalized number of bits flowing across any link in the considered system model, where this metric appears in various settings such as in the recent work in [12]. Notice that we will assume, throughout the paper, uniform computational capabilities across the mappers and uniform assignment of reduce functions across the reducers, as is commonly assumed (see for example the original work in [6]).

Remark 2. When \( \alpha = 1 \), there are \( K = \Lambda \) mapper-reducer pairs. If we consider each pair to be a single computing server and in absence of download-cost considerations, the proposed system model trivially coincides with the original setting in [6]. Hence, since the results in this paper will hold for any \( \alpha \in [\Lambda] \), the proposed model can in fact be considered as a proper extension of the original coded distributed computing model. In addition, the definition of the communication load in (6) is the same as in the original CDC setting in [6], and so this explains why — as it will be clarified in Remark 4 — our coded scheme coincides with the original CDC setting when both \( \alpha = 1 \) and the download cost is neglected.

Remark 3. If in a data center one separates the roles of computing nodes as we suggest, we might have idle times for mappers and reducers. Nevertheless, the fact that mappers are idle in the reduce phase and vice versa should be seen as an opportunity rather than as a problem. Indeed, we remind that Fig. 1 simply represents a theoretical abstraction of the considered MADC problem with combinatorial topology, and so one should recall that mappers and reducers are most likely computing nodes that are part of a bigger computing network, where each computing node simply represents a shared network resource within a data center. As a consequence, the mappers can be exploited for other purposes within the data center when they are idle, i.e., during the reduce phase, and vice versa for what concerns the reduce nodes. This consideration should further legitimize the idea of decoupling the map phase and the reduce phase, since this makes the system more modular and flexible in terms of computational efficiency.

III. MAIN RESULTS

In this section we will provide our main contributions. As we have already mentioned, we will first consider a setting where the download cost is neglected. Subsequently, we will provide some additional results for the more realistic scenario where the cost of delivering data from the mappers to the reducers is non-zero.

A. Characterizing the Communication Load

The first result that we provide is the achievable computation-communication tradeoff provided by the coded scheme that will be presented in its general form in Section IV. As a reminder, download-cost considerations are not considered here, but are postponed to the next section. The result is formally stated in the following theorem.

Theorem 1 (Achievable Bound). Consider the MADC setting with combinatorial topology. Then, the optimal communication load \( L^*(r) \) is upper bounded by \( L_{UB}(r) \) which is a piecewise linear curve with corner points

\[
(r, L_{UB}(r)) = \left(r, \frac{\Lambda - \alpha}{r} \left(\frac{\Lambda - \alpha}{r} \right)^{-1}\right), \quad \forall r \in [\Lambda - \alpha + 1]. \tag{10}
\]

Proof. The proof of the scheme is reported in Section IV. \(\square\)

Remark 4. As we already mentioned in Remark 2, if we set \( \alpha = 1 \) and we consider each mapper-reducer pair as a unique entity, we obtain the same system model in [6]. Interestingly, we can see that, if we specialize the result in Theorem 1 to the case \( \alpha = 1 \), we obtain the same computation-communication tradeoff as in [6, Theorem 1], as our topology can be considered to be a proper extension of the original CDC model.

We proceed to construct an information-theoretic converse on the communication load of the MADC setting. As it will be
pointed out in the general proof in Section V, the construction of the converse takes inspiration from [10, Lemma 2] as well as from ideas in [22]. Essentially, the bound here manages to merge the approach in [10, Lemma 2], where a converse bound is built using key properties of the entropy function, with the index coding techniques in [22], where the nodes of a side information graph are iteratively selected in a proper way to systematically identify large acyclic subgraphs that are used to develop a tight converse. The result is formally stated in the following.

**Theorem 2** (Converse Bound). Consider the MADC setting with combinatorial topology. Then, the optimal communication load \( L^*(r) \) is lower bounded by \( L_{LB}(r) \) which is a piecewise linear curve with corner points

\[
(r, L_{LB}(r)) = \left( \frac{\Lambda}{r + \alpha}, \frac{\Lambda \alpha}{\alpha \Lambda} \right), \quad \forall r \in [\Lambda - \alpha + 1].
\]

**Proof.** The proof is described in Section V.

Finally, from the results in Theorem 1 and Theorem 2, we can provide an order optimality guarantee for the MADC model. Indeed, comparing the achievable performance and the converse bound, we conclude that the two are within a constant multiplicative gap. We see this in the following theorem.\(^3\)

**Theorem 3** (Order Optimality). For the MADC system with combinatorial topology, \( \Lambda \) mappers and \( K = \binom{\Lambda}{\alpha} \) reducers for a fixed value of \( \alpha \in [2 : \Lambda] \), the achievable performance in Theorem 1 is within a factor of at most 1.5 from the optimal.

**Proof.** The proof is described in Appendix A.

In Fig. 2 we can see a comparison between the original CDC framework and the proposed MADC model. More specifically, for the first setting we consider \( \Lambda = 10 \) pairs of mappers and reducers, where each pair \( \lambda \in [10] \) can be considered as a unique computing server having its own subset \( M_\lambda \) of assigned files. For the second setting we consider \( \Lambda = 10 \) mappers and \( K = \binom{10}{2} = 45 \) reducers, where there is a reducer connected to any \( \alpha = 2 \) mappers. The performance of our proposed MADC model is better than the original CDC framework and this can be attributed to the fact that each reducer has access to \( \alpha = 2 \) mappers, which consequently implies (intuitively) much less data to be transferred over the common-bus link.

**Remark 5.** As our combinatorial architecture cannot be directly compared with the original CDC model, the purpose of Fig. 2 remains mainly to offer an indication of the communicational efficiencies of the proposed model. Unfortunately, any direct comparison with other topologies with the same access degree \( \alpha \) remains unattainable due to the lack of such topologies in the literature.

\(^3\)The order optimality result in Theorem 3 excludes the value \( \alpha = 1 \). Indeed, it can be verified that for such case the achievable performance in Theorem 1 and the converse in Theorem 2 are within a factor of at most 2. However, we already know that the coded scheme in [6] is exactly optimal when \( \alpha = 1 \). Hence, such value is neglected when comparing the aforementioned results.
We proceed by proposing an information-theoretic converse on the max-link communication load. The result is presented in the following theorem.

**Theorem 5** (Converse Bound). Consider the MADC setting with combinatorial topology. Then, the optimal max-link communication load \( L^*_{\text{max-link}}(r) \) is lower bounded by \( L^\text{max-link,UB}(r) \) which is given by

\[
L^\text{max-link,UB}(r) = \frac{1}{2} \sum_{j \in [\Lambda]} \left( \frac{(\Lambda)}{\alpha + j} - \frac{(\lambda_j - j)\alpha}{\alpha} \right) \tilde{a}_j^* N
\]

(14)

where the vector \( \tilde{a}_* = (\tilde{a}_1^*, \ldots, \tilde{a}_\Lambda^*) \) is the optimal solution to the linear program in (13).

**Proof.** The proof is described in Section VII. \( \square \)

Finally, we can compare the results in Theorem 4 and Theorem 5 to establish the gap to optimality of the achievable schemes for both the communication load and the download cost described in Section VII.

**Theorem 6** (Order Optimality). For the MADC system with combinatorial topology, \( \Lambda \) mappers and \( K = \binom{\Lambda}{\alpha} \) reducers for a fixed value of \( \alpha \in [\Lambda] \), the achievable performance in Theorem 4 is within a factor of at most 4 from the optimal.

**Proof.** The proof is described in Appendix B. \( \square \)

**C. Characterizing a General Weighted Load**

We devote this last part to show how the results developed to characterize the communication load \( L^* \) and the max-link load \( L^\text{max-link} \) can be employed to directly characterize also a general weighted load \( L_w = w_1 L + w_2 J \). The following theorems provide an achievable bound, a converse bound and an order optimality result, respectively.

**Theorem 7** (Achievable Bound). Consider the MADC setting with combinatorial topology. Then, the optimal weighted load \( L^*_w(r) \) is upper bounded by \( L_{w,UB}(r) \) which is given by

\[
L_{w,UB}(r) = \sum_{j \in [\Lambda]} \left( w_1 \frac{(\Lambda - \alpha)}{\alpha + j} \left( \frac{(\lambda_j - j)\alpha}{\alpha} \right) + w_2 \frac{(\lambda_j - j)\alpha}{\alpha} \right) \tilde{a}_j^* N
\]

(15)

where the vector \( \tilde{a}_* = (\tilde{a}_1^*, \ldots, \tilde{a}_\Lambda^*) \) is the optimal solution to the linear program

\[
\min_{\tilde{a}_M} \sum_{j \in [\Lambda]} \left( w_1 \frac{(\Lambda - \alpha)}{\alpha + j} + w_2 \frac{(\lambda_j - j)\alpha}{\alpha} \right) \tilde{a}_j M N
\]

subject to

\[
\tilde{a}_j M \geq 0, \quad \forall j \in [\Lambda]
\]

(16a)

\[
\sum_{j \in [\Lambda]} \tilde{a}_j M N = 1
\]

(16b)

(16c)

and where \( \tilde{a}_M = (\tilde{a}_M^1, \ldots, \tilde{a}_M^\Lambda) \) is the control variable.

**Proof.** The proof follows from a weighted linear combination of the achievable schemes for both the communication load and the download cost already derived in Section VII. \( \square \)

**Theorem 8** (Converse Bound). Consider the MADC setting with combinatorial topology. Then, the optimal max-link communication load \( L^*_w(r) \) is lower bounded by \( L^\text{max-link,UB}(r) \) which is given by

\[
L^\text{max-link,UB}(r) = \sum_{j \in [\Lambda]} \left( w_1 \frac{(\Lambda)}{\alpha + j} + w_2 \frac{(\lambda_j - j)\alpha}{\alpha} \right) \tilde{a}_j N N
\]

(17)

where the vector \( \tilde{a}_* = (\tilde{a}_1^*, \ldots, \tilde{a}_\Lambda^*) \) is the optimal solution to the linear program in (16).

**Proof.** The proof follows from a weighted linear combination of the converse bounds for both the communication load and the download cost already derived in Section VII. \( \square \)

**Theorem 9** (Order Optimality). For the MADC setting with combinatorial topology, \( \Lambda \) mappers and \( K = \binom{\Lambda}{\alpha} \) reducers for a fixed value of \( \alpha \in [\Lambda] \), the achievable performance in Theorem 7 is within a factor of at most 2 from the optimal.

**Proof.** The proof works along the same lines as the proof in Appendix B. \( \square \)

**IV. PROOF OF ACHIEVABLE BOUND IN THEOREM 1**

We assume that there are \( \Lambda \) mappers and \( K = \binom{\Lambda}{\alpha} \) reducers, and we assume the aforementioned combinatorial topology where each reducer is exactly and uniquely connected to \( \alpha \) mappers. We then consider some arbitrary computation load \( r \in \Lambda - \alpha + 1 \) and we consider \( Q = \eta_2 K \) output functions with \( \eta_2 \in \mathbb{N}^+ \), allowing us to separate the \( Q \) functions into \( K \) disjoint groups \( \mathcal{W}_U \) for each \( U \in [\Lambda] \), so that each reducer is assigned \( \eta_2 \) functions, corresponding to \( \left| \mathcal{W}_U \right| = \eta_2 \) for each \( U \in [\Lambda] \).

**A. Map Phase**

This phase follows the same mapping strategy proposed by the authors in [6]. Hence, the input database is split in \( \binom{\Lambda}{r} \) disjoint batches, each containing \( \eta_1 = N/\binom{\Lambda}{r} \) files, where we assume that \( N \) is large enough such that \( \eta_1 \in \mathbb{N}^+ \). Consequently, we have a batch of files for each \( T_1 \subseteq [\Lambda] \) such that \( |T_1| = r \), which implies

\[
\{w_1, \ldots, w_N\} = \bigcup_{T_1 \subseteq [\Lambda] : \left|T_1\right| = r} \mathcal{B}_{T_1}
\]

(18)

where we denote by \( \mathcal{B}_{T_1} \) the batch of \( \eta_1 \) files associated with the label \( T_1 \). Then, mapper \( \lambda \in [\Lambda] \) is assigned all batches \( \mathcal{B}_{T_1} \) having \( \lambda \in T_1 \), which means that

\[
\mathcal{M}_\lambda = \{\mathcal{B}_{T_1} : T_1 \subseteq [\Lambda], |T_1| = r, \lambda \in T_1\}.
\]

(19)
We can see that the computation load constraint is satisfied, since we have
\[
\sum_{\lambda \in [\Lambda]} |M_\lambda| / N = \frac{\Lambda \eta_1 \left(\frac{\Lambda}{r-1}\right)}{\eta_1(\frac{1}{r})} = r \tag{20}
\]

Then, each mapper computes \( Q \) intermediate values for each assigned input file, so for each \( \lambda \in [\Lambda] \) we have \( V_\lambda = \{ v_{q,n} : q \in [Q], w_n \in M_\lambda \} \). Since then each reducer has access to \( \alpha \) mappers, reducer \( U \in [\Lambda], \alpha \) can retrieve\(^4\) the intermediate values in \( V_U = \{ v_{q,n} : q \in [Q], w_n \in M_U \} \) recalling that \( M_U = \cup_{\lambda \in U} M_\lambda \). Since \( |V_U| = Q\eta_1 \left(\frac{\Lambda}{r}\right) - \left(\frac{\Lambda}{r-\alpha}\right) \) for each \( U \in [\Lambda], \alpha \), we can conclude that each computing node has access to all the intermediate values when \( r \geq \Lambda - \alpha + 1 \). Hence, we focus on the non-trivial regime \( r \in [\Lambda - \alpha + 1] \) for any given \( \Lambda \) and \( \alpha \).

**B. Shuffle Phase**

Consider reducer \( U \in [\Lambda], \alpha \). Let \( S \subseteq ([\Lambda] \setminus U) \) with \( |S| = r \). First, for each \( R \subseteq (S \cup U) \) such that \( |R| = \alpha \) and \( R \neq U \), and for \( T_1 = (S \cup U) \setminus R \), reducer \( U \) concatenates the intermediate values \( \{ v_{q,n} : q \in W_R, w_n \in B_{T_1} \} \) into the symbol \( U_{W_R,T_1} = (v_{q,n} : q \in W_R, w_n \in B_{T_1}) \in \mathbb{F}_{2^{\eta_2|\Lambda|}} \). Subsequently, such symbol is evenly split in \( \left(\frac{\Lambda}{r}\right) - 1 \) segments as
\[
U_{W_R,T_1} = (U_{W_R,T_1,T_2} : T_2 \subseteq (R \cup T_1), |T_2| = \alpha, T_2 \neq R).
\]
(21)

Then, reducer \( U \) constructs the coded message
\[
\bigoplus_{R \subseteq (S \cup U) : |R| = \alpha, R \neq U} U_{W_R,(S \cup U) \setminus R,U} \tag{22}
\]
for each \( S \subseteq ([\Lambda] \setminus U) \) with \( |S| = r \), and finally concatenates all of them into the following message
\[
X_U = \left( \bigoplus_{R \subseteq (S \cup U) : |R| = \alpha, R \neq U} U_{W_R,(S \cup U) \setminus R,U} : S \subseteq ([\Lambda] \setminus U), |S| = r \right)
\]
(23)

which is multicast to all other reducers via the error-free broadcast channel.

**C. Reduce Phase**

Consider reducer \( U \in [\Lambda], \alpha \). Since such reducer is connected to \( \alpha \) mappers, it misses a total of \( \eta_2 \eta_1 \left(\frac{\Lambda}{r-\alpha}\right) \) intermediate values, i.e., it misses \( \eta_2 \) intermediate values for each of the \( \eta_1 \) files in each batch that is not assigned to the mappers in \( U \). More precisely, reducer \( U \) misses the symbol \( U_{W_{\lambda},T_1} \) for each \( T_1 \subseteq ([\Lambda] \setminus U) \) with \( |T_1| = r \). We know that during the shuffle phase such symbol is evenly split in \( \left(\frac{\Lambda}{r}\right) - 1 \) segments as
\[
U_{W_{\lambda},T_1} = (U_{W_{\lambda},T_1,T_2} : T_2 \subseteq (U \cup T_1), |T_2| = \alpha, T_2 \neq U).
\]
(24)

For each \( T_2 \subseteq (U \cup T_1) \) with \( |T_2| = \alpha \) and \( T_2 \neq U \), we can verify that reducer \( U \) can decode \( U_{W_{\lambda},T_1,T_2} \) from \( X_{T_2} \). Indeed, there exists an \( S \subseteq ([\Lambda] \setminus T_2) \) with \( |S| = r \) such that \( S = (U \cup T_1) \setminus T_2 \). For such \( S \), the corresponding coded message in \( X_{T_2} \) is
\[
\bigoplus_{R \subseteq (S \cup T_2) : |R| = \alpha, R \neq T_2} U_{W_R,(S \cup T_2) \setminus R,T_2} = \bigoplus_{R \subseteq (U \cup T_1) : |R| = \alpha, R \neq T_2} U_{W_R,(U \cup T_1) \setminus R,T_2} = U_{W_{\lambda},T_1,T_2} \bigoplus \bigoplus_{R \subseteq (U \cup T_1) : |R| = \alpha, R \neq T_2} U_{W_R,(U \cup T_1) \setminus R,T_2} \tag{25}
\]

Notice that reducer \( U \) can cancel the interference term by using the intermediate values retrieved from mappers in \( U \), so it can correctly decode \( U_{W_{\lambda},T_1,T_2} \). By following the same rationale for each \( T_2 \subseteq (U \cup T_1) \) with \( |T_2| = \alpha \) and \( T_2 \neq U \), we can conclude that reducer \( U \) can correctly recover \( U_{W_{\lambda},T_1} \), and can do so for each \( T_1 \subseteq ([\Lambda] \setminus U) \), completely recovering all the \( \eta_2 \eta_1 \left(\frac{\Lambda}{r-\alpha}\right) \) missing intermediate values. The same holds for any other \( U \in [\Lambda], \alpha \), and so we can conclude that each reducer is able to recover from the multicast messages of other reducers all the missing intermediate values.

**D. Communication Load**

The communication load guaranteed by the coded scheme described above is given by
\[
L_{UB}(r) = \frac{\sum_{U \in [\Lambda], \alpha} |X_U|}{Q \times N \times T} \tag{27}
\]
\[
= \left(\frac{\Lambda}{r}\right) \eta_2 \eta_1 \left(\frac{\Lambda}{r-\alpha}\right) T / \left(\frac{\Lambda}{r}\right) - 1 \tag{28}
\]
\[
= \left(\frac{\Lambda}{r}\right) \left(\frac{\Lambda}{r-\alpha}\right) T / \left(\frac{\Lambda}{r}\right) - 1 \tag{29}
\]
for each \( r \in [\Lambda-\alpha+1] \). Notice that the lower convex envelope of the achievable points \( \{ (r, L_{UB}(r)) : r \in [\Lambda-\alpha+1] \} \) is achievable by adopting the memory-sharing strategy presented in [6]. The proof is concluded.

\[\square\]

**V. PROOF OF CONVERSE BOUND IN THEOREM 2**

**A. Preliminaries**

We begin the proof by introducing some useful notation. For \( q \in [Q] \) and \( n \in [N] \), we let \( V_{q,n} \) be an i.i.d. random variable and we let \( v_{q,n} \) be the realization of \( V_{q,n} \). Then, we define
\[
D_U := \{ v_{q,n} : q \in W_U, n \in [N] \} \tag{30}
\]
\[
C_U := \{ v_{q,n} : q \in [Q], w_n \in M_U \} \tag{31}
\]
\[
Y_U := (D_U, C_U). \tag{32}
\]
Recalling that we denote by \( X_U \) the multicast message transmitted by reducer \( U \in [\Lambda], \alpha \), the equation
\[
H(X_U | C_U) = 0 \tag{33}
\]
holds, since \( X_U \) is a function of the intermediate values retrieved by reducer \( U \). Moreover, for any map-shuffle-reduce

---

\(^4\)Since we are presenting here the proof of the achievable bound in Theorem 1, we remind that in such case the download cost is neglected.
scheme, each reducer $\mathcal{U} \in [\Lambda]_\alpha$ has to be able to correctly recover all the intermediate values $D_{\mathcal{U}}$ given the transmissions of all reducers $X_{[\Lambda]_\alpha} := (X_{\mathcal{U}} : \mathcal{U} \in [\Lambda]_\alpha)$ and given the IVs $C_{\mathcal{U}}$ computed by the mappers in $\mathcal{U}$. Thus, the equation

$$H(D_{\mathcal{U}} | X_{[\Lambda]_\alpha}, C_{\mathcal{U}}) = 0$$  \hspace{1cm} (34)$$
holds for each $\mathcal{U} \in [\Lambda]_\alpha$.

B. Developing the Lower Bound

For a given file assignment $\mathcal{M} := (\mathcal{M}_1, \ldots, \mathcal{M}_\Lambda)$, we let $L_\mathcal{M}$ be the corresponding communication load under this assignment $\mathcal{M}$. Then, we provide a lower bound on $L_\mathcal{M}$ for any given file assignment in the following lemma.

**Lemma 1.** Consider a specific file assignment $\mathcal{M} = (\mathcal{M}_1, \ldots, \mathcal{M}_\Lambda)$. Let $e = (c_1, \ldots, c_\Lambda)$ be a permutation of the set $[\Lambda]$ and define

$$D_i := (D_{\mathcal{U}^i} : \mathcal{U}^i \subseteq \{c_1, \ldots, c_i\}, |\mathcal{U}^i| = \alpha, c_i \in \mathcal{U}^i)$$  \hspace{1cm} (35)$$
and

$$C_i := (C_{\mathcal{U}^i} : \mathcal{U}^i \subseteq \{c_1, \ldots, c_i\}, |\mathcal{U}^i| = \alpha, c_i \in \mathcal{U}^i)$$  \hspace{1cm} (36)$$
for each $i \in \{\alpha : \Lambda\}$. Then, the communication load is lower bounded by

$$L_\mathcal{M} \geq \frac{1}{K\cdot N\cdot T} \sum_{i \in \{\alpha : \Lambda\}} H(D_i | C_i, \gamma_{i-1}).$$  \hspace{1cm} (38)$$

**Proof.** The proof is described in Appendix C. \hfill \Box

Now, we proceed with the proof. Denote by $a^T$ the number of files whose intermediate values are required by each reducer $\mathcal{U} \in [\Lambda]_\alpha$ that does not have access to the intermediate values of all those files that are not mapped by the mappers in $\mathcal{U}$. The term $a^T$ represents the number of files where each intermediate value is required by each reducer $\mathcal{U} \in [\Lambda]_\alpha$. Consequently, we can rewrite the expression in (43) as

$$L_\mathcal{M} \geq \frac{1}{K\cdot N\cdot T} \sum_{i \in \{\alpha : \Lambda\}} H(D_i | C_i, \gamma_{i-1})$$  \hspace{1cm} (39)$$

$$= \frac{1}{K\cdot N\cdot T} \sum_{i \in \{\alpha : \Lambda\}} \sum_{\mathcal{U}^i \subseteq \{c_1, \ldots, c_i\} : |\mathcal{U}^i| = \alpha, c_i \in \mathcal{U}^i} H(D_{\mathcal{U}^i} | C_i, \gamma_{i-1})$$  \hspace{1cm} (40)$$

$$= \frac{1}{K\cdot N\cdot T} \sum_{i \in \{\alpha : \Lambda\}} \sum_{\mathcal{U}^i \subseteq \{c_1, \ldots, c_i\} : |\mathcal{U}^i| = \alpha, c_i \in \mathcal{U}^i} \sum_{T \subseteq [\Lambda] \setminus \{c_1, \ldots, c_i\}} \sum_{|\mathcal{U}'| = \alpha, c_i \in \mathcal{U}'} a^T \eta_2 T$$  \hspace{1cm} (41)$$

$$= \frac{1}{K\cdot N} \sum_{i \in \{\alpha : \Lambda\}} \sum_{\mathcal{U}^i \subseteq \{c_1, \ldots, c_i\} : |\mathcal{U}^i| = \alpha, c_i \in \mathcal{U}^i} \sum_{|\mathcal{U}'| = \alpha, c_i \in \mathcal{U}'} a^T \eta_2 T$$  \hspace{1cm} (42)$$

$$= \frac{1}{K\cdot N} \sum_{\mathcal{U} \in [\Lambda]_\alpha} \sum_{\mathcal{U}' \subseteq \{c_1, \ldots, c_\Lambda\} : |\mathcal{U}'| = \alpha, c_i \in \mathcal{U}'} \sum_{T \subseteq [\Lambda] \setminus \{c_1, \ldots, c_i\}} a^T \eta_2 T$$  \hspace{1cm} (43)$$

where $\eta_2$ is defined as the total number of files which are mapped by exactly $j$ mappers.

If we build a bound as the one in Lemma 1 for each permutation of the set $[\Lambda]$ and we sum up all these bounds together, we obtain the expression

$$L_\mathcal{M} \geq \frac{1}{K\cdot N!} \sum_{e \in S_\Lambda} \sum_{i \in \{\alpha : \Lambda\}} \sum_{\mathcal{U}^i \subseteq \{c_1, \ldots, c_i\} : |\mathcal{U}^i| = \alpha, c_i \in \mathcal{U}^i} \sum_{T \subseteq [\Lambda] \setminus \{c_1, \ldots, c_i\}} \sum_{|\mathcal{U}'| = \alpha, c_i \in \mathcal{U}'} a^T$$  \hspace{1cm} (44)$$

where we recall that $S_\Lambda$ represents the group of all permutations of $[\Lambda]$. Our goal now is to simplify this expression and we start doing so by counting how many times each term $a^T$ appears in the RHS of (43) for all fixed $T \subseteq [\Lambda]$ with $|T| = j$ and $j \in \{\alpha : \Lambda\}$.

First, we focus on some reducer $\mathcal{U} \subseteq ([\Lambda] \setminus T)$ with $|\mathcal{U}| = \alpha$. We can see that $a^T$ appears in the RHS of (43) for all those permutations in $S_\Lambda$ for which $\mathcal{U} = \mathcal{U}'$ for some $i \in \{\alpha : \Lambda\}$ such that $\mathcal{U}' \subseteq \{c_1, \ldots, c_i\}$ with $|\mathcal{U}'| = \alpha$ and $c_i \in \mathcal{U}'$, and such that $T \subseteq ([\Lambda] \setminus \{c_1, \ldots, c_i\})$ with $|T| = j$. Denoting by $\mathcal{P}_{\mathcal{U}, T}$ the set of such permutations, we can see that

$$|\mathcal{P}_{\mathcal{U}, T}| = \alpha! j! (\Lambda - \alpha - j)! \binom{\Lambda}{\alpha + j}$$  \hspace{1cm} (45)$$

times. The same rationale holds for any $\bar{a}^T$ where $T \subseteq [\Lambda]$ and $|T| = j$ with $j \in \{\alpha : \Lambda\}$. Consequently, we can rewrite the expression in (43) as

$$L_\mathcal{M} \geq \frac{1}{K\cdot N!} \sum_{e \in S_\Lambda} \sum_{i \in \{\alpha : \Lambda\}} \sum_{\mathcal{U}^i \subseteq \{c_1, \ldots, c_i\} : |\mathcal{U}^i| = \alpha, c_i \in \mathcal{U}^i} \sum_{T \subseteq [\Lambda] \setminus \{c_1, \ldots, c_i\}} \sum_{|\mathcal{U}'| = \alpha, c_i \in \mathcal{U}'} a^T$$  \hspace{1cm} (46)$$

$$= \frac{1}{K\cdot N!} \sum_{\mathcal{U} \in [\Lambda]_\alpha} \sum_{e \in S_\Lambda} \sum_{i \in \{\alpha : \Lambda\}} \sum_{T \subseteq [\Lambda] \setminus \{c_1, \ldots, c_i\}} \binom{\Lambda - j}{\alpha} \alpha! j!$$  \hspace{1cm} (47)$$

$$\times (\Lambda - \alpha - j)! \binom{\Lambda}{\alpha + j} \bar{a}^T$$  \hspace{1cm} (48)$$

$$= \frac{1}{K\cdot N} \sum_{\mathcal{U} \in [\Lambda]_\alpha} \sum_{\mathcal{U}' \subseteq \{c_1, \ldots, c_\Lambda\} : |\mathcal{U}'| = \alpha, c_i \in \mathcal{U}'} \sum_{T \subseteq [\Lambda] \setminus \{c_1, \ldots, c_i\}} \sum_{|\mathcal{U}'| = \alpha, c_i \in \mathcal{U}'} \bar{a}^T$$  \hspace{1cm} (49)$$

where $\bar{a}^T := \sum_{T \subseteq [\Lambda] \setminus \{c_1, \ldots, c_i\}} \bar{a}^T$ is defined as the total number of files which are mapped by exactly $j$ mappers. For any given file assignment $\mathcal{M}$ and for any given computation load $r \in [K]$, the fact that $|\mathcal{M}_1| + \cdots + |\mathcal{M}_\Lambda| \leq rN$ also implies that $\bar{a}^T \bar{a}_\mathcal{M} \geq 0$ for each $j \in \{\alpha : \Lambda\}$, as well as implies that
∑_{j∈[Λ]} \tilde{a}_M^j = N and that ∑_{j∈[Λ]} j\tilde{a}_M^j ≤ rN. Thus, we can further lower bound the above using Jensen's inequality and the fact that \((\frac{\Lambda}{\alpha+j})/\binom{\alpha}{j}\) is convex and decreasing in \(j\). Hence, we can write

\[ L_M^* \geq \frac{1}{K} \sum_{j∈[Λ]} \frac{\binom{\Lambda}{j}}{\binom{\alpha}{j}} \tilde{a}_M^j \binom{\alpha}{j} N \]  

(50)

\[ \geq \frac{1}{K} \binom{\alpha}{\alpha+r} \]  

(51)

\[ = \frac{\binom{\Lambda}{\alpha+r}}{\binom{\alpha}{\alpha+r}} \]  

(52)

where (51) holds due to ∑_{j∈[Λ]} \tilde{a}_M^j = N and the computation constraint ∑_{j∈[Λ]} j\tilde{a}_M^j ≤ rN.

Given that (52) is independent of the file assignment \(M\) and lower bounds \(L_M^*\) for any \(M\) such that \(|M_1| + \cdots + |M_Λ| ≤ rN\), we can further write

\[ L^*(r) \geq \inf_{M:|M_1|+\cdots+|M_Λ|≤rN} L_M \]  

(53)

\[ \geq \inf_{M:|M_1|+\cdots+|M_Λ|≤rN} \frac{\binom{\Lambda}{\alpha+r}}{\binom{\alpha}{\alpha+r}} \]  

(54)

\[ = \frac{\binom{\Lambda}{\alpha+r}}{\binom{\alpha}{\alpha+r}} \]  

(55)

\[ = L_{LB}(r). \]  

(56)

Notice that the bound \(L_{LB}(r)\) can be extended to include also the non-integer values of \(r\) as described in [6]. This concludes the proof. \(\square\)

VI. PROOF OF ACHIEVABLE BOUND IN THEOREM 4

As we mentioned in the statement of Theorem 4, the coded scheme depends on the solution of the linear program in (13). Hence, the first step is to evaluate the optimal solution \(\tilde{a}_s^* = (\tilde{a}_s^1, \ldots, \tilde{a}_s^\Lambda)\). Next, we partition the file database in \(\Lambda\) parts, where we denote by \(L_j\) the \(j\)-th part, which contains \(|L_j| = \tilde{a}_s^j\) files for each \(j \in [\Lambda]\). Then, each part \(j \in [\Lambda]\) of the database is split in \(\binom{\alpha}{j}\) batches containing \(\eta_j\) files each for some \(\eta_j \in \mathbb{N}\), so that \(\tilde{a}_s^j = \eta_j \binom{\alpha}{j}\) for each \(j \in [\Lambda]\). This implies

\[ \{w_1, \ldots, w_N\} = \bigcup_{j∈[\Lambda]} L_j \]  

(57)

\[ = \bigcup_{j∈[\Lambda]} \bigcup_{T_i∈[\Lambda]:|T_i|=j} B_j,T_i \]  

(58)

where we denote by \(B_j,T_i\) the batch containing \(\eta_j\) files associated with the label \(T_i\). Then, mapper \(\lambda \in [\Lambda]\) is assigned all batches \(B_j,T_i\) having \(\lambda \in T_i\) for each \(j \in [\Lambda]\), which implies

\[ \mathcal{M}_\Lambda = \{B_j,T_i : j \in [\Lambda], T_i \subseteq [\Lambda], |T_i| = j, \lambda \in T_i\}. \]  

(59)

The computation load constraint is satisfied, since we have

\[ \sum_{\lambda∈[\Lambda]} |\mathcal{M}_\lambda| \frac{\Lambda}{N} = \frac{\sum_{\lambda∈[\Lambda]} \eta_j (\lambda-1)}{N} \]  

(60)

\[ = \frac{\sum_{\lambda∈[\Lambda]} \eta_j \tilde{a}_s^j \binom{\alpha}{j}}{N} \leq r \]  

(62)

where the last inequality holds under the constraint in (13d).

Our goal is to provide an achievable scheme for the max-link communication load. Recalling that we denote by \(L\) and \(J\) the communication load and the download cost, respectively, we will have

\[ L_{\text{max-link}}^*(r) \leq L_{\text{max-link,UL}}(r) = \max (L, J). \]  

(63)

A. Communication Load

For what concerns the communication load, we can take advantage of the achievable scheme described in Section IV.

Simply, the scheme in Section IV is applied \(\Lambda\) times, one time per partition \(L_j\) which is considered as an independent input database. If we denote by \(L_j\) the communication load when we focus on the part \(L_j\), we have that \(L_j\) is given by

\[ L_j = \frac{\binom{\Lambda-\alpha}{j}}{\binom{\alpha}{j}} \frac{\tilde{a}_s^j}{\binom{\alpha}{j} (\binom{\alpha}{j} - 1) \binom{\alpha}{j}} \]  

(64)

for each \(j \in [\Lambda]\). Hence, the overall communication load \(L\) is given by

\[ L = \sum_{j∈[\Lambda]} L_j = \sum_{j∈[\Lambda]} \frac{\binom{\Lambda-\alpha}{j}}{\binom{\alpha}{j}} \frac{\tilde{a}_s^j}{\binom{\alpha}{j} (\binom{\alpha}{j} - 1) \binom{\alpha}{j}} N. \]  

(65)

B. Download Cost

We remind that the download cost is defined as

\[ J = \max_{\lambda∈[\Lambda]} \max_{U∈[\Lambda]:\lambda∈U} R^U_\Lambda \]  

(66)

where \(R^U_\Lambda\) represents the number of bits which are sent from mapper \(\lambda\) to reducer \(U\). This quantity is minimized if the number of bits transmitted over each link connecting a mapper to a reducer is the same. This can be accomplished as follows.

Consider a reducer \(U \in [\Lambda]\) and a mapper \(\lambda \in U\). According to the file assignment above, mapper \(\lambda\) computes the IVs in the set \(\mathcal{V}_\lambda = \{v_{q,n} : q \in [Q], w_n \in \mathcal{M}_\lambda\}\). The set \(\mathcal{V}_\lambda\) can equivalently be written as follows

\[ \mathcal{V}_\lambda = \{\mathcal{V}_{\lambda,S} : i ∈ [\alpha], S ⊆ (U \setminus \{\lambda\}), |S| = i - 1\} \]  

(67)

where \(\mathcal{V}_{\lambda,S}\) is defined as

\[ \mathcal{V}_{\lambda,S} := \{v_{q,n} : q ∈ [Q], w_n ∈ \mathcal{M}^{\Lambda \cup S}\} \]  

(68)

and where \(\mathcal{M}^{\Lambda \cup S} := \bigcap_{S⊆[\Lambda]\setminus\{\lambda\}} \mathcal{M}_\lambda\). This simply says that the set \(\mathcal{V}_{\lambda,S}\) contains the IVs which are mapped by mapper \(\lambda\) and the \((i-1)\) mappers in \(S\). Hence, if we evenly split \(\mathcal{V}_{\lambda,S}\) in \(i\) segments as follows

\[ \mathcal{V}_{\lambda,S} = (\mathcal{V}_{\lambda,S,i} : s ∈ (\Lambda \cup S)) \]  

(69)
we simply let mapper $\lambda$ send $\mathcal{V}_{\lambda,S,\lambda}$. This implies

$$R^\ell_\lambda = \sum_{i \in [\alpha]} \sum_{S \subseteq (\mathcal{U} \setminus \{\lambda\}); |S| = i - 1} |\mathcal{V}_{\lambda,S,\lambda}|$$

$$= \sum_{i \in [\alpha]} \sum_{S \subseteq (\mathcal{U} \setminus \{\lambda\}); |S| = i - 1} \frac{|\mathcal{V}_{\lambda,S}|}{i}$$

$$= \sum_{i \in [\alpha]} \sum_{S \subseteq (\mathcal{U} \setminus \{\lambda\}); |S| = i - 1} \sum_{j \in [\alpha]} \eta_j (\lambda)_{j-i} QT$$

$$= \sum_{j \in [\alpha]} \sum_{i \in [\alpha]} \eta_j (\lambda)_{j-i} QT \left(\alpha - 1\right)$$

for each $\lambda \in [\Lambda]$ and $\mathcal{U} \in [\Lambda]_\alpha$ with $\lambda \in \mathcal{U}$. Hence, we can further write

$$J = \max_{\lambda \in [\Lambda]} \max_{\mathcal{U} \in [\Lambda]_\alpha} \frac{R^\ell_\lambda}{QNT}$$

$$= \frac{1}{QNT} \sum_{j \in [\alpha]} \sum_{i \in [\alpha]} \eta_j (\lambda)_{j-i} QT \left(\alpha - 1\right)$$

$$= \sum_{j \in [\alpha]} \sum_{i \in [\alpha]} \frac{(\lambda)_{j-i}}{N}$$

recalling that $\tilde{a}_j = \eta_j (\lambda)_{j}$ for each $j \in [\Lambda]$. Further, the following lemma holds.

**Lemma 2.** For any non-negative integers $\Lambda$, $\alpha$ and $j$, we have

$$\sum_{i \in [\alpha]} \frac{(\lambda)_{j-i}}{i (\lambda)} = \frac{(\lambda) - (\lambda)_{j-i}}{\alpha (\lambda)}.$$  

**Proof.** The proof is described in Appendix D.

As a consequence, the download cost $J$ is equivalently given by

$$J = \sum_{j \in [\Lambda]} \frac{(\lambda) - (\lambda)_{j-i}}{\alpha (\lambda)} \tilde{a}_j$$

$$= \sum_{j \in [\Lambda]} \frac{(\lambda) - (\lambda)_{j-i}}{\alpha (\lambda)} \tilde{a}_j$$

**C. Max-Link Communication Load**

Since we have now the expressions for both $L$ and $J$, we can write explicitly the achievable max-link communication load as follows

$$L_{\max\text{-link}, UB}(r) = \max (L, D)$$

$$= \max \left( \sum_{j \in [\Lambda]} \frac{(\lambda) - (\lambda)_{j-i}}{\alpha (\lambda)} \tilde{a}_j \tilde{a}_j \right)$$

The expression above coincides with the achievable expression in Theorem 4. The proof is concluded.

**VII. PROOF OF CONVERSE BOUND IN THEOREM 5**

We quickly recall that $L_M$ denotes the communication load under the file assignment $\mathcal{M} = (\mathcal{M}_1, \ldots, \mathcal{M}_\Lambda)$ and that we know

$$L_M \geq \sum_{j \in [\Lambda]} \frac{(\lambda)_{j-i}}{\alpha (\lambda)} \tilde{a}_j$$

from Section V. Then, after denoting by $J_M$ and by $L_{\max\text{-link}, M}(r) = \max(L_M, J_M)$ the download cost and the max-link communication load, respectively, under file assignment $\mathcal{M}$, we can write

$$L_{\max\text{-link}, M}(r) = \max(L_M, J_M)$$

$$\geq \max \left( \sum_{j \in [\Lambda]} \frac{(\lambda)_{j-i}}{\alpha (\lambda)} \tilde{a}_j, J_M \right).$$

To develop a lower bound on $J_M$, we start from the definition of the download cost, so that we have

$$J_M = \max_{\lambda \in [\Lambda]} \max_{\mathcal{U} \in [\Lambda]_\alpha} \frac{R^\ell_\lambda}{QNT}$$

$$\geq \frac{1}{\alpha (\lambda) QNT} \sum_{j \in [\Lambda]} \sum_{i \in [\alpha]} \frac{(\lambda)_{j-i}}{\alpha (\lambda)} \tilde{a}_j$$

$$= \frac{1}{\alpha (\lambda) QNT} \sum_{j \in [\Lambda]} \sum_{i \in [\alpha]} \frac{(\lambda)_{j-i}}{\alpha (\lambda)} \tilde{a}_j$$

where $R^\ell_\lambda$ is defined as

$$R^\ell_\lambda := \sum_{\lambda \in \mathcal{U}}$$

to represent the overall number of bits received by reducer $\mathcal{U} \in [\Lambda]_\alpha$. Now, since each reducer $\mathcal{U}$ is expected to receive all the IVs mapped by the mappers in $\mathcal{U}$, we have

$$R^\ell \geq H(C^\ell)$$

where we recall that $C^\ell = \{V_{q,a} : q \in [Q], w_n \in M_{\mathcal{U}}\}$ from Section V. Hence, we can write

$$J_M \geq \frac{1}{\alpha (\lambda) QNT} \sum_{j \in [\Lambda]} \sum_{i \in [\alpha]} \tilde{a}_j$$

$$\geq \frac{1}{\alpha (\lambda) QNT} \sum_{j \in [\Lambda]} H(C^\ell)$$

$$= \frac{1}{\alpha (\lambda) QNT} \sum_{j \in [\Lambda]} \sum_{\mathcal{U} \subseteq [\Lambda] : \mathcal{U} \neq \emptyset} \tilde{a}_j$$

$$= \frac{1}{\alpha (\lambda) N} \sum_{j \in [\Lambda]} \sum_{\mathcal{U} \subseteq [\Lambda] : \mathcal{U} \neq \emptyset} \left( \frac{(\lambda)_{j-i}}{\alpha (\lambda)} \tilde{a}_j \right)$$

$$= \sum_{j \in [\Lambda]} \frac{(\lambda)_{j-i}}{\alpha (\lambda)} \tilde{a}_j \tilde{a}_j$$

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recalling that \( \tilde{a}^{M} = \sum_{T \subseteq [\Lambda]} \alpha_{|T|} = j \tilde{a}^{T} \). To conclude, for a given file assignment \( M \), the max-link communication load is lower bounded as

\[
L_{\text{max-link}, M}(r) \geq \max \left( \sum_{j \in [\Lambda]} \left( \frac{\Lambda}{\alpha + j} \right) \left( \frac{\alpha}{\alpha} \right) \frac{\tilde{a}^{j}_{M}}{N} J_{M} \right) \tag{89}
\]

\[
\geq \max \left( \sum_{j \in [\Lambda]} \left( \frac{\Lambda}{\alpha + j} \right) \left( \frac{\alpha}{\alpha} \right) \frac{\tilde{a}^{j}_{M}}{N}, \sum_{j \in [\Lambda]} \left( \frac{\Lambda}{\alpha} \right) \frac{\tilde{a}^{j}_{M}}{N} \right) \tag{90}
\]

\[
\geq \frac{1}{2} \sum_{j \in [\Lambda]} \left( \frac{\Lambda}{\alpha + j} \frac{\alpha}{\alpha} \right) \frac{\tilde{a}^{j}_{M}}{N}, \tag{91}
\]

Since each file assignment \( M \) such that \( |M| + \cdots + |M_{\Lambda}| \leq rN \) also implies that \( \tilde{a}^{j}_{M} \geq 0 \) for each \( j \in [\Lambda] \), as well as implies that \( \sum_{j \in [\Lambda]} \tilde{a}^{j}_{M} = N \) and that \( \sum_{j \in [\Lambda]} j \tilde{a}^{j}_{M} \leq rN \), the max-link load \( L_{\text{max-link}}(r) \) is lower bounded by the solution to the following linear program

\[
\min \frac{1}{2} \sum_{j \in [\Lambda]} \left( \frac{\Lambda}{\alpha + j} \frac{\alpha}{\alpha} \right) \frac{\tilde{a}^{j}_{M}}{N}, \tag{92a}
\]

subject to

\[
\tilde{a}^{j}_{M} \geq 0, \quad \forall j \in [\Lambda] \tag{92b}
\]

\[
\sum_{j \in [\Lambda]} \frac{\tilde{a}^{j}_{M}}{N} = 1 \tag{92c}
\]

\[
\sum_{j \in [\Lambda]} \frac{j \tilde{a}^{j}_{M}}{N} \leq r \tag{92d}
\]

where \( \tilde{a}_{M} = (\tilde{a}^{1}_{M}, \ldots, \tilde{a}^{\Lambda}_{M}) \) is the control variable. The proof is concluded. \( \square \)

VIII. CONCLUSION

In this work, we introduced multi-access distributed computing, focusing on the MADC model with combinatorial topology, which implies \( \Lambda \) mappers and \( K = \binom{\Lambda}{\alpha} \) reducers, so that there is a reducer for any set of \( \alpha \) mappers. Neglecting at first the download cost from mappers to reducers and so focusing only on the inter-reducer communication load, we proposed a novel scheme which, together with an information-theoretic converse, characterizes the optimal communication load within a constant multiplicative gap of 1.5. Subsequently, we jointly considered the setting which keeps into account the download cost and for such scenario we characterized the optimal max-link communication load within a multiplicative factor of 4. We point out that the here introduced achievable shuffling scheme generalizes the original coded scheme in [6] (corresponding to the case \( \alpha = 1 \)).

Interesting future directions could include the study of the here proposed MADC setting when mappers and reducers have heterogeneous computational resources. A careful study of other multi-access network topologies is also another challenging research direction. Reflecting a design freedom, the search for the best possible topology, for a given computation load, remains a very pertinent open problem in distributed computing.

APPENDIX A

PROOF OF ORDER OPTIMALITY IN THEOREM 3

To prove the order optimality result in Theorem 3, we need to upper bound the ratio \( L_{UB}(r)/L^{*}(r) \) for each \( r \in [\Lambda - \alpha + 1] \). We start by noting that the following

\[
L_{UB}(r)/L^{*}(r) \leq L_{UB}(r)/L_{LB}(r) \tag{93}
\]

\[
= \left( \frac{\Lambda - \alpha}{r} \right) \left( \frac{\alpha}{r} \right) \left( \frac{\Lambda}{\alpha} \right) \left( \frac{\alpha}{\alpha} \right) \left( r + \alpha \right) \tag{94}
\]

\[
= \left( \frac{\Lambda - \alpha}{r} \right) \left( \frac{\alpha}{\alpha} \right) \left( \frac{\alpha}{\alpha} \right) \left( r + \alpha \right) \tag{95}
\]

holds. Further, we notice that \( b_{r} \) is decreasing in \( r \), since

\[
b_{r+1} = \frac{r + 1 + \alpha}{r + 1} - 1 \tag{96}
\]

\[
= 1 - \frac{1}{r + 1 + \alpha} \tag{97}
\]

\[
= 1 - \frac{1}{r + 1 + \alpha} \left( \frac{1}{r + 1 + \alpha} \right) \tag{98}
\]

\[
< 1 - \frac{1}{r + \alpha} \tag{99}
\]

\[
= b_{r} \tag{100}
\]

for each \( r \in \mathbb{N}^{+} \). Thus, considering that \( r \in [\Lambda - \alpha + 1] \), we can further write

\[
\frac{L_{UB}(r)}{L^{*}(r)} \leq \left( \frac{r + \alpha}{r} \right) - 1 \tag{102}
\]

\[
\leq \frac{\alpha + 1}{\alpha} \tag{103}
\]

where the last term is upper bounded when \( \alpha \) is set to its minimum value. Now, after neglecting the value \( \alpha = 1 \) — in which case the corresponding achievable performance in Theorem 1 was already proved to be exactly optimal in [6] — we focus on the case where \( \alpha \in [2 : \Lambda] \), which implies that

\[
\frac{L_{UB}(r)}{L^{*}(r)} \leq \frac{\alpha + 1}{\alpha} \tag{104}
\]

\[
\leq \frac{3}{2}. \tag{105}
\]

The proof is concluded. \( \square \)
APPENDIX B

PROOF OF ORDER OPTIMALITY IN THEOREM 6

From Theorem 4 we know that $L^*_{\text{max-link}}(r)$ is upper bounded as

$$L^*_{\text{max-link}}(r) \leq L_{\text{max-link,UB}}(r) \leq \max \left( \sum_{j \in [\Lambda]} \frac{\Lambda}{\Lambda^+} \left( \frac{\Lambda^+}{\Lambda^+} - 1 \right) \Lambda^+ \right)$$

where $\Lambda := \Lambda^+ / \Lambda^+$. Hence, we can evaluate the gap to optimality from the ratio $d = \frac{L_{\text{max-link,UB}}(r)}{L_{\text{max-link,LB}}(r)}$. Now, we can see that $c_j = d_j$ when $j > \Lambda - \alpha$. Else, when $j \in [\Lambda - \alpha]$, we have

$$d_j \leq \frac{c_j}{d_j} \leq \frac{(\Lambda^+ - \alpha) + \alpha}{\alpha}$$

and

$$d_j \leq \frac{c_j}{d_j} \leq \frac{(\Lambda^+ - \alpha) + \alpha}{\alpha + 1}$$

Hence, we can conclude that

$$\frac{L_{\text{max-link,UB}}(r)}{L_{\text{max-link,LB}}(r)} \leq 2 \max \left( \frac{c_j}{d_j} \right) \leq 2 \max \left( \frac{c_j}{d_j} \right)$$

and

$$\frac{L_{\text{max-link,UB}}(r)}{L_{\text{max-link,LB}}(r)} \leq 2 \max \left( \frac{c_j}{d_j} \right) \leq 2 \max \left( \frac{c_j}{d_j} \right)$$

The proof is concluded. \square

APPENDIX C

PROOF OF LEMMA 1

Consider a permutation $c = (c_1, \ldots, c_{\Lambda})$ of the set $[\Lambda]$.

We know that $H(D_U | X_{[\Lambda]}) = 0$ holds for any valid shuffle scheme and for each $U \in [\Lambda]$. Given this, for $U^* = \{c_1, \ldots, c_{\Lambda}\}$ we can write

$$H(X_{[\Lambda]}) \geq H(X_{[\Lambda]} | C_{U^*})$$

and

$$H(X_{[\Lambda]} | C_{U^*}) = H(D_{U^*} | X_{[\Lambda]}, C_{U^*})$$

Hence, we can conclude that

$$H(X_{[\Lambda]} | C_{U^*}) = H(D_{U^*} | C_{U^*})$$

where (131) follows from the fact that conditioning does not increase entropy, and where (133) holds because of the
decodability condition \( H(D_{i+1} \mid X_{[i]}^{[k]}, C_{i}) = 0 \) for each \( i \in [k+1 : K] \). Similarly, for each \( i \in [0 : \alpha] \), we can write

\[
H(X_{[i]}^{[k]} \mid Y_{i+1}) \geq H(X_{[i]}^{[k]} \mid C_i, Y_{i+1}) 
= H(X_{[i]}^{[k]}, D_i \mid C_i, Y_{i+1}) 
- H(D_i \mid X_{[i]}^{[k]}, C_i, Y_{i+1}) 
= H(X_{[i]}^{[k]}, D_i \mid C_i, Y_{i+1})
\]

where again (136) is true as conditioning does not increase entropy, and where (138) follows because

\[
H(D_i \mid X_{[i]}^{[k]}, C_i, Y_{i+1}) \leq H(D_i \mid X_{[i]}^{[k]}, C_i) \leq \sum_{i \in [0 : \alpha]} H(D_i \mid X_{[i]}^{[k]}, C_i) \leq 0
\]

and so it suffices to show that

\[
\sum_{i \in [0 : \alpha]} \left( \Lambda - \alpha \right) \left( \alpha \right)_{j-i} = \sum_{i \in [0 : \alpha]} \left( \Lambda - \alpha \right) \left( \alpha \right)_{j-i}. \tag{150}
\]

Consider first the case \( j \leq \alpha \). This means that we can write

\[
\sum_{i \in [0 : \alpha]} \left( \Lambda - \alpha \right) \left( \alpha \right)_{j-i} = \sum_{i \in [0 : \alpha]} \left( \Lambda - \alpha \right) \left( \alpha \right)_{j-i} + \sum_{i \in [\alpha+1 : \alpha]} \left( \Lambda - \alpha \right) \left( \alpha \right)_{j-i} = 0
\]

where \( \sum_{i \in [\alpha+1 : \alpha]} \left( \Lambda - \alpha \right) \left( \alpha \right)_{j-i} = 0 \) since we have \( \left( \alpha \right)_{j-i} = 0 \) for \( i \in [j+1 : \alpha] \). Similarly, if we consider \( j \geq \alpha \), we have

\[
\sum_{i \in [0 : \alpha]} \left( \Lambda - \alpha \right) \left( \alpha \right)_{j-i} = \sum_{i \in [0 : \alpha]} \left( \Lambda - \alpha \right) \left( \alpha \right)_{j-i} + \sum_{i \in [\alpha+1 : \alpha]} \left( \Lambda - \alpha \right) \left( \alpha \right)_{j-i} = 0
\]

where \( \sum_{i \in [\alpha+1 : \alpha]} \left( \Lambda - \alpha \right) \left( \alpha \right)_{j-i} = 0 \) since we have \( \left( \alpha \right)_{j-i} = 0 \) for \( i \in [j+1 : \alpha] \). Hence, for any value of \( j \in [0 : \alpha] \), we can conclude that

\[
\sum_{i \in [0 : \alpha]} \left( \Lambda - \alpha \right) \left( \alpha \right)_{j-i} = \sum_{i \in [0 : \alpha]} \left( \Lambda - \alpha \right) \left( \alpha \right)_{j-i} = \left( \Lambda \right)_{j}. \tag{155}
\]

The proof is concluded.

\[\square\]

### APPENDIX D

**Proof of Lemma 2**

First, we rewrite the equality in Lemma 2 as

\[
\sum_{i \in [\alpha]} \left( \Lambda - \alpha \right) \left( \alpha \right)_{j-i} = \left( \Lambda \right)_{j} \tag{145}
\]

Thus, proving the equality in Lemma 2 is equivalent to showing that the following equality

\[
\sum_{i \in [0 : \alpha]} \left( \Lambda - \alpha \right) \left( \alpha \right)_{j-i} = \left( \Lambda \right)_{j} \tag{148}
\]

holds. From Vandermonde’s identity, we know that

\[
\sum_{i \in [0 : \alpha]} \left( \Lambda - \alpha \right) \left( \alpha \right)_{j-i} = \left( \Lambda \right)_{j} \tag{149}
\]
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