GENERAL DECIDABILITY RESULTS FOR ASYNCHRONOUS SHARED-MEMORY PROGRAMS: HIGHER-ORDER AND BEYOND

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ABSTRACT. The model of asynchronous programming arises in many contexts, from low-level systems software to high-level web programming. We take a language-theoretic perspective and show general decidability and undecidability results for asynchronous programs that capture all known results as well as show decidability of new and important classes. As a main consequence, we show decidability of safety, termination and boundedness verification for higher-order asynchronous programs—such as OCaml programs using Lwt—and undecidability of liveness verification already for order-2 asynchronous programs. We show that under mild assumptions, surprisingly, safety and termination verification of asynchronous programs with handlers from a language class are decidable iff emptiness of languages is decidable for the underlying language class. Moreover, we show that configuration reachability and liveness (fair termination) verification are equivalent, and decidability of these problems implies decidability of the well-known “equal-letters” problem on languages. Our results close the decidability frontier for asynchronous programs.

1. Introduction

Asynchronous programming is a common way to manage concurrent requests in a system. In this style of programming, rather than waiting for a time-consuming operation to complete, the programmer can make asynchronous procedure calls which are stored in a task buffer pending later execution. Each asynchronous procedure, or handler, is a sequential program. When run, it can change the global shared state of the program, make internal synchronous procedure calls, and post further instances of handlers to the task buffer. A scheduler repeatedly and non-deterministically picks pending handler instances from the task buffer and executes their code atomically to completion. Asynchronous programs appear in many domains, such as operating system kernel code, web programming, or user applications on mobile platforms. This style of programming is supported natively or through libraries for most programming environments. The interleaving of different handlers hides latencies...
of long-running operations: the program can process a different handler while waiting for an external operation to finish. However, asynchronous scheduling of tasks introduces non-determinism in the system, making it difficult to reason about correctness.

An asynchronous program is finite-data if all program variables range over finite domains. Finite-data programs are still infinite state transition systems: the task buffer can contain an unbounded number of pending instances and the sequential machine implementing an individual handler can have unboundedly large state (e.g., if the handler is given as a recursive program, the stack can grow unboundedly). Nevertheless, verification problems for finite-data programs have been shown to be decidable for several kinds of handlers [SV06, JM07, CV07, GM12]. Several algorithmic approaches have been studied, which tailor to (i) the kinds of permitted handler programs and (ii) the properties that are checked.

State of the art. We briefly survey the existing approaches and what is known about the decidability frontier. The Parikh approach applies to (first-order) recursive handler programs. Here, the decision problems for asynchronous programs are reduced to decision problems over Petri nets [GM12]. The key insight is that since handlers are executed atomically, the order in which a handler posts tasks to the buffer is irrelevant. Therefore, instead of considering the sequential order of posted tasks along an execution, one can equivalently consider its Parikh image. Thus, when handlers are given as pushdown systems, the behaviors of an asynchronous program can be represented by a (polynomial sized) Petri net. Using the Parikh approach, safety (formulated as reachability of a global state), termination (whether all executions terminate), and boundedness (whether there is an a priori upper bound on the task buffer) are all decidable for asynchronous programs with recursive handlers, by reduction to corresponding problems on Petri nets [SV06, GM12]. Configuration reachability (reachability of a specific global state and task buffer configuration), fair termination (termination under a fair scheduler), and fair non-starvation (every pending handler instance is eventually executed) are also decidable, by separate ad hoc reductions to Petri net reachability [GM12]. A “reverse reduction” shows that Petri nets can be simulated by polynomial-sized asynchronous programs (already with finite-data handlers).

In the downclosure approach, one replaces each handler with a finite-data program that is equivalent up to “losing” handlers in the task buffer. Of course, this requires that one can compute equivalent finite-data programs for a given class of handler programs. This has been applied to checking safety for recursive handler programs [ABQ09]. Finally, a bespoke rank-based approach has been applied to checking safety when handlers can perform restricted higher-order recursion [CV07].

Contribution. Instead of studying individual kinds of handler programs, we consider asynchronous programs in a general language-theoretic framework. The class of handler programs is given as a language class $C$: An asynchronous program over a language class $C$ is one where each handler defines a language from $C$ over the alphabet of handler names, as well as a transformer over the global state. This view leads to general results: we can obtain simple characterizations of which classes of handler programs permit decidability. For example, we do not need the technical assumptions of computability of equivalent finite-data programs from the Parikh and the downclosure approach.

Our first result shows that, under a mild language-theoretic assumption, safety and termination are decidable if and only if the underlying language class $C$ has a decidable
emptiness problem.\(^1\) Similarly, we show that boundedness is decidable iff finiteness is decidable for the language class \(C\). These results are the best possible: decidability of emptiness (resp., finiteness) is a requirement for safety and termination verification already for verifying the safety or termination (resp., boundedness) of one sequential handler call. As corollaries, we get new decidability results for all these problems for asynchronous programs over higher-order recursion schemes, which form the language-theoretic basis for programming in higher-order functional languages such as OCaml [Kob09, Ong15], as well as other language classes (lossy channel languages, Petri net languages, etc.).

Second, we show that configuration reachability, fair termination, and fair starvation are mutually reducible; thus, decidability of any one of them implies decidability of all of them. We also show decidability of these problems implies the decidability of a well-known combinatorial problem on languages: given a language over the alphabet \(\{a, b\}\), decide if it contains a word with an equal number of \(a\)s and \(b\)s. Viewed contrapositively, we conclude that all these decision problems are undecidable already for asynchronous programs over order-2 pushdown languages, since the equal-letters problem is undecidable for this class.

Together, our results “close” the decidability frontier for asynchronous programs, by demonstrating reducibilities between decision problems heretofore studied separately and connecting decision problems on asynchronous programs with decision problems on the underlying language classes of their handlers.

While our algorithms do not assume that downclosures are effectively computable, we use downclosures to prove their correctness. We show that the safety, termination, and boundedness problems are invariant under taking downclosures of runs; this corresponds to taking downclosures of the languages of handlers.

The observation that safety, termination, and boundedness depend only on the downclosure suggests a possible route to implementation. If there is an effective procedure to compute the downclosure for class \(C\), then a direct verification algorithm would replace all handlers by their (regular) downclosures, and invoke existing decision procedures for this class. Thus, we get a direct algorithm based on downclosure constructions for higher-order recursion schemes, using the string of celebrated recent results on effectively computing the downclosure of word schemes [Zet15, IKO16, CPSW16].

We find our general decidability result for asynchronous programs to be surprising. Already for regular languages, the complexity of safety verification jumps from \(\text{NL (NFA emptiness)}\) to \(\text{EXPSpace (Petri net coverability)}\): asynchronous programs are far more expressive than individual handler languages. It is therefore unexpected that safety and termination remain decidable whenever they are decidable for individual handler languages.

2. Preliminaries

**Basic Definitions.** We assume familiarity with basic definitions of automata theory (see, e.g., [HMU07, Sip97]). If \(w\) is a word over \(\Sigma\) and \(\Gamma \subseteq \Sigma\) is a subalphabet, then the *projection of \(w\) onto \(\Gamma\)*, denoted \(\text{Proj}_\Gamma(w)\), is the word obtained by erasing from \(w\) each symbol that does not belong to \(\Gamma\). For a language \(L \subseteq \Sigma^*\), define \(\text{Proj}_\Gamma(L) = \{\text{Proj}_\Gamma(w) \mid w \in L\}\). The *subword order* \(\sqsubseteq\) on \(\Sigma^*\) is defined as \(w \sqsubseteq w'\) for \(w, w' \in \Sigma^*\) if \(w\) can be obtained

\(^1\)The “mild language-theoretic assumption” is that the class of languages forms an effective full trio: it is closed under intersections with regular languages, homomorphisms, and inverse homomorphisms. Many language classes studied in formal language theory and verification satisfy these conditions.
from \(w'\) by deleting some letters from \(w'\). For example, \(abba \sqsubseteq bababa\) but \(abba \not\sqsubseteq baaba\). The downclosure \(\downarrow w\) with respect to the subword order, of a word \(w \in \Sigma^*\), is defined as \(\downarrow w := \{ w' \in \Sigma^* \mid w' \sqsubseteq w \}\). The downclosure \(\downarrow L\) of a language \(L \subseteq \Sigma^*\) is given by \(\downarrow L := \{ w' \in \Sigma^* \mid \exists w \in L: w' \sqsubseteq w \}\). Recall that the downclosure \(\downarrow L\) of any language \(L\) is a regular language [Hai69].

A multiset \(m : \Sigma \rightarrow \mathbb{N}\) over \(\Sigma\) maps each symbol of \(\Sigma\) to a natural number. Let \(M[\Sigma]\) be the set of all multisets over \(\Sigma\). We treat sets as a special case of multisets where each element is mapped onto 0 or 1. For example, we write \(m = [a, a, c]\) for the multiset \(m \in M[\{a, b, c, d\}]\) with \(m(a) = 2\), \(m(b) = m(d) = 0\), and \(m(c) = 1\). We also use the notation \([m] = \sum_{\sigma \in \Sigma} m(\sigma)\). The Parikh image \(\text{Parikh}(w) \in M[\Sigma]\) of a word \(w \in \Sigma^*\) is the multiset such that \(\text{Parikh}(w)(a)\) is the number of times \(a\) occurs in \(w\).

Given two multisets \(m, m' \in M[\Sigma]\) we define the multiset \(m \oplus m' \in M[\Sigma]\) for which, for all \(a \in \Sigma\), we have \((m \oplus m')(a) = m(a) + m'(a)\). We also define the natural order \(\preceq\) on \(M[\Sigma]\) as follows: \(m \preceq m'\) iff there exists \(m'' \in M[\Sigma]\) such that \(m \oplus m'' = m'\). We also define \(m' \preceq m\) for \(m \preceq m'\) analogously: for all \(a \in \Sigma\), we have \((m \ominus m')(a) = m(a) - m'(a)\).

For \(\Gamma \subseteq \Sigma\) we regard \(m \in M[\Gamma]\) as a multiset of \(M[\Sigma]\) where undefined values are sent to 0.

**Language Classes and Full Trios.** A language class is a collection of languages, together with some finite representation. Examples are the regular (e.g. represented by finite automata) or the context-free languages (e.g. represented by pushdown automata (PDA)). A relatively weak and reasonable assumption on a language class is that it is a full trio, that is, it is closed under each of the following operations: taking intersection with a regular language, taking homomorphic images, and taking inverse homomorphic images. Equivalently, a language class is a full trio iff it is closed under rational transductions [Ber79] as we explain below.

We assume that all full trios \(C\) considered in this paper are effective: Given a language \(L\) from \(C\), a regular language \(R\), and a homomorphism \(h\), we can compute a representation of the languages \(L \cap R, h(L),\) and \(h^{-1}(L)\) in \(C\).

Many classes of languages studied in formal language theory form effective full trios. Examples include the regular and the context-free languages [HMU07], the indexed languages [Aho68, DG86], the languages of higher-order pushdown automata [Mas74], higher-order recursion schemes (HORS) [Dam82, HMOS08], Petri nets (both coverability and reachability languages) [Gre78, Jan79], and lossy channel systems (see Section 4.1). (While HORS are usually viewed as representing a tree or collection of trees, one can also view them as representing a word language, as we explain in Section 5.)

Informally, a language class defined by non-deterministic devices with a finite-state control that allows \(\varepsilon\)-transitions and imposes no restriction between input letter and performed configuration changes (such as non-deterministic pushdown automata) is always a full trio: The three operations above can be realized by simple modifications of the finite-state control. The deterministic context-free languages are a class that is not a full trio.

An asynchronous transducer \(T\) is a tuple \(T = (Q, \Gamma, \Sigma, E, q_0, F)\) with a finite set of states \(Q\), finite output alphabet \(\Gamma\), finite input alphabet \(\Sigma\), a finite set of edges \(E \subseteq Q \times \Gamma^* \times \Sigma^* \times Q\), initial state \(q_0 \in Q\) and set of final states \(F \subseteq Q\). We write \(p \xrightarrow{u/v} q\) if \((p, v, u, q) \in E\) and the machine reads \(u\) in state \(p\), outputs \(v\) and moves to state \(q\). We also write \(p \xrightarrow{u/w} q\) if there are states \(q_0, q_1, \ldots, q_n\) and words \(u_1, u_2, \ldots, u_n, v_1, v_2, \ldots, v_n\) such that \(p = q_0, q = q_n, w' = u_1 u_2 \cdots u_n, w = v_1 v_2 \cdots v_n\) and \(q_{i-1} \xrightarrow{u_i/v_i} q_i\) for all \(0 \leq i \leq n\).
The transduction $T \subseteq \Gamma^* \times \Sigma^*$ generated by the transducer $\mathcal{T}$ is the set of tuples $(v, u) \in \Gamma^* \times \Sigma^*$ such that $q_0 \xrightarrow{v/u} q_f$ for some $q_f \in F$. Given a language $L \subseteq \Sigma^*$, we define $\text{TL} := \{v \in \Gamma^* \mid \exists u \in L: (v, u) \in T\}$. A transduction $T \subseteq \Gamma^* \times \Sigma^*$ is rational if it is generated by some asynchronous transducer.

A language class $\mathcal{C}$ is a full trio if and only if it is closed under each of the following operations:

- intersection with a regular language,
- taking homomorphic images, and
- taking inverse homomorphic images.

It is well known that these two concepts are equivalent:

**Theorem 2.1** (Berstel [Ber79]). A language class is closed under rational transductions if and only if it is a full trio.

**Asynchronous Programs: A Language-Theoretic View.** We use a language-theoretic model for asynchronous shared-memory programs.

**Definition 2.2.** Let $\mathcal{C}$ be an (effective) full trio. An asynchronous program (AP) over $\mathcal{C}$ is a tuple $\mathfrak{P} = (D, \Sigma, (L_c)_{c \in \mathcal{C}}, d_0, m_0)$, where $D$ is a finite set of global states, $\Sigma$ is an alphabet of handler names, $(L_c)_{c \in \mathcal{C}}$ is a family of languages over $\Sigma$ from $\mathcal{C}$, one for each $c \in \mathcal{C}$ where $\mathfrak{c} = D \times \Sigma \times D$ is the set of contexts, $d_0 \in D$ is the initial state, and $m_0 \in M[\Sigma]$ is a multiset of initial pending handler instances.

A configuration $(d, m) \in D \times M[\Sigma]$ of $\mathfrak{P}$ consists of a global state $d$ and a multiset $m$ of pending handler instances. For a configuration $c$, we write $c.d$ and $c.m$ for the global state and the multiset in the configuration respectively. The initial configuration $c_0$ of $\mathfrak{P}$ is given by $c_0.d = d_0$ and $c_0.m = m_0$. The semantics of $\mathfrak{P}$ is a labeled transition system over the set of configurations, with the transition relation $\xrightarrow{\sigma} \subseteq (D \times M[\Sigma]) \times (D \times M[\Sigma])$ given by

$$(d, m \oplus \sigma) \xrightarrow{\sigma} (d', m' \oplus \sigma') \text{ iff } \exists w \in L_{d \sigma d'}: \text{Parikh}(w) = m'$$

Moreover, we write $(d, m) \rightarrow (d', m')$ if there exists a $\sigma \in \Sigma$ with $(d, m) \xrightarrow{\sigma} (d', m')$. We use $\rightarrow^*$ to denote the reflexive transitive closure of the relation $\rightarrow$. A configuration $c$ is said to be reachable in $\mathfrak{P}$ if $(d_0, m_0) \rightarrow^* c$.

Intuitively, the set $\Sigma$ of handler names specifies a finite set of procedures that can be invoked asynchronously. The shared state takes values in $D$. When a handler is called asynchronously, it gets added to a bag of pending handler calls (the multiset $m$ in a configuration). The language $L_{d \sigma d'}$ captures the effect of executing an instance of $\sigma$ starting from the global state $d$, such that on termination, the global state is $d'$. Each word $w \in L_{d \sigma d'}$ captures a possible sequence of handlers posted during the execution.

Suppose the current configuration is $(d, m)$. A non-deterministic scheduler picks one of the outstanding handlers $\sigma \in m$ and executes it. Executing $\sigma$ corresponds to picking one of the languages $L_{d \sigma d'}$ and some word $w \in L_{d \sigma d'}$. Upon execution of $\sigma$, the new configuration has global state $d'$ and the new bag of pending calls is obtained by taking $m$, removing an instance of $\sigma$ from it, and adding the Parikh image of $w$ to it. This reflects the current set of pending handler calls—the old ones (minus an instance of $\sigma$) together with the new ones added by executing $\sigma$. Note that a handler is executed atomically; thus, we atomically update the global state and the effect of executing the handler.
global var turn = ref 0 and x = ref 0;
let rec s1 () = if * then begin post a; s1 (); post b end
let rec s2 () = if * then begin post a; s2 (); post b end else post b
let a () = if ! turn = 0 then begin turn := 1; x := !x + 1 end else post a
let b () = if ! turn = 1 then begin turn := 0; x := !x - 1 end else post b
let s3 () = post s3; post s3
let c () = if !t = 0 then t := 1 else post c
let d () = if !t = 1 then t := 2 else post d
let f () = if !t = 2 then t := 0 else post f
let cc x = post c; x
let dd x = post d; x
let ff x = post f; x
let id x = x
let rec produce g x = if * then produce (h g) (ff x) else g x
let s4 () = produce id ()

Figure 1: Examples of asynchronous programs

Let us see some examples of asynchronous programs. It is convenient to present these examples in a programming language syntax, and to allow each handler to have internal actions that perform local tests and updates to the global state. As we describe informally below, and formally in Appendix A, when \( C \) is a full trio, internal actions can be “compiled away” by taking an intersection with a regular language of internal actions and projecting the internal actions away. Thus, we use our simpler model throughout.

Examples. Figure 1 shows some simple examples of asynchronous programs in an OCaml-like syntax. Consider first the asynchronous program in lines 1–5. The alphabet of handlers is \( s1, s2, a, \) and \( b \). The global states correspond to possible valuations to the global variables turn and \( x \); assuming turn is a Boolean and \( x \) takes values in \( \mathbb{N} \), we have that \( D = \{0, 1\} \times \{0, 1, \omega\} \), where \( \omega \) abstracts all values other than \( \{0, 1\} \). Since \( s1 \) and \( s2 \) do not touch any variables, for \( d, d' \in D \), we have \( L_{d,s1,d'} = \{a^n b^n | n \geq 0\} \), \( L_{d,s2,d'} = \{a^n b^{n+1} | n \geq 0\} \), and \( L_{d,s1,d'} = L_{d,s2,d'} = \emptyset \) if \( d' \neq d \).

For the languages corresponding to \( a \) and \( b \), we use syntactic sugar in the form of internal actions; these are local tests and updates to the global state. In our example, we have \( L_{(0,0),a,(1,1)} = \{\varepsilon\} \), \( L_{(1,x),a,(1,x)} = \{a\} \) for all values of \( x \), and similarly for \( b \). The meaning is that, starting from a global state \((0,0)\), executing the handler will lead to the global state \((1,1)\) and no handlers will be posted, whereas starting from a global state in which turn is 1, executing the handler will keep the global state unchanged but post an instance of \( a \). Note that all the languages are context-free.

Consider an execution of the program from the initial configuration \(((0,0), [s1])\). The execution of \( s1 \) puts \( n \) as and \( n \) bs into the bag, for some \( n \geq 0 \). The global variable turn is used to ensure that the handlers \( a \) and \( b \) alternate updating \( x \). When turn is 0, the handler for \( a \) increments \( x \) and sets turn to 1, otherwise it re-posts itself for a future execution. Likewise, when turn is 1, the handler for \( b \) decrements \( x \) and sets turn back to 0, otherwise
it re-posts itself for a future execution. As a result, the variable $x$ never grows beyond 1. Thus, the program satisfies the safety property that no execution sets $x$ to $\omega$.

It is possible that the execution goes on forever: for example, if $s1$ posts an $a$ and a $b$, and thereafter only $b$ is chosen by the scheduler. This is not an “interesting” infinite execution as it is not fair to the pending $a$. In the case of a fair scheduler, which eventually always picks an instance of every pending task, the program terminates: eventually all the $a$s and $b$s are consumed when they are scheduled in alternation. However, if instead we started with $[s2]$, the program will not terminate even under a fair scheduler: the last remaining $b$ will not be paired and will keep executing and re-posting itself forever.

Now consider the execution of $s3$. It has an infinite fair run, where the scheduler picks an instance of $s3$ at each step. However, the number of pending instances grows without bound. We shall study the boundedness problem, which checks if the bag can become unbounded along some run. We also study a stronger notion of fair termination, called fair termination, which asks that every instance of a posted handler is executed under any fair scheduler. The execution of $s3$ is indeed fair, but there can be a specific instance of $s3$ that is never picked: we say $s3$ can starve an instance.

The program in lines 9–20 is higher-order (produce and $h$ take functions as arguments). The language of $s4$ is the set $\{c^n a^n f^n \mid n \geq 0\}$, that is, it posts an equal number of $c$s, $d$s, and $f$s. It is an indexed language; we shall see (Section 5) how this and other higher-order programs can be represented using higher-order recursion schemes (HORS). Note that the OCaml types of produce : $(o \rightarrow o) \rightarrow o \rightarrow o$ and $h : (o \rightarrow o) \rightarrow o \rightarrow o$ are higher-order.

The program is similar to the first: the handlers $c$, $d$, and $f$ execute in “round robin” fashion using the global state $t$ to find their turns. Again, we use internal actions to update the global state for readability. We ask the same decision questions as before: does the program ever reach a specific global state and does the program have an infinite (fair) run? We shall see later that safety and termination questions remain decidable, whereas fair termination does not.

### 3. Decision Problems on Asynchronous Programs

We now describe decision problems on runs of asynchronous programs.

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**Runs, preruns, and downclosures.** A prerun of an AP $\mathcal{P} = (D, \Sigma, (L_c)_{c \in \Sigma}, d_0, m_0)$ is a finite or infinite sequence $\rho = (e_0, n_0), (e_1, n_1), (e_2, n_2), \ldots$ of alternating elements of tuples $(e_i, n_i) \in D \times M[\Sigma]$ and symbols $\sigma_i \in \Sigma$. The set of preruns of $\mathcal{P}$ will be denoted $\text{Preruns}(\mathcal{P})$. Note that if two asynchronous programs $\mathcal{P}$ and $\mathcal{P}'$ have the same $D$ and $\Sigma$, then $\text{Preruns}(\mathcal{P}) = \text{Preruns}(\mathcal{P}')$. The length, denoted $|\rho|$, of a finite prerun $\rho$ is the number of configurations in $\rho$. The $i^{th}$ configuration of a prerun $\rho$ will be denoted $\rho(i)$.

We define an order $\leq$ on preruns as follows: For preruns $\rho = (e_0, n_0), (e_1, n_1), (e_2, n_2), \ldots$ and $\rho' = (e'_0, n'_0), (e'_1, n'_1), (e'_2, n'_2), \ldots$, we define $\rho \preceq \rho'$ if $|\rho| = |\rho'|$ and $e_i = e'_i$, $\sigma_i = \sigma'_i$ and $n_i \leq n'_i$ for each $i \geq 0$. The downclosure $\downarrow R$ of a set $R$ of preruns of $\mathcal{P}$ is defined as $\downarrow R = \{\rho \in \text{Preruns}(\mathcal{P}) \mid \exists \rho' \in R. \rho \preceq \rho'\}$.

A run of an AP $\mathcal{P} = (D, \Sigma, (L_c)_{c \in \Sigma}, d_0, m_0)$ is a prerun $\rho = (d_0, m_0), (d_1, m_1), (d_2, m_2), \ldots$ starting with the initial configuration $(d_0, m_0)$, where for each $i \geq 0$, we have $(d_i, m_i) \xrightarrow{\sigma_{i+1}} (d_{i+1}, m_{i+1})$. The set of runs of $\mathcal{P}$ is denoted $\text{Runs}(\mathcal{P})$. Finally, $\downarrow \text{Runs}(\mathcal{P})$ is the downward closure of $\text{Runs}(\mathcal{P})$ with respect to $\preceq$. 
An infinite run \( c_0 \xrightarrow{\sigma_0} c_1 \xrightarrow{\sigma_1} \ldots \) is called fair if for all \( i \geq 0 \), if \( \sigma \in c_i.m \) then there is some \( j \geq i \) such that \( c_j \xrightarrow{\sigma} c_{j+1} \). That is, whenever an instance of a handler \( \sigma \) is posted, some instance of \( \sigma \) is executed later. Fairness does not guarantee that every specific instance of a handler is executed eventually. We say that an infinite fair run starves a handler \( \sigma \) if there exists an index \( J \geq 0 \) such that for each \( j \geq J \), we have (i) \( c_j.m(\sigma) \geq 1 \) and (ii) whenever \( c_j \xrightarrow{\sigma} c_{j+1} \), we have \( c_j.m(\sigma) \geq 2 \). In this case, even if the run is fair, a specific instance of \( \sigma \) may never be executed.

Now we give the definitions of the various decision problems.

**Definition 3.1** (Properties of finite runs). The Safety (Global state reachability) problem asks, given an asynchronous program \( \mathcal{P} \) and a global state \( d_f \in D \), is there a reachable configuration \( c \) such that \( c.d = d_f \)? If so, \( d_f \) is said to be reachable (in \( \mathcal{P} \)) and unreachable otherwise. The Boundedness (of the task buffer) problem asks, given an asynchronous program \( \mathcal{P} \), is there an \( N \in \mathbb{N} \) such that for every reachable configuration \( c \), we have \( |c.m| \leq N \)? If so, the asynchronous program \( \mathcal{P} \) is bounded; otherwise it is unbounded. The Configuration reachability problem asks, given an asynchronous program \( \mathcal{P} \) and a configuration \( c \), is \( c \) reachable?

**Definition 3.2** (Properties of infinite runs). All the following problems take as input an asynchronous program \( \mathcal{P} \). The Termination problem asks if all runs of \( \mathcal{P} \) are finite. The Fair Non-termination problem asks if \( \mathcal{P} \) has some fair infinite run. The Fair Starvation problem asks if \( \mathcal{P} \) has some fair run that starves some handler.

Our main result in this section shows that many properties of an asynchronous program \( \mathcal{P} \) only depend on the downclosure \( \downarrow \text{Runs}(\mathcal{P}) \) of the set \( \text{Runs}(\mathcal{P}) \) of runs of the program \( \mathcal{P} \). The proof is by induction on the length of runs. Please see Appendix B for details. For any AP \( \mathcal{P} = (D, \Sigma, (L_c)_{c \in \mathbb{E}}, d_0, m_0) \), we define the AP \( \downarrow \mathcal{P} = (D, \Sigma, (\downarrow L_c)_{c \in \mathbb{E}}, d_0, m_0) \), where \( \downarrow L_c \) is the downclosure of the language \( L_c \) under the subword order.

**Proposition 3.3.** Let \( \mathcal{P} = (D, \Sigma, (L_c)_{c \in \mathbb{E}}, d_0, m_0) \) be an asynchronous program. Then \( \downarrow \text{Runs}(\mathcal{P}) = \downarrow \text{Runs}(\downarrow \mathcal{P}) \). In particular, the following holds. (1) For every \( d \in D \), \( \mathcal{P} \) can reach \( d \) if and only if \( \downarrow \mathcal{P} \) can reach \( d \). (2) \( \mathcal{P} \) is terminating if and only if \( \downarrow \mathcal{P} \) is terminating. (3) \( \mathcal{P} \) is bounded if and only if \( \downarrow \mathcal{P} \) is bounded.

Intuitively, safety, termination, and boundedness is preserved when the multiset of pending handler instances is “lossy”: posted handlers can get lost. This corresponds to these handlers never being added to the task buffer. However, if a run demonstrates reachability of a global state, or non-termination, or unboundedness, in the lossy version, it corresponds also to a run in the original problem (and conversely).

In contrast, simple examples show that configuration reachability, fair termination, and fair non-starvation properties are not preserved under downclosures.

4. General Decidability Results

In this section, we characterize those full trios \( \mathcal{C} \) for which particular problems for asynchronous programs over \( \mathcal{C} \) are decidable. Our decision procedures will use the following theorem, summarizing the results from [GM12], as a subprocedure.

**Theorem 4.1** [GM12]. Safety, boundedness, configuration reachability, termination, fair non-termination, and fair non-starvation are decidable for asynchronous programs over regular languages.
Algorithm 1: Checking Safety

**Input:** Asynchronous program $\mathcal{P} = (D, \Sigma, (L_c)_{c \in \varepsilon}, d_0, m_0)$ over $\mathcal{C}$, state $d \in D$

run concurrently

```plaintext
begin
/* find a safe overapproximation */
foreach tuple $(A_c)_{c \in \varepsilon}$ of regular languages $A_c \subseteq \Sigma^*$ do
  if $L_c \cap (\Sigma^* \setminus A_c) = \emptyset$ for each $c \in \mathcal{C}$ then
    if $\mathcal{P}' = (D, \Sigma, (A_c)_{c \in \varepsilon}, d_0, m_0)$ does not reach $d$ then
      return $d$ is not reachable.
begin
/* find a run reaching $d$ */
foreach prerun $\rho$ of $\mathcal{P}$ do
  if $\rho$ is a run that reaches $d$ then
    return $d$ reachable.
```

4.1. Safety and termination. Our first main result concerns the problems of safety and termination.

**Theorem 4.2.** Let $\mathcal{C}$ be a full trio. The following are equivalent:

(i) Safety is decidable for asynchronous programs over $\mathcal{C}$.
(ii) Termination is decidable for asynchronous programs over $\mathcal{C}$.
(iii) Emptiness is decidable for $\mathcal{C}$.

**Proof.** We begin with “(i)⇒(iii)”. Let $K \subseteq \Sigma^*$ be given. We construct $\mathcal{P} = (D, \Sigma, (L_c)_{c \in \varepsilon}, d_0, m_0)$ such that $m_0 = [\sigma]$, $D = \{d_0, d_1\}$, $L_{d_0, \sigma, d_1} = K$ and $L_c = \emptyset$ for $c \neq (d_0, \sigma, d_1)$. We see that $\mathcal{P}$ can reach $d_1$ iff $K$ is non-empty. Next we show “(ii)⇒(iii)”.

Consider the alphabet $\Gamma = (\Sigma \cup \{\varepsilon\}) \times \{0, 1\}$ and the homomorphisms $g: \Gamma^* \rightarrow \Sigma^*$ and $h: \Gamma^* \rightarrow \{\sigma\}^*$, where for $x \in \Sigma \cup \{\varepsilon\}$, we have $g((x, i)) = x$ for $i \in \{0, 1\}$, $h((x, 1)) = \sigma$, and $h((x, 0)) = \varepsilon$. If $R \subseteq \Gamma^*$ is the regular set of words in which exactly one position belongs to the subalphabet $(\Sigma \cup \{\varepsilon\}) \times \{1\}$, then the language $K' := h(g^{-1}(K) \cap R)$ belongs to $\mathcal{C}$. Note that $K'$ is $\emptyset$ or $\{\sigma\}$, depending on whether $K$ is empty or not. We construct $\mathcal{P}' = (D, \{\sigma\}, (L_c)_{c \in \varepsilon}, d_0, m_0)$ with $D = \{d_0\}$, $m_0 = [\sigma]$, $L_{d_0, \sigma, d_0} = K'$ and all languages $L_c = \emptyset$ for $c \neq (d_0, \sigma, d_0)$. Then $\mathcal{P}'$ is terminating iff $K$ is empty.

To prove “(iii)⇒(i)”, we design an algorithm deciding safety assuming decidability of emptiness. Given asynchronous program $\mathcal{P}$ and state $d$ as input, the algorithm consists of two semi-decision procedures: one which searches for a run of $\mathcal{P}$ reaching the state $d$, and the second which enumerates regular overapproximations $\mathcal{P}'$ of $\mathcal{P}$ and checks the safety of $\mathcal{P}'$ using Theorem 4.1. Each $\mathcal{P}'$ consists of a regular language $A_c$ overapproximating $L_c$ for each context $c$ of $\mathcal{P}$. We use decidability of emptiness to check that $L_c \cap (\Sigma^* \setminus A_c) = \emptyset$ to ensure that $\mathcal{P}'$ is indeed an overapproximation.

Algorithm 1 clearly gives a correct answer if it terminates. Hence, we only have to argue that it always does terminate. Of course, if $d$ is reachable, the first semi-decision procedure will terminate. In the other case, termination is due to the regularity of downclosures: if $d$ is not reachable in $\mathcal{P}$, then Proposition 3.3 tells us that $\downarrow \mathcal{P}$ cannot reach $d$ either. But $\downarrow \mathcal{P}$ is an asynchronous program over regular languages; this means there exists a safe regular overapproximation and the second semi-decision procedure terminates.
Algorithm 2: Checking Termination

Input: Asynchronous program \( \Psi = (D, \Sigma, (L_c)_{c \in \mathcal{C}}, d_0, m_0) \) over \( \mathcal{C} \)
run concurrently

\[
\begin{align*}
\text{begin} & \quad \text{/* find a terminating overapproximation */} \\
\quad \text{foreach tuple } (A_c)_{c \in \mathcal{C}} \text{ of regular languages } A_c \subseteq \Sigma^* \text{ do} \\
\quad \quad \text{if } L_c \cap (\Sigma^* \setminus A_c) = \emptyset \text{ for each } c \in \mathcal{C} \text{ then} \\
\quad \quad \quad \text{return } \Psi \text{ terminates.}
\end{align*}
\]

\[
\begin{align*}
\text{begin} & \quad \text{/* find a self-covering run */} \\
\quad \text{foreach prerun } \rho \text{ of } \Psi \text{ do} \\
\quad \quad \text{if } \rho \text{ is a self-covering run then} \\
\quad \quad \quad \text{return } \Psi \text{ does not terminate.}
\end{align*}
\]

To prove “(iii)⇒(ii)”, we adopt a similar method as for safety. Algorithm 2 for termination consists of two semi-decision procedures. By standard well-quasi-ordering arguments, an infinite run of an asynchronous program \( \Psi \) is witnessed by a finite self-covering run. The first semi-decision procedure enumerates finite self-covering runs (trying to show non-termination). The second procedure enumerates regular asynchronous programs \( \Psi' \) that overapproximate \( \Psi \). As before, to check termination of \( \Psi' \), it applies the procedure from Theorem 4.1. Clearly, the algorithm's answer is always correct. Moreover, it gives an answer for every input. If \( \Psi \) does not terminate, it will find a self-covering sequence. If \( \Psi \) does terminate, then Proposition 3.3 tells us that \( \downarrow \Psi \) is a terminating finite-state overapproximation. This implies that the second procedure will terminate in that case.

Let us point out a particular example. The class \( \mathcal{L} \) of languages of lossy channel systems is defined like the class of languages of Well-Structured Transition Systems (WSTS) with upward-closed sets of accepting configurations as in [GRB07], except that we only consider lossy channel systems [ABJ98] instead of arbitrary WSTS. Then \( \mathcal{L} \) forms a full trio with decidable emptiness. Although downclosures of lossy channel languages are not effectively computable (an easy consequence of [May03]), our algorithm employs Theorem 4.2 to decide safety and termination.

4.2. Boundedness.

**Theorem 4.3.** Let \( \mathcal{C} \) be a full trio. The following are equivalent:

(i) Boundedness is decidable for asynchronous programs over \( \mathcal{C} \).

(ii) Finiteness is decidable for \( \mathcal{C} \).

**Proof.** Clearly, the construction for “(i)⇒(iii)” of Theorem 4.2 also works for “(i)⇒(ii)”: \( \Psi \) is unbounded iff \( K \) is infinite.

For the converse, we first note that if finiteness is decidable for \( \mathcal{C} \) then so is emptiness. Given \( L \subseteq \Sigma^* \) from \( \mathcal{C} \), consider the homomorphism \( h: (\Sigma \cup \{\lambda\})^* \to \Sigma^* \) with \( h(a) = a \) for every \( a \in \Sigma \) and \( h(\lambda) = \varepsilon \). Then \( h^{-1}(L) \) belongs to \( \mathcal{C} \) and \( h^{-1}(L) \) is finite if and only if \( L \) is empty: in the inverse homomorphism, \( \lambda \) can be arbitrarily inserted in any word. By Theorem 4.2, this implies that we can also decide safety. As a consequence of considering only full trios, it is easy to see that the problem of context reachability reduces to safety: a
Algorithm 3: Checking Boundedness

Input: Asynchronous program \( \mathcal{P} = (D, \Sigma, (L_c)_{c \in \mathcal{C}}, d_0, m_0) \) over \( \mathcal{C} \)

\[ F \leftarrow \emptyset, I \leftarrow \emptyset; \]

\textbf{foreach} context \( c = (d, \sigma, d') \in \mathcal{C} \) \textbf{do}

\textbf{if} \( L_c \) is infinite \textbf{then}

\textbf{if} \( c \) is reachable in \( \mathcal{P} \) \textbf{then}

\hspace{1em} /* using algorithm for safety */

\hspace{1em} \textbf{return} \( \mathcal{P} \) is unbounded.

\hspace{1em} \( I \leftarrow I \cup \{c\} \)

\textbf{else}

\hspace{1em} \( F \leftarrow F \cup \{c\} \)

\textbf{foreach} context \( c \in F \) \textbf{do}

\hspace{1em} \textbf{foreach} finite set \( A \subseteq \Sigma^* \) \textbf{do}

\hspace{2em} /* find a finite \( A \) with \( L_c = A \) */

\hspace{3em} \textbf{if} \( L_c \cap (\Sigma^* \setminus A) = \emptyset \) \textbf{and} \( L_c \cap \{w\} \neq \emptyset \) \textbf{for each} \( w \in A \) \textbf{then}

\hspace{4em} \( A_c \leftarrow A; \)

\hspace{4em} \textbf{break};

\hspace{4em} /* end inner foreach */

\textbf{foreach} context \( c \in I \) \textbf{do}

\hspace{1em} \( A_c \leftarrow \emptyset \)

\hspace{1em} \textbf{if} \( \mathcal{P}' = (D, \Sigma, (A_c)_{c \in \mathcal{C}}, d_0, m_0) \) \textbf{is bounded} \textbf{then}

\hspace{2em} \textbf{return} \( \mathcal{P} \) is bounded.

\textbf{else}

\hspace{2em} \textbf{return} \( \mathcal{P} \) is unbounded.

context \( c = (d, \sigma, d') \in \mathcal{C} \) is reachable in \( \mathcal{P} \) if there is a reachable configuration \( (d, m) \) in \( \mathcal{P} \) with \( m(\sigma) \geq 1 \). This latter condition can be checked by moving from \( d \) to a special state via a \( \sigma \) transition.

We now explain Algorithm 3 for deciding boundedness of a given asynchronous program \( \mathcal{P} = (D, \Sigma, (L_c)_{c \in \mathcal{C}}, d_0, m_0) \). For every context \( c \), we first check if \( L_c \) is infinite (feasible by assumption). This partitions the set of contexts of \( \mathcal{P} \) into sets \( I \) and \( F \) which are the contexts for which the corresponding language \( L_c \) is infinite and finite respectively. If any context in \( I \) is reachable, then \( \mathcal{P} \) is unbounded. Otherwise, all the reachable contexts have a finite language. For every finite language \( L_c \) for some \( c \in F \), we explicitly find all the members of \( L_c \). This is possible because any finite set \( A \) can be checked with \( L_c \) for equality. \( L_c \subseteq A \) can be checked by testing whether \( L_c \cap (\Sigma^* \setminus A) = \emptyset \) and \( L_c \cap \{w\} \neq \emptyset \) for each \( w \in A \), which can be done the same way. We can now construct asynchronous program \( \mathcal{P}' \) which replaces all languages for contexts in \( I \) by \( \emptyset \) and replaces those corresponding to \( F \) by the explicit description. Clearly \( \mathcal{P}' \) is bounded iff \( \mathcal{P} \) is bounded (since no contexts from \( I \) are reachable) and the former can be decided by Theorem 4.1.

We observe that boundedness is strictly harder than safety or termination: There are full trios for which emptiness is decidable, but finiteness is undecidable, such as the languages of reset vector addition systems [DFS98] (see [TZ19] for a definition of the language class) and languages of lossy channel systems.

4.3. Configuration reachability and liveness properties. Theorems 4.2 and 4.3 completely characterize for which full trios safety, termination, and boundedness are decidable.
We turn to configuration reachability, fair termination, and fair starvation. We suspect that it is unlikely that there is a simple characterization of those language classes for which the latter problems are decidable. However, we show that they are decidable only for a limited range of infinite-state systems. To this end, we prove that decidability of any of these problems implies (a) decidability of the others as well, and also implies (b) the decidability of a simple combinatorial problem (called the Z-intersection problem) that is known to be undecidable for many expressive classes of languages.

Let \( Z \subseteq \{a, b\}^* \) be the language \( Z = \{w \in \{a, b\}^* \mid |w|_a = |w|_b \} \). The Z-intersection problem for a language class \( \mathcal{C} \) asks, given a language \( K \subseteq \{a, b\}^* \) from \( \mathcal{C} \), whether \( K \cap Z \neq \emptyset \). Informally, \( Z \) is the language of all words with an equal number of as and bs and the Z-intersection problem asks if there is a word in \( K \) with an equal number of as and bs.

**Theorem 4.4.** Let \( \mathcal{C} \) be a full trio. The following statements are equivalent:

(i) Configuration reachability is decidable for asynchronous programs over \( \mathcal{C} \).

(ii) Fair termination is decidable for asynchronous programs over \( \mathcal{C} \).

(iii) Fair starvation is decidable for asynchronous programs over \( \mathcal{C} \).

Moreover, if decidability holds, then Z-intersection is decidable for \( \mathcal{C} \).

**Proof.** We prove Theorem 4.4 by providing reductions among the three problems and showing that Z-intersection reduces to configuration reachability. We use diagrams similar to automata to describe asynchronous programs. Here, circles represent global states of the program and we draw an edge

\[
\text{ in case we have } L_{d,\sigma,d'} = L \text{ in our asynchronous program } \mathcal{P}. \]

Furthermore, we have \( L_{d,\sigma,d'} = \emptyset \) whenever there is no edge that specifies otherwise. To simplify notation, we draw an edge \( \text{ in an asynchronous program for a word } w \in \Sigma^*, w = \sigma_1 \ldots \sigma_n \) with \( \sigma_1, \ldots, \sigma_n \in \Sigma \), to symbolize a sequence of states

\[
\text{which removes } [\sigma_1, \ldots, \sigma_n] \text{ from the task buffer and posts a multiset of handlers specified by the language } L. \]

**Proof of “(ii)⇒(i)”** Given an asynchronous program \( \mathcal{P} = (D, \Sigma, (L_c)_{c \in \mathcal{C}}, d_0, m_0) \) and a configuration \( (d_f, m_f) \in D \times M[\Sigma] \), we construct asynchronous program \( \mathcal{P}' \) as follows. Let \( z \) be a fresh letter and let \( m_f = [\sigma_1, \ldots, \sigma_n] \). We obtain \( \mathcal{P}' \) from \( \mathcal{P} \) by adding a new state \( d'_f \) and including the following edges:

\[
\text{Starting from } (d_0, m_0 \oplus [z]), \text{ the program } \mathcal{P}' \text{ has a fair infinite run iff } (d_f, m_f) \text{ is reachable in } \mathcal{P}. \text{ The ‘if’ direction is obvious. Conversely, } z \text{ has to be executed in any fair run } \rho \text{ of } \mathcal{P}' \text{ which implies that } d'_f \text{ is reached by } \mathcal{P}' \text{ in } \rho. \text{ Since only } z \text{ can be executed at } d'_f \text{ in } \rho, \text{ this means that the multiset is exactly } m_f \text{ when } d'_f \text{ is reached during } \rho. \text{ Clearly this initial segment of } \rho \text{ corresponds to a run of } \mathcal{P} \text{ which reaches the target configuration.} \]

**Proof of “(iii)⇒(ii)”** We construct \( \mathcal{P}' = (D, \Sigma', (L'_c)_{c \in \mathcal{C}'}, d_0, m_0') \) given

\[
\mathcal{P} = (D, \Sigma, (L_c)_{c \in \mathcal{C}}, d_0, m_0) \]
over \( C \) as follows. Let \( \Sigma' = \Sigma \cup \{ \mathbf{s} \} \), where \( \mathbf{s} \) is a fresh handler. Replace each edge
\[
d \xrightarrow{\sigma|L} d' \quad \text{by} \quad d \xrightarrow{\sigma|L \cup L_s} d' \xrightarrow{\mathbf{s}|\varepsilon}
\]
at every state \( d \in D \). Moreover, we set \( m'_0 = m_0 \oplus [\mathbf{s}] \). Then \( \mathcal{P}' \) has an infinite fair run that starves some handler if and only if \( \mathcal{P} \) has an infinite fair run. From an infinite fair run \( \rho \) of \( \mathcal{P} \), we obtain an infinite fair run of \( \mathcal{P}' \) which starves \( \mathbf{s} \), by producing \( \mathbf{s} \) while simulating \( \rho \) and consuming it in the loop. Conversely, from an infinite fair run \( \rho' \) of \( \mathcal{P}' \) which starves some \( \tau \), we obtain an infinite fair run \( \rho \) of \( \mathcal{P} \) by omitting all productions and consumptions of \( \mathbf{s} \) and removing two extra instances of \( \mathbf{s} \) from all configurations.

Proof of “(i)⇒(iii)” From \( \mathcal{P} = (D, \Sigma, (L_c)_{c \in \mathcal{E}}, d_0, m_0) \) over \( C \), for each subset \( \Gamma \subseteq \Sigma \) and \( \tau \in \Sigma \), we construct an asynchronous program \( \mathcal{P}_{\Gamma, \tau} = (D', \Sigma', (L'_c)_{c \in \mathcal{E}'}, d'_0, m'_0) \) over \( C \) such that a particular configuration is reachable in \( \mathcal{P}_{\Gamma, \tau} \) if and only if \( \mathcal{P} \) has a fair infinite run \( \rho_{\Gamma, \tau} \), where \( \Gamma \) is the set of handlers that is executed infinitely often in \( \rho_{\Gamma, \tau} \) and \( \rho_{\Gamma, \tau} \) starves \( \tau \). Since there are only finitely many choices for \( \Gamma \) and \( \tau \), decidability of configuration reachability implies decidability of fair starvation. The idea is that a run \( \rho_{\Gamma, \tau} \) exists if and only if there exists a run
\[
(d_0, m_0) \xrightarrow{\sigma_1} \cdots \xrightarrow{\sigma_n} (d_n, m_n) = (e_0, n_0) \xrightarrow{\gamma_1} (e_1, n_1) \xrightarrow{\gamma_2} \cdots \xrightarrow{\gamma_k} (e_k, n_k),
\]
(4.1)
where \( \bigcup_{i=1}^k \{ \gamma_i \} = \Gamma \), for each \( 1 \leq i \leq k \) \( n_i \in \mathcal{M}(\Gamma) \), \( m_n \leq n_k \), and for each \( i \in \{ 1, \ldots, k \} \) with \( \gamma_i = \tau \), we have \( n_{i-1}(\tau) \geq 2 \). In such a run, we call \( (d_0, m_0) \xrightarrow{\sigma_1} \cdots \xrightarrow{\sigma_n} (d_n, m_n) \) its first phase and \( (e_0, n_0) \xrightarrow{\gamma_1} \cdots \xrightarrow{\gamma_k} (e_k, n_k) \) its second phase.

Let us explain how \( \mathcal{P}_{\Gamma, \tau} \) reflects the existence of a run as in Eq. (4.1). The set \( \Sigma' \) of handlers of \( \mathcal{P}_{\Gamma, \tau} \) includes \( \Sigma, \bar{\Sigma} \) and \( \hat{\Sigma} \), where \( \hat{\Sigma} = \{ \sigma \mid \sigma \in \Sigma \} \) and \( \bar{\Sigma} = \{ \hat{\sigma} \mid \sigma \in \Sigma \} \) are disjoint copies of \( \Sigma \). This means, a multiset \( M(\Sigma') \) contains multisets \( m' = m \oplus \hat{m} \oplus \bar{m} \) with \( m \in M(\Sigma), \ \hat{m} \in M(\hat{\Sigma}), \) and \( \bar{m} \in M(\bar{\Sigma}) \). A run of \( \mathcal{P}_{\Gamma, \tau} \) simulates the two phases of \( \rho \). While simulating the first phase, \( \mathcal{P}_{\Gamma, \tau} \) keeps two copies of the task buffer, \( m \) and \( \hat{m} \). The copying is easily accomplished by a homomorphism with \( \sigma \mapsto \hat{\sigma} \bar{\sigma} \) for each \( \sigma \in \Sigma \). At some point, \( \mathcal{P}_{\Gamma, \tau} \) switches into simulating the second phase. There, \( \bar{m} \) remains unchanged, so that it stores the value of \( m_n \) in Eq. (4.1) and can be used in the end to make sure that \( m_n \leq n_k \).

Hence, in the second phase, \( \mathcal{P}_{\Gamma, \tau} \) works, like \( \mathcal{P} \), only with \( \Sigma \). However, whenever a handler \( \sigma \in \Sigma \) is executed, it also produces a task \( \hat{\sigma} \). These handlers are used at the end to make sure that every \( \gamma \in \Gamma \) has been executed at least once in the second phase. Also, whenever \( \tau \) is executed, \( \mathcal{P}_{\Gamma, \tau} \) checks that at least two instances of \( \tau \) are present in the task buffer, thereby ensuring that \( \tau \) is starved.

In the end, a distinguished final state allows \( \mathcal{P}_{\Gamma, \tau} \) to execute handlers in \( \Gamma \) and \( \hat{\Gamma} \) simultaneously to make sure that \( m_n \leq n_k \). In its final state, \( \mathcal{P}_{\Gamma, \tau} \) can execute handlers \( \hat{\gamma} \in \hat{\Gamma} \) and \( \gamma \in \Gamma \) (without creating new handlers). In the final configuration, there can be no \( \hat{\sigma} \) with \( \sigma \in \Sigma \setminus \Gamma \), and there has to be exactly one \( \hat{\gamma} \) for each \( \gamma \in \Gamma \). This guarantees that (i) each handler in \( \Gamma \) is executed at least once during the second phase, (ii) every handler executed in the second phase is from \( \Gamma \), and (iii) \( m_n \) contains only handlers from \( \Gamma \) (because handlers from \( \Sigma \) cannot be executed in the second phase).

Let us now describe \( \mathcal{P}_{\Gamma, \tau} \) in detail. We have \( \Sigma' = \Sigma \cup \hat{\Sigma} \cup \bar{\Sigma} \cup \{ \mathbf{z} \} \), where \( \mathbf{z} \) is a fresh letter. The set of states is \( D' = D \cup \bar{D} \cup \{ d_f \} \), where \( \bar{D} = \{ d \mid d \in D \} \) is a disjoint copy of \( D \) for simulating the second phase, and \( d_f \) is a fresh state. Moreover, we change the languages
$L_c$ in the following way:

\[
\begin{align*}
    d & \xrightarrow{\sigma \mid L} d' \\
    & \sim
    \begin{cases}
        (d, \sigma \mid L) & \text{if } \sigma \neq \tau \\
        (d, \tau \mid L\tau) & \text{if } \sigma = \tau
    \end{cases}
\end{align*}
\]

where $h^\pm : \Sigma^* \to (\Sigma \cup \bar{\Sigma})^*$ is the homomorphism with $\sigma \mapsto \sigma \bar{\sigma}$ for every $\sigma \in \Sigma$. This means, for every context $c = (d, \sigma, d')$, we have $d \xrightarrow{\sigma \mid h^\pm(L)} d'$ in $\mathcal{P}_{\Gamma, \tau}$. Note that since $C$ is a full trio, it is in particular closed under homomorphisms. Hence, $h^\pm(L)$ belongs to $C$. Moreover, $\mathcal{P}_{\Gamma, \tau}$ can spontaneously switch to simulating the second phase: For each $d \in D$, we have

\[
\begin{array}{c}
    d \\
\longrightarrow
\end{array}
\]

Here, the handler $z$ merely allows us to move to state $\bar{d}$ without interfering with the other handlers. In the second phase, $\mathcal{P}_{\Gamma, \tau}$ simulates $\mathfrak{P}$ slightly differently. We perform the following replacement:

\[
\begin{align*}
    (d, \sigma \mid L) & \sim
    \begin{cases}
        (d, \sigma \mid L\bar{\sigma}) & \text{if } \sigma \neq \tau \\
        (d, \tau \mid L\tau) & \text{if } \sigma = \tau
    \end{cases}
\end{align*}
\]

Note that since $C$ is a full trio, the languages $L_v = \{uv \mid u \in L\}$ belong to $C$ for every word $v$. Finally, $\mathcal{P}_{\Gamma, \tau}$ can spontaneously switch to its distinguished state $d_f$, so that we have for every $\bar{d} \in \bar{D}$ and every $\gamma \in \Gamma$:

\[
\begin{align*}
    \gamma \gamma & \mid \{\varepsilon\} \\
\end{align*}
\]

The initial configuration of $\mathcal{P}_{\Gamma, \tau}$ is $(d_0, m_0')$, where $m_0' = m_0 \oplus \bar{m}_0 \oplus \{z\}$, where $\bar{m}_0$ is obtained from $m_0$ by replacing every occurrence of $\sigma \in \Sigma$ in $m_0$ with $\bar{\sigma}$. The final configuration is $(d_f, m_f)$, where $m_f \in M[\Gamma]$ is the multiset with $m_f(\gamma) = 1$ for every $\gamma \in \Gamma$. It is now straightforward to check that $(d_f, m_f)$ is reachable in $\mathcal{P}_{\Gamma, \tau}$ if and only if $\mathfrak{P}$ has an infinite fair run that starves $\tau$.

**Decidability of $Z$-intersection** To complete the proof of Theorem 4.4, we reduce $Z$-intersection to configuration reachability. Given $K \subseteq \{a, b\}^*$ from $C$, we construct the asynchronous program $\mathfrak{P} = (D, \Sigma, (L_c)_{c \in \mathfrak{C}}, d_0, m_0)$ over $C$ where $D = \{d_0, 0, 1\}$, $\Sigma = \{a, b, c\}$, by including the following edges:

\[
\begin{array}{c}
    d_0 \\
\longrightarrow
\end{array}
\]

The initial task buffer is $m_0 = \{c\}$. Then clearly, the configuration $(0, \square)$ is reachable in $\mathfrak{P}$ if and only if $K \cap Z \neq \emptyset$.

If the construction seems abstract, recall the example from Section 2: the procedure $s1$ plays the role of $K$ and generates strings from its language in $\{a, b\}^*$. The procedures $a$ and $b$ take turns to ensure there is an equal number of them; the states 0 and 1 are values of turn.

\[
\square
\]
Theorem 4.4 is useful in the contrapositive to show undecidability. For example, one can show undecidability of $Z$-intersection for languages of lossy channel systems (see Section 4.1): One expresses reachability in a non-lossy FIFO system by making sure that the numbers of enqueue- and dequeue-operations match. Thus, for asynchronous programs over lossy channel systems, the problems of Theorem 4.4 are undecidable. We also use Theorem 4.4 in Section 5 to conclude undecidability for higher-order asynchronous programs, already at order 2.

5. Higher-Order Asynchronous Programs

We apply our general decidability results to asynchronous programs over (deterministic) higher-order recursion schemes (HORS). Kobayashi [Kob09] has shown how higher-order functional programs can be modeled using HORS. In his setting, a program contains instructions that access certain resources. The path language of the HORS produced by Kobayashi is the set of possible sequences of instructions. For us, the input program contains \texttt{post} instructions (which post handlers i.e. add tasks to the task buffer) and we translate higher-order programs with \texttt{post} instructions into a HORS whose path language is used as the language of handlers.

We recall some definitions from [Kob09]. The set of \texttt{types} is defined by the grammar $A ::= \sigma | A \rightarrow A$. The \texttt{order} $\text{ord}(A)$ of a type $A$ is inductively defined as $\text{ord}(\sigma) = 0$ and $\text{ord}(A \rightarrow B) := \max(\text{ord}(A) + 1, \text{ord}(B))$. The \texttt{arity} of a type is inductively defined by $\text{arity}(\sigma) = 0$ and $\text{arity}(A \rightarrow B) = \text{arity}(B) + 1$. We assume a countably infinite set $\text{Var}$ of typed variables $x : A$. For a set $\Theta$ of typed symbols, the set $\Theta$ of \texttt{terms} generated from $\Theta$ is the least set which contains $\Theta$ such that whenever $s : A \rightarrow B$ and $t : A$ belong to $\Theta$, then also $st : B$ belongs to $\Theta$. By convention the type $\sigma \rightarrow \ldots (\sigma \rightarrow (\sigma \rightarrow \sigma))$ is written $\sigma \rightarrow \ldots \rightarrow \sigma \rightarrow \sigma$ and the term $((t_1t_2)t_3\cdots)t_n$ is written $t_1t_2\cdots t_n$. We write $\bar{x}$ for a sequence $(x_1, x_2, \ldots, x_n)$ of variables.

A higher-order recursion scheme (HORS) is a tuple $\mathcal{S} = (\Sigma, N, R, S)$ where $\Sigma$ is a set of typed \texttt{terminal} symbols of types of order 0 or 1, $N$ is a set of typed \texttt{non-terminal} symbols (disjoint from terminal symbols), $S : \sigma$ is the start non-terminal symbol and $R$ is a set of rewrite rules $Fx_1x_2\cdots x_n \rightarrow t$ where $F : A_1 \rightarrow \cdots \rightarrow A_n \rightarrow \sigma$ is a non-terminal in $N$, $x_i : A_i$ for all $i$ are variables and $t : \sigma$ is a term generated from $\Sigma \cup N \cup \{x_1, x_2, \ldots, x_n\}$. The order of a HORS is the maximum order of a non-terminal symbol. We define a rewrite relation $\rightarrow$ on terms over $\Sigma \cup N$ as follows: $F\bar{x} \rightarrow t[\bar{x}/\bar{a}]$ if $F\bar{x} \rightarrow t \in R$, and if $t \rightarrow t'$ then $ts \rightarrow t's$ and $st \rightarrow st'$. The reflexive, transitive closure of $\rightarrow$ is denoted $\rightarrow^*$. A \texttt{sentential form} $t$ of $\mathcal{S}$ is a term over $\Sigma \cup N$ such that $S \rightarrow^* t$.

If $N$ is the maximum arity of a symbol in $\Sigma$, then a (possibly infinite) tree over $\Sigma$ is a partial function $tr$ from $\{0, 1, \ldots, N - 1\}$ to $\Sigma$ that fulfills the following conditions: $\varepsilon \in \text{dom}(tr)$, $\text{dom}(tr)$ is closed under prefixes, and if $tr(w) = a$ and $\text{arity}(a) = k$ then $\{j \mid wj \in \text{dom}(tr)\} = \{0, 1, \ldots, k - 1\}$.

A \texttt{deterministic} HORS is one where there is exactly one rule of the form $Fx_1x_2\cdots x_n \rightarrow t$ for every non-terminal $F$. Following [Kob09], we show how a deterministic HORS can be used to represent a higher-order pushdown language arising from a higher-order functional program. Sentential forms can be seen as trees over $\Sigma \cup N$. A sequence $\Pi$ over $\{0, 1, \ldots, N - 1\}$ is a \texttt{path} of $tr$ if every finite prefix of $\Pi \in \text{dom}(tr)$ and $\Pi$ is maximal. The set of finite paths in a tree $tr$ will be denoted Paths$(tr)$. Unless otherwise specified, a path is finite in our
context. Associated with any path \( \Pi = n_1, n_2, \ldots, n_k \) is the word

\[ w_\Pi = tr(n_1)tr(n_1n_2)\cdots tr(n_1n_2\cdots n_k). \]

We will also write \( w_\Pi(\bar{m}) \) to denote the letter \( tr(\bar{m}) \) where \( \bar{m} = n_1n_2\ldots n_l \) for some \( l \leq k \).

We associate a value tree \( T_\mathcal{S} \) associated with a deterministic HORS \( \mathcal{S} \) in the following way. For a sentential form \( t \) define the finite tree \( t^\perp \) by case analysis: \( t^\perp = f \) if \( f \) is a terminal symbol and \( t^\perp = t_1t_2^\perp \) if \( t = t_1t_2 \) and \( t_1^\perp \neq \perp \), and \( t^\perp = \perp \) otherwise.

Intuitively, the tree \( t^\perp \) is obtained by replacing any subterm whose root is a non-terminal by the nullary symbol \( \perp \). We define the partial order \( \leq_t \) on \( \text{dom}(\Sigma) \cup \{ \perp \} \) by \( \perp \leq_t a \) for all \( a \in \text{dom}(\Sigma) \), which is extended to trees by

\[ t \leq_t s \iff \forall \bar{n} \in \text{dom}(t): \bar{n} \in \text{dom}(s) \land t(\bar{n}) \leq_t s(\bar{n}) \]

The value tree generated by a HORS \( \mathcal{S} \) is denoted \( T_\mathcal{S} \) and is obtained by repeated application of the rewrite rules to the start symbol \( S \). To make this formal, we write \( \text{lub}(T) \) for the least upper bound of a collection \( T \) of trees with respect to the order \( \leq_t \). Then we set \( T_\mathcal{S} := \text{lub}(\{t^\perp \mid S \rightarrow^* t\}) \). Any HORS for which the value tree \( T_\mathcal{S} \) does not contain any \( \perp \) symbol is called productive. All HORS dealt with in this paper are assumed to be productive. Checking if a given HORS is productive is decidable, see, e.g., [Gre16].

Let \( \Sigma_1 := \{a \in \Sigma \mid \text{arity}(a) = 1\} \). The path language \( L_p(\mathcal{S}) \) of a deterministic HORS \( \mathcal{S} \) is defined as \( \{\text{Proj}_{\Sigma_1}(w_n) \mid \Pi \in \text{Paths}(T_\mathcal{S})\} \). The tree language \( L_t(\mathcal{S}) \) associated with a (nondeterministic) HORS \( \mathcal{S} \) is the set of sentential forms of \( \mathcal{S} \) which contain only labels from \( \Sigma \).

The deterministic HORS corresponding to the higher-order function \( s4 \) from Figure 1 is given by \( \mathcal{S} = (\Sigma, \mathcal{N}, \mathcal{R}, S) \), where

\begin{align*}
\Sigma &= \{\text{br} : o \rightarrow o \rightarrow o, c, d, f : o \rightarrow o, e : o\} \\
\mathcal{N} &= \{S : o, F : (o \rightarrow o) \rightarrow o \rightarrow o, H : (o \rightarrow o) \rightarrow o \rightarrow o, I : o \rightarrow o\} \\
\mathcal{R} &= \{S \rightarrow FI e, I x \rightarrow x, F G x \rightarrow \text{br}(F (H G) (fx)) (G x), H G x \rightarrow c(G(dx))\}
\end{align*}

The path language \( L_p(\mathcal{S}) = \{c^n d^n f^n \mid n \geq 0\} \). To see this, apply the reduction rules to get the value tree \( T_\mathcal{S} \) shown on the right:

\[ S \rightarrow FI e \rightarrow \text{br} (F (H I) (fe)) (Ie) \]
\[ \rightarrow \text{br} (F (H I) (fe)) e \]
\[ \rightarrow \text{br} (F (H I) (fe)) (HI(fe)) (Ie) \]
\[ \rightarrow \text{br} (F (H I) (fe)) (c(I(d(fe)))) (Ie) \]
\[ \rightarrow \text{br} (F (H I) (fe)) (c(d(fe))) (Ie) \]
\[ \rightarrow \cdots \]

A HORS \( \mathcal{S} \) is called a word scheme if it has exactly one nullary terminal symbol \( e \) and all other terminal symbols \( \tilde{\Sigma} \) are of arity one. The word language \( L_w(\mathcal{S}) \subseteq \Sigma^* \) defined by \( \mathcal{S} \) is \( L_w(\mathcal{S}) = \{a_1a_2\cdots a_n \mid (a_1a_2\cdots (a_n(e)) \cdots) \in L_t(\mathcal{S})\} \). We denote by \( \mathcal{H} \) the class of languages \( L_w(\mathcal{S}) \) that occur as the word language of a higher-order recursion scheme \( \mathcal{S} \). Note that path languages and languages of word schemes are both word languages.
over the set $\tilde{\Sigma}$ of unary symbols considered as letters. They are connected by the following proposition, a proof of which is given in Appendix C.2

**Proposition 5.1.** For every order-$n$ HORS $\mathcal{S} = (\Sigma, N, S, R)$ there exists an order-$n$ word scheme $\mathcal{S}' = (\Sigma', N', S', R')$ such that $L_p(\mathcal{S}) = L_w(\mathcal{S}')$.

A consequence of [Kob09] and Prop. 5.1 is that the “post” language of higher-order functional programs (i.e. the language corresponding to sequences of newly spawned tasks) can be modeled as the language of a word scheme. Hence, we define an *asynchronous program over HORS* as an asynchronous program over the language class $\mathcal{H}$ and we can use the following results on word schemes.

**Theorem 5.2.** HORS and word schemes form effective full trios [CPSW16]. Emptiness [KO11] and finiteness [Par17] of order-$n$ word schemes are $(n - 1)$-EXPTIME-complete.

Now Theorems 4.2 and 4.3, together with Theorem 5.1 imply the decidability results in Corollary 5.3. The undecidability result is a consequence of Theorem 4.4 and the undecidability of the $Z$-intersection problem for indexed languages or equivalently, order-2 pushdown automata as shown in [Zet15]. Order-2 pushdown automata can be effectively turned into order-2 OI grammars [DG86], which in turn can be translated into order-2 word schemes [Dam82]. A direct (and independent) proof of undecidability of the $Z$-intersection problem for order-2 word schemes has appeared in [Kob19, Theorem 4].

**Corollary 5.3.** For asynchronous programs over HORS: (1) Safety, termination, and boundedness are decidable. (2) Configuration reachability, fair termination, and fair starvation are undecidable already at order-2.

### 6. A Direct Algorithm and Complexity Analysis

We say that *downclosures are computable* for a language class $\mathcal{C}$ if for a given description of a language $L$ in $\mathcal{C}$, one can compute an automaton for the regular language $\downarrow L$. A consequence of Proposition 3.3 and Theorem 4.1 is that if one can compute downclosures for some language class, then one can avoid the enumerative approaches of Section 4 and get a “direct algorithm.” The direct algorithm replaces each handler by its downclosure and then invokes the decision procedure summarized in Theorem 4.1.

#### 6.1. Higher-Order Recursion Schemes

The direct algorithm for asynchronous programs over HORS relies on the recent breakthrough results on computing downclosures.

**Theorem 6.1** [Zet15, HKO16, CPSW16]. Downclosures are effectively computable for $\mathcal{H}$.

Unfortunately, current techniques do not yet provide a complexity upper bound based on the above theorem. To understand why, we describe the current algorithm for computing downclosures. In [Zet15], it was shown that in a full trio $\mathcal{C}$, downclosures are computable if and only if the *diagonal problem* for $\mathcal{C}$ is decidable. The latter asks, given a language $L \subseteq \Sigma^*$, whether for every $k \in \mathbb{N}$, there is a word $w \in L$ with $|w|_\sigma \geq k$ for every $\sigma \in \Sigma$. The diagonal problem was then shown to be decidable for higher-order pushdown automata [HKO16] and

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2The models of HORS (used in model checking higher-order programs [Kob09]) and word schemes (used in language-theoretic exploration of downclosures [HKO16, CPSW16]) are somewhat different. Thus, we show an explicit reduction between the two formalisms.
then for word schemes [CPSW16]. However, the algorithm from [Zet15] to compute downclosures using an oracle for the diagonal problem employs enumeration to compute a downclosure automaton. Thus, the enumeration is hidden inside the downclosure computation.

We conjecture that downclosures can in fact be computed in elementary time (for word schemes of fixed order). This would imply an elementary time procedure for asynchronous programs over HORS as well.

6.2. Context Free Languages. For handlers over context-free languages, given e.g., as pushdown automata, Ganty and Majumdar show an \textsc{ExpSpace} upper bound. Precisely, the algorithm of [GM12] constructs for each handler a polynomial-size Petri net with certain guarantees (forming a so-called adequate family of Petri nets) that accepts a Parikh equivalent language. These Petri nets are then used to construct a larger Petri net, polynomial in the size of the asynchronous program and the adequate family of Petri nets, in which the respective property (safety, boundedness, or termination) can be phrased as a query decidable in \textsc{ExpSpace}. A natural question is whether there is a downclosure-based algorithm with the same asymptotic complexity.

Our goal is to replace the Parikh-equivalent Petri nets with Petri nets recognizing the downclosure of a language. It is an easy consequence of Proposition 3.3 that the resulting Petri nets can be used in place of the adequate families of Petri nets in the procedures for safety, termination, and boundedness of [GM12]. Thus, if we can ensure these Petri nets are polynomial in the size of the handler, we get an \textsc{ExpSpace} upper bound. Unfortunately, a finite automaton for \(\downarrow L\) may require exponentially many states in the pushdown automaton [BLS15]. Thus a naive approach gives a \(2\text{ExpSpace}\) upper bound.

We show here that for each context-free language \(L\), one can construct in polynomial time a 1-bounded Petri net accepting \(\downarrow L\). (Recall that a Petri net is 1-bounded if every reachable marking has at most one token in each place.) This is a language theoretic result of independent interest. When used in the construction of [GM12], this matches the \textsc{ExpSpace} upper bound.

As a byproduct, our translation yields a simple direct construction of a finite automaton for \(\downarrow L\) when \(L\) is given as a pushdown automaton. This is of independent interest because earlier constructions of \(\downarrow L\) always start from a context-free grammar and produce (of necessity!) exponentially large NFAs [vL78, Cou91, BLS15].

We begin with some preliminary definitions.

**Pushdown automata.** If \(\Gamma\) is an alphabet, we write \(\bar{\Gamma} = \{\bar{\gamma} \mid \gamma \in \Gamma\}\). Moreover, if \(x = \bar{\gamma}\), then we define \(\bar{x} = \gamma\). For a word \(v \in (\Gamma \cup \bar{\Gamma})^*\), \(v = v_1 \cdots v_n\), \(v_1, \ldots, v_n \in \Gamma \cup \bar{\Gamma}\), we set \(\bar{v} = \bar{v}_n \cdots \bar{v}_1\). A pushdown automaton is a tuple \(A = (Q, \Sigma, \Gamma, E, q_0, q_f)\), where \(Q\) is a finite set of states, \(\Sigma\) is its input alphabet, \(\Gamma\) is its stack alphabet, \(E\) is a finite set of edges, \(q_0 \in Q\) is its initial state, and \(F \subseteq Q\) is its set of final states. An edge is a four-tuple \((p, R, v, q)\), where \(p, q \in Q\), \(R \subseteq \Sigma^*\) is a regular language, and \(v \in \Gamma \cup \bar{\Gamma} \cup \{\varepsilon\}\). We also write

\[
p \xrightarrow{R,v} q
\]

to denote an edge \((p, R, v, q) \in E\). Intuitively, it tells us that from state \(q\), we can read any word in \(R\) as input and modify the stack as specified by \(v\). Here, \(v \in \Gamma\) means we push \(v\) onto the stack. Moreover, \(v \in \bar{\Gamma}\), \(v = \bar{\gamma}\), means we pop \(\gamma\) from the stack. Finally, \(v = \varepsilon\) means we do not change the stack. Let us make this formal. A configuration of \(A\) is a pair \((q, w)\) with \(q \in Q\) and \(w \in \Gamma^*\). For configurations \((q, w)\) and \((q', w')\), we write \((q, w) \xrightarrow{v,w} (q', w')\) if there
is an edge \((q, R, v, q')\) in \(A\) such that \(u \in R\) and (i) if \(v = \varepsilon\), then \(w' = w\), (ii) if \(v \in \Gamma\), then \(w' = vw\) and (iii) if \(v = \gamma\) for \(\gamma \in \Gamma\), then \(w = w'\gamma\).

A run in \(A\) is a sequence \((q_0, w_0), \ldots, (q_n, w_n)\) of configurations and words \(u_1, \ldots, u_n \in \Sigma^*\) such that \((q_{i-1}, w_{i-1}) \xrightarrow{u_i} (q_i, w_i)\) for \(1 \leq i \leq n\). Its length is \(n\) and its initial and final configuration are \((q_0, w_0)\) and \((q_n, w_n)\), respectively. The run is said to read the word \(w_1 \cdots w_n\). The stack height of the run is defined as \(\max\{|w_i| \mid 0 \leq i \leq n\}\). We call the run positive (resp. dually positive) if \(|w_i| \geq |w_0|\) (resp. \(|w_i| \geq |w_n|\)) for every \(1 \leq i \leq n\), i.e. if the stack never drops below its initial height (resp. is always above its final height).

We write \((q, w) \xrightarrow{u} (q', w')\) for configurations \((q, w), (q', w')\) if there is a run with initial configuration \((q, w)\) and final configuration \((q', w')\) that reads \(u\). If there is such a run with stack height \(\leq h\), then we write \((q, w) \xrightarrow{w}_h (q', w')\). The language accepted by \(A\) is

\[
L(A) = \{ u \in \Sigma^* \mid (q_0, \varepsilon) \xrightarrow{u}_h (q_f, \varepsilon) \}.
\]

There is also a language accepted with bounded stack height. For \(h \in \mathbb{N}\), we define

\[
L_h(A) = \{ u \in \Sigma^* \mid (q_0, \varepsilon) \xrightarrow{u}_h (q_f, \varepsilon) \}.
\]

Thus, \(L_h(A)\) is the set of words accepted by \(A\) using runs of stack height at most \(h\).

In order to exploit the symmetry between forward and backward computations in pushdown automata, we will consider dual pushdown automata. The dual automaton of \(A\), denoted \(\bar{A}\), is obtained from \(A\) by changing each edge \(p \xrightarrow{R} q\) into \(q \xrightarrow{R^\text{rev}} p\). Then \(L(\bar{A}) = L(A)^\text{rev}\). We will sometimes argue by duality, which is the principle that every statement that is true for any \(A\) is also true for any \(\bar{A}\).

**Petri nets.** A (labeled) Petri net is a tuple \(N = (\Sigma, S, T, \partial_0, \partial_1, \lambda, m_0, m_f)\) where \(\Sigma\) is its input alphabet, \(S\) is a finite set of places, \(T\) is a finite set of transitions, \(\partial_0, \partial_1 : T \to \mathbb{N}[S]\) are maps that specify an input marking \(\partial_0(t)\) and an output marking \(\partial_1(t)\) for each transition \(t \in T\), \(\lambda : T \to \Sigma \cup \{\varepsilon\}\) assigns labels to transitions, and \(m_0, m_f\) are its initial and final marking. More generally, multisets \(m \in \mathbb{N}[S]\) are called markings of \(N\).

For markings \(m_1, m_2 \in \mathbb{N}[S]\) and \(a \in \Sigma \cup \{\varepsilon\}\), we write \(m_1 \xrightarrow{a} m_2\) if there is a transition \(t \in T\) with \(\lambda(t) = a\), \(m_1 \geq \partial_0(t)\), and \(m_2 = m_1 \oplus \partial_0(t) \ominus \partial_1(t)\). Moreover, we write \(m_1 \xrightarrow{w} m_2\) if there are \(n \in \mathbb{N}\), \(a_1, \ldots, a_n \in \Sigma \cup \{\varepsilon\}\), and markings \(m_0', \ldots, m'_n\) such that \(w = a_1 \cdots a_n\) and \(m_1 = m_0' a_1 \xrightarrow{a_1} m_1' a_2 \xrightarrow{a_2} \cdots a_n \xrightarrow{a_n} m'_n = m_2\). Furthermore, we write \(m_1 \Rightarrow m_2\) if there exists a \(w \in \Sigma^*\) with \(m_1 \xrightarrow{w} m_2\). The language accepted by \(N\) is \(L(N) = \{ w \in \Sigma^* \mid m_0 \xrightarrow{w} m_f \}\).

For \(k \in \mathbb{N}\), a Petri net \(N\) is \(k\)-bounded if for every marking \(m \in \mathbb{N}[S]\) with \(m_0 \Rightarrow m\), we have \(|m| \leq k\).

Our main results are as follows.

**Theorem 6.2** (Succinct Downclosures for PDAs). Given a pushdown automaton \(A\), one can construct in polynomial time a pushdown automaton \(\bar{A}\) so that \(\downarrow L(\bar{A}) = \downarrow L(A)\). Moreover, we have \(L(\bar{A}) = L_h(\bar{A})\) for some bound \(h\) that is polynomial in the size of \(A\).

As a pushdown automaton with bounded stack can be simulated by a 1-bounded Petri net (essentially, by keeping places for each position in the stack), we get the following corollary and also the promised EXPSPACE upper bound.
Corollary 6.3. Given a pushdown automaton $A$, one can construct in polynomial time a 1-bounded Petri net $N$ with $L(N) = \downarrow L(A)$.

We now prove Theorem 6.2. The augmented automaton $\tilde{A} = (Q, \Sigma, \tilde{\Gamma}, \hat{E}, q_0, q_f)$ is defined as follows. We first compute for any $p, q \in Q$ the subalphabet $\Delta_{p,q}(A) \subseteq \Sigma$ with

$$\Delta_{p,q}(A) = \{ a \in \Sigma | \exists u \in M_{p,q}(A), \ |u|_a \geq 1 \},$$

where $M_{p,q}(A) = \{ u \in \Sigma^* | \exists v \in \Gamma^*: (p, \varepsilon) \xrightarrow{u,v} (p, v), (q, v) \xrightarrow{(q, \varepsilon)} \}$. Note that it is easy to construct in polynomial time a pushdown automaton $A_{p,q}$ for the language $M_{p,q}(A)$:

$A_{p,q}$ has a set $Q_p \cup Q_q$ of states consisting of two disjoint copies of the states of $A$. The transitions between two states in $Q_p$ are inherited from $A$ while for two states in $Q_q$ we replace the input on any transition by $\varepsilon$ but retain the stack operations from $A$. The start state is $p \in Q_p$ and the final state is $q \in Q_q$. There is an $\varepsilon$-transition from $p \in Q_p$ to $q \in Q_q$. This concludes the construction of $A_{p,q}$.

Since $a \in \Delta_{p,q}(A)$ iff $M_{p,q}(A) \cap \Sigma^*a\Sigma^* \neq \emptyset$, we can decide in polynomial time whether a given $a \in \Sigma$ belongs to $\Delta_{p,q}(A)$ by checking the PDA for $M_{p,q}(A) \cap \Sigma^*a\Sigma^* \neq \emptyset$ obtained by product construction for emptiness. Thus, we can compute $\Delta_{p,q}(A)$ in polynomial time. We construct $A$ as follows. For any $p, q \in Q$, we introduce a fresh stack symbol $[p,q]$ and then we add edges

$$p \xrightarrow{\Delta_{p,q}(A)^*[p,q]} p, \quad q \xrightarrow{\Delta_{q,p}(A)^*[p,q]} q.$$  \hspace{1cm} (6.1)

The following lemma tells us that $L(\tilde{A})$ has the same downward closure as $A$.

Lemma 6.4. $L(A) \subseteq L(\tilde{A}) \subseteq \downarrow L(A)$.

Proof. Since the inclusion $L(A) \subseteq L(\tilde{A})$ is obvious, we prove $L(\tilde{A}) \subseteq \downarrow L(A)$. We proceed by induction on the number $m$ of executions of new edges (i.e. those from Eq. (6.1)). More specifically, we show that if $u \in L(\tilde{A})$ is accepted using $m$ executions of new edges, then there is a $u' \in L(\tilde{A})$ such that $u \subseteq u'$ and $u'$ is accepted using $< m$ executions of new edges.

Suppose $u$ is accepted using a run $\rho$ with $m > 0$ executions of new edges. Then $\rho$ must apply one edge $p \xrightarrow{\Delta_{p,q}(A)[p,q]} p$ and thus also the edge $q \xrightarrow{\Delta_{q,p}(A)[p,q]} q$ to remove the letter $[p,q]$ from the stack. Thus, $\rho$ decomposes as $\rho = \rho_1\rho_2\rho_3\rho_4\rho_5$, where $\rho_2$ and $\rho_4$ are the executions of the new edges. Let $u = u_1u_2u_3u_4u_5$ be the corresponding decomposition of $u$.

The run $\rho_1$ must end in state $p$ and with some stack content $v \in \Gamma^*$. Then, $\rho_3$ is a run from $(p, w[p, q])$ to $(q, w[p, q])$ and $\rho_5$ is a run from $(q, w)$ to $(q_f, \varepsilon)$ with $q_f \in F$.

Since $u_2$ and $u_4$ are read while executing the new edges, we have $u_2 \in \Delta_{q,p}(A)^*$ and $u_4 \in \Delta_{q,p}(A)^*$. We can therefore write $u_2 = r_1 \cdots r_k$ and $u_4 = s_1 \cdots s_\ell$ with $r_1, \ldots, r_k \in \Delta_{q,p}(A)$ and $s_1, \ldots, s_\ell \in \Delta_{q,p}(A)$. By definition, this means for each $1 \leq i \leq k$, there is a word $\tilde{r}_i \in M_{p,q}(A)$ that contains the letter $r_i$. Likewise, for every $1 \leq i \leq \ell$, there is a $\tilde{s}_i \in M_{q,p}(A)$ that contains $s_i$.

Since $\tilde{r}_i \in M_{p,q}(A)$ and $\tilde{s}_j \in M_{q,p}(A)$ for $1 \leq i \leq k$ and $1 \leq j \leq \ell$, there are words $x_i$ and $y_j$ in $\Gamma^*$ so that

$$(p, \varepsilon) \xrightarrow{\tilde{r}_i} (p, x_i) \quad \text{and} \quad (q, x_i) \xrightarrow{(q, \varepsilon)}$$

$$(p, \varepsilon) \xrightarrow{(p, y_j)} \quad \text{and} \quad (q, y_i) \xrightarrow{(q, \varepsilon)} (q, \varepsilon)$$
We can therefore construct a new run \( \rho' = \rho_1 \rho_2 \rho_3 \rho_4 \rho_5 \), where
\[
\begin{align*}
\rho_2' &: (p, w) \xrightarrow{\tilde{r}_1} \cdots \xrightarrow{\tilde{r}_k} (p, wx_1 \cdots x_k) \Rightarrow \cdots \Rightarrow (p, wx_1 \cdots x_k y_1 \cdots y_1) \\
\rho_4' &: (q, wx_1 \cdots x_k y_1 \cdots y_1) \xrightarrow{\tilde{s}_1} \cdots \xrightarrow{\tilde{s}_i} (q, wx_1 \cdots x_k) \Rightarrow \cdots \Rightarrow (q, w).
\end{align*}
\]
Moreover, since \( \rho_3 \) is a positive run from \((p, w)\) to \((q, w)\), we obtain \( \rho'_3 \) from \( \rho_3 \) by replacing the prefix \( w \) of every stack by \( wx_1 \cdots x_k y_1 \cdots y_1 \).

Then \( \rho' \) reads some word \( u_1 \tilde{r}_1 \cdots \tilde{r}_k f u_3 \tilde{s}_1 \cdots \tilde{s}_j g u_5 \) for \( f, g \in \Sigma^* \). Note that since \( r_i \) occurs in \( \tilde{r}_i \) and \( s_i \) occurs in \( \tilde{s}_j \), we have \( u = u_1 u_2 u_3 u_4 u_5 \subseteq u_1 \tilde{r}_1 \cdots \tilde{r}_k f u_3 \tilde{s}_1 \cdots \tilde{s}_j g u_5 \).

We now show that every word in \( \hat{A} \) is accepted by a run with bounded stack height.

**Lemma 6.5.** \( L(\hat{A}) = L_h(\hat{A}) \), where \( h = 2|Q|^2 \).

Before we prove Lemma 6.5, we need another observation. Just like Lemma 6.4, one can show that for any \( p, q \in Q \), we have \( M_{p,q}(\hat{A}) \subseteq M_{p,q}(\hat{A}) \subseteq \downarrow M_{p,q}(\hat{A}) \) and in particular
\[
\Delta_{p,q}(\hat{A}) = \Delta_{p,q}(\hat{A}) \quad \Delta_{q,p}(\hat{A}) = \Delta_{q,p}(\hat{A}),
\]
where the second identity follows from the first: Duality yields \( \Delta_{q,p}(\hat{A}) = \Delta_{p,q}(\hat{A}) \) and since \( \hat{A} \) and \( \hat{A} \) are isomorphic (i.e. they are the same up to a renaming of stack symbols), we have \( \Delta_{q,p}(\hat{A}) = \Delta_{q,p}(\hat{A}) \).

We now prove Lemma 6.5. Let \( u \in L(\hat{A}) \). We show that any minimal length accepting run \( \rho \) reading \( u \) must have stack height \( \leq h \) and hence \( u \in L_h(\hat{A}) \).

Suppose the stack height of \( \rho \) is larger than \( h = 2|Q|^2 \).

**Claim:** \( \rho \) decomposes into runs \( \rho_1, \rho_2, \rho_3, \rho_4, \rho_5 \) reading \( u_1, u_2, u_3, u_4, u_5 \), respectively, so that there are \( p, q \in Q \) and \( w \in \Gamma^* \) with
- \( \rho_2 \) is a positive run from \((p, w)\) to \((p, wv)\) of length \( \geq 2 \)
- \( \rho_3 \) is a positive run from \((p, wv)\) to \((q, wv)\)
- \( \rho_1 \) is a dually positive run from \((q, wv)\) to \((q, w)\)

Let \( c_h \) be a configuration along \( \rho \) with stack height at least \( 2|Q|^2 + 1 \). Then there exist \(|Q|^2 \) configurations \( c_2 \Rightarrow c_4 \Rightarrow \cdots \Rightarrow c_2|Q|^2 \Rightarrow c_h \) along \( \rho \) such that \( c_{2i} \) is the last time that the stack height is \( 2i \) before visiting \( c_h \). Symmetrically, we have \( c_h \Rightarrow c_2|Q|^2 \Rightarrow \cdots \Rightarrow c_4 \Rightarrow c_2 \) where \( c_{2i} \) is the first occurrence of stack height \( 2i \) after visiting \( c_h \). Clearly by definition the run between consecutive \( c_{2i} \) (resp. \( c_{2i}^\prime \)) configurations is positive (resp. dually positive). Additionally, the length of the run between them must be at least two, because the stack heights differ by two. Considering the pair of states at each \( c_{2i}, c_{2i}^\prime \), there are \(|Q|^2 \) possibilities. Hence there must exist indices \( 2i < 2j \) such that the \( c_{2i} \) and \( c_{2j} \) have the same state \( p \) and \( c_{2i}^\prime \) and \( c_{2j}^\prime \) have the same state \( q \). It is now clear that \( \rho_2 : c_{2i} \Rightarrow c_{2j}^\prime, \rho_3 : c_{2j} \Rightarrow c_{2j}^\prime \) and \( \rho_4 : c_{2j}^\prime \Rightarrow c_{2i} \) satisfy the conditions of the claim.

These conditions imply that \( u_2 \in M_{p,q}(\hat{A}) \) and \( u_4 \in M_{q,p}(\hat{A}) \). Therefore, we have \( u_2 \in \Delta_{p,q}(\hat{A})^* = \Delta_{p,q}(\hat{A})^* \) and \( u_4 \in \Delta_{q,p}(\hat{A})^* = \Delta_{q,p}(\hat{A})^* \) by Eq. (6.2).

We obtain the run \( \rho' \) from \( \rho \) as follows. We replace \( \rho_2 \) by a single execution of the edge \( p \xrightarrow{\Delta_{p,q}(\hat{A})^* | [p,q]} \) reading \( u_2 \). Moreover, we replace \( \rho_4 \) by a single execution of the edge \( q \xrightarrow{\Delta_{q,p}(\hat{A})^* | [p,q]} \) reading \( u_4 \). Then \( \rho' \) is clearly a run reading \( u = u_1 u_2 u_3 u_4 u_5 \). Furthermore, since \( \rho_2 \)
has length $\geq 2$, but the single edge used instead in $\rho'$ only incurs length $1$, $\rho'$ is strictly shorter than $\rho$. This is in contradiction to the minimal length of $\rho$. This completes the proof of Lemma 6.5 and thus also Theorem 6.2.

Remark. The augmented automaton $\hat{A}$ yields a very simple construction of a finite automaton (of exponential size) for $\downarrow L(A)$. First, it is easy to construct a finite automaton for $L_h(\hat{A})$. Then, by introducing $\varepsilon$-edges, we get a finite automaton for $\downarrow L_h(\hat{A})$, which, by Lemmas 6.4 and 6.5, equals $\downarrow L(A)$.

It is now straightforward to construct a polynomial size 1-bounded Petri net $N = (\Sigma, S, T, \partial_0, \partial, m_0, m_f)$ with $L(N) = L_h(\hat{A})$, thus proving Corollary 6.3. First, by adding states, we turn $\hat{A}$ into a pushdown automaton $A' = (Q', \Sigma, \hat{\Gamma}, E', q_0, q_f)$, where every edge reads at most one letter, i.e., every edge $p \xrightarrow{R} q$ in $A'$ has $R = \{x\}$ for some $x \in \Sigma \cup \{\varepsilon\}$. (this is done by ‘pasting’ the automaton for $R$ in place of the edge). Moreover, we add $\varepsilon$-edges, so that for every edge $p \xrightarrow{x} q$, we also have an edge $p \xrightarrow{\varepsilon} q$. Then clearly $L_h(A') = \downarrow L_h(\hat{A}) = \downarrow L(A)$.

The net $N$ has a place $p$ for each state $p$ of $A'$ and for each $i \in \{1, \ldots, h\}$ and $\gamma \in \hat{\Gamma}$, it has a place $(i, \gamma)$. Moreover, for each $i \in \{0, \ldots, h\}$, it has a place $s_i$. Here, the idea is that a configuration $c = (p, \gamma_1, \ldots, \gamma_n)$ of $A'$ with $\gamma_1, \ldots, \gamma_n \in \hat{\Gamma}$ is represented as a marking $m_c = [p, (1, \gamma_1), \ldots, (n, \gamma_n), s_0]$. We call a marking of this form a stack marking and will argue that every reachable marking in $N$ is a stack marking. The transitions in $N$ correspond to edges in $A'$. For each edge $p \xrightarrow{x} q$ in $\hat{A}$, we add the following transitions:

- if $v = \gamma$ for some $\gamma \in \hat{\Gamma}$, then we have for every $1 \leq n \leq h$ a transition $t$ with $\partial_0(t) = [p, (n, \gamma), s_n]$, $\partial_1(t) = [q, s_{n-1}]$, and $\lambda(t) = x$.
- if $v \in \hat{\Gamma}$, then for every $0 \leq n < h$, we add a transition $t$ with $\partial_0(t) = [p, s_n]$, $\partial_1(t) = [q, {n+1}, v, s_{n+1}]$, and $\lambda(t) = x$.
- if $v = \varepsilon$, then we add a transition $t$ with $\partial_0(t) = [p]$, $\partial_1(t) = [q]$, and $\lambda(t) = x$.

Then clearly every reachable marking is a stack marking and we have $c \xrightarrow{c'} c'$ for configurations $c, c'$ of $A'$ of stack height $\leq h$ if and only if $m_c \xrightarrow{c} m_c'$. Therefore, if we set $m_0 = [q_0, s_0]$ and $m_f = [q_f, s_0]$ as initial and final marking, we have $L(N) = L_h(A') = \downarrow L(A)$. This completes the proof of Corollary 6.3.

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We have commented in the examples of Section 2 that internal actions and internal updates
mean that there is a sequence of transitions with labels \( w \) specifying the effect of each internal action on the global state \( d \). Let \( \delta \) be a deterministic finite state automaton where \( \delta \) is the transition relation from \( d \) to \( d' \). We will write \( d \xrightarrow{w} d' \) to mean that there is a sequence of transitions with labels \( w_1w_2...w_n = w \) in the automaton \( R \) using which we can reach \( d' \) from \( d \).

For alphabets \( \Sigma, \Sigma' \) with \( \Sigma \subseteq \Sigma' \), the Parikh map \( \text{Parikh}: \Sigma^* \rightarrow \mathbb{N}[\Sigma] \) maps a word \( w \in \Sigma^* \) to a multiset \( \text{Parikh}(w) \) such that \( \text{Parikh}(w)(a) \) is the number of occurrences of \( a \) in \( w \).

APPENDIX A. Compiling away internal actions

We have commented in the examples of Section 2 that internal actions and internal updates of the global state are useful in modeling asynchronous programs from their programming language syntax. Indeed, we note that the definition of asynchronous programs in [GM12] additionally uses a separate alphabet of internal actions, in addition to the alphabet of handler posts. We show how a model of asynchronous programs with internal actions can be reduced to our, simpler, model.

Let \( \mathcal{C} \) be a language class over an alphabet \( \Sigma \) of handler names. The definition of asynchronous programs with internal actions, as used by [SV06, JM07, GM12], is as follows.\(^3\) An asynchronous program over \( \mathcal{C} \) with internal actions (aka AP over \( \mathcal{C} \) with internal actions) is a tuple \( \mathfrak{P} = (D, \Sigma, \Sigma_i, L, R, d_0, m_0) \), where \( D, \Sigma, d_0, m_0 \) are as in Definition 2.2, \( \Sigma_i \) is an alphabet of internal actions disjoint from \( \Sigma \), the set \( L = (L_\sigma)_{\sigma \in \Sigma} \) consists of languages from \( \mathcal{C} \) (one for each handler \( \sigma \in \Sigma \)), and \( R = (D, \Sigma \cup \Sigma_i, \delta) \) a deterministic finite state automaton where \( D \) is the set of states, \( \Sigma \cup \Sigma_i \) the alphabet and \( \delta \) the transition relation specifying the effect of each internal action on the global state \( D \). We will write \( d \xrightarrow{w} d' \) to mean that there is a sequence of transitions with labels \( w_1w_2...w_n = w \) in the automaton \( R \) using which we can reach \( d' \) from \( d \).

\(^3\)The language class \( \mathcal{C} \) in [GM12] is fixed to be the class of context free languages. Their definition generalizes to any language class.
for each $a \in \Sigma$. For example, $\text{Parikh}(abbab)(a) = 2$, $\text{Parikh}(abbab)(b) = 3$ and $\text{Parikh}(c) = []$. For a language $L$, we define $\text{Parikh}(L) = \{ \text{Parikh}(w) \mid w \in L \}$. If the alphabet $\Sigma$ is not clear from the context, we write $\text{Parikh}_\Sigma$ (usually $\Sigma' = \Sigma$).

The semantics of a $\Psi$ is given as a labeled transition system over the set of configurations, with a transition relation $\rightarrow \subseteq (D \times \mathcal{M}[\Sigma]) \times \Sigma \times (D \times \mathcal{M}[\Sigma])$ defined as follows: let $m, m' \in \mathcal{M}[\Sigma]$, $d, d' \in D$ and $\sigma \in \Sigma$

\[(d, m \oplus [\sigma]) \xrightarrow{\sigma} \Psi (d', m' \oplus m') \]

iff

\[\exists w \in (\Sigma \cup \Sigma_i)^* : d \xrightarrow{w}_R d' \land w \in L_\sigma \land m' = \text{Parikh}_\Sigma(w) .\]

We now show that internal actions can be compiled away. Thus, for the algorithms presented in this paper, we use the more abstract, language-theoretic version of Definition 2.2, while we use internal actions as syntactic sugar in examples.

**Lemma A.1.** Let $C$ be a full trio. Given an AP $\Psi_i$ with internal actions over $C$, one can construct an AP $\Psi$ over $C$ such that both have identical sets of runs.

**Proof.** The proof is along similar lines to that of Lemmas 4.3, 4.5 in [GM12]. Given $\Psi_i = (D, \Sigma, \Sigma_i, \mathcal{L}, R, d_0, m_0)$ we construct $\Psi = (D, \Sigma, (L_c)_{c \in \mathcal{C}}, d_0, m_0)$ such that

\[(d, m \oplus [\sigma]) \xrightarrow{\sigma} \Psi_i (d', m' \oplus m') \]

iff

\[(d, m \oplus [\sigma]) \xrightarrow{\sigma} \Psi (d', m' \oplus m') \]

Let $L(R_{d,d'})$ be the language of the automaton $R$ when $d$ is the initial state and $d'$ is the accepting state. We define $L_{d,d'}$ as:

\[L_{d,d'} := \text{Proj}_\Sigma(L_\sigma \cap L(R_{d,d'}))\]

First observe that the projection operation is a homomorphism and $L(R_{d,d'})$ is a regular language; hence by virtue of $C$ being a full trio $L_{d,d'}$ as defined above is in $C$. The conditions $\exists w \in (\Sigma \cup \Sigma_i)^* : d \xrightarrow{w}_R d' \land w \in L_\sigma \land m' = \text{Parikh}_\Sigma(w)$ and $\exists w \in L_{d,d'} : \text{Parikh}(w) = m'$ are seen to be equivalent from the definition of $L_{d,d'}$, concluding the proof of the lemma.

**APPENDIX B. PROOF OF PROPOSITION 3.3**

We prove the following proposition.

Let $\Psi = (D, \Sigma, (L_c)_{c \in \mathcal{C}}, d_0, m_0)$ be an asynchronous program. Then $\downarrow \text{Runs}(\downarrow \Psi) = \downarrow \text{Runs}(\Psi)$. In particular,

(1) For every $d \in D$, $\Psi$ can reach $d$ if and only if $\downarrow \Psi$ can reach $d$.

(2) $\Psi$ is terminating if and only if $\downarrow \Psi$ is terminating.

(3) $\Psi$ is bounded if and only if $\downarrow \Psi$ is bounded.

**Proof.** A run of the asynchronous program $\Psi$ is defined as a sequence $c_0, \sigma_1, c_1, \sigma_2, \ldots$ containing alternating elements of configurations $c_i$ and letters $\sigma_i$ beginning with the configuration $c_0 = (d_0, m_0)$. First we observe that

\[\text{Runs}(\Psi) \subseteq \text{Runs}(\downarrow \Psi) \quad (B.1)\]

because every transition enabled in $\Psi$ is also enabled in $\downarrow \Psi$. Next, we claim:

\[\forall \rho \in \text{Runs}(\downarrow \Psi) \exists \rho' \in \text{Runs}(\Psi) \rho \preceq \rho' \quad (B.2)\]
Let \( \rho_k = (d_0, m_0), \sigma_1, (d_1, m_1), \sigma_2, ..., \sigma_k, (d_k, m_k) \) be the \( 2k + 1 \)-length prefix of \( \rho \). We show that for each \( \rho_k \) there exists \( \rho'_k \in \text{Runs}(\mathcal{P}) \) such that \( \rho|_k \leq \rho'_k \) and in addition, \( \forall k \forall i \leq k \rho'_k(i) = \rho'_{k+1}(i) \). We can then define \( \rho'(i) := \rho'_i(i) \) and clearly \( \rho \leq \rho' \).

We prove by induction on \( k \).

**Base Case:** \( \rho|_0 = \rho'_0 = (d_0, m_0) \).

**Induction Step:** Let \( \rho_k = (d_0, m_0), \sigma_1, (d_1, m_1), \sigma_2, ..., (d_k, m_k) \in \text{Runs}(\downarrow \mathcal{P}) \). By induction hypothesis there is \( \rho'_{k-1} = (d_0, m_0), \sigma_1, (d_1, m'_1), \sigma_2, ..., (d_{k-1}, m'_{k-1}) \in \text{Runs}(\mathcal{P}) \) such that \( \rho_{k-1} \leq \rho'_{k-1} \).

\[
(d_{k-1}, m_{k-1}) \trianglelefteq (d_k, m_k)
\]

\[
\Rightarrow \exists m''_{k-1} : m_{k-1} = m''_{k-1} \oplus [\sigma_k] \land (d_{k-1}, m''_{k-1} \oplus [\sigma_k]) \trianglelefteq (d_k, m_k)
\]

\[
\Rightarrow \exists w \in \Sigma^* : w \in \downarrow L_{d_{k-1}} \sigma_d_{d_k} \land \text{Parikh}(w) \oplus m''_{k-1} = m_k
\]

\[
\Rightarrow \exists w' \in \Sigma^* : w \subseteq w' \land w' \in \downarrow L_{d_{k-1}} \sigma_d_{d_k}
\]

\[
\Rightarrow (d_{k-1}, m_{k-1}) \trianglelefteq (d_k, m_k \oplus m_{\Delta}) \text{ where } m_{\Delta} \oplus \text{Parikh}(w) = \text{Parikh}(w')
\]

\[
\Rightarrow (d_{k-1}, m'_{k-1}) \trianglelefteq (d_k, m'_k) \text{ where } m'_k = m_k \oplus m_{\Delta} \oplus (m'_{k-1} \ominus m_{k-1})
\]

Defining \( \rho'_k := \rho'_{k-1}, \sigma_k, (d_k, m'_k) \) we see that \( \rho|_k \leq \rho'_k \), completing the proof of Equation B.2. We are now ready to show that \( \downarrow \text{Runs}(\mathcal{P}) = \downarrow \text{Runs}(\mathcal{Q}) \). The direction \( \downarrow \text{Runs}(\mathcal{P}) \subseteq \downarrow \text{Runs}(\mathcal{Q}) \) follows immediately from Equation (B.1). Conversely, let

\[
\rho = (s_0, n_0), \sigma_1, (s_1, n_1), \sigma_2, ... \in \downarrow \text{Runs}(\mathcal{Q})
\]

\[
\Rightarrow \exists \rho' \in \text{Runs}(\mathcal{Q}) \rho \leq \rho'
\]

\[
\Rightarrow \exists \rho'' \in \text{Runs}(\mathcal{Q}) \rho' \leq \rho'' \text{ by Equation (B.2)}
\]

\[
\Rightarrow \rho \in \downarrow \text{Runs}(\mathcal{Q})
\]

We have proved that \( \downarrow \text{Runs}(\mathcal{Q}) = \downarrow \text{Runs}(\mathcal{P}) \). We now show that each of the three properties i.e. safety, termination and boundedness only depend on the downclosure of the runs.

**Safety:**

\[
d \text{ is reachable in } \mathcal{Q} \iff \exists \rho = (d_0, m_0), \sigma_1, (d_1, m_1), \sigma_2, ..., \sigma_k, (d_k, m_k) \in \text{Runs}(\mathcal{Q}) : d_k = d
\]

By Equation B.1, we know \( \rho \in \text{Runs}(\mathcal{Q}) \) implies \( \rho \in \downarrow \text{Runs}(\mathcal{Q}) \). Conversely, if there is \( \rho' = (s_0, n_0), \sigma_1, (s_1, n_1), \sigma_2, ..., \sigma_k, (s_k, n_k) \in \downarrow \text{Runs}(\mathcal{Q}) \) with \( s_k = d \), then by Equation B.2, there is \( \rho = (d_0, m_0), \sigma_1, (d_1, m_1), \sigma_1, ..., \sigma_k, (d_k, m_k) \in \text{Runs}(\mathcal{Q}) \) with \( \rho' \leq \rho \) which implies \( d_k = d \). Hence we have

\[
d \text{ is reachable in } \mathcal{Q} \iff \exists \rho = (s_0, n_0), \sigma_1, (s_1, n_1), \sigma_2, ..., \sigma_k, (s_k, n_k) \in \downarrow \text{Runs}(\mathcal{Q}) : s_k = d
\]
By a similar argument as above we also have:

**Termination:**

- $\Psi$ does not terminate
  - iff $\exists \rho \in \text{Runs}(\Psi)$: $\rho$ is infinite
  - iff $\exists \rho \in \downarrow\text{Runs}(\Psi)$: $\rho$ is infinite

**Boundedness:**

- $\Psi$ is bounded
  - iff $\exists N \in \mathbb{N} \forall \rho \in \text{Runs}(\Psi) \forall i \mid |\rho(2i).m| < N$
  - iff $\exists N \in \mathbb{N} \forall \rho \in \downarrow\text{Runs}(\Psi) \forall i \mid |\rho(2i).m| < N$

In each of the three cases, the property only depends on the downclosure and hence one may equivalently replace $\Psi$ by $\downarrow\Psi$ since $\downarrow\text{Runs}(\downarrow\Psi) = \downarrow\text{Runs}(\Psi)$.

**Appendix C. Proof of Proposition 5.1**

We begin with a simple observation. For every HORS $\mathcal{H} = (\Sigma, \mathcal{N}, S, \mathcal{R})$, there exists another HORS $\mathcal{H}' = (\Sigma', \mathcal{N}', S, \mathcal{R}')$ where $\Sigma' = \{\text{br}, e\} \cup \bar{\Sigma}$ with $\text{br}$ of arity 2, $e$ of arity 0 and all symbols in $\bar{\Sigma}$ of arity 1; such that $\mathcal{L}_p(\mathcal{H}) = \mathcal{L}_p(\mathcal{H}')$.

By rewriting every terminal symbol $A$ of arity $n \geq 2$ using the rule

$$Ax_1x_2 \cdots x_n \rightarrow \text{br}(x_1\text{br}(x_2\text{br}(\cdots \text{br}(x_{n-1}x_n)))$$

and by replacing the occurrence of every nullary symbol in $\mathcal{H}$ by $e$, we get $\mathcal{H}'$, which has the same path language.

We assume due to the above observation that $\Sigma = \{\text{br}, e\} \cup \bar{\Sigma}$ where $\text{br}$ is binary, $e$ is nullary and all letters in $\bar{\Sigma}$ are unary. Define $\mathcal{H}' = (\Sigma', \mathcal{N}', S, \mathcal{R}')$ as $\Sigma' = \Sigma \cup \{e\}, \mathcal{N}' = \mathcal{N} \cup \{B : o \rightarrow (o \rightarrow o), \mathcal{R}' = \{r[\text{br}/B] \mid r \in \mathcal{R}\} \cup \{Bxy \rightarrow x, Bxy \rightarrow y\}$, where by $r[\text{br}/B]$ we mean the rule $r$ with $\text{br}$ uniformly replaced by $B$. Note that the only new non-terminal symbol introduced is $B$, which is of order 1. Hence the obtained word scheme $\mathcal{H}'$ is of the same order as $\mathcal{H}$.

$\mathcal{L}_p(\mathcal{H}) \subseteq \mathcal{L}_w(\mathcal{H}')$: Let $w \in \mathcal{L}_p(\mathcal{H})$. Therefore there exists a finite path $\Pi$ in a sentential form $t$ such that $\forall \bar{n} \in \text{dom}(\Pi) w_{\Pi}(\bar{n}) \in \Sigma$. We derive $t' := t[\text{br}/B]$ using the corresponding rules in $\mathcal{R}'$. Note that the corresponding path $\Pi'$ in $t'$ satisfies $\forall \bar{n} \in \text{dom}(\Pi') w_{\Pi'}(\bar{n}) \in \Sigma \cup \{B\}$. We then apply either $Bxy \rightarrow x$ or $Bxy \rightarrow y$ to each $B$ in $t'$ according to the path $\Pi'$ to obtain $w$ in the word scheme.

$\mathcal{L}_w(\mathcal{H}') \subseteq \mathcal{L}_p(\mathcal{H}')$: We define the order $\leq_{\text{pre}}$ on sequences of natural numbers $\bar{n}, \bar{m}$ as $\bar{n} \leq_{\text{pre}} \bar{m}$ if $\bar{m} = \bar{n}k$ for some sequence $k$.

Suppose that for given a sentential form $t'$ of $\mathcal{H}'$ there exists a sentential form $t$ of $\mathcal{H}$ and a map $\alpha : \text{dom}(t') \rightarrow \text{dom}(t)$ (simply called embedding henceforth) satisfying the following conditions:

- $\forall \bar{n} \in \text{dom}(t')$
  
  $$t'(\bar{n}) = \begin{cases} B & \text{if } t(\alpha(\bar{n})) = \text{br} \\ t(\alpha(\bar{n})) & \text{otherwise.} \end{cases} \quad (C.1)$$
\( \forall n, \bar{m} \in \text{dom}(t'), \bar{n} \preceq \bar{m} \) implies 
\[ \alpha(\bar{n}) \preceq \alpha(\bar{m}), \quad \text{and} \]
\[ (\forall i \ (\bar{n} \preceq \bar{l} \preceq \bar{m}) \text{ implies } t'(\bar{l}) \notin \tilde{\Sigma}) \text{ implies } \]
\[ (\forall k \ (\alpha(\bar{n}) \preceq \bar{k} \preceq \alpha(\bar{m}) \text{ implies } t(\bar{k}) \notin \tilde{\Sigma}) \]
\[ \quad \text{(C.3)} \]

Informally, Equations C.1, C.2 and C.3 state the following: \( \alpha \) preserves labels, except for the case of \( \mathcal{N} \mathcal{R} \) where it maps to \( \mathcal{B} \), \( \alpha \) preserves the order \( \preceq \) and the images of any two nodes with node labels from \( \tilde{\Sigma} \) with no node label from \( \tilde{\Sigma} \) in between are mapped to two such nodes with the same property.

We will show by induction on the length of the derivation that such a pair \((t, \alpha)\) always exists given some \( t' \). Let us see how the existence of such \( t \) and \( \alpha \) gives us the proposition. Consider a word \( w = w_1 w_2 ... w_n \in \mathcal{L}_w(\mathcal{J}') \). In other words, there is a term \( t' = w_1(w_2...(w_n(e))...) \) such that \( S' \rightarrow^* t' \). Correspondingly to this, we have a sentential form \( S \rightarrow^* t \) and \( \alpha \) satisfying the given conditions. In particular, there exists a path \( \Pi \) with \( \text{dom}(\Pi) \subseteq \text{dom}(t) \) which is the path in \( t \) connecting the \( \alpha(\varepsilon) \) to \( \alpha(0^n) \). It is immediate that \( \text{Proj}_{\mathcal{J}'}(\Pi) = w \). It remains to show the existence of \( t \) and \( \alpha \) by induction on the length of derivations.

**Base case:** This is trivial since \( t = t' = S \).

**Induction step:** Suppose \( t'_0 \rightarrow_{r \in \mathcal{R}'} t' \) where \( t'_0 \) is a sentential form. By induction hypothesis, there is a sentential form \( t_0 \) of \( \mathcal{J} \) and \( t'_0 \) embeds into \( t_0 \) via map \( \alpha_0 \). Assume that the rule \( r \) is applied at position \( \bar{n} \) on \( t'_0 \). We now have two cases to consider:

**Case 1:** The rule \( r \) is \( \mathcal{B}x \rightarrow y \) (the case \( \mathcal{B}xy \rightarrow y \) being symmetric). By induction hypothesis, we have \( t_0 \) and an embedding \( \alpha_0 \) of \( t'_0 \) into \( t_0 \). Refering to Figure 2, we see that \( t \) can be taken to be \( t_0 \) and \( \alpha \) maps all nodes in the subtree \( u'_0 \) into \( u_0 \) as before, while the subtree \( l'_0 \) rooted at \( \bar{n} \) is mapped into \( l_0 \). It is immediate that \( \alpha \) preserves the order and since by induction hypothesis \( \alpha_0(\bar{n}) \) has label \( \mathcal{B} \), Equation C.3 is also satisfied by \( \alpha \) since no new \( \tilde{\Sigma} \) labelled nodes have been added.

**Case 2:** We demonstrate for the case when a non-terminal of arity two is replaced for an easier reading of the proof: the rule \( r \) is \( \mathcal{N}xy \rightarrow t_1 \) for some nonterminal \( \mathcal{N} \neq \mathcal{B} \). Refering to Figure 3, the rule replaces the subtree rooted at \( \bar{n} \) in \( t'_0 \) with \( t_1[x/l'_0, y/r'_0] \) where \( l'_0, r'_0 \) are respectively the left and right subtrees of the subtree rooted at \( \bar{n} \) in \( u'_0 \). For nodes in the subtrees \( u'_0 \) of \( t' \), \( \alpha \) mimics \( \alpha_0 \). By induction we know that the subtrees \( l'_0, r'_0 \) of \( t'_0 \) embed respectively into \( l_0, r_0 \) of \( t_0 \). Thus \( \alpha \) maps every subtree \( l'_0 \) (resp. \( r'_0 \)) rooted at \( \bar{m} \) (resp. \( \bar{k} \)) in \( t_1[x/l'_0, y/r'_0] \) to the corresponding subtree \( l_0 \) (resp. \( r_0 \)) rooted at \( \alpha(\bar{m}) \) (resp. \( \alpha(\bar{k}) \)). Note that \( \alpha(\bar{m}) \) can be defined as the root of the \( i \)-th occurrence of \( l_0 \) from left to right if \( \bar{m} \) is the \( i \)-th occurrence of \( l'_0 \) from left to right. Nodes in \( t_1[x/l'_0, y/r'_0] \) which are not in any of the \( l'_0 \) (or \( r'_0 \)) subtrees (i.e.) between \( \bar{n} \) and \( \bar{m} \) (or \( \bar{k} \)) have corresponding nodes in \( t_1[x/l_0, y/r_0] \) to which they can be mapped. Label preservation and order preservation immediately follow by appeal to the induction hypothesis. In order to see that Equation C.3 holds, consider consecutive \( \tilde{\Sigma} \) nodes \( \bar{n}', \bar{m}' \) in \( t' \) (i.e. \( \bar{n}' \prec \bar{m}' \) and there are no \( \tilde{\Sigma} \) labels in between). If both \( \bar{n}' \) and \( \bar{m}' \) are in \( u'_0 \) or in one of the \( l'_0 \) (or \( r'_0 \)) then the induction hypothesis applies. In the case where \( \bar{n}' \in u'_0 \) and \( \bar{m}' \in l'_0 \) (resp. \( r'_0 \)) this means that no \( \tilde{\Sigma} \) labels are present.
in the path from $\bar{n}$ to $\bar{m}$ (resp. $\bar{k}$), $\bar{n}'$ to $\bar{n}$ nor $\bar{m}$ (resp. $\bar{k}$) to $\bar{m}'$. The path from $\alpha(\bar{n})$ to $\alpha(\bar{m})$ is identical to that from $\bar{n}$ to $\bar{m}$ (resp. $\bar{k}$). The induction hypothesis also implies that the first $a$ label for some $s \in \tilde{\Sigma}$ in $l'_0$ must be mapped to first $a$ label in $l_0$ (or $u'_0$ does not contain any $\tilde{\Sigma}$ labels). Hence there are no $\tilde{\Sigma}$ labels between $\alpha(\bar{n})$ (resp. $\alpha(\bar{k})$) and $\alpha(\bar{m}')$ and similarly between $\alpha(\bar{m}')$ and $\alpha(\bar{n})$. The final case is when either $\bar{n}'$ or $\bar{m}'$ lies between $\alpha(\bar{n})$ and $\alpha(\bar{m})$ (resp. $\alpha(\bar{k})$). There are subcases here to consider when the other point lies in $u'_0$, $l_0$ or also between $\bar{n}$ and $\bar{m}$. In all of these subcases, it easily follows that there are no extra $\tilde{\Sigma}$ labels introduced in between two consecutive nodes which are in the image of $\alpha$.

Figure 2: Construction of embedding $\alpha$ from $\alpha_0$ in the case of the rule $Bxy \rightarrow x$
Figure 3: Construction of embedding $\alpha$ from $\alpha_0$ when the rule is $N_{xy} \rightarrow t_1$