1. Introduction

Process of energy recuperation is well known phenomena, especially in the field of traction applications. Nowadays, the recuperation of energy is used in the industrial, commercial and also in the consumer sector. The main factor, which allows this breakthrough, was development of technology in production of power semiconductor structures and research of new topologies of main circuit of switched mode power supplies (SMPS), which are working in two-quadrant (2Q bi-directional) operation [1]–[3]. Although, there are now a variety of schemes of DC/DC bidirectional converters, the disadvantage of these solutions is high complexity, or lack of versatility, which is associated with limited utilization of mentioned topologies. The main reason for this are specific requirements for their properties, particularly possibility of change of electrical values as wide as possible. Another disadvantage of existing solutions in terms of universal utilization is relatively high material costs.

Nowadays, the existing solutions of bidirectional converters can by divided as follows: The first group includes cascade DC/DC converters in buck-boost topology, second group are non-isolated half-bridge topologies, in another group the multi-tank non-isolated Cuk and Sepic topologies are presented and last group involves so-called split topologies, using pi-filters at the input and output of converter. The mentioned drawback in the comparison with proposed solution is considerably higher number of components in the system resulting in higher complexity of topology, lack of versatility and insufficient dynamical range of input and output parameters.

2. Bidirectional step-up/step-down converter with MCC

The main circuit of proposed converter, based on boost DC-DC converter [4]-[6], is composed of three main parts (Fig. 1). The first is given by primary capacitive filter together with primary transistor T1. Second part consists of the most important part of converter - bifilar wound coil, whose windings are connected in order to create auto-transformer with common ground on the primary and secondary side. If target application requires total galvanic isolation between primary and secondary side, it is possible to realize the proposed converter as an isolated bidirectional converter. This approach can also be done by simple modification of the main circuit with the use of auxiliary switch. The last, third part of the proposed converter is composed of secondary transistor T2, of a filter and of load/appliance.

It can be seen from Fig. 1 that the primary as well as secondary part of converter have in principle the same functionality. Based on this property both parts can behave as input or output of the converter (the secondary side is dissymmetrical to the primary side), and, therefore, the energy transfer from source to load and vice versa can be simply and effectively realized. Simultaneously the modification of the electrical variables magnitudes (increase or decrease) can be done in a wide regulation range. Based on the operating conditions, the converter functionality is held in the first or third quadrant of operating characteristic.

The main advantages compared to other solution of bidirectional DC/DC converters are:

- extra-wide regulation range of electrical variables at given output (step-up/step-down)

**Keywords:** DC-DC power converter, bidirectional step-up/step-down converter, duty cycle factor, transfer function, parasitic parameters, steady-state operation.
The proposed converter, whose principal schematic is shown in Fig. 1 is classified as DC-DC converter. The following analysis is oriented on the determination of state space variables and on the investigation of voltage-transfer characteristic in both direct and recuperative mode of operation.

The operation of converter both for directions (energy transfer from source into load) and for recuperative (energy transfer from load into source) mode can be divided into two operating intervals:
- interval $R(t_0 - t_1)$: transistor $T_1$ ($T_2$) closed, transistor $T_2$ ($T_1$) open;
- interval $R(t_1 - T)$: transistor $T_1$ ($T_2$) open, transistor $T_2$ ($T_1$) closed.

Fig. 2 shows schematics of the proposed converter, whereby parasitic resistances are considered, and due to fact that their presence is influencing voltage transfer characteristic. This impact is negative and, therefore, must be accepted during state space model setting and its consequent derivation.

The state space model for the proposed converter is derived for the operating condition when the converter operates at direct mode. Here, it must be noted that considering recuperative operating mode the state space will be the same, whereby only one change applies specifically for the input/output arguments in the case of voltages and currents (input will act as output and vice versa).
Interval $I(t_0 - t_1)$

\[
\frac{d}{dt}\begin{pmatrix} i(t_1) \\ u(t_1) \end{pmatrix} = \begin{pmatrix} -\frac{r_s}{L_1} & 0 \\ \frac{1}{L_1} & 0 \end{pmatrix} \begin{pmatrix} i(t_1) \\ u(t_1) \end{pmatrix} + \begin{pmatrix} \frac{1}{C_1} \\ 0 \end{pmatrix} u(t_1) + \begin{pmatrix} 0 \\ 0 \end{pmatrix}.
\]

(1)

where:
- $i(t_1)$ is the current in the primary side
- $u(t_1)$ is output capacitor voltage
- $r_s$ is the sum of series parasitic resistances
- $r_p$ is the parallel parasitic resistances
- $L_1$ is value of primary inductance
- $U_1$ is input voltage
- $R_2$ is load resistance

Interval $I(t_1 - T)$

\[
\frac{d}{dt}\begin{pmatrix} i(t_2) \\ u(t_2) \end{pmatrix} = \begin{pmatrix} -\frac{r_p}{L_2} & -\frac{1}{L_2} \\ \frac{1}{C_2} & -\frac{1}{L_2} \end{pmatrix} \begin{pmatrix} i(t_2) \\ u(t_2) \end{pmatrix} + \begin{pmatrix} \frac{1}{L_2} \\ 0 \end{pmatrix} u(t_2) + \begin{pmatrix} 0 \\ 0 \end{pmatrix}.
\]

(2)

The voltage transfer characteristic can be derived from the comparison of ripple current during the first and second interval. The analysis outgoes from the equation of inductor’s voltage:

\[
u_i = L \frac{di_i}{dt}.
\]

(3)

After linearization, the next formula is valid for ripple current:

\[
\Delta I_i = \frac{U \cdot D \cdot T}{L}.
\]

(4)

where:
- $D$ is duty cycle,
- $T$ is switching period of converter.

Comparing the value of ripple current during interval when transistor T1 is closed with the ripple current from the interval when transistor T1 is open leads to:

\[
\frac{U_1 \cdot D \cdot T}{L_1} = \frac{U_2 \cdot (1 - D) \cdot T}{L_2}.
\]

(5)

When $L_1 = L_2$, thus $N_1 = N_2$, where $N_1$ and $N_2$ are numbers of primary or secondary turns and all parasitic resistances are neglected, then the next equation for approximate computation of voltage transfer function can be written:

\[
\frac{U_2}{U_1} = \frac{D}{(1 - D)}.
\]

(6)

Figure 3 shows graphical interpretation of voltage transfer characteristic with ideal waveform (without parasitic ones), and voltage transfer waveform when parasitic resistances are being considered.

3. Simulation investigations of proposed converter’s properties

In this chapter we would like to investigate the voltage transfer functions of the proposed converter based on parametrical simulations from OrCAD/Pspice. Based on this, and after comparisons with theoretical assumptions it is possible to demonstrate existing deviations in voltage transfer function.

Next figure shows voltage transfer function of the proposed converter whose input/output parameters are as follows:

- $U_{IN} = 24$ V, $f_{SW} = 50$ kHz, $L_1 = L_2 = 300 \mu$H($N_1 = N_2$),
- $U_{OUT} = \text{var}$, $I_{OUT} = \text{var}$, $P_{OUT} = \text{max.} 25$ W.
- load resistance $R_2 = 48 \Omega$
- Parasitics $r_s = 0.24, 0.48, 0.96$ and $1.44 \Omega$

Load 100, 200 and 20%, $R = 48, 24$ and $240 \Omega$.

Figure 3 Voltage transfer function of proposed converter at 100% power loading and various parasitic resistances from 0 to 1.44 Ohm

Figure 4 Voltage transfer function of proposed converter at 20, 100 and 200% of power loading
It is evident from Figs. 3 and 4 that:
- idealized relation of voltage transfer ration \( U_2/U_1 \) on duty cycle \( D \) (i.e. \( D/(1-D) \)) is not valid for entire range of duty cycle \((0;1)\);
- consequently, voltage transfer characteristic is not monotonic one but it has a local extreme at which derivative of the transfer is changing; it is a critical point of characteristic;
- control system used should be acting just in ‘secure’ range from 0 to a critical value of duty cycle \( D_{crit} \), otherwise it must have a variable structure.

4. Experimental verification of bidirectional step-up/step-down converter with MCC

The experimental set-up was built (Fig. 5) with parameters: \( U_{IN} = 24 \text{ V}, f_{SW} = 50 \text{ kHz}, L_1 = L_2 = 300 \mu\text{H}, N_1 = N_2, C \), load resistance \( R_2 = 48, 24, 240 \Omega, P_{OUT} \) var. 25 W as maximum, depending on load resistance.

There are results carried-out by measurement on experimental set-up of the converter at 20%, 100%, and 200 % of the load under resistive loading.

Output power can be calculated from the carried-out transfer characteristics in Fig. 6
\[
P_{OUT} = \frac{U_2^2}{R_2}; \quad P_{out} = \frac{(2\cdot24)^2}{48} = \frac{4\cdot24^2}{2\cdot24} = 48 \text{ W}
\]

at duty cycle \( D = 0.75 \);
\[
P_{out} = \frac{(1\cdot24)^2}{48} = \frac{14\cdot24^2}{2\cdot24} = 12 \text{ W}
\]

at duty cycle \( D = 0.50 \);
\[
P_{out} = \frac{(1.41\cdot24)^2}{48} = \frac{2\cdot24^2}{2\cdot24} = 24 \text{ W}
\]

at duty cycle \( D = 0.625 \);

It is possible to calculate the requested power (24 W) also under 24 Ω load resistance
\[
P_{out} = \frac{(1\cdot24)^2}{24} = \frac{1.24^2}{1\cdot24} = 24 \text{ W}
\]

at duty cycle \( D = 0.65 \).

So, for a fair design of the converter elements it is necessary to determine at what output voltage we want the requested power. Possible improvements of operation and efficiency of the converter are to be reached by works [8]–[10].

5. Conclusion

The bidirectional step-up/step-down converter was presented in the paper. Simulation experiment results worked-out using OrCAD/PSpice programming environment showed that voltage transfer characteristics feature two parts: the first one with positive derivative part, and the second one with negative derivative part. Maximal output voltage doesn’t depend only on a duty cycle of electronic switches but also on the parasitic parameter values of the converter circuit elements. The experimental set-up measurements verified results of simulation and theoretical assumptions.

Presented solution of bidirectional converters based on buck-boost DC/DC topology consists of substantial lesser components than classical cascade DC/DC converters or non-isolated half-bridge topologies, and features: an extremely wide regulation range of output voltage, low complexity of main circuit within achievement of high universality, and possibility of isolated and non-isolated versions. Such a converter system can be utilized as a part of power semiconductor system in traction, automotive, or industrial applications like renewable energy sources.

As future work we suppose the investigation of transient behavior –, efficiency analysis- and control system design of that type of bidirectional step-up/step-down converter.
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