Correlation of a generation-recombination center with a deep level trap in GaN
X. S. Nguyen, K. Lin, Z. Zhang, B. McSkimming, A. R. Arehart, J. S. Speck, S. A. Ringel, E. A. Fitzgerald, and S. J. Chua

Citation: Applied Physics Letters 106, 102101 (2015); doi: 10.1063/1.4914393
View online: http://dx.doi.org/10.1063/1.4914393
View Table of Contents: http://scitation.aip.org/content/aip/journal/apl/106/10?ver=pdfcov
Published by the AIP Publishing

Articles you may be interested in
Deep levels in as-grown and electron-irradiated n-type GaN studied by deep level transient spectroscopy and minority carrier transient spectroscopy
J. Appl. Phys. 119, 095707 (2016); 10.1063/1.4943029

Deep traps in n-type GaN epilayers grown by plasma assisted molecular beam epitaxy
J. Appl. Phys. 115, 023102 (2014); 10.1063/1.4861180

Investigation of deep levels in bulk GaN material grown by halide vapor phase epitaxy
J. Appl. Phys. 114, 153702 (2013); 10.1063/1.4825052

Deep level transient spectroscopy in plasma-assisted molecular beam epitaxy grown Al 0.2 Ga 0.8 N / GaN interface and the rapid thermal annealing effect
Appl. Phys. Lett. 97, 112110 (2010); 10.1063/1.3491798

Effects of thermal annealing on deep-level defects and minority-carrier electron diffusion length in Be-doped InGaAsN
J. Appl. Phys. 97, 073702 (2005); 10.1063/1.1871334
Correlation of a generation-recombination center with a deep level trap in GaN

X. S. Nguyen,1,a) K. Lin,2 Z. Zhang,3 B. McSkimming,4 A. R. Arehart,3 J. S. Speck,4 S. A. Ringel,3 E. A. Fitzgerald,1,5 and S. J. Chua1,2,6,a)

1Low energy electronic system IRG, Singapore MIT Alliance for Research and Technology Center, 1 CREATE way, Singapore 138602
2Institute of Materials Research and Engineering, 3, Research Link, Singapore 117602
3Department of Electrical and Computer Engineering, The Ohio State University, Columbus, Ohio 43210, USA
4Materials Department, University of California, Santa Barbara, California 93106-5050, USA
5Department of Materials Science and Engineering, Massachusetts Institute of Technology, Room 13-5153, 77 Mass. Ave., Cambridge, Massachusetts 02139, USA
6Department of Electrical and Computer Engineering, National University of Singapore, Block E4, Engineering Drive 3, Singapore 117583

(Received 5 February 2015; accepted 26 February 2015; published online 9 March 2015; corrected 6 April 2015)

We report on the identification of a deep level trap centre which contributes to generation-recombination noise. A n-GaN epilayer, grown by MOCVD on sapphire, was measured by deep level transient spectroscopy (DLTS) and noise spectroscopy. DLTS found 3 well documented deep levels at $E_C - 0.26$ eV, $E_C - 0.59$ eV, and $E_C - 0.71$ eV. The noise spectroscopy identified a generation recombination centre at $E_C - 0.65 \pm 0.1$ eV with a recombination lifetime of 65 $\mu$s at 300 K. This level is considered to be the same as the one at $E_C - 0.59$ eV measured from DLTS, as they have similar trap densities and capture cross section. This result shows that some deep levels contribute to noise generation in GaN materials.

GaN, a wide and large bandgap semiconductor, is an important material for high power high frequency devices.1–3 GaN is typically grown on Sapphire, Silicon Carbide, or Silicon substrates. Due to the large lattice mismatch between GaN and the substrates, a large amount of dislocations are generated. Other defects are also created due to vacancies and formation of complexes. These defects affect the efficiency of light emission from the material and current collapse in GaN HEMT.4,5 Therefore, it is important to find out their concentrations and the energy levels within the band-gap.

Noise spectroscopy (NS) measuring low frequency noise (LFN) in the range up to 1 kHz can identify the generation–recombination (G-R) contributions in causing fluctuations in the transistor channel current. It is therefore a very useful method to complement other techniques for analysing the quality of semiconductor materials and devices. Application of NS in GaN is widely used in analysing GaN HEMT and GaN MOS-HEMT performance1,4–6 and traps in the AlGaN and GaN layers of HEMT.5 LFN is also used to identify trap levels in oxide-GaN interface in MOS-HEMT.5 However, LFN has not been applied to characterize GaN epilayer. On the other hand, Deep Level Transient Spectroscopy (DLTS), invented by DV Lang,4 has been widely used to study n-GaN and p-GaN grown by MOCVD and MBE on sapphire, SiC, and silicon. The deep level traps commonly seen are located at $E_C - 0.25$ eV; $E_C - 0.60$ eV; and $E_C - 0.90$ eV in MOCVD grown GaN and at $E_C - 0.25$ eV; $E_C - 0.60$ eV; and $E_C - 0.68$ eV in MBE grown GaN.8–10 This work focuses on using LFN measurement to identify a G-R level with a deep level measured by DLTS. While DLTS is able to yield trap parameters such as their capture cross-section, energy level, and concentration, G-R noise is able to measure the trap level, capture-cross section, and also its concentration. Thus, by matching the common parameters, the deep level is identified with the G-R trap. Moreover, noise spectroscopy is quite an effective technique when DLTS fails: levels with very small capture cross sections and levels in semi-insulators.11–13

In order to conduct DLTS and low-frequency noise and G-R noise measurement in n-GaN wafer, GaN Schottky diodes were fabricated. The structure of n-GaN Schottky diode is shown in Figure 1. Ammonia based molecular beam epitaxy was used to grow a layer of lightly doped GaN:Si with thickness of 500 nm on a heavily doped GaN:Si on a Luminlog standard c-plane GaN grown by MOVPE on sapphire with a threading dislocation density of about mid $10^7$ cm$^{-2}$. To form the Schottky contact, a Ni/Au thin layer was deposited. The Ohmic contact was formed by depositing a Ti/Al/Ni/Au layer.

The setup for noise measurement is shown in Figure 1(b). In the setup, the device was biased by a battery voltage source to eliminate extraneous noise. The device was probed in the EM shielded box. The current through the device was amplified using a Stanford SR 560 low noise current amplifier. Next, the current signal was analysed using HP 35670 Dynamic signal analyser to obtain the power spectral density (PSD) which was displayed on a computer.

The main parts of DLTS system are (1) Boonton 7200 capacitance meter, (2) TTPX probe station, (3) HP 33220—function generator, (4) Oscilloscope, and (5) data acquisition box. The temperature of the sample being probed can be...
cooled down to 78 K by flowing of liquid N2. All devices were connected to the PC using GPIB cables. The DLTS measurement was controlled by a Labview program.

In order to prepare for noise and DLTS measurements, I-V and C-V characteristic of the Schottky diode were measured and presented in Figure 2. The leakage current of the diode is measured to be 10⁻²A/cm² at -4 V biased. At a forward bias of 1 V, the current density is about 1 A/cm². Following Cheung and Cheung, the series resistance and ideality factor were calculated from the I-V plot using the equation given below:

\[ \frac{dV}{dln(I)} = \frac{R_S}{n} + \frac{kT}{q}, \]

where \( R_S \) is the series resistance and \( n \) is the ideality factor. Using Eq. (1), the ideality factor was calculated to be 1.09 and \( R_S \) is 68 \( \Omega \). These results show that the Ohmic and Schottky contacts are suitable for making noise and DLTS measurements.

The C-V characteristic shows the typical curve in Fig. 2(c) with the value of the capacitance of about 40 pF at 0 V biased. Using the following equation:

\[ N(X_d) = \frac{\varepsilon(X_d)}{q} = \frac{C^3}{\varepsilon_0 A^2 \frac{dC}{dV}}, \]

the carrier concentration, \( N \), was calculated to be 3.2 \( \times 10^{16} \) cm⁻³. The electronic charge \( q = 1.602 \times 10^{-19} \) C, \( A = 0.089 \) mm² is device area, \( \varepsilon = 8.9 \) is permittivity of GaN, \( X_d = A \varepsilon S/C \) \( \mu m \) is the depletion width of the Schottky junction. This value of carrier concentration obtained from C-V measurement agrees well with that determined separately by Hall measurement. This value will be used later in the G-R noise and DLTS calculations.

The low-frequency noise in a GaN Schottky diode is due to the fluctuation of electron density and electron mobility. Under forward bias, the fluctuation of electron density is partly due to trapping and de-trapping of electrons from the deep level defects. This random capture and emission of carriers are the source of the generation-recombination (G-R) noise. The capture and emission processes involve free electrons and electron-traps and/or free holes and hole-traps. These traps can be either interface traps, or bulk traps present in the GaN layer. The noise voltage spectral density versus frequency, shown in Figure 3(a), includes a 1/f noise and an additional Lorentzian at low frequencies. The Lorentzian is G-R noise which occurs due to the presence of local centers in the band gap of the semiconductor. The voltage power spectrum density (PSD) of the fluctuations in the number of carriers is found to be:

\[ S_V(f) = \frac{B}{f} + A \left( \frac{1}{1 + (2\pi f)^2} \right) + 4k_B T R_S \left( V^2 / Hz \right). \]

Here, \( B \) and \( A \) are the amplitudes of the 1/f and G-R processes in the volume of the semiconductor, \( f \) is frequency, \( \tau \) is the characteristic time constant of the G-R processes, \( T \) is temperature, and \( R_S = 68 \) \( \Omega \) is sample resistance. The deviation of the spectrum from 1/f dependence is fitted to a Lorentzian function according to Eq. (3) and the result is shown in the inset of Figure 3(a). This allows the time constant of the G-R process, \( \tau \), to be extracted at a certain temperature. It is seen that within the temperature of 300 K, \( \tau \) is found to be 65 \( \mu s \).

The trap level is found from the Arrhenius plot of \( \ln(\tau T^2) \) vs 1/T following the equation develop by Scholz et al.:

\[ \ln(\tau T^2) \approx \frac{\Delta E}{k_B T} - \ln \left( \frac{4K^2 \sigma_n}{gh^3} \left[ 6\pi^3 m_e^{3/2} m_h^{1/2} \right]^{1/2} \right). \]

\( \Delta E = E_C - E_T \), \( E_T \) is the trap energy, \( g = 1 \) is the ground state degeneracy factor. The value of \( \Delta E \) was determined from the slope of the Arrhenius plot, shown in Figure 3(b). The value of capture cross section \( \sigma_n \) was determined from y-intercept follows Eq. (4). The trap energy level \( \Delta E \) is extracted and found to be as at \( E_C = -0.65 \pm 0.10 \) eV. Capture cross-section \( \sigma_n \) was found as \( \sim 10^{-14} \) cm².

The density of the trap was calculated from amplitude of G-R processes in the volume of the semiconductor using following equation:

\[ A = 4N_T/V_n d^2, \]

where \( A \) is the amplitude

**FIG. 1.** (a) Structure of the GaN Schottky diode and (b) the noise measurement setup.

**FIG. 2.** I-V (a) and C-V (b) characteristic of n-GaN Schottky diode, and (c) carrier concentration of the top layer of n-GaN.
of the G-R process in the volume of the semiconductor, $N_T$ is the trap density, $V$ is the sample volume, and $N_d$ is the donor concentration. The trap density was found to be $N_T \approx 10^{10}$ cm$^{-3}$ with $A = 1.342 \times 10^{-7}$.

The n-GaN Schottky diode was characterized by DLTS to obtain the deep trap parameters. These traps are found to be electron traps. The DLTS measurement of the change in the diode capacitance $\Delta C$ was carried out from 80 K to 400 K. Figure 4(a) shows the DLTS spectrum at a rate window of $80 \text{s}^{-1}$ which determines the value of $e_n$. As seen in the DLTS spectrum, three peaks located at about 160 K, 320 K, and 360 K were observed. Each peak represents a deep level trap in the GaN layer. In DLTS, the variation of the emission rate of a trap, $e_n$, with temperature, $T$, is given by

$$\ln\left(\frac{T^2}{e_n}\right) = \left(E_C - E_T\right)\frac{1}{k_BT} - \ln\left(\sigma_n'/a\right), \quad (5)$$

where $E_C$ and $E_T$ are the conduction band edge and the trap energy, respectively, $\sigma_n$ is the trap cross-section, and $\gamma_n$ is a constant related to the effective mass of the electron. In DLTS measurement, assuming the capture cross-section $\sigma_n$ is temperature independence. In order to identify the trap parameters, $\ln(T^2/e_n)$ versus $1/k_BT$ was plotted and shown in Figure 4(b), $e_n$ which is the reciprocal of the trap lifetime, $\tau$, is obtained from the value of the rate window and $T$ is the temperature at which the peak in $\Delta C$ versus temperature occurs. The slope of the Arrhenius plot gives the energy level. The $y$-intercept of the plot shows the capture cross-section of the traps. In Figure 4(b), three deep level traps were found to be located at $E_C - 0.26 \text{ eV}$, $E_C - 0.59 \text{ eV}$, and $E_C - 0.71 \text{ eV}$. Among these traps, the one at $E_C - 0.59 \text{ eV}$ is the dominant one. Table I shows the peak temperature and the corresponding rate window which determine $e_n$ of the peak located at $E_C - 0.59 \text{ eV}$.

The concentration and capture-cross-section of this trap are found to be $8 \times 10^{14}$ cm$^{-3}$ and $1.8 \times 10^{-14}$ cm$^{-2}$, respectively. This trap corresponds to the trap located at $E_C - (0.65 \pm 0.10) \text{ eV}$ found in LFN measurement as they have similar trap densities and capture cross-section.

This paper shows that noise spectroscopy can be used to identify the deep levels measured from DLTS that contribute to G-R noise. DLTS is able to characterize the energy level, trap density, and trap lifetime or emission rate of the traps. From G-R noise measurement, the energy level, trap density, and capture cross-section can also be identified. The measurements made on the n-GaN sample showed that of the 3 deep levels measured by DLTS at $E_C - 0.26 \text{ eV}$, $E_C - 0.59 \text{ eV}$, and $E_C - 0.71 \text{ eV}$, the one at $E_C - 0.59 \text{ eV}$ contributes to the G-R noise as their trap densities and capture cross section match closely to one from values another obtained by DLTS and noise spectroscopy.

This research was supported by the National Research Foundation Singapore through the Singapore MIT Alliance for Research and Technology’s LEES IRG research programme.

\begin{table}[h]
\centering
\begin{tabular}{|c|c|c|c|c|c|c|c|}
\hline
Peak temperature, K & 302 & 314 & 319 & 331 & 345 & 359 \\
\hline
Rate window, $e_n$, $s^{-1}$ & 20 & 50 & 80 & 200 & 400 & 1000 \\
\hline
\end{tabular}
\caption{Peak temperature and the corresponding rate window of the peak located at $E_C - 0.59 \text{ eV}$.}
\end{table}

\textsuperscript{6}D. C. Look, Z. Q. Fang, and B. Claflin, J. Cryst. Growth \textbf{281}, 143–150 (2005).
\textsuperscript{7}R. S. Pengelly, S. M. Wood, J. W. Milligan, S. T. Sheppard, and W. L. Pribble, \textit{IEEE Trans. Microwave Theory Tech.} \textbf{60}, 1764 (2012).
\textsuperscript{8}B. M. Green, K. K. Chu, E. M. Chumbes, J. A. Smart, J. R. Shealy, and L. F. Eastman, \textit{IEEE Electron Device Lett.} \textbf{21}, 268 (2000).
\textsuperscript{9}A. E. Chernyakov, M. M. Sobolev, V. V. Ratnikov, N. M. Shmidt, and E. B. Yakimo, \textit{Superlattices Microstruct.} \textbf{45}, 301 (2009).
5. S. C. Binari, P. B. Klein, and T. E. Kazior, Proc. IEEE 90, 1048 (2002).
6. A. V. Vertiatchikh and E. F. Lester, IEEE Electron Device Lett. 24, 535 (2003).
7. D. V. Lang, J. Appl. Phys. 45, 3023 (1974).
8. Z.-Q. Fang, D. C. Look, J. Jasinski, M. Benamara, Z. Liliental-Weber, and R. J. Molnar, Appl. Phys. Lett. 78, 332 (2001).
9. A. Hierro, D. Kwon, S. A. Ringel, M. Hansen, and J. S. Speck, Appl. Phys. Lett. 76, 3064 (2000).
10. A. Hierro, S. A. Ringel, M. Hansen, J. S. Speck, and U. K. Mishra, Appl. Phys. Lett. 77, 1499 (2000).
11. M. E. Levinshtein and S. L. Rumyantsev, Semicond. Sci. Technol. 9, 1183 (1994).
12. V. P. Kunets, T. A. Morgan, Y. I. Mazur, V. G. Dorogan, P. M. Lytvyn, M. E. Ware, D. Guzun, J. L. Shultz, and G. J. Salamo, J. Appl. Phys. 104, 103709 (2008).
13. D. Sodini, A. Touboul, G. Lecoy, and M. Vavelli, Electron. Lett. 12, 42 (1976).
14. S. K. Cheung and N. W. Cheung, Appl. Phys. Lett. 49, 85 (1986).
15. S. M. Sze, Physics of Semiconductor Device (John Wiley & Sons, 2006).
16. F. Scholz, J. M. Hwang, and D. K. Schroder, Solid-State Electron. 31, 205 (1988).
17. F. J. Scholz and J. W. Roach, Solid-State Electron. 35, 447 (1992).
18. P. Blood and J. W. Orton, The Electrical Characterization of Semiconductors: Majority Carriers and Electron States (Academic, San Diego, CA, 1992).