Efficient organic photomemory with photography-ready programming speed

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We propose a device architecture for a transistor-type organic photomemory that can be programmed fast enough for use in electrical photography. Following the strategies used in a flash memory where an isolated charge storage node or floating gate is employed, the proposed organic photomemory adopts an isolated photo-absorption zone that is embedded between upper and lower insulator layers without directly interfacing with a semiconductor channel layer. This isolated photo-absorption zone then allows the device to operate in electrically 'on' state, in which the high electric-field region can have a maximal spatial overlap with the illuminated area for efficient and facile light-programming. With the proposed approach, a significant threshold voltage shift is attained even with the exposure time as short as 5 ms. High quality dielectric layers prepared by initiated chemical vapor deposition ensure erasing to occur only with electrical signal in a controlled manner. Retention time up to 700 s is demonstrated.

Recent advances in organic electronics have proven their significance and competence in optoelectronic applications¹–⁶. In particular, organic photosensors have been regarded promising due to their numerous potential applications in image sensing⁷–⁹, optoelectronic communication³–⁶, and health care monitoring¹⁰–¹¹. They have been developed with various structures and operating mechanisms¹⁴ such as photoconductors¹⁵,¹⁶, photodiodes¹⁷–¹⁹, phototransistors²⁰–²², and photomemories. Among them, photomemories combine both photosensors and memories, and thus enable simplified circuitry by eliminating the need for independent memory components to store the output of the photosensors²³–²⁹. In particular, photomemories based on a transistor structure provide additional advantages, in that they allow for further simplification of pixel-level structure by serving as photosensors, memories, and switching devices all at once. For that reason, organic photomemories based on a transistor structure have been proposed with various operating mechanisms, including a photochromic channel semiconductor²³, a quantum dot embedded channel semiconductor²⁵, interface trap²⁴,²⁶,²⁸, or an additional layer inserted between a channel semiconductor and a gate dielectric layer²⁷. (Hereafter, "organic photomemory (OPM)" refers to the devices based on a transistor structure, unless specified otherwise.)

Most OPMs reported to date, however, have needed prolonged light-programming time (t_{prgm}), typically longer than a second. To the best of our knowledge, even the fastest device required a t_{prgm} as large as 0.25 s²⁷. A short t_{prgm} is an important performance factor for any practical applications of photosensing or imaging systems. For example, in electrophotography, minimum shutter speed is ultimately limited by the response time of the photosensor. In an X-ray imaging system, the necessary amount of X-ray exposure may be too large for a human body if a sensor does not respond fast enough. Hence, substantial progress needs to be made to reduce t_{prgm} as much as possible. Here, we propose a novel device architecture for OPMs based on an isolated photo-absorption zone (PAZ) that leads to a significantly reduced t_{prgm}.

Results and Discussion

The structure of the proposed OPM device is presented in Fig. 1a. It is based on a bottom-gate, top-contact n-channel organic thin-film transistor (OTFT), adopting a semiconductor channel made of evaporated C₆₀. It incorporates a 30 nm-thick C₇₀ layer as a PAZ in between two polymer gate insulating layers, poly(1,3,5-trimethyl-1,3,5-trivinyl cyclotrisiloxane) (pV3D3) deposited by initiated chemical vapor deposition (iCVD), which has...
This is in a clear contrast with the proposed OPM, which exhibits a linear increase in $\Delta V_{th}$ without the PAZ, which does not exhibit any sign of light-programming function (See Supplementary Fig. S1).

If desired, materials with a particular spectral absorption band may also be chosen, for example, for a targeted color sensitivity. The role of the PAZ in light-programming is verified using a device having essentially the same structure but recently been demonstrated as a versatile gate insulator that can be down-scaled to a few tens of nanometers while retaining excellent insulating properties (Fig. 1b)\textsuperscript{30}.

The overall device structure in this work is inherited from a flash memory, which is in essence a transistor with a floating gate embedded in between a tunneling dielectric layer and a blocking dielectric layer\textsuperscript{31}. For a photo-induced memory programming, the floating gate is replaced with a PAZ. In this way, the PAZ is isolated from the channel itself, and from the interface made with the channel, leading to an inherent advantage in that it allows one to individually choose the material best suited for the photosensitive function and the semiconductor role, respectively, with little concern for cross-interference. Unlike a typical flash memory where a tunneling dielectric layer is thinner than a blocking dielectric layer, the thicknesses of the two insulating layers are designed to be the same, so that the device is programmed not by carriers exchanged through a tunneling dielectric layer by tunneling, but by the carriers photo-generated within the PAZ.

The principle of operation behind the light-induced programming in the proposed device is schematically shown in the enlarged cross-sectional view in Fig. 1a, and involves several sequential steps: 1) generation of excitons in the PAZ upon the absorption of light; 2) exciton dissociation into separate electrons and holes under the influence of the gate-bias ($V_G$) induced electric field; 3) drift of the separated electrons and holes toward the opposite sides of the PAZ/dielectric interfaces; and 4) accumulation/trap of electrons and holes at the PAZ/dielectric interfaces, respectively. This spatially separated distribution of positive and negative charges causes an internal potential difference across the PAZ and induces a threshold voltage shift ($\Delta V_{th}$) in the opposite direction with respect to the applied $V_G$. The relationship between $\Delta V_{th}$ and the photo-generated and trapped carrier density ($n_{charge}$ in cm$^{-2}$) is described by\textsuperscript{32,33}:

$$|\Delta V_{th}| = |q|n_{charge}d_{PAZ}\varepsilon_{PAZ}$$

where $q$ is the electronic charge, $d_{PAZ}$ and $\varepsilon_{PAZ}$ is the thickness and the permittivity of a given PAZ, respectively.

Figure 2a presents the transfer curves of the proposed OPM device at the initial state and after various stimuli conditions, which consist of illumination (3.76 mWcm$^{-2}$ white LED) and/or $V_G$ pulse (22 V, 50 ms) with both of the source and drain electrodes grounded. The electrical characteristics of the device, except $V_{th}$, remain virtually unaltered after light-programming. A significant $\Delta V_{th}$ in a negative direction is observed when both the light and the $V_G$ pulse are applied simultaneously, while only a negligible shift is observed when the light and $V_G$ pulse stimuli are applied individually. (Hereafter, “light-programming” thus refers to the case where both illumination and $V_G$ pulse stimuli are applied at the same time, unless specified otherwise.)

Note that the light-programming voltage of 22 V is larger than ca. 12 V, typically used for programming of non-volatile flash memories. To cope with this issue to some degree, one may scale down the bottom and top dielectric layers. While it is challenging to maintain the insulating properties of polymeric layers at thickness below several tens of nanometers, iCVD-based pV3D3 layers were shown to hold well their excellent insulating characteristics even at a thickness of 10–20 nm or less\textsuperscript{9}, making it likely to further bring down the programming bias by decreasing the thickness of the bottom and top pV3D3 layers.

The role of the PAZ in light-programming is verified using a device having essentially the same structure but without the PAZ, which does not exhibit any sign of light-programming function (See Supplementary Fig. S1). This is in a clear contrast with the proposed OPM, which exhibits a linear increase in $|\Delta V_{th}|$ as the irradiance of the incident light increases (Fig. 2b).

From the discussions made above regarding the operation mechanism and the experimental photo-electrical characteristics shown in Fig. 2, it can be easily seen that the PAZ is the most important component affecting the high photo-responsivity of the proposed OPM, and $C_{eq}$ was adopted due to its high photon absorption in the visible spectral range (Supplementary Fig. S2). Depending on applications, $C_{eq}$ may be replaced with another highly absorbing organic or inorganic semiconductor provided that process for its preparation is compatible with the overall fabrication process. If desired, materials with a particular spectral absorption band may also be chosen, for example, for a targeted color sensitivity.
C70 to the pV3D3 deposition process is also observed in electrical properties, as well as in the capacitance-voltage characteristics obtained from the Al/C70/pV3D3/Al device (Fig. 3c); the adequate switching behavior from charge depletion to accumulation upon increase in bias can be clearly seen, as expected. All these results indicate that C70 retains its semiconductor characteristics from both optical and electrical perspectives.

For the channel semiconductor, high electrical performance and low photo-absorption are preferred. C60 was previously shown to exhibit high mobility of up to 1.5 cm²/Vs and stable operation to electrical stress when deposited on the pV3D3 surface in a TFT geometry (Fig. 2a; Supplementary Fig. S3). The photo-absorption of C60 is much smaller than that of C70 due to its spherical symmetry (Fig. 3a)34, and thus a reduction in the photo-absorption in the C70 PAZ due to the optical “filter effect” of C60 can be minimized (Fig. 3a; Supplementary Fig. S2). The observed lack of light-programming behavior in the control device without the PAZ suggests that the C60 channel layer is unable to show any significant photo-induced memory by itself, and the absorption in the C70 layer is indeed a main origin for the photo-response (Fig. 3a; Supplementary Fig. S1).

For the C60 layer to properly operate as a PAZ, its electrical and optical characteristics should not be degraded during the device fabrication process. This could be critical because organic semiconductors are typically prone to damage by heat or reactive chemicals which might be introduced in the process of depositing the insulating layer on its top. In this respect, the iCVD processed pV3D3 is expected to play a key role, not only for its excellent insulating properties but also for the damage-free benefit of the iCVD process, which is a solvent-free, vapor-based technique performed at a low substrate temperature of approximately 40 °C30. Absorption spectra of a C70 layer during the device fabrication process. This could be critical because organic semiconductors are typically prone to damage by heat or reactive chemicals which might be introduced in the process of depositing the insulating layer on its top. In this respect, the iCVD processed pV3D3 is expected to play a key role, not only for its excellent insulating properties but also for the damage-free benefit of the iCVD process, which is a solvent-free, vapor-based technique performed at a low substrate temperature of approximately 40 °C30. Absorption spectra of a C70 layer shown in Fig. 3b turn out to be virtually identical before and after the deposition of pV3D3. Similar immunity of C70 to the pV3D3 deposition process is also observed in electrical properties, as well as in the capacitance-voltage characteristics obtained from the Al/C70/pV3D3/Al device (Fig. 3c); the adequate switching behavior from charge depletion to accumulation upon increase in bias can be clearly seen, as expected. All these results indicate that C70 retains its semiconductor characteristics from both optical and electrical perspectives.

It is also noteworthy that the two insulating layers play a dual role; they work as gate dielectrics and, at the same time, provide the PAZ with physical and electrical isolation from the gate and the channel. Approximately 38 nm-thick pV3D3 polymer layers are used for both of the insulating layers. They show a leakage current density of around 10⁻⁹ Acm⁻² for an electrical field intensity of up to 3 MVcm⁻¹ (Fig. 3d)30. This excellent insulating property provides sufficient electrical isolation of the PAZ during the light-programming and reading processes, so that the light-induced charges will not be interfered by exchange of charges with the gate or channel via tunneling. A sudden increase in the leakage current density is observed with an electric field over 3.5 MVcm⁻¹, and it was previously shown to originate from Fowler-Nordheim tunneling (see Supplementary Fig. S4 for details), indicating that the ideal insulating behavior of pV3D3 layers is enabled by their sufficiently low defect densities30. In terms of optical property, the pV3D3 layers do not absorb any visible light due to its wide bandgap over 8 eV (Fig. 3a, Supplementary Figs S2 and S5)30, and that is also beneficial for the proposed photomemory operation originating in the absorption of PAZ.

ΔV_th was measured as a function of t_prog using the collimated illumination (3.76 mWcm⁻²) from a white LED and a gate bias of 22 V (Fig. 4a). Most of all, it is noteworthy that the device exhibits a distinguishable ΔV_th over 2.5 V even with the gate bias pulse as short as 5 ms. This level of programming time is far smaller than those of the other OPM reported so far. In most of those devices, light-programming was performed in the electrically ‘off’ state because their photosensitive parts involved channels themselves; in that case the gate-induced charges in the ‘on’ state could easily shadow the photo-induced effect and thus ‘off’ state programming is preferred. However, the gate-induced electric field in the ‘off’ state is applied mostly under the area of the source/drain electrodes that is overlapped with a gate electrode. This could be disadvantageous because light enters into the device layers (channels or dielectrics) mostly through the channel area that is not covered by the metallic source/drain electrodes (Fig. 4b)33–29. In that case, the light-programming, requiring both electric field and photon-absorption, is discouraged by significantly low electric field in the channel area, or by the lack of photons in the region under source/drain electrodes.
In contrast, the OPM proposed in this work conducts light-programming in the ‘on’ state thanks to the isolation of the PAZ from its channel (Fig. 1a). In this way, the electric field by $V_G$ can be applied to the whole channel area using accumulated charges at the channel; consequently, locations where both electric field and light are strong can be well matched with a maximal spatial overlap, enabling fast and efficient photomemory operation (Fig. 4c). The external quantum efficiency (ηEQE) of the proposed OPM can be estimated for constant, uniform illumination of a monochromatic light with the wavelength of $\lambda$ by:

$$\eta_{EQE} = \Phi_{light} \frac{n_{charge}}{t_{prgm}/(hc/\lambda)} \propto A_{abs} \eta_{sep} \eta_{cc}$$

where $\Phi_{light}$ is the irradiance of the incident light, $h$ is the Planck constant, $c$ is the speed of light, $A_{abs}$ is the absorption at the photoactive layer (PAZ in the present work), $\eta_{sep}$ is the probability of the charge separation from excitons, and $\eta_{cc}$ is the efficiency for photo-generated carriers to be collected, i.e., transported and trapped, at the PAZ/dielectric interfaces. Note that the positive correlation of $\eta_{EQE}$ on the magnitude of electric field mainly results from those of $\eta_{sep}$ and $\eta_{cc}$ 35,36. Hence, low electric field across PAZ would result in low EQE and would thus require a long $t_{prgm}$ for $n_{charge}$ to reach a value corresponding to a target $\Delta V_{th}$.

In the case of the proposed OPM used in Fig. 2b, $\eta_{EQE}$ is estimated to be as large as ca. 10% at $\lambda = 528$ nm for $t_{prgm} = 50$ ms, being consistent with the observed efficient, fast programming capability of the proposed OPM. It is noteworthy that $\eta_{EQE}$ is a monotonically decreasing function of $t_{prgm}$ because $n_{charge}$ is a sublinear function of $t_{prgm}$ as can be seen from Fig. 4a, where $\Delta V_{th}$ (and thus $n_{charge}$) is proportional to the logarithm of $t_{prgm}$. This saturation like-behavior originates from the fact that $\eta_{sep}$ and $\eta_{cc}$ decrease as the electric field decreases 35,36 and that the electric field in the PAZ due to $V_G$ is reduced by the compensating field resulting from accumulated/ trapped charges at each interface of PAZ that increase with $t_{prgm}$.

The response of the proposed OPM may also be described in terms of the exposure ($H$) used for photographic films 37, which is a parameter defined as the irradiance of light times the exposed time. This is particularly the case because the signal in the proposed OPM depends on $n_{charge}$ formed during a given period of time rather than $n_{charge}$ per unit time. As the optical density is given by a monotonically increasing function of $H$ in a photographic film, $|\Delta V_{th}|$ increases with $H$ in the proposed OPMs (Supplementary Fig. S6) with $H$ of the OPM defined by:
Eventually, the detectivity of a given OPM may be determined by the minimum $H$ leading to a measurable $|\Delta V_{th}|$ considering its error range. The proposed OPM exhibited $|\Delta V_{th}|$ as large as a few volts even with the $H$ of 0.015 mW·s·cm$^{-2}$, confirming its effectiveness in utilizing the incident light, leading to efficient and fast programming.

To erase the stored information in the proposed OPM, one approach would be to apply gate bias with the opposite polarity to that of programming operation. However, this is expected to be inefficient for the same reason that the effective area of the gate-induced electric field is limited, in the turned-off state, only to the region under the source/drain electrodes while the charges to be erased are distributed in the region under the channel. Hence, we propose an erasing signal having the same polarity as the $V_{G}$ for programming. If one applies $V_{G}$ large enough, Fowler-Nordheim tunneling current occurs through both bottom and top insulating layers, and thus trapped carriers at the bottom or top C$_{70}$/pV3D3 interfaces can be taken out or recombined with injected carriers with the opposite polarity (Fig. 5a). The erasing $V_{G}$ in this scheme is estimated to be 40 V, which corresponds to an electric field in the pV3D3 layers of over 4.4 MVcm$^{-1}$, corresponding to the Fowler-Nordheim tunneling regime in the leakage current density vs. electric field intensity characteristics shown in Fig. 3d (see Supplementary Fig. S7 for further details). For the programmed device with $\Delta V_{th}$ of $-6$ V, the negatively shifted $V_{th}$ is shown to be fully recovered with this erasing $V_{G}$ and saturated at the initial $V_{th}$ after about 200 ms, indicating that stored carriers are completely bleached (Fig. 5b). The energy level alignment between Al gate electrode and C$_{70}$, C$_{60}$ semiconductor layers and the identical thickness of the bottom and top pV3D3 layers ensures the same tunneling current through both the bottom and top insulating layers, thereby preserving the charge neutrality during the erase process$^{38-40}$. The charge neutral behavior in the PAZ can be verified by the fact that memory characteristics remain almost constant with the repeated programming and erasing cycles (Fig. 5c). The proposed photomemory exhibited homogeneous light-programming and electrical erasing properties over 50 times of endurance test (Supplementary Fig. S8).

The retention time for the programmed state was about 700 s and 10 ks, to maintain 90% and 50% of the programmed $\Delta V_{th}$, respectively (Fig. 5d). 700-s retention will be long enough for light or image sensor applications in which brightness can be mapped in space at a given time and refreshed in a short interval. With such memory characteristics, “rolling-shutter” operation typically needed for CMOS-based image sensors is not necessary, and instead desirable “global shutter” operation can be realized$^{41}$. If one is simply to use the criterion to verify the presence of light, 10 ks retention may suffice for various short-term applications where the stored visual information may be retrieved within several hours.

In summary, an organic photomemory (OPM) with an isolated photo-absorption zone (PAZ) was proposed in a flash memory-like transistor structure. The PAZ made of C$_{70}$ was surrounded by iCVD-grown top and bottom insulator layers and thus isolated from the channel. This isolation of the PAZ allows light-programming to be done with the channel ‘on,’ enabling full use of the PAZ under the whole channel region and thus leading to substantial reduction in light-programming time, down to as low as 5 ms. This result carries significant meaning as it is the first report of an OPM whose light-programming speed is comparable to the ordinary frame speed of a camera. Tunneling-based electrical erasing of the photo-stored information was also performed, demonstrating that the OPM could be used repeatedly. Together with the extensibility to individual color sensing or global shutter system as well as the simplicity that can be offered, the proposed OPM may open up the possibility for next-generation applications such as image- or X-ray mappers that would benefit from the large-area capability and form factor advantages of organic materials.
Methods

Device fabrication. Glass substrates were cleaned in an ultrasonic bath with detergent-dissolved deionized water, deionized water, acetone, and 2-propanol in sequence, and were then dried in a vacuum oven. Al (70 nm) was thermally evaporated in a vacuum chamber for the gate electrode. pV3D3 was deposited by iCVD process as a bottom insulating layer, and then, a C70 film was evaporated as a PAZ in the vacuum chamber, followed by the deposition of the top pV3D3 layer by iCVD process. Finally, C60 (50 nm) and Al (70 nm) were deposited by thermal evaporation for a semiconducting channel layer and source/drain electrodes. Sample transfer process was controlled such that there should be no ambient air exposure after C70 deposition throughout the fabrication and characterization process. Every layer except the insulating layers was patterned by metal shadow masks.

The device used in the irradiance-dependence measurement had the thicknesses of 59 nm, 26 nm, and 55 nm for top insulator, PAZ, and bottom insulator, respectively, and the rest of the results were obtained from a device having the thicknesses of 36 nm, 30 nm, 39 nm for the respective layers. The thickness of the pV3D3 layers and the C70 layer was estimated from their capacitance in a metal-insulator-metal structure with Al for both metal electrodes. All the memory devices had a channel length and width of 200 μm and 1000 μm, respectively.

Optical analysis. The calculation of absorptions was performed using a MATLAB code based on a transfer matrix formalism dealing with the optical properties of a multi-layer thin film structure. The optical constants of each layer composing the proposed device were obtained by spectroscopic ellipsometry (Supplementary Fig. S2). Absorption spectra were measured in ambient air using a UV/Vis/NIR Spectrometer (Lambda 950, Perkin Elmer).

Device characterization. Devices characteristics were measured in a nitrogen-filled glove box under controlled dark ambient. Both green and white LED light sources were collimated and had the area of illumination large enough to cover the whole channel region of the measured devices. The dark condition was maintained during the characterization of the OPM devices.

The capacitances were measured using a precision LCR meter (HP4284, Agilent) and the current versus voltage characteristics were measured using a semiconductor parameter analyzer (HP4155A, Agilent). The field effect mobility was extracted by a transconductance method from linear region transfer characteristics.
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Author Contributions
M.K. and S.Y. conceived an idea on organic photomemory devices. M.K., S.L. and H.M. designed associated experiments. H.S. and S.G.I. carried out the deposition of pV3D3 polymer dielectric layers by iCVD process. M.K., S.L. and H.M. characterized the thickness and the electrical properties of pV3D3 with metal-insulator-metal devices and C$_{60}$ transistors. H.K. performed optical analysis of C$_{60}$, C$_{70}$, pV3D3 layers and optical simulation for absorption spectra of each layer with customized MATLAB code. M.K. designed and fabricated organic photomemory devices and measured irradiance response, light-programming speed, electrical erasing time, and retention time. All authors read and discussed the results of manuscript. S.Y. contributed as a corresponding author.

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