A New Fault Location Method Independent of Line Parameters

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\textbf{Abstract}—To restore power feeding as soon as possible and reduce repair costs and labor, a precise and robust fault location method for transmission lines is proposed. This method is based on the current and voltage synchronously collected by the phasor measurement units (PMUs) at two terminals of the line and does not require line parameters to calculate the fault distance. The line parameter is not approximately constant, but is affected by power load, temperature, and humidity, which affects the accuracy of most fault location algorithms that rely on line parameters. Therefore, the method proposed in this paper is robust and accurate. The method is based on the sequence fault component network and synchronous measurement technology, which is not affected by the system’s pre-fault state, fault type, fault inception angle, and fault phase. Then, the method is verified in PSCAD/EMTDC by choosing different path resistances, fault types, fault inception angles, load currents, and line transpositions. A large number of simulation results show that the proposed method has high accuracy and robustness.

1. INTRODUCTION

With the development of society and growth of economic activities, power grid has played an increasingly important role. In order to meet the needs of society, the voltage level and capacity of the power grid are also increasing. Among many protection devices in the power grid, fault location equipment can calculate the distance from the reference point to the fault point. When faults occur, in order to reduce labor costs and restore power as soon as possible, it is necessary to develop a more accurate and faster fault location method. In the past two decades, related fault location techniques have been proposed in the literatures.

The fault location of transmission lines can be divided into three categories [1]: fault analysis algorithm, traveling wave location algorithm [2, 3], and intelligent location algorithm according to different location principles [4–6]. Fault analysis method requires the relevant system parameters and voltage and current to be obtained by line measuring device. According to different sources of line parameter acquired, it is divided into single terminal method and two terminals method [7, 8].

Single terminal fault location method calculates the distance through the impedance of the transmission from measuring terminal to fault point. This algorithm requires that the parameters include voltage and current measured from one end, line impedance, zero sequence impedance, and path resistance [9, 10]. Therefore, the single terminal method is relatively simple and easy to implement but not accurate. The zero-sequence impedance needs to be known in advance but is difficult to obtain accurately and is affected by the path resistance and line parameters. Obviously, the advantage of the algorithm in this paper is that it does not require zero-sequence components and line parameters in the derivation process. The algorithm does not require zero sequence data to locate the fault.

The two terminals fault location method is not affected by the path resistance, and it does not need to judge the fault type [11–13]. Compared with the single-terminal method, the two-terminal
method tends to have higher accuracy. The method needs to obtain the information of the measuring devices at both ends of the line, so it requires advanced communication technology and complicated device. Data sampling at both ends can be either synchronized or unsynchronized [14–17]. Synchronous data sampling increases the complexity of the algorithm, but the positioning accuracy is high, and synchronization algorithms are also required. Asynchronous sampling reduces the complexity of the algorithm, but the positioning accuracy is reduced, and asynchronous algorithm is required. With the development of communication technology, it is more convenient and reliable to obtain data at both ends simultaneously.

Jiang et al. [18] have proposed a single terminal fault distance method, which uses phase current and voltage calculation at one end. Liao [19] has developed a positioning method that uses data at both ends and requires data synchronization. Yu et al. [20] has proposed a positioning method for series compensation lines. Liao and Elangovan [21] have proposed a technique for fault location, using asynchronous data at two terminals or three terminals. Feng and Abur [22] have proposed a fault location technique for transmission lines using Kalman filter. Zhao et al. [23] have developed fault location technique based on two terminals. The algorithm is accurate but requires line parameters, and does not consider the effects of parameter changes on the calculation results.

The above-mentioned fault location methods all need to know line parameters and line length in advance. However, line parameters, especially zero sequence parameters, are not always known. Even if available, the line parameters can vary with temperature, humidity, and load. Therefore, inaccurate parameters may affect the location accuracy of the algorithms.

Zhang et al. [24] and Liao [25] have proposed the fault location algorithm that does not require line parameters. The algorithm only uses voltage and current to solve asymmetric fault location. The algorithm proposed in this paper can locate various faults without prefault data. The algorithm only uses positive and negative sequence fault components and does not require zero sequence components, so it is not affected by zero sequence coupling. The algorithm uses the voltage and current measured at both ends, evolved from synchronous data sampling. The algorithm does not require a high sampling frequency, compared to the fault location method based on the traveling wave principle.

This paper describes in detail the data sampling, transformation, fault component extraction, and location algorithm in Section 2. For different fault types, path resistances, fault inception angles, line transpositions, and line loads, a large number of simulations are carried out with PSCAD in Section 3.

2. DERIVATION OF THE FAULT LOCATION ALGORITHM

In this section, a precise fault location for transmission lines independent of line parameters is presented. The detailed evolution process for asymmetrical and three-phase short-circuit faults is given. The algorithm implementation steps are as follows.

1. Obtain the instantaneous values of the three-phase voltage and current on both sides of the transmission line;
2. Perform fault component extraction and obtain the fault components of the three-phase voltage and current on both sides of the line;
3. Differentially filter the fault component of the three-phase current to suppress the attenuated DC component in the fault current;
4. Obtain the corresponding fundamental frequency phasor component through full-wave Fourier transform;
5. Achieve amplitude and phase compensation for the fundamental frequency component of the fault;
6. Filter sequence to obtain the positive and negative sequence fault components of the voltage and current on both sides of the line.

2.1. System Description

Figure 1 illustrates the single-line diagram of the selected 200 km, 500 kV transmission line. The PMU is installed at both ends of the line to measure the instantaneous values of the three-phase voltage and current, where
Figure 1. Single-line diagram of transmission line.

F fault location,
S sending end of the line,
R receiving end of the line,
D transmission line length,
l fault distance.

In Fig. 2, different short-circuit faults with path resistance are shown. The algorithm in this paper solves the problem of fault location for asymmetric short-circuit and three-phase short-circuit (TPH). Asymmetric short circuits include double-phase short circuits (DPH), single-phase to ground short circuits (SPG), and double-phase to ground short circuits (DPG).

Figure 2. Different fault diagrams. (a) TPH. (b) DPH. (c) SPG. (d) DPG.

2.2. Fundamental Frequency Phasor Component and Sequence Fault Component

In order to reduce the detection error of system oscillation and frequency offset, Equation (1) is used to extract the fault component.

\[
\Delta f(k) = [f(k) - f(k - 2N)] - [f(k - 2N) - f(k - 4N)]
\]  

(1)
where $f$ is defined as electrical physical quantities such as voltage or current, and $N$ is the number of sampling points per cycle.

Using the fault extraction method of Equation (1), the current of the fault extraction algorithm proposed is smaller, which reduces the detection error of system oscillation and frequency offset. The detailed deduction process is described in the literature [23].

Differential filtering performs amplitude and phase compensation for the current phasor of the fault component. Differential filtering is as follows:

$$d\Delta i(t) = \Delta i(t) - \Delta i(t - \Delta T)$$

where $\Delta i(t)$ is the instantaneous value of three-phase fault component current, and $d\Delta i(t)$ is the instantaneous value of three-phase fault component current after differential filtering. $\Delta T$ is the sampling period.

From Taylor series, Equation (2) can be rewritten as:

$$d\Delta i(t) = \Delta i(t) - \Delta i(t - \Delta T)$$

$$= I_0 e^{-\frac{t}{\tau}} + I_{m1} \sin (\omega t + \theta_1) - \left[ I_0 e^{-\left(\frac{(t-\Delta T)}{\tau}\right)} + I_{m1} \sin (\omega t + \theta_1 - \omega \Delta T) \right]$$

$$\approx -\frac{1}{\tau f_s} I_0 e^{-\frac{t}{\tau}} + 2I_{m1} \sin \left( \frac{\pi}{N} \right) \sin \left( \frac{(\omega t + \theta_1)}{2} - \frac{\pi}{N} \right)$$

where $\tau$ is the time constant, $I_{m1}$ the AC component amplitude of current fault component, and $N$ the number of sampling points per cycle. From Equation (3), the DC component is very small.

Fault phase components equation is the following:

$$\Delta U_1 = \frac{1}{3} \left( \Delta U_a + \alpha \Delta U_b + \alpha^2 \Delta U_c \right)$$

$$\Delta I'_1 = \frac{1}{3} \left( d\Delta I_a + \alpha d\Delta I_b + \alpha^2 d\Delta I_c \right)$$

where $\alpha = \exp(j2\pi/3)$.

The amplitude and phase angle of the wave after the differential filter are different from that of the initial wave. Equation (6) is valid when the sampling frequency is 2000 Hz. Amplitude and phase compensation Equation is as follows:

$$\Delta_i = \Delta I'_1 \cdot 6.37 \angle (-85.5^\circ)$$

where $\Delta I'_1$ is the phasor form corresponding to Equation (3). The amplitude ratio is 6.37, and the phase difference is $-85.5^\circ$.

2.3. Asymmetrical Fault Algorithm

Through the above-mentioned digital processing process, the positive sequence, negative sequence, and zero sequence fault components of voltage and current are calculated. For asymmetric short-circuit faults, this algorithm only uses positive sequence and negative sequence fault components. Therefore, the transmission line shown in Fig. 1 can be represented by sequence component networks, including: positive (1), negative (2), and zero (0). The positive sequence and negative sequence component network diagrams used in the algorithm are shown in Figs. 3 and 4.

In Figs. 3 and 4, the voltage balance equation is as follows:

$$\Delta \bar{U}_{S1} - z_1 \Delta I_{S1} = \Delta \bar{U}_{R1} - z(D-l)\Delta I_{R1}$$

$$\bar{U}_{S2} - z_1 I_{S2} = \bar{U}_{R2} - z(D-l)I_{R2}$$

where $\Delta \bar{U}_{S1}$, $\Delta \bar{U}_{S2}$ are respectively positive and negative sequence fault phase voltages at the bus on the sending side; $\bar{U}_{R1}$, $\bar{U}_{R2}$ are respectively positive and negative sequence fault phase voltages at the bus on the receiving side; $\Delta I_{S1}$, $\Delta I_{S2}$ are respectively positive and negative sequence fault phase currents at the bus on the sending side; $I_{R1}$, $I_{R2}$ are respectively positive and negative sequence fault phase currents at the bus on the receiving side; $z$ is the line impedance per unit length.
From Equations (7) and (8), the fault location solution is as follows:

\[ zl = \frac{(\Delta U_{S1} - \Delta U_{R1}) \Delta i_{R1}}{\Delta i_{S1} \Delta i_{R1} - \Delta i_{S2} \Delta i_{R1}} \]  
(9)

\[ z(D - l) = \frac{(\Delta U_{S1} - \Delta U_{R1}) \Delta i_{S1} - (\Delta U_{S1} - \Delta U_{R1}) \Delta i_{S2}}{\Delta i_{S1} \Delta i_{R2} - \Delta i_{S2} \Delta i_{R1}} \]  
(10)

In order to find a fault location solution independent of line parameters, \( l \) is expressed as a percentage of \( D \)

\[ l\% = 100 \frac{zl}{zl + z(D - l)} \]  
(11)

An equation independent of line parameters is obtained by substituting Eqs. (9) and (10) into Eq. (11). Then the fault distance can be obtained as follows:

\[ l\% = 100 \frac{(\Delta U_{S1} - \Delta U_{R1}) \Delta i_{R1} - (\Delta U_{S1} - \Delta U_{R1}) \Delta i_{S2} \Delta i_{R1}}{(\Delta U_{S1} - \Delta U_{R1}) (\Delta i_{S1} + \Delta i_{R1})} \]  
(12)

For asymmetric faults, only the positive sequence and negative sequence components are used, and the result of fault location is more accurate. Path resistance is not used in Equation (12), and it will not affect the result of location.

### 2.4. Three-Phase Fault Algorithm

For symmetric faults, there is no negative sequence component, which limits the application of Equation (12).
In Fig. 5, the voltage balance equation is as follows

\[ \Delta \hat{U}_{S1} - zl \Delta \hat{I}_{S1} = \hat{U}_f \]  
(13)

\[ \Delta \hat{U}_{R1} - z(D - l) \Delta \hat{I}_{R1} = \hat{U}_f \]  
(14)

For three phase faults, Equations (13) and (14) can be rewritten as

\[ zl = \frac{\Delta \hat{U}_{S1} - \hat{U}_f}{\Delta \hat{I}_{S1}} \]  
(15)

\[ z(D - l) = \frac{\Delta \hat{U}_{R1} - \hat{U}_f}{\Delta \hat{I}_{R1}} \]  
(16)

By substituting Eqs. (15) and (16) into Eq. (11), \( l \) is expressed as a percentage of \( D \), and the fault distance is as follows:

\[ l\% = 100 \frac{\left( \Delta \hat{U}_{S1} - \hat{U}_f \right) \Delta \hat{I}_{R1}}{\left( \Delta \hat{U}_{S1} - \hat{U}_f \right) \Delta \hat{I}_{R1} + \left( \Delta \hat{U}_{R1} - \hat{U}_f \right) \Delta \hat{I}_{S1}} \]  
(17)

When a metallic three-phase short circuit occurs, the voltage at fault point is zero. Equation (17) can be rewritten as

\[ l\% = 100 \frac{\Delta \hat{U}_{S1} \Delta \hat{I}_{R1}}{\Delta \hat{U}_{S1} \Delta \hat{I}_{R1} + \Delta \hat{U}_{R1} \Delta \hat{I}_{S1}} \]  
(18)

When a three-phase short circuit with arc and path resistance occurs, the voltage at the fault point cannot be ignored. The voltage at fault point in Equation (18) is not available. In this case, the algorithm will produce errors. The subsequent simulation of the algorithm gives errors. As for the asynchronous errors in data sampling, they will occur at both ends of the line when the three-phase short circuit occurs. This means that the phase voltage and current of Equation (18) are not affected by synchronization errors.

For asymmetric faults and three-phase faults given above, the algorithm given above does not require prior knowledge of the fault type and fault phase. When a negative sequence component is detected, it can be known that an asymmetric fault has occurred.

3. PERFORMANCE EVALUATION

An overhead transmission line model is built in PSCAD/EMTDC to validate the algorithm. From Fig. 1, the voltage level is 500 kV, and the line length is 400 km. The parameters of the simulation model are shown in Table 1. The line parameters are shown in Table 2.
Table 1. Parameters of the simulation model.

| Parameters | System (S) | System (R) |
|------------|------------|------------|
| $U_{LL_{RMS}}$ [kV] | 515 | 500 |
| $\phi_1$ [°] | 0 | -15 |
| $R$ [Ω] | 0.013 | 0.005 |
| $L$ [H] | 0.0752 | 0.0623 |
| $R_0$ [Ω] | 4.5123 | 3.5610 |
| $L_0$ [H] | 0.0865 | 0.5631 |

Table 2. Line parameters.

| Parameters | $p$- and $n$-sequence | 0-sequence |
|------------|------------------------|------------|
| Resistance Ω/km | 0.0210 | 0.152 |
| Inductance mH/km | 0.872 | 2.985 |
| Capacitance µF/km | 0.015 | 0.007 |

3.1. Asymmetrical Fault Evaluation

In this section, Equation (12) is used to carry out a large number of tests for asymmetric faults including metallic faults and faults with transition resistance. The types of failures tested include DPG, DPH, and SPG. The point of failure is set to 60 km away from the sender. The fault initiation time is $t = 20$ ms. The simulation adopts synchronous sampling, and the sampling frequency is 2 kHz.

The different metallic asymmetry faults are shown in Fig. 6 to Fig. 8. From the simulation results, the fault location distance can be correctly calculated within 20 ms for metallic asymmetry faults.

Figure 6. Metallic DPG.
The different asymmetry faults with path resistance $R_f = 30 \, \Omega$ are shown in Fig. 9 to Fig. 11. From the simulation results, the algorithm has nothing to do with transition resistance.
Figure 9. DPG with transition resistance $R_f = 30\,\Omega$.

Figure 10. DPH with transition resistance $R_f = 30\,\Omega$.

3.2. Symmetrical Fault Evaluation

The fault start time, sampling frequency, system and line parameters, and fault points are the same as asymmetric faults. Symmetrical fault simulation uses Equation (18) to calculate the distance to fault. Figure 12 shows the fault location current and fault distance under a metallic three-phase short
Figure 11. SPG with transition resistance $R_f = 30 \Omega$.

Figure 12. Metallic TPH.

circuit. For a 400 km line, the fault distance obtained by Equation (18) is 59.38 km. Figs. 13 and 14 respectively shows the fault location current and fault distance under a three-phase short circuit with transition resistance $R_f = 1 \Omega$, $R_f = 5 \Omega$. For the same 400 km line, the fault distance obtained by
Equation (18) is 57.85 km in Fig. 13.

When the distance to the fault is calculated with Equation (18), due to neglecting the fault resistance, for a metallic three-phase short circuit occurs, the result obtained by the algorithm is
accurate. At this time, the fault arc resistance is proportional to the arc length. In the case of a large fault current such as 10 kA, a small path resistance has little effect on the calculation of fault distance. However, high impedance will cause great errors in the calculation results of the algorithm.

In this study, the method of evaluating error is as follows [26]:

$$\text{Error} (%) = \frac{|l_{\text{exact}} - l_{\text{est}}|}{D} \cdot 100\%$$  \hspace{1cm} (19)

where $l_{\text{exact}}$ is the actual location, $l_{\text{est}}$ the estimated location, and $D$ the length of the line.

Only 5% of power system short circuits are three-phase short circuits, of which the vast majority are human factors, and the dominant ones are metallic short circuits and arc short circuits. High impedance three-phase short circuits are quite rare. In this case, the simulation results will be given in Table 3.

### Table 3. TPH error.

| $R_f$ [Ω] | Error (%) on different fault location |
|-----------|----------------------------------------|
|           | 30 km | 60 km | 150 km | 240 km |
| $R_f = 0$ | 0.0853 | 0.0984 | 0.0962 | 0.0952 |
| $R_f = 0.25$ | 0.0923 | 0.1286 | 0.1562 | 0.2010 |
| $R_f = 1$ | 0.6658 | 0.7852 | 0.5362 | 0.4538 |
| $R_f = 2$ | 1.8594 | 1.1566 | 1.6952 | 1.8410 |
| $R_f = 5$ | 2.6851 | 2.6852 | 3.0153 | 2.8651 |
| $R_f = 10$ | 6.2674 | 7.5621 | 6.5920 | 7.9611 |

### 3.3. Effect of the Line Load Condition

In order to evaluate the algorithm in different load conditions, Tables 4 to 7 respectively give evaluation results under no-load and heavy-load.

### Table 4. SPG.

| load states | Fault location (km) | SPG |
|-------------|---------------------|-----|
|             | Est. (km) | Error (%) |
| no-load     | 30 | 30.48 | 0.120 |
|             | 60 | 60.45 | 0.113 |
|             | 150 | 151.68 | 0.420 |
|             | 240 | 241.22 | 0.305 |
| Heavy-load  | 30 | 31.82 | 0.455 |
|             | 60 | 61.56 | 0.390 |
|             | 150 | 151.68 | 0.420 |
|             | 240 | 242.52 | 0.630 |

It can be seen that the algorithm proposed in this paper is not affected by the line load condition comparing the results in the two cases of heavy load or light load before the fault. The distance to fault calculated by the algorithm is only related to the fault point. When the distance to fault point is set to 30 km, 60 km, 150 km, and 240 km, respectively, the error percentage does not exceed 0.63.

### 3.4. Effect of Line Transposition Condition

In order to evaluate the effects of line transposition on the algorithm, for different types of faults, the results of the fault location algorithm in this paper are given. It can be seen from Tables 8 to 11 that
the algorithm proposed in this paper is not affected by the line transposition condition comparing the line transposition and untransposition.

3.5. Effect of Fault Inception Angle

In order to evaluate the effects of fault inception angle on the algorithm, for different types of faults, the results of the fault location algorithm are given on heavily loaded lines (i.e., a power angle difference of...
Table 8. SPG.

| load states   | Fault location (km) | SPG Est.(km) | Error (%) |
|---------------|---------------------|--------------|-----------|
|               | 30                  | 30.88        | 0.22      |
| transposition | 60                  | 60.69        | 0.17      |
|               | 150                 | 151.45       | 0.36      |
|               | 240                 | 241.96       | 0.49      |
|               | 30                  | 31.96        | 0.49      |
| untransposition| 60                  | 61.57        | 0.39      |
|               | 150                 | 151.46       | 0.37      |
|               | 240                 | 242.42       | 0.60      |

Table 9. DPG.

| load states   | Fault location (km) | DPG Est.(km) | Error (%) |
|---------------|---------------------|--------------|-----------|
|               | 30                  | 30.96        | 0.22      |
| transposition | 60                  | 60.52        | 0.13      |
|               | 150                 | 151.63       | 0.41      |
|               | 240                 | 241.42       | 0.35      |
|               | 30                  | 31.01        | 0.50      |
| untransposition| 60                  | 58.99        | 0.25      |
|               | 150                 | 148.74       | 0.31      |
|               | 240                 | 242.28       | 0.57      |

Table 10. DPH.

| load states   | Fault location (km) | DPH Est.(km) | Error (%) |
|---------------|---------------------|--------------|-----------|
|               | 30                  | 30.55        | 0.14      |
| transposition | 60                  | 60.93        | 0.23      |
|               | 150                 | 151.56       | 0.39      |
|               | 240                 | 242.32       | 0.58      |
|               | 30                  | 32.03        | 0.51      |
| untransposition| 60                  | 58.98        | 0.26      |
|               | 150                 | 151.80       | 0.45      |
|               | 240                 | 242.85       | 0.71      |

20°). It can be seen from Table 12. that the algorithm proposed is not affected by the fault inception angle. The path resistance in the case of an asymmetric fault is 20Ω, and a three-phase fault is a metallic fault. The fault point is 100 km away from the sender. Inception angle is set to 0°, 45°, 90°, and 180°, respectively.
Table 11. TPH.

| load states | Fault location (km) | TPH Est. (km) | Error (%) |
|-------------|---------------------|--------------|-----------|
| transposition |                    |              |           |
| 30          | 30.94               | 0.24         |           |
| 60          | 60.89               | 0.22         |           |
| 150         | 150.27              | 0.07         |           |
| 240         | 241.08              | 0.27         |           |
| untransposition |                |              |           |
| 30          | 30.93               | 0.23         |           |
| 60          | 60.52               | 0.13         |           |
| 150         | 150.65              | 0.16         |           |
| 240         | 240.79              | 0.20         |           |

Table 12. The effect of fault inception angle on the positioning result.

| Fault Type | Inception angle (degrees) | Estmi (km) | Error (%) |
|------------|---------------------------|------------|-----------|
| SPG        | 0                         | 101.52     | 0.380     |
|            | 45                        | 101.36     | 0.340     |
|            | 90                        | 101.3      | 0.325     |
|            | 180                       | 101.45     | 0.363     |
| DPG        | 0                         | 101.62     | 0.405     |
|            | 45                        | 101.49     | 0.372     |
|            | 90                        | 101.23     | 0.308     |
|            | 180                       | 101.69     | 0.422     |
| DPH        | 0                         | 101.05     | 0.262     |
|            | 45                        | 100.98     | 0.245     |
|            | 90                        | 100.96     | 0.240     |
|            | 180                       | 100.81     | 0.203     |
| TPH        | 0                         | 101.66     | 0.415     |
|            | 45                        | 101.42     | 0.355     |
|            | 90                        | 101.25     | 0.313     |
|            | 180                       | 101.58     | 0.395     |

4. CONCLUSIONS

This paper proposes a fault location algorithm based on fault sequence components that is independent of parameters. The algorithm is an extension of the fault location algorithm at both ends. Using PMU devices installed at both ends of the line, the algorithm collects current and voltage to complete synchronous information sampling. The fault location algorithm is suitable for asymmetric faults and symmetrical faults. It is not affected by zero sequence coupling. For asymmetric faults, it is not affected by the path resistance. The algorithm is simple and efficient, because it is not affected by fault type, fault starting angle, line transposition, and line load. The simulation results prove that the algorithm efficiently and accurately determines the fault location.
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