Online Optimization of Wireless Powered Mobile-Edge Computing for Heterogeneous Industrial Internet of Things

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Abstract—A spurt of progress in wireless power transfer (WPT) and mobile edge computing (MEC) provides a promising approach for Industrial Internet of Things (IIoT) to enhance the quality and productivity of manufacturing. Scheduling in such a scenario is challenging due to congested wireless channels, time-dependent energy constraints, complicated device heterogeneity, and prohibitive signaling overheads. In this paper, we first propose an online algorithm, called energy-aware resource scheduling (ERS), to maximize the system utility comprising throughput and fairness, with consideration on both system sustainability and stability. Based on Lyapunov optimization and convex optimization techniques, the proposed algorithm achieves asymptotic optimality for heterogeneous IIoT systems without prior knowledge of network state information (NSI). Subsequently, we extend the ERS algorithm to a more realistic scenario where the overhead and delay of NSI feedbacks are non-negligible. The optimal scheduling decisions of the scenario are provided, and the optimality loss on system utility under outdated NSI is analyzed. Simulations verify our theoretical claims and demonstrate the gains of our proposed ERS algorithm over alternative benchmark schemes.

Index Terms—Wireless power transfer, mobile edge computing, heterogeneous Industrial Internet of Things, outdated information, Lyapunov optimization.

I. INTRODUCTION

INDUSTRIAL Internet of Things (IIoT) is regarded as a revolutionary approach to optimize industrial production processes and improve economic benefits [1]–[3]. Ubiquitous wireless devices (WDs) in IIoT work as the sources to collect an enormous amount of data from the ambient environment. Analysis and extraction of those data can add value to the production cycle, thus improving the efficiency and accuracy of manufacturing [4]. Generally, the data will be transmitted to and processed at a nearby radio access point (AP) to facilitate network model training and intelligent decisions, e.g., machine learning for feature extraction [5] and fuzzy Q-learning for load balancing [6]. To this end, endowing APs with powerful mobile edge computing (MEC) functionalities will be a major form of IIoT scenarios [7]–[9].

A typical use case enabled by MEC in IIoT is warehouse environmental monitoring. In such an application, different types of WDs are deployed around the storehouse, responsible for collecting and transmitting data from surroundings to APs in order to maintain a favorable storage environment. Therefore, the system would prefer high throughput with reliability and practicality but pays less attention to latency. Different from conventional MEC networks which focus on radio/computing resources optimization of homogeneous devices with real-time network state information (NSI), scheduling in the considered IIoT scenario faces several technical challenges:

1) Sustainability: The IIoT devices are increasingly empowered with wireless power transfer (WPT) capability, so as to harvest energy from wireless signals, recharge their limited battery, and prolong the lifetimes of the devices. This is critical to guarantee the sustainability with WPT capability since frequent replacement of batteries for a massive number of devices would incur prohibitive operational/maintenance cost in IIoT. However, the transmission of wireless power consumes spectrum resources and needs to be jointly optimized with the decisions of data offloading and processing in the AP.

2) Heterogeneity: The IIoT devices (such as industrial sensors, actuators, and controllers) have heterogeneous computing/storage/battery capabilities. Such inhomogeneity would necessitate specific and meticulous algorithms for different types of devices.

3) Scalability: Given the stochastic and time-varying features of the IIoT environment, real-time NSI is vital for the effectiveness of resource allocation. However, due to limited signaling resources, it may not be practical for the AP to acquire real-time NSI from a wide range of IIoT devices. The aforementioned joint design under heterogeneous IIoT environment is required to be scaled to operate in the presence of outdated NSI.

A. Related Work

These challenges have yet to be addressed in the literature, especially in heterogeneous IIoT. Although there are extensive works about resource scheduling in MEC [10]–[12], the joint optimization for bandwidth, computing, and energy intake/output is of insufficient study. Offline algorithm for the joint optimization would require complete non-causal information of networks and may suffer from the curse of dimensionality when the system is in large scale. To address these issues, a perturbed Lyapunov technique was employed to decouple the spatial- and time-dependency of multiple resources in [13] and [14]. However, these analyses may not be suitable for IIoT, since their energy harvesting processes
were modeled as stochastic energy packet arrivals. The highly variable of energy supplies was unfavorable for system control and manual management.

Recent years have witnessed the possibility of integrating WPT with MEC. WPT, especially in the form of wireless powered communication network (WPCN) [15]–[20], has been envisioned as an promising paradigm to provide adaptive and sustainable energy supply for battery-powered devices. In [21], a computation rate maximization problem was formulated in wireless powered MEC with binary offloading, and solved by developing two algorithms based on coordinate descent and alternating direction method of multipliers. Reference [22] explored the benefits of user cooperation in minimizing WPT-MEC system energy by solving a min-max problem through a two-phase method. For the scenario with multi-antenna AP, a semiclosed solution for AP’s energy consumption minimization was derived under a computation latency constraint [23]. However, these algorithms implicitly assumed that the AP has sufficient computing capability. In contrast, the optimal design for resource-limited AP, which is widespread in IIoT due to stringent production cost consideration, and its sustainability analysis are lack of study.

There have been separate studies over the optimization of WPT/MEC for the devices with limited [15], [23], [24] (referred to as Type-I hereafter) or sufficient [16]–[18], [24] (referred to as Type-II in the sequel) battery sizes. Type-I devices, with high self-discharge rate, consume all the harvested energy for data offloading in each time slot. Therefore, its resource scheduling usually works in a myopic manner. On the contrary, so long as the battery capacity allows, Type-II devices can store the unused energy harvested in the current slot for future use. Some papers noticed the heterogeneity and proposed separate scheduling approaches for the two types of WDs [19], [20]. However, none of the existing works can be extended to the considered heterogeneous IIoT system, since the joint optimization for Type-I/II devices is highly coupled and dependent on each other.

The aforementioned works all optimize their objectives under the assumption of real-time NSI. It is generally impractical due to systems’ stochasticity and unpredictability, partial feedback, and non-negligible transmission delay. In [13] and [25], new analytic frameworks were proposed and applied to accommodate outdated NSI, which were able to diminish the optimality loss asymptotically. Chenshan et al in [20] later extended those frameworks to the scenario of Internet of Things with finite device buffers. Furthermore, a multi-timescale online algorithm was developed in [27], where the future queue backlogs were approximated by the current backlogs. However, all these works did not quantize the tradeoff between feedback signaling and performance loss.

B. Contributions

Distinctively different from the existing approaches, this paper designs an asymptotically optimal scheduling method for heterogeneous IIoT, where WDs with different battery capabilities are powered by WPT. Our objective is to maximize a time-average system utility considering both throughput and fairness, and enable the optimization to be tolerant to partial outdated NSI. The key contributions of this paper are as follows:

- We develop an energy-aware resource scheduling (ERS) algorithm to tackle time coupling among data collection, energy transmission, and data offloading in IIoT by leveraging Lyapunov technique. A stochastic optimization problem is formulated and then transformed into a series of convex issues with the aid of the data backlog analysis from both the AP and WDs. Moreover, we theoretically prove that the proposed algorithm achieves close-to-optimal performance with a known deviation, presenting an \([O(1/V),O(1/V)]\)-tradeoff between utility and stability.

- The proposed ERS algorithm is favorable for multiple resource scheduling in IIoT by exploiting the diversity of heterogeneous devices. The objective functions of scheduling Type-I and Type-II devices are meticulously formulated into a similar form and are jointly optimized subject to data stability and energy sustainability.

- We extend the proposed algorithm to a more practical scenario where only outdated NSI is available for scheduling optimization due to partial feedback and transmission delay. Besides, we derive the optimal decisions for the scheduling process and analyze its optimality loss in the presence of stale NSI.

Extensive simulations verify the theoretical analyses of the proposed online algorithm for heterogeneous IIoT. It is shown that the proposed approach, with only partial outdated NSI, can increase the system throughput while maintaining fairness among Type-I/II devices. Simulations also reveal the impact of feedback overhead and interval on throughput, thereby providing guidelines for the design of compulsory feedback interval in practical implementation.

Note that the selection of Lyapunov optimization techniques in this paper is to design asymptotically optimal and low-complexity resource scheduling approach, which is of practical importance since IIoT requires efficient resource utilization and quick response. Apart from Lyapunov optimization, the heuristic, game-theoretic, submodular as well as reinforcement learning-based approaches can also be applied to solve the stochastic optimization problem. However, all these approaches can result in optimality loss due to myopic schedule (e.g., heuristic method) or high complexity and learning time (e.g., Q-learning). As a result, in the proposed algorithm, we first leverage Lyapunov optimization to decouple the time- and space- couplings in scheduling decisions, and then efficiently solve the decoupled sub-problems by applying convex optimization techniques.

The rest of this paper is organized as follows. The next section introduces the system model of the considered IIoT and the problem formulation. Section III proposes the online scheduling algorithm and analyzes its performance theoretically. Section IV provides the extension of the online algorithm for a more practical scenario. Simulation results are presented in Section V, followed by the conclusion in Section VI.
II. SYSTEM MODEL AND PROBLEM FORMULATION

A. System Overview

We consider an IIoT system consisting of an AP endowed with MEC and WPT functionalities, and $N$ heterogeneous WDs indexed by $\mathcal{N} = \{1, 2, \cdots, N\}$. As shown in Fig. 1(a), WDs in the system are divided into sets of Type-I and Type-II devices, denoted by $\mathcal{N}_1$ and $\mathcal{N}_2$, respectively. Similar to WPCN, we assume the system operates in a frame-based time-division-multiplexing manner [15]–[17], [19]. Fig. 1(b) illustrates the system’s time frame structure with duration $T$, which consists of two phases. In the first phase, the first $\mu_0(t)T$ amount of time is assigned for downlink WPT, where $\mu_0(t) \in [0, 1]$ denotes its allocated time portion at slot $t$. In the second phase, the rest $(1 - \mu_0(t))T$ amount of time is used for the computation offloading in the uplink. The time allocated for WD $i$ to offload is denoted by $\mu_i(t)T$, where $\mu_i(t) \in [0, 1]$. Consider ambient data analysis for IIoT, the computation results at the AP is not required to be returned to WDs. Hence, the time portions $\mu_i(t)$ satisfies

$$\sum_{i \in \mathcal{N}} \mu_i(t) \leq 1, \quad \mu_i(t) \geq 0, \quad \forall i \in \mathcal{N},$$

where $\mathcal{N} \triangleq \{0\} \cup \mathcal{N}$ represents the set of the AP and all WDs.

We consider that the AP has a reliable power supply and a constant transmit power $P_0$ by connecting with the power grid. It is assumed that the AP and all WDs have one single antenna each. Compared to $P_0$, the energy harvested from noises and received uplink signals from other WDs is much smaller, thus is assumed to be negligible [15]–[17], [19]. Within time slot $t$, the amount of energy harvested by the $i$-th WD can be expressed as [15], [21]

$$e_i^H(t) = \xi_i P_0 h_i(t) \mu_0(t) T, \quad i \in \mathcal{N},$$

where $\xi_i \in (0, 1)$ is the energy harvesting efficiency, and $h_i(t)$ is the channel power gain between the AP and the $i$-th WD. Without loss of generality, we assume $e_i^H(t) \leq e_i^{H, \text{max}}$, where $e_i^{H, \text{max}}$ is the maximum harvested energy in one slot recorded from a long time.

By exploiting the channel reciprocity in time-division mode system [15]–[21], the AP can acquire both the uplink and downlink channel states at the beginning of each time slot with little signaling cost. This can be achieved by each WD sending its pilot for channel measurement at the AP [19], [28]. Hence, the achievable offloading data size of WD $i$ (in bits) within time slot $t$ can be given by [21], [22]

$$c_i(t) = \mu_i(t) T W \log_2(1 + \frac{P_i(t) h_i(t)}{N_0}), \quad i \in \mathcal{N},$$

where $W$ denotes the communication bandwidth, $P_i(t)$ represents the transmit power of WD $i$, and $N_0$ denotes the noise power at the receiver of the AP. Given the limited uplink transmission power, $c_i(t)$ in (3) is upper bounded by $e_i^{H, \text{max}}$.

The notations used in this paper are summarized in Table I.

| Notation | Description |
|----------|-------------|
| $\mathcal{N}(\mathcal{N}_1, \mathcal{N}_2)$ | Set of all (Type-I, Type-II) wireless devices |
| $\xi_i$ | Energy harvesting efficiency of device $i$ |
| $P_0$ | Transmit power of AP |
| $P_i(t)$ | Offloading transmit power of device $i$ at slot $t$ |
| $h_i(t)$ | Channel power gain between AP and device $i$ at slot $t$ |
| $T$ | Interval of a time block |
| $W$ | System bandwidth |
| $\mu_0(t)$ | Time portion for wireless power transfer at slot $t$ |
| $\mu_i(t)$ | Time portion for device $i$ to offload at slot $t$ |
| $c_i(t)$ | Achievable offloading data size of device $i$ at slot $t$ |
| $E_i(t)$ | Available energy of device $i$ at slot $t$ |
| $e_i^H(t)$ | Energy harvested by device $i$ at slot $t$ |
| $A_i(t)$ | Data that can be collected by device $i$ within slot $t$ |
| $a_i(t)$ | Data collected at device $i$ within slot $t$ |
| $Q_i(t)$ | Data queue backlog of device $i$ at slot $t$ |
| $S_i(t)$ | Data queue backlog of AP at slot $t$ |
| $r_i(t)$ | Available data processing speed of AP at slot $t$ |
| $\mathcal{Y}$ | Time-average of any stochastic process $X$ |
| $U_i(\cdot)$ | Utility of device $i$ |

B. Energy Models for Heterogeneous Devices

Energy models for Type-I and Type-II devices are entirely different due to their battery management capabilities, which are further discussed in the following.

1) Type-I devices: This type of WDs is equipped with batteries that have low energy storage capacity and high self-discharge rate. In this case, the harvested but unused energy within the current slot will be depleted and cannot be kept for future use. As a result, if $\mu_i(t) \neq 0$, $i$-th WD will manage its energy in a myopic manner, i.e., exhausting all the harvested energy for offloading. Thus, we have $P_i(t) = 0$ if WD $i$ stays...
silent in the offloading phase, otherwise, WD $i$ will offload with transmit power
\[ P_i(t) = \frac{\eta_i e_i^H(t)}{\mu_i(t)}T, \quad i \in \mathcal{N}_1, \tag{4} \]
where $\eta_i$ represents the fixed portion of the harvested energy that is used for data offloading. As the device exhausts all the harvested energy for uplink transmission, $\eta_i = 1$ in the sequel. The maximum transmit power constraint for Type-I devices is neglected here since the harvested energy from WPT at each slot is small in practice.\(^{[21]}\)

2) Type-II devices: Different from Type-I devices, each device $i \in \mathcal{N}_2$ is equipped with a rechargeable battery that has a high energy storage capacity and low discharge rate. As long as the maximum battery capacity $\theta_i$ is not exceeded, a Type-II device can save the harvested energy in its battery for offloading in the following slots when the channel fading condition is unfavorable. Denote $E(t) = (E_i(t), i \in \mathcal{N}_2)$ as the vector of Type-II devices’ energy queue sizes at slot $t$, and $e(t) = (e_i(t), i \in \mathcal{N}_2)$ as the vector of their corresponding uplink transmit energy. The available energy in WD $i$ evolves according to the dynamic equation
\[ E_i(t + 1) = \min\{E_i(t) + e_i^H(t), \theta_i\} - e_i(t), \quad i \in \mathcal{N}_2. \tag{5} \]
At any time slot $t$, the transmit energy $e_i(t)$ must follow the energy-available condition
\[ e_i(t) \leq E_i(t) + e_i^H(t), \quad i \in \mathcal{N}_2, \tag{6} \]
such that the energy used would not exceed what has been cumulatively harvested so far. Meanwhile, since a Type-II device can accumulate much energy before its transmission, when offloading, it should also satisfy the maximum transmit power constraint
\[ e_i(t) \leq P_i^\text{max} \mu_i(t)T, \quad i \in \mathcal{N}_2, \tag{7} \]
where $P_i^\text{max}$ is $i$-th WD's maximum transmit power.

C. Data Collection and Queueing Models

At any slot $t$, the volume of data that can be collected by WD $i$ is denoted by $A_i(t)$. Due to the dynamic changes of environment in IIoT, we assume $A_i(t)$ to be i.i.d. over slots with a maximum $A_i^\text{max}$.\(^{[10], [13]}\) Given the data freshness and the limited data buffer of WD, only part of the data, denoted by $a_i(t)$, would be collected into the data buffer. Hence, we have
\[ 0 \leq a_i(t) \leq A_i(t) \leq A_i^\text{max}, \quad \forall i \in \mathcal{N}. \tag{8} \]
Let $Q(t) = (Q_i(t), i \in \mathcal{N})$ denote the backlog of data queue of WDs at slot $t$, i.e., any collected but not have been offloaded data that are queued in the data buffer. At every slot, the network decides how much data should be collected (i.e., $a_i(t)$) and how much existing data in the buffer to offload (i.e., $c_i(t)$) according to the network state. Follow the basic operations of cascaded queueing systems\(^{[10], [13]}\) (e.g., the edge computing network where the data arrival and offloading operate on a first-in-first-out basis), the amount of data to be collected (i.e., $a_i(t)$) is determined at slot $t$ and queued into the data buffer at the beginning of slot $t + 1$. Therefore, the data backlog $Q_i(t)$ evolves as follows:
\[ Q_i(t + 1) = [Q_i(t) - c_i(t)]^+ + a_i(t), \quad i \in \mathcal{N}, \tag{9} \]
where $[x]^+ = \max\{x, 0\}$. In some special cases, for example, at the early stage of the network, $Q_i(t)$ may be smaller than $c_i(t)$ as the collection of data takes time. Here, the first term in the right-hand-side (RHS) of equation (9) is used to guarantee that the device cannot offload more data than what remains in its data buffer when $Q_i(t) < c_i(t)$ happens.\(^{[10], [29]}\)

The AP maintains $\mathcal{N}$ data buffers $S(t) = (S_i(t), i \in \mathcal{N})$ to store the data that offloaded from WDs but have not been processed. Constrained by the limited computing capability of the AP, at most $r_i(t)$ amount of data from $i$-th WD can be processed at slot $t$, where $r_i(t)$ is a stochastic number with the maximum $r_i^\text{max}$.\(^{[13]}\) Naturally, the data queue dynamics at the AP are given by
\[ S_i(t + 1) = [S_i(t) - r_i(t)]^+ + \min\{c_i(t), Q_i(t)\}, \tag{10} \]
where the second term in the RHS of (10) represents the data-available constraint of WD $i$, which means the WD cannot offload more data than what it has stored. In the case when $c_i(t) > Q_i(t)$, the excessive offloading rate will be used for transmitting dummy data.\(^{[10], [29]}\) We will later show that the optimization is still asymptotically optimal by doing this.

D. Problem Formulation

Consider warehouse environmental monitoring services in an IIoT system, unfair data collection and offloading would reduce the overall system performance. This is because we may waste too many resources for offloading redundant data from one place while lacking enough data from other areas for analysis. Therefore, in this paper, we aim at maximizing the system throughput while guaranteeing fairness among WDs.

However, fairness in the considered scenario is not easy to achieve due to the doubly near-far phenomenon\(^{[15]}\) and heterogeneity of the WDs, which is further discussed in section\([\checkmark]\) To address this issue, instead of directly maximizing the throughput, we formulate the problem as a sum-utility maximization problem as follows
\[
\begin{aligned}
P: & \max_{\mu(t), e(t), a(t)} \sum_{i \in \mathcal{N}} U_i(a_i(t)) \\
& \text{s.t. } (1), (6), (7), (8), \\
& \text{C1: } Q_i < \infty, \forall i \in \mathcal{N}, \\
& \text{C2: } S_i < \infty, \forall i \in \mathcal{N},
\end{aligned}
\]
where $a(t) = (a_i(t), i \in \mathcal{N})$, $X = \lim_{\tau \to \infty} \frac{1}{\tau} \sum_{\tau=0}^{\tau-1} E\{X(\tau)\}$ defines the time average expectation of any stochastic process $X(t)$, constraints C1 and C2 ensure the stability of all the data queues, and the utility function based on proportional fairness\(^{[13], [30]}\) is defined as $U_i(x) = \log(1 + x)$. Here, $\log(\cdot)$ denotes the natural logarithm.

As can be seen from the utility function, the marginal utility decreases as the amount of data collected by a WD increases. As a result, the system would be inclined to collect equal amounts of data from different WDs rather than show an
apparent preference for some devices. This thereby ensures system fairness.

The problem \( P \) is a typical stochastic optimization problem. An offline optimization of \( P \) would require complete non-causal NSI, which is impossible in practice due to the highly stochastic and unpredictable characteristics of IIoT systems. Even if such NSI is available, the problem is still challenging due to its high computational complexity. Therefore, we are motivated to propose online algorithm to jointly schedule \( \hat{\mu}(t), e(t) \) and \( a(t) \) without future NSI.

### III. Online Schedule of Heterogeneous Devices

In this section, we develop an online decision-making algorithm by employing Lyapunov optimization. The original problem \( P \) is decoupled into a series of deterministic per-time slot problems at independent time slots, which are then solved by convex optimization technique. We later prove that the proposed algorithm preserves asymptotic optimality compared with the optimum of the original problem.

#### A. Problem Transformation

Define \( \Theta(t) = [Q(t), E(t), S(t)] \) as the concatenated vector of the queues at WDs and the AP. To handle the time-coupling in problem \( P \), we first define a non-negative perturbed Lyapunov function as

\[
L(\Theta(t)) = \frac{1}{2} \left\{ \sum_{i \in N} S_i(t)^2 + \sum_{i \in N} Q_i(t)^2 + \sum_{i \in N} (E_i(t) - \theta_i)^2 \right\},
\]

(11)

where \( \theta_i \) is a weight perturbation, also the battery capacity of Type-II devices mentioned in Section III. We will later show that \( \Theta(t) \) can be satisfied with a proper choice of \( \theta_i \). Meanwhile, when minimizing \( L(\Theta(t)) \), we push the data backlogs at both the AP and WDs towards zero, which is equivalent to satisfying stability constraints \( C1 \) and \( C2 \) in \( P \). Then the one-slot conditional Lyapunov drift can be expressed as

\[
\Delta(\Theta(t)) = \mathbb{E} \left[ L(\Theta(t + 1)) - L(\Theta(t)) \mid \Theta(t) \right],
\]

(12)

where the expectation is taken with respect to the random NSI and the control actions. Based on Lyapunov optimization technique, we consider minimizing the drift-plus-penalty expression

\[
\Delta_V(\Theta(t)) = \Delta(\Theta(t)) - V \mathbb{E} \left\{ \sum_{i \in N} U_i(a_i(t)) \mid \Theta(t) \right\},
\]

(13)

instead of directly leveraging the objection in \( P \).

**Lemma 1:** For any optimization decisions made on slot \( t \), and all possible values of \( \Theta(t) \), the drift-plus-penalty expression for all slot \( t \) satisfies

\[
\Delta_V(\Theta(t)) \leq B_1 - V \mathbb{E} \left\{ \sum_{i \in N} U_i(a_i(t)) \mid \Theta(t) \right\} + \sum_{i \in N} Q_i(t) \mathbb{E} \{ a_i(t) - c_i(t) \mid \Theta(t) \} + \sum_{i \in N} S_i(t) \mathbb{E} \{ c_i(t) - r_i(t) \mid \Theta(t) \} + \sum_{i \in N} [E_i(t) - \theta_i] \mathbb{E} \{ e_i^H(t) - e_i(t) \mid \Theta(t) \},
\]

(14)

where \( V > 0 \) is a control parameter that affects an explicit tradeoff between system utility and data backlogs, \( B_1 \) denotes a finite constant that satisfies

\[
B_1 = \frac{1}{2} \left\{ \sum_{i \in N} [(A_i^{max})^2 + (r_i^{max})^2] + 2 \sum_{i \in N} (c_i^{max})^2 \right\} + \frac{1}{2} \left\{ \sum_{i \in N} [(e_i^{max})^2 + (P_i^{max})^2] \right\}
\]

(15)

**Proof:** Please refer to Appendix A

Minimizing \( \Delta_V(\Theta(t)) \) is still difficult due to its dynamics. Instead, we are motivated to minimize the RHS of (14) according to the principle of opportunistically minimizing an expectation \( \mathbb{E} \) in a per-slot manner, subject to the constraints (1), (7), and (8).

#### B. Optimal Scheduling Decisions

In this subsection, we present an Energy-aware Resource Scheduling algorithm with Real-time NSI (ERS-RN algorithm) to address the above problem. Since \( a(t) \) and \( e(t), \hat{\mu}(t) \) can be decoupled with each other and are independent of the current backlog \( \Theta(t) \), the minimization problem can be separated into two optimization sub-problems as follows.

1) **Distributed Data Collection:** For each time slot \( t \), the data collection sub-problem can be decomposed for individual WD. The optimal data collection strategy can be obtained by solving

\[
P1: \min_{a_i(t)} Q_i(t)a_i(t) - VU_i(a_i(t))
\]

s.t. \( \Theta(t) \).

The problem \( P1 \) is a convex optimization problem since the objective function is convex and its constraint is linear. Hence, the optimum is either at the stationary point of \( a_i(t) = V/Q_i(t) - 1 \) or on one of the boundaries. For any WD \( i \in N \), the optimal data collection strategy is thus chosen by

\[
a_i(t) = \begin{cases} A_i(t), & V \geq (A_i(t) + 1)Q_i(t) \\ \frac{V}{Q_i(t)} - 1 + , & \text{otherwise} \end{cases}
\]

(16)

The given policy indicates that the amount of data collected by each WD depends on the length of its backlog. A device is inclined to collect more data when its backlog is small.
2) Joint Energy and Time Allocation: According to (14), the optimal transmit powers $e(t)$ and the optimal time portions $\bar{\mu}(t)$ can be derived by solving

$$
P_2: \min_{e(t), \bar{\mu}(t)} \sum_{i \in N} [S_i(t) - Q_i(t)]c_i(t) + \sum_{i \in N} [E_i(t) - \theta_i][e_i^H(t) - e_i(t)]$$

s.t. (1), (4).

Due to the constraint (1), $e(t)$ and $\bar{\mu}(t)$ in problem $P_2$ cannot be completely decoupled. Generally, $P_2$ is a non-convex optimization problem. To solve this problem, we first define a set $N^*_t \triangleq \{i \mid i \in N, S_i(t) \geq Q_i(t)\}$, and then find an interesting property of its optimization as shown in the following theorem.

**Theorem 1:** For any device $i \in N_t \cap N_2$, the term $[S_i(t) - Q_i(t)]c_i(t) + [E_i(t) - \theta_i][e_i^H(t) - e_i(t)]$ in the objective function of $P_2$ gets its minimum when $\mu_i(t) = 0, e_i(t) = 0$. Besides, for arbitrary WD $i \in N_t \cap N_1$, its optimal time portion is given by $\mu_i(t) = 0$.

**Proof:** Please refer to Appendix B

**Remark 1:** Theorem 1 suggests that the AP prefers to serve WDs who have a larger data backlog than that queuing at the AP. This result is in accordance with our intuition that when the waiting queue at the AP is large, offloading data from a WD whose queue length is small only brings transmission energy loss and more execution delay.

By excluding the devices in $N_t$, we denote the remaining Type-I WDs to be scheduled as $N_2$. Similarly, let $N_0^*$ collect the Type-II devices that are not in $N_t$. For the sake of brevity, we define $D_i(t) = [S_i(t) - Q_i(t)]TW; \beta_i(t) = h_i(t)/(N_iT); \delta_i(t) = \xi_i P_0 h_i^2(t)/N_0$ in the sequel. Then the problem $P_2$ can be simplified and rewritten as follows

$$
P_3: \min_{\mu(t)} \sum_{i \in N_2^*} D_i(t)\phi_i(t) - \sum_{i \in N_2^*} [E_i(t) - \theta_i]e_i(t) + \sum_{i \in N_2^*} D_i(t)\psi_i(t) + \mu_0(t)TP_i \sum_{i \in N_2} [E_i(t) - \theta_i]\xi_i h_i(t)$$

s.t. $C_3$: $\sum_{i \in N_2} \mu_i(t) \leq 1, \mu_i(t) > 0, \forall i \in \hat{N} \setminus N_1$,

$C_4$: $e_i(t) \leq p_{\text{max}} \mu_i(t), i \in N_2^*$,

where $v(t) = \{e_i(t) | i \in N_2^* \} \cup \{\mu_i(t) | i \in \hat{N} \setminus N_1\}$ collects all variables that is to be optimized, and

$$
\phi_i(t) = \mu_i(t) \log_2(1 + \beta_i(t)e_i(t)/\mu_i(t)), i \in N_2^*,
$$

$$
\psi_i(t) = \mu_i(t) \log_2(1 + \delta_i(t)\mu_0(t)/\mu_i(t)), i \in N_1^*.
$$

Note from (17) that $\phi_i(t)$ is a perspective function of $f(x) = \log_2(1 + \beta_i(t)x)$. Since $f(x)$ is a concave function of $x \geq 0$, $\phi_i(t)$ is jointly concave with respect to $e_i(t) > 0$ and $\mu_i(t) > 0$ for arbitrary $i \in N_2^*$. Similarly, we can prove that $\psi_i(t)$ is concave in $\{\mu_0(t), \mu_i(t) : \forall i \in N_1^*\}$ when $\mu_0(t) > 0$ and $\mu_i(t) > 0$. Since the objective function in $P_3$ is a negative weighted sum of $\phi_i(t), \psi_i(t)$ and several linear functions, the objective function of $P_3$ is convex in $v(t)$. Meanwhile, the constraints $C_3$ and $C_4$ are linear, and thus convex. Therefore, the problem $P_3$ is a convex optimization problem, which can be effectively solved by off-the-shelf toolbox CVX [3] at a maximal computational complexity order of $O((|N_2| + |N_1| + 1)^3, C))$. Here, $(|N_2| + |N_1| + 1)$ represents the number of variables to be optimized, $C$ is the cost to evaluate the objective function together with its first and second derivatives.

Based on the above analysis, the whole process of the ERS-RN algorithm is summarized in Algorithm 1. We call the algorithm energy-aware because the AP conducts the energy allocation by itself and obtains the real-time knowledge of all devices’ energy storages without any feedback from WDs. Note that the joint energy and time allocation depends on real-time data queue lengths fed back by all WDs. Hence, the optimization of time portions can only be coordinated at the AP in a centralized manner.

**Algorithm 1: ERS-RN Algorithm**

At the AP:
1: Observe $Q(t), S(t), E(t), r(t)$ and channel gains;
2: Choose $v(t)$ by solving $P_3$;
3: Inform $v(t)$ to WDs and execute WPT;
4: Update $E_i(t)$ at each Type-II device $i$ by $v(t)$ and (5).

At any WD $i$:
5: Observe $Q_i(t), A_i(t)$ and $c_i(t)$;
6: Decide $\alpha_i(t)$ based on (15);
7: Offload data and give feedback $Q_i(t)$ to the AP in the arranged sequence;
8: Update $Q_i(t)$ according to (9).

At the AP:
9: Update $S(t)$ according to (10).

**C. Performance Analysis**

In this subsection, we start by showing that the proposed ERS-RN algorithm is asymptotically optimal. Denote $U^*$ as the offline optimum of problem $P$, which can only be obtained by using full non-causal knowledge of the IoT network. Let $U^*$ denote the long-term time-average utility achieved by the ERS-RN algorithm. Then, we can establish the following theorem.

**Theorem 2:** Suppose the problem $P$ is feasible, the gap between $U^*$ and $U^*$ satisfies

$$
U^* - U^* \leq B_1/V.
$$

**Proof:** Please refer to Appendix C

**Remark 2:** Theorem 2 suggests that the achievable time-average utility of the proposed method can be arbitrarily close to the offline optimum by setting a sufficiently large $V$. In other words, the proposed approach is asymptotically optimal in terms of solving the original problem $P$. However, it is not the larger the value of $V$, the better. The increase of $V$ would also cause a negative impact on system data backlogs, as revealed in the following theorem.
Theorem 3: At any time slot, the data backlogs at WD $i \in \mathcal{N}$ and the AP are all upper bounded, with upper bounds given by

\[ Q_i^{\text{max}} = V + A_i^{\text{max}}, \]
\[ S_i^{\text{max}} = V + A_i^{\text{max}} + e_i^{\text{max}}. \]

Proof: Please refer to Appendix [D].

With Theorem 3 we then provide the value of the weight perturbation parameter $\theta_i$ and prove the energy-availability constraint (6) is always satisfies in the following theorem.

Theorem 4: With the proposed algorithm, the energy storage capacity (or the perturbation parameter) $\theta_i$ is given as follows

\[ \theta_i = \frac{(V + A_i^{\text{max}})e_i^{\text{min}}}{P_i^{\text{max}}}, \]

where $e_i^{\text{min}}$ is the minimum transmit energy other than zero. The equation (22) ensures there is enough energy in the battery for optimal schedule. In other words, $E_i(t) + e_i^{\text{min}}(t) > P_i(t)T$.

Proof: Please refer to Appendix [E].

Theorem 3 and [E] show that $Q_i^{\text{max}}$, $S_i^{\text{max}}$, and $\theta_i$ increase monotonically with the control parameter $V$. A larger $V$ leads to a higher system utility at the expense of larger queue lengths at all WDs and the AP. On the other hand, a tiny $V$ impairs the asymptotic optimality of achievable utility but reduces the demands for large storage spaces of the system. This reveals a $[O(1/V), O(V)]$ tradeoff between the system utility and the required storage spaces. Therefore, instead of giving each WD a substantial data and energy storage space, we can reduce the manufacturing costs of IIoT systems by embedding appropriate data buffers and batteries for WDs based on the expected system utility.

IV. RESOURCE SCHEDULING IN IIoT WITH IMPLEMENTATION CONSIDERATIONS

In this section, we consider a more general setting in which the overhead and delay for transmitting NSI cannot be overlooked. We develop a modified version of the ERS-RN algorithm, called ERS-ON, to address the issues brought by practical application, and further analyze its performance theoretically.

A. Practicability Considerations

As discussed, real-time NSI feedback (i.e., $Q(t)$) is needed at the AP for the joint optimization of energy allocations and time portions. However, the transmission of NSI would occupy the time for data offloading, which may jeopardize network performance, or even prohibit the system from working. To avoid explosive feedback costs, systems may send back only partial NSI, as the majority of feedbacks have less contribution for optimization. Moreover, even if all WDs give continuous feedbacks at every slot, the AP can only get outdated NSI (e.g., $Q(t-1)$ at slot $t$) since it has to make the scheduling decision before obtaining $Q_i(t)$ in the uplink phase at slot $t$. As a result, the proposed ERS-RN algorithm may be hard to be directly applied to real systems. Some modifications are needed in the presence of outdated NSI.

Therefore, in this section, we proceed to consider a more practical scenario of the considered IIoT system, where any device $i$ that has been assigned time for offloading at slot $t$ should spare at least $\epsilon_i T$ time to give feedback of its data backlog. Here, the time portion $\epsilon_i$ is a constant obtained by historical experience such that there is always enough energy for transmitting an $L$ bits feedback. WDs that have not gotten any uplink chances (i.e., silent) at slot $t$ are generally not required for feedbacks in order to reduce system NSI overheads. The only exception is for those WDs that remain silent in the past $m$ slots, in case the shortage of NSI update at the AP permanently prevents some WDs from offloading. For notation simplicity, we denote those WDs that reach their compulsory feedback interval $mT$ at time slot $t$ as $M_t$ in the sequel.

B. ENERGY-AWARE RESOURCE SCHEDULING ALGORITHM WITH OUTDATED NSI

The outdated NSI (i.e., data backlogs at WDs) acquired through the above feedback mechanism has a direct impact on the optimization of problem P3. To overcome the limitation of the ERS-RN algorithm, we develop a modified version of the ERS algorithm with Outdated NSI (ERS-ON) by approximating the latest feedback of WDs’ data backlog values as the current values. Let $\hat{Q}_i(t)$ denote the approximation of $i$-th WD’s data backlog that the AP holds at time slot $t$. Hence, we have $\hat{Q}_i(t) = Q_i(t - \tau_i)$, where $\tau_i \in \{1, \cdots, m\}$. Then, the problem P3 can be transformed into

\begin{align*}
\text{P4: } \min_{n(t)} & \sum_{i \in \mathcal{N}_2} \hat{D}_i(t)\phi_i(t) - \sum_{i \in \mathcal{N}_2} [E_i(t) - \theta_i]e_i(t) \\
& + \sum_{i \in \mathcal{N}_2} \hat{D}_i(t)\psi_i(t) + \mu_i(t)TP_i \sum_{i \in \mathcal{N}_2} [E_i(t) - \theta_i]h_i(t) \\
\text{s.t. } & C5: \sum_{i \in \mathcal{N}_2 \setminus \mathcal{N}_1} \mu_i(t) \leq 1 - \sum_{i \in \mathcal{M}_t} \epsilon_i, \mu_i(t) > \epsilon_i, \forall i \in \mathcal{N} \setminus \mathcal{N}_i,
\end{align*}

where $\hat{D}_i(t) = [S_i(t) - \hat{Q}_i(t)]TW$, and C5 ensures there is enough time for offloading and feedback in each time slot. Similar to P3, problem P4 can be solved by CVX or other convex optimization techniques as the constraint C5 preserves convexity.

Let $\{\epsilon_i^*(t) \mid i \in \mathcal{N}_2\}$ collect the optimal energy solutions and $\{\mu_i^*(t) \mid i \in \mathcal{N} \setminus \mathcal{N}_i\}$ denote the optimal time portions of problem P4. The time portions for WDs and the AP at time slot $t$ can thus be given by

\[ \mu_i(t) = \begin{cases} 0, & i \in \mathcal{N}_1 \setminus \mathcal{M}_t \\ \epsilon_i^*, & i \in \mathcal{M}_t \\ \mu_i^*(t), & \text{otherwise}, \end{cases} \]

where $\mu_i(t) = 0$ means WD $i$ keeps silent at slot $t$.

Different from the time portions allocation, the energy policies for WDs vary with their types. For Type-I devices, same like the statement in section II, all the harvested energy would be consumed for offloading/feedback or be depleted. On the contrary, Type-II devices in $\mathcal{N}_2 \cap \mathcal{M}_t$ can dynamically
adjust their transmit energy to ensure the channel capacity equals the data size of feedback, i.e., \( L = TW\phi_t(t) \), thus saving excessive energy for future use. More specifically, the energy allocation policy for device \( i \in \mathcal{N}_2 \) satisfies

\[
e_i(t) = \begin{cases} 
\frac{\beta_i(t)}{\gamma_i(t)} [2^{\beta_i(t)} - 1], & i \in \mathcal{N}_2 \cap \mathcal{M}_t \\
\epsilon_i^*(t), & i \in \mathcal{N}_2^\prime \\
0, & \text{otherwise},
\end{cases}
\]

(24)

where \( l_i = L/(e_iTW) \). The allocated transmit energy of Type-II devices would not violate the constraints (6) and (7) since we have a large \( \beta_i(t) \) and small \( l_i \) when WD \( i \in \mathcal{N}_2 \cap \mathcal{M}_t \).

C. Asymptotic Optimality Analysis under Outdated NSI

The use of outdated NSI will inevitably impair the performance of systems. In the worst case, a WD would lose \( m \) chances to offload during the past \( mT \) time duration due to a lack of information update at the AP. For any slot \( t \), the difference between the approximate values maintained at the AP and the actual data backlogs at WDs are bounded by

\[
Q_i(t) - \hat{Q}_i(t) = \sum_{\tau=t-mT}^{t} [Q_i(\tau) - Q_i(\tau - 1)] \leq r_i A_i^\text{max} \leq m A_i^\text{max},
\]

(25)

where the first inequality is derived from (8) and (9). For analytical tractability, we denote \( \hat{U}^* \) as the utility obtained by using \( \hat{Q}_i(t) \) for optimization. Based on (25), we can prove that the optimality loss due to the outdated knowledge of \( Q(t) \) is strictly upper bounded as shown in the following theorem.

**Theorem 5:** Suppose the problem \( P \) is feasible, the gap between \( U^* \) and \( \hat{U}^* \) is bounded:

\[
U^* - \hat{U}^* \leq (B_2 + B_3 \sum_{i \in \mathcal{N}} \epsilon_i) / V;
\]

(26)

where \( B_2 = B_1 + m \sum_{i \in \mathcal{N}} (A_i^\text{max})^2 \) and \( B_3 = 2 \sum_{i \in \mathcal{N}} (\epsilon_i^\text{max})^2 \).

**Proof:** Please refer to Appendix H

Theorem 5 proves that the asymptotic optimality of the proposed ERS-ON algorithm is not compromised with outdated NSI. Compared with Theorem 2 the optimality loss caused by outdated NSI is no greater than a constant divided by \( V \). Moreover, the optimality loss is linear to \( m \) and increases with the growth of feedback overheads, which coincides with the intuition that a long compulsory feedback interval and a large overhead result in weak system performances.

**V. Simulations**

In this section, we verify the analysis and evaluate the performance of the proposed algorithm via MATLAB simulations. The parameters used in our simulation are taken from the existing synthetic data set [22] and 3GPP specifications [34], [35], to capture the features of practical wireless channels. Detailed information is listed in Table II. The initial values of \( Q(t) \), \( S(t) \) and \( E(t) \) are set to be zero for the fair comparison with [13]. In our simulations, we set \( T = 100 \)ms and consider five Type-I devices and five Type-II devices. Specifically, we set the distance between the AP and Type-I devices as \( d_1 = (d_i, i \in \mathcal{N}_1) = [3, 5, 7, 9, 11] \)m. Without loss of generality, we consider \( d_2 = (d_i, i \in \mathcal{N}_2) = d_1 \). The channel is modeled after the Rayleigh fading model and is set as \( h_i(t) = 10^{-3}d_i^{-\alpha}h(t) \) [15], where \( \alpha \) denotes the pass-loss exponent, \( h(t) \) is an exponentially distributed random variable with unit mean which represents the short-term fading. We assume that \( \alpha = 2 \) and the channel reciprocity holds for the uplink and downlink. The spectrum bandwidth for offloading and the receiver noise power are set as \( W = 0.2 \)MHz and \( N_0 = 10^{-9} \)W, respectively. For any device \( i \in \mathcal{N}_i \), the energy harvesting efficiency \( \xi_i = 0.8 \) and \( A_i^\text{max} = 1 \)Mbps. At the AP, the transmission power \( P_0 = 2W \), \( r_i^\text{max} = 50 \)kbps, and \( m = 4 \). For analysis simplicity, we assume \( \epsilon_i = \epsilon = 0.005s \) for any \( i \in \mathcal{N} \) in the sequel unless otherwise stated.

We introduce four benchmark algorithms to evaluate the performance of the proposed ERS-ON algorithm, which work as follows:

1) Homogeneous device optimization with outdated NSI (HDO-ON): all WDs are seen as Type-I devices and optimized without considering their heterogeneity.

2) Equal offloading time with outdated NSI (EOT-ON): all WDs are given equal offloading time at each slot. The optimization of \( e(t) \) and \( \mu_0(t) \) in this scheme is similar to the ERS-ON algorithm.

3) Proportional fairness without NSI (PFN): the allocation of offloading time portions are based on proportional fairness method with no knowledge of \( Q(t) \). In the scheme, overheads for feedback can be saved.

---

| TABLE II | SYSTEM SIMULATION PARAMETERS |
|---|---|
| **Basic Radio Configuration Parameters** | |
| Spectrum Bandwidth \( W \) (MHz) | 0.2 |
| Pass Loss Exponent \( \alpha \) | 2 |
| \( P_0 / P_0^\text{max} / N_0 \) (W) | 2/1/10^{-9} |
| \( e_i^\text{min} / e_i^\text{max} \) (mJ) | 0.005/0.16 |
| \( c_i^\text{max} / L \) (Kbits) | 10000/0.016 |
| Channel Power Gain \( h_i(t) = 10^{-3}d_i^{-\alpha}h(t) \) | |
| **Other Simulation Parameters** | |
| Energy Harvesting Efficiency \( \xi_i \) | 0.8 |
| Slot Duration (ms) | 1000 |
| Simulation Run-Time (slots) | 1000 |
As expected, the proposed algorithm significantly outperforms slows down, which coincides with what revealed in Theorem 5.

The poor performances of GAN and GAN. This is because GAN wastes much offloading time that the ERS-ON algorithm achieves higher throughput than WDs in system optimization. Besides, we can also observe the heterogeneity, HDO-ON achieves the high fairness at the cost of tremendous throughput. Furthermore, Fig. 3(b) shows the fairness of Type-I and Type-II devices, where $V$ is set as 300. On one hand, we can see from EOT-ON, PFN, and GAN that heterogeneous battery capacities would lead to an uneven offloading opportunity. Different from the above schemes, our proposed ERS-ON algorithm strikes a good balance between Type-I and Type-II devices, showing its superiority in handling the doubly near-far problem caused by different $d_i$.

Jain’s index is used to measure fairness among WDs [13]. We observe from Fig. 3(a) that HDO-ON has almost the same high fairness with ERS-ON algorithm, which is barely affected by the choice of $V$. However, with less concern on the heterogeneity, HDO-ON achieves the high fairness at the cost of tremendous throughput. Furthermore, Fig. 3(b) shows the fairness of Type-I and Type-II devices, where $V$ is set as 300. On one hand, we can see from EOT-ON, PFN, and GAN that heterogeneous battery capacities would lead to an uneven offloading opportunity. Different from the above schemes, our proposed ERS-ON algorithm strikes a good balance between Type-I and Type-II devices, showing its superiority in handling systems with heterogeneous WDs. On the other hand, the near to one Jain’s index also indicates that ERS-ON can also well tackle the doubly near-far problem caused by different $d_i$.

Although higher throughput can be achieved by choosing a larger $V$, it is not always better for the ERS-ON algorithm to do so. As shown in Fig. 4(a) the increase of $V$ would also result in linear growth of data backlogs at both the WD and the AP. By jointly considering Figs. 3(a) and 4 we can observe
that the utility-backlog performance follows the [O(1/V), O(V)] tradeoff, which verifies Theorem 3 and 5. Besides, the results also provide guidelines for the design of data buffer lengths of WDs in IIoT, since an appropriate V can minimize the cost of embedding lengthy storage spaces given a fixed throughput expectation. By using ERS-ON algorithm, as depicted in Fig. 4(a), we can keep the data buffer length at the WD to a small level compared with benchmarks, especially when V is large. In Fig. 4(b), the curve for GAN is dropped since its queue length at the AP increases as time goes by and never reaches a steady state. It can be observed that EOT-ON maintains shorter data backlogs at the AP side. This is because its volume of offloading data is much less than ERS-ON. Furthermore, we also observe that scheme with NSI feedback has a shorter data backlog at the AP than one without.

For implementation considerations, the impact of \( \epsilon \) and \( m \) on average throughput is further evaluated. We set \( \epsilon \) to vary from 0.005s to 0.025s, and carry out simulations under different compulsory feedback intervals. Fig. 5 shows that the average throughput linearly reduces as \( \epsilon \) increases. This is because the NSI feedback process takes up more time originally assigned for offloading when \( \epsilon \) becomes larger. Compared with \( m = 8 \) and \( m = 12 \), ERS-ON with \( m = 4 \) achieves a higher throughput, which corresponds to the conclusion in Theorem 5. Besides, in Fig. 5 ERS-ON with complete NSI feedback represents that WD has to uplink their NSI in every slot. Its throughput performance outperforms the others when \( \epsilon = 0.005s \). However, the achievable throughput decreases rapidly as \( \epsilon \) grows, and gets worse than ERS-ON with \( m = 12 \) when \( \epsilon = 0.025s \). This reveals that more NSI contributes to the optimization of resource allocation when the overhead of feedback is small. Meanwhile, many resources would be wasted by complete NSI feedback when the signaling cost is relatively large. In this case, it is even better to choose an appropriately large \( m \).

Although the above sections only consider the scenario of a single AP, the proposed algorithm can be readily extended to the scenario of multiple cellular base stations by applying frequency reuse technique [60]. As the name suggests, the technique utilizes a fraction of the total frequency band in each cell, such that no two neighbor cells use the same frequency. As a result, inter-cell interference can be reduced, and each cell can run the proposed Algorithm 1 independently with its allocated bandwidth.

VI. Conclusion

Given the data collection and offloading in WPT-MEC based heterogeneous IIoT systems, we have proposed an energy-aware resource scheduling algorithm for throughput maximization and offloading fairness. The performance of such an algorithm was analyzed under a typical scenario, where overhead and delay of feedbacks were non-negligible. Simulations show that the proposed algorithm can achieve asymptotic optimality, and effectively tackle the doubly near-far problem as well as the unfairness caused by heterogeneous battery capabilities. As a result, the use of the algorithm is fully justified by its merits on high throughput, fairness and perpetual operation in implementation.

The integration of the machine-to-machine (M2M) communications to the wireless powered MEC network is promising to tackle the scalability problem in large-scale applications [67]. In such a scenario, the proposed algorithm is still valuable. We can first divide IIoT devices into different clusters according to their proximity and choose the device with the best channel condition in each cell to serve as the cluster head. The AP only need to schedule those cluster heads for data offloading with the proposed algorithm, while the other devices migrate their data to nearby cluster heads through M2M interactions. By this way, the computational complexity at the AP can be brought down, and the feedback of NSI can also be reduced. However, M2M communication brings interference for the data offloaded to the AP. How to leverage spatial reuse to coordinate the scheduling leaves for future work.

APPENDIX A

Note that for any \( x \geq 0, y \geq 0, z \geq 0 \), the following inequality

\[
([x − y] + z)^2 \leq x^2 + y^2 + z^2 + 2x(z − y).
\]  
always holds. Squaring both sides of (29), summing over \( i \in N \), dividing both sides by 2 and combining (27), we obtain

\[
\frac{1}{2} \sum_{i \in N} [Q_i^2(t + 1) − Q_i^2(t)] \\
\leq \frac{1}{2} \sum_{i \in N} [c_i^2(t) + a_i^2(t)] + \sum_{i \in N} Q_i(t) [a_i(t) − c_i(t)] \\
\leq \frac{1}{2} \sum_{i \in N} [((c_i^\text{max})^2 + (A_i^\text{max})^2)] + \sum_{i \in N} Q_i(t) [a_i(t) − c_i(t)].
\]  

Similarly, using (5) and (10), we have
\[ \frac{1}{2} \sum_{i \in N_2} [(E_i(t + 1) - \theta_i)^2 - (E_i(t) - \theta_i)^2] \]
\[ \leq \frac{1}{2} \sum_{i \in N_2} [(e_i(t))^2 + e_i^2(t)] + \sum_{i \in N_2} [E_i(t) - \theta_i] \{ e_i^2(t) - e_i(t) \} \]
\[ \leq \frac{1}{2} \sum_{i \in N_2} [(e_i(t))^2 + (P_{\max}^2)T^2] + \sum_{i \in N_2} [E_i(t) - \theta_i] \{ e_i(t) - e_i(t) \}, \]
\[ (29) \]

and
\[ \frac{1}{2} \sum_{i \in N} [S_i^2(t + 1) - S_i^2(t)] \]
\[ \leq \frac{1}{2} \sum_{i \in N} [b_i^2(t) + (\min\{c_i(t), Q_i(t)\})^2] \]
\[ + \sum_{i \in N} S_i(t) \{ \min\{c_i(t), Q_i(t)\} - r_i(t) \} \]
\[ \leq \frac{1}{2} \sum_{i \in N} [b_i^2(t) + c_i^2(t)] + \sum_{i \in N} S_i(t) [c_i(t) - r_i(t)] \]
\[ \leq \frac{1}{2} \sum_{i \in N} [(r_i^2_{\max} + (e_i^2_{\max})^2) + \sum_{i \in N} S_i(t) [c_i(t) - r_i(t)] \]
\[ (30) \]

Substituting (28)–(30) into (12) yield (15). Taking expectation of the Lyapunov drift conditioned on \( \Theta(t) \) and subtracting \( V \mathbb{E}\{\sum_{i \in N} U_i(a_i(t)) \mid \Theta(t)\} \) to both sides proves the result.

\section*{APPENDIX B}

By substituting (2) into the objective function of problem P2, the problem can be reformulated into minimizing
\[ \sum_{i \in N_2} [S_i(t) - Q_i(t)] c_i(t) - \sum_{i \in N_2} [E_i(t) - \theta_i] e_i(t) \]
\[ + \sum_{i \in N_1} [S_i(t) - Q_i(t)] c_i(t) + \mu_0(t) \sum_{i \in N_2} [E_i(t) - \theta_i] \xi_i t P_0 h_i(t) T. \]
\[ (31) \]

The last term in (31) a decreasing function of \( \mu_0(t) \) since \( \sum_{i \in N_2} [E_i(t) - \theta_i] \xi_i t P_0 h_i(t) T \) is a non-positive constant. For any WD \( i \in N_i \cap N_1 \), we can choose \( \mu_i(t) = 0 \) to minimize the term \( [S_i(t) - Q_i(t)] c_i(t) \) and re-allocate the originally allocated time portion of WD \( i \) for \( \mu_0(t) \). For arbitrary \( i \in N_i \cap N_2 \), as \( c_i(t) \) is a non-decreasing function of \( e_i(t) \) and \( E_i(t) < \theta_i \), the term \( [S_i(t) - Q_i(t)] c_i(t) - [E_i(t) - \theta_i] e_i(t) \) is non-decreasing with \( e_i(t) \). Hence, the minimal value of the first and second terms in (31) is 0 when \( e_i(t) = 0 \). In such condition, \( \mu_i(t) \) has no contribution to the value of the objective function. It is better to make \( \mu_i(t) = 0 \) and add the value of \( \mu_0(t) \) so as to minimize the objective function.

\section*{APPENDIX C}

Based on (31), for all \( \sigma > 0 \), there exists a feasible stationary policy \( \Pi^1 \) that chooses \( a^1(t), e^1(t) \) and \( \mu^1(t) \) such that \( a^1(t), e^1(t) \) and \( e^{H1} \) satisfy:
\[ U^* - \mathbb{E}\{\sum_{i \in N} U_i(a_i^1(t))\} \leq \sigma, \]
\[ \mathbb{E}\{a_i^1(t) - c_i^1(t)\} \leq \sigma, \quad i \in N, \]
\[ \mathbb{E}\{e_i^1(t) - r_i(t)\} \leq \sigma, \quad i \in N, \]
\[ \mathbb{E}\{e_i^{H1}(t) - e_i^1(t)\} \leq \sigma, \quad i \in N_2. \]
\[ (32) \]

The 0-additive approximation ensures by (12) is as follows
\[ \Delta v(\Theta(t)) \leq \mathbb{B}_1 - \mathbb{V} \mathbb{E}\left\{\sum_{i \in N} U_i(a_i(t)) \mid \Theta(t)\right\} \]
\[ + \sum_{i \in N} Q_i(t) \mathbb{E}\left\{a_i^1(t) - c_i^1(t) \mid \Theta(t)\right\} \]
\[ + \sum_{i \in N} S_i(t) \mathbb{E}\{e_i^1(t) - r_i(t) \mid \Theta(t)\} \]
\[ + \sum_{i \in N_2} [E_i(t) - \theta_i] \mathbb{E}\{e_i^{H1}(t) - e_i^1(t) \mid \Theta(t)\}, \]
\[ (33) \]

By substituting (32) into (33) and taking \( \sigma \to 0 \), we have
\[ \Delta(\Theta(t)) - \mathbb{V} \mathbb{E}\left\{\sum_{i \in N} U_i(a_i(t)) \mid \Theta(t)\right\} \leq \mathbb{B}_1 - \mathbb{V} U^* \]
\[ (34) \]

Using iterated expectations and telescoping sums over \( t \), dividing both sides by \( V t \) and rearranging terms yields
\[ \frac{1}{t} \sum_{t=0}^{t-1} \mathbb{E}\{\sum_{i \in N} U_i(a_i(t))\} \geq U^* - \mathbb{B}_1 V \]
\[ V \mathbb{E}\{L(\Theta(0))\} \]
\[ (35) \]

Since \( \mathbb{E}\{L(\Theta(0))\} < \infty \) holds, taking limits on both sides of (35) as \( t \to \infty \) proves the result.

\section*{APPENDIX D}

The proof can be obtained by mathematical induction. We first show that \( Q_i(t) \) is deterministic bounded by \( V + A_{\max}^i \) for all \( t \). This clearly holds for \( t = 0 \) as \( Q_i(0) = 0 \). Suppose it holds for time slot \( t \), then we show it also holds for time slot \( t + 1 \). Consider the case when \( Q_i(t) \leq V \). Since the buffer can increase by at most \( A_{\max}^i \) at any slot, we have \( Q_i(t + 1) \leq V + A_{\max}^i \). Then consider the other case when \( Q_i(t) \leq V + A_{\max}^i \). In this case, the data admission \( a_i(t) \) should choose to be zero according to (10). Hence, we have
\[ Q_i(t + 1) = [Q_i(t) - c_i(t)]^+ \leq Q_i(t) \leq V + A_{\max}^i. \]
In other words, \( Q_i(t + 1) \leq V + A_{\max}^i \) holds at \( t + 1 \), which completes the proof of the upper bound of \( Q_i(t) \).

Similarly, we proceed to prove \( S_i(t) \leq V + A_{\max}^i + c_{\max}^i \) by mathematical induction. Again, it holds for \( t = 0 \) as \( S_i(t) = 0 \) at the beginning and we suppose it also holds in time slot \( t \). When \( S(t) \leq V + A_{\max}^i \), from (19), we obtain
\[ S_i(t + 1) = [S_i(t) - r_i(t)]^+ + \min\{c_i(t), Q_i(t)\} \]
\[ \leq S_i(t) + c_{\max}^i \]
\[ \leq V + A_{\max}^i + c_{\max}^i. \]
\[ (36) \]

Otherwise, in the case when \( S_i(t) \geq V + A_{\max}^i \), using the previously proven conclusion (20) yields \( S_i(t) \geq Q_i(t) \). This
means the corresponding backlog at the AP is larger than the queue length at \( i \)-th WD, i.e., \( i \in \mathcal{N}_i \). In such a condition, the AP will allocate no offloading time for WD \( i \) according to Theorem 1. Therefore, no data from WD \( i \) arrive at the AP at time slot \( t \), i.e., \( c_i(t) = 0 \). We have

\[
S_i(t+1) = [S_i(t) - r_i(t)]^+ \leq S_i(t) \leq V + A_i^{\text{max}} + c_i^{\text{max}} \tag{37}
\]

From (36) and (37), we can prove that \( S_i(t+1) \leq V + A_i^{\text{max}} + c_i^{\text{max}} \) holds at time slot \( t+1 \). This thus concludes the proof.

**APPENDIX E**

If \( \theta_i \) is set according to (22) and \( E_i(t) \leq P_i^{\text{max}}T \) at slot \( t \), we obtain

\[
E_i(t) - \theta_i \leq -\frac{(V + A_i^{\text{max}})c_i^{\text{max}}}{c_i^{\text{min}}} \tag{38}
\]

Considering arbitrary WD \( i \in \mathcal{N}_2 \), we have

\[
[S_i(t) - Q_i(t)c_i(t)]e_i(t) - [E_i(t) - \theta_i]c_i(t) \geq -Q_i(t)c_i(t) + (V + A_i^{\text{max}})c_i^{\text{max}} \geq 0 \tag{39}
\]

where the second inequality holds because (20) is always satisfied. According to the proof in Theorem 1, WD \( i \) will not be allocated time for offloading if (39) holds. This proves that \( E_i(t) > P_i^{\text{max}}T \) is guaranteed under the given \( \theta_i \), thus we have \( E_i(t) + e_i^H(t) > P_i(t)T \).

**APPENDIX F**

Before the proof of Theorem 5, we first prove the following theorem through mathematical induction.

**Theorem 6:** At each time slot, the difference between the backlogs at each WD \( i \in \mathcal{N} \) and the AP is strictly bounded, which is given by

\[
S_i(t) - Q_i(t) \leq 2c_i^{\text{max}}. \tag{40}
\]

**Proof:** The upper bound holds at slot \( t = 0 \) given that \( S_i(0) = Q_i(0) = 0, \forall i \in \mathcal{N} \). Suppose it holds at time slot \( t \). Then, if \( S_i(t) - Q_i(t) < 0 \), we have

\[
Q_i(t+1) = [Q_i(t) - c_i(t)]^+ + a_i(t) \geq Q_i(t) - c_i(t) + a_i(t). \tag{41}
\]

Combining (40) and (41), we see that

\[
S_i(t+1) - Q_i(t+1) \leq S_i(t) - Q_i(t) + 2c_i(t) - a_i(t) < 2c_i(t) - a_i(t) \leq 2c_i^{\text{max}}
\]

On the other hand, if \( 0 \leq S_i(t) - Q_i(t) \leq 2c_i^{\text{max}} \), \( c_i(t) = 0 \) according to Theorem 1. Hence, we have

\[
S_i(t+1) - Q_i(t+1) = [S_i(t) - r_i(t)]^+ - Q_i(t) - a_i(t) \leq S(t) - Q_i(t) \leq 2c_i^{\text{max}}
\]

As a result, the upper bound also holds at time slot \( t+1 \), which concludes the proof.

Owing to the feedback mechanism of queue lengths, part of the offloading opportunity will be occupied. Hence, only \( (1 - \sum_{i \in \mathcal{M}_i} c_i)T \) can be used for WPT and offloading in each time duration. This will have a direct influence on \( c_i(t) \) and \( e_i^H \) as can be seen from (2) and (3). Let \( F(t) \) denote the function inside the expectation on the RHS of the drift bound in (14). Similar to \( F(t) \), we are motivated to minimize the expectation of \( \hat{F}(t) \) under the feedback mechanism, where

\[
\hat{F}(t) = -V \sum_{i \in \mathcal{N}} U_i(a_i(t))
\]

\[
+ \sum_{i \in \mathcal{N}} \hat{Q}_i(t)|a_i(t) - (1 - \epsilon_i)e_i(t)|
\]

\[
+ \sum_{i \in \mathcal{N}} S_i(t)(1 - \epsilon_i)e_i(t) - r_i(t)]
\]

\[
+ |E_i(t) - \theta_i|[(1 - \epsilon_i)e_i^H(t) - e_i(t)].
\]

Since the real-time queue length is always larger than the approximate queue backlog, combined with (25), we have

\[
\hat{Q}_i(t) \leq Q_i(t) \leq \hat{Q}_i(t) + mA_i^{\text{max}} \tag{43}
\]

Therefore, we can derive that

\[
F(t) - \hat{F}(t) = \sum_{i \in \mathcal{N}} [Q_i(t) - \hat{Q}_i(t)]a_i(t) + \sum_{i \in \mathcal{M}_i} \epsilon_i \sum_{i \in \mathcal{N}} S_i(t)c_i(t)
\]

\[
+ \sum_{i \in \mathcal{N}} \hat{Q}_i(t)(1 - \sum_{i \in \mathcal{M}_i} \epsilon_i) - Q_i(t)c_i(t)
\]

\[
+ \sum_{i \in \mathcal{M}_i, i \in \mathcal{N}_2} E_i(t) - \theta_i c_i^H(t)
\]

\[
\leq m \sum_{i \in \mathcal{M}_i} (A_i^{\text{max}})^2 + 2 \sum_{i \in \mathcal{N}} (c_i^{\text{max}})^2
\]

\[
\leq m \sum_{i \in \mathcal{M}_i} (A_i^{\text{max}})^2 + 2 \sum_{i \in \mathcal{N}} (c_i^{\text{max}})^2 + \sum_{i \in \mathcal{N}} \sum_{i \in \mathcal{N}} [S_i(t) - Q_i(t)]c_i(t)
\]

\[
\leq m \sum_{i \in \mathcal{M}_i} (A_i^{\text{max}})^2 + 2 \sum_{i \in \mathcal{N}} (c_i^{\text{max}})^2 + \sum_{i \in \mathcal{N}} \sum_{i \in \mathcal{N}} [S_i(t) - Q_i(t)]c_i(t)
\]

where the first inequality holds since \( E_i(t) \leq \theta_i \) and (43) are satisfied, and the second inequality is derived from Theorem 5. Submitting (44) into (14), we have

\[
\Delta_V(\Theta(t)) = \Delta(\Theta(t)) - V \mathbb{E}\left\{ \sum_{i \in \mathcal{N}} U_i(a_i(t)) | \Theta(t) \right\}
\]

\[
\leq B_2 + B_3 \sum_{i \in \mathcal{N}} \epsilon_i + \mathbb{E}\{\hat{F}(t) | \Theta(t)\},
\]

where

\[
B_2 = B_1 + m \sum_{i \in \mathcal{N}} (A_i^{\text{max}})^2 \quad \text{and} \quad B_3 = 2 \sum_{i \in \mathcal{N}} (c_i^{\text{max}})^2.
\]

We can then prove this theorem by using the similar argument in the proof of Theorem 2.

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