Antimony selenide (Sb$_2$Se$_3$) is an ideal photovoltaic candidate profiting from its advantageous material characteristics and superior optoelectronic properties, and has gained considerable development in recent years. However, the further device efficiency breakthrough is largely plagued by severe open-circuit voltage ($V_{OC}$) deficit under the existence of multiple defect states and detrimental recombination loss. In this work, an effective absorber layer growth engineering involved with vapor transport deposition and post-selenization is developed to grow Sb$_2$Se$_3$ thin films. High-quality Sb$_2$Se$_3$ with large compact crystal grains, benign [hk1] growth orientation, stoichiometric chemical composition, and suitable direct bandgap are successfully fulfilled under an optimized post-selenization scenario. Planar Sb$_2$Se$_3$ thin-film solar cells with substrate configuration of Mo/Sb$_2$Se$_3$/CdS/ITO/Ag are constructed. By contrast, such engineering effort can remarkably mitigate the device $V_{OC}$ deficit, owing to the healed detrimental defects, the suppressed interface and space-charge region recombination, the prolonged carrier lifetime, and the enhanced charge transport. Accordingly, a minimum $V_{OC}$ deficit of 0.647 V contributes to a record $V_{OC}$ of 0.513 V, a champion device with highly interesting efficiency of 7.40% is also comparable to those state-of-the-art Sb$_2$Se$_3$ solar cells, paving a bright avenue to broaden its scope of photovoltaic applications.
ratio of open-circuit voltage ($V_{OC}$) to its S-Q limit ($V_{OC}^{SQ}$, equals to $0.941 \times E_g/q - 0.171 \, \text{V}$) is mostly less than 50%, indicating severe $V_{OC}$ deficit (defined as $E_g/q - V_{OC}$).[16] According to the balance principle, the net loss at $V_{OC}$ shows a quasi-linear relationship with $E_g$, thus a minimum $V_{OC}$ deficit is about 0.24 V regarding $E_g$ of 1.2 eV for Sb$_2$Se$_3$.[17] However, the $V_{OC}$ deficit of the highest efficiency Sb$_2$Se$_3$ solar cell is greater than 0.8 V, much worse than that of perovskite (0.291 V), c-Si (0.360 V), organic (0.490 V), chalcogenide-based CIGS (0.346 V), CdTe (0.593 V), and even CZTS/Se (0.617 V) solar cells.[5] which has become the core bottleneck for further breakthrough of device efficiency. In this perspective, Tang et al. conducted a systematic analysis of $V_{OC}$ loss with respect to the intrinsic material properties and device characteristics.[16] It claimed that the complicated deep defects (e.g., $V_{SR}$, $Sb_{SR}$, $Se_{SSB}$), short carrier lifetime (0.1–1 ns), low doping density ($\approx 10^{13}$ cm$^{-3}$), large Urbach energy (30–40 meV), as well as the inevitable interface recombination and space-charge region (SCR) recombination are the main challenges. Actually, to cope such issues, various strategies have been attempted, such as absorber engineering of post-selenization,[18] co-evaporation of Sb$_2$Se$_3$/Se,[19] and external doping,[20] interfaces engineering of band alignment modification or hole transport layers introduction.[21,22]

Despite those great efforts, the results are still unsatisfactory. For heterojunction thin-film solar cells, the absorber material as well as the interface quality synergistically determine the final device efficiency. Thus, high-quality Sb$_2$Se$_3$ absorber layer should be first prepared with controllable deposition processes and growth dynamics. Various film deposition techniques, such as hydrothermal deposition,[23] solution processing,[24] thermal (or rapid thermal) evaporation,[12,25] closed-space sublimation (CSS),[26] pulsed-laser deposition (PLD),[27] magnetron sputtering deposition (MSD),[18] and vapor transport deposition (VTD),[9] have been developed to prepare Sb$_2$Se$_3$ absorber layer. Among them, the VTD processed Sb$_2$Se$_3$ thin film showed an improved crystallinity, reduced deep defects, and suppressed trap-assisted recombination, leading to a superstrate structured champion Sb$_2$Se$_3$ solar cell (PCE = 7.6%, $V_{OC}$ = 0.42 V), as reported by Tang et al.[9] In parallel, our group recently displayed a self-assembled growth of Sb$_2$Se$_3$ thin film with large crystal grains, benign preferential orientation, and accurate chemical composition via an effective MSD and post-selenization involved process, resulting in an interesting substrate structured Sb$_2$Se$_3$ solar cell (PCE = 6.84%, $V_{OC}$ = 0.504 V).[21] Thus, combining the advantageous VTD process (e.g., adjustable operating possibility, low cost and fast turnaround) and the compensatory post-selenization heat atmosphere to control Sb$_2$Se$_3$ thin film growth kinetics, which might efficiently diminish the bulk defects/impurities and/or surface/interface trap states induced $V_{OC}$ deficit, and therefore improve the device $V_{OC}$ and PCE, that really seems interesting and needs exploration. In addition, the substrate device configuration has some advantages like tailoring the absorber layer independently and engineering the interface efficiently.[28] which matches well with this proposed Sb$_2$Se$_3$ scenario, and is really worth in-depth investigation.

In this work, an effective absorber layer growth engineering of pre-VTD combined with post-selenization was developed to prepare Sb$_2$Se$_3$ thin films. High-quality Sb$_2$Se$_3$ with large compact grains, benign [hk1] growth orientation, and stoichiometric composition were successfully fulfilled under an optimized post-selenization scenario. Sb$_2$Se$_3$ thin-film solar cells with substrate configuration of Mo/Sb$_2$Se$_3$/CdS/ITO/Ag were constructed. The additional post-selenization heat treatment has been demonstrated to remarkably improve the device $V_{OC}$ and PCE, which can be ascribed to the improved absorber layer and heterojunction quality, the reduced interfacial and bulk defects as well as the defects-assisted recombination. As a result, a champion device with highly competitive PCE of 7.40% was achieved, a minimum $V_{OC}$ deficit of 0.647 V contributed to a record $V_{OC}$ of 0.513 V for all reported Sb$_2$Se$_3$ solar cells, paving a bright avenue to broaden its scope of TFPV applications.

2. Results and Discussion

As shown in Figure 1, a two-step thermal deposition process involving pre-VTD and post-selenization was developed to prepare Sb$_2$Se$_3$ thin films. Figure 2a shows the XRD patterns of the as-prepared Sb$_2$Se$_3$ thin films, including the VTD processed pristine sample (Control), and the counterparts underwent post-selenization at different temperatures (i.e., 380, 400, 420, and 440°C), which are denoted as C-380, C-400, C-420, and C-440, respectively. All the samples exhibit prominent peaks in good agreement with the JCPDS standard card (No. 15–0861) of the orthorhombic phase of Sb$_2$Se$_3$ without a second phase, indicating the thermally induced crystal re-growth and crystallinity improvement of the Sb$_2$Se$_3$ thin films under Se atmosphere. Notably, the diffraction peaks of (120), (130), (230), and (240) show sharp characteristics, indicating a preferential growth orientation of [hk0] for Control sample. However, an obvious evolution to [hk1] orientation along this particular direction. The significant variations of the diffraction peak intensities under appropriate post-selenization heat treatment, e.g., at temperatures of 400 and 420°C, suggesting thermally induced crystal re-growth and crystallinity improvement of the Sb$_2$Se$_3$ thin films under Se atmosphere. Notably, the diffraction peaks of (120), (130), (230), and (240) show sharp characteristics, indicating a preferential growth orientation of [hk0] for Control sample. However, an obvious evolution to the dominant [hk1] growth orientation can be observed for the post-selenized samples, especially for C-420 thin film. In order to evaluate it more intuitively, the texture coefficient (TC) of the major diffraction peaks were calculated based on the following equation[29]}

$$TC_{hkl} = \frac{I_{hkl}}{\sum_{i} I_{i}} \left( \frac{1}{N} \sum_{i=1}^{N} \frac{I_{i}}{I_{hkl}} \right)$$

where $I_{hkl}$ and $I_{i}$ are the diffraction peak intensities of [hkl] planes in the measured and standard XRD pattern of Sb$_2$Se$_3$, respectively. Large TC value of a diffraction peak indicates preferred orientation along this particular direction. The significant variation of TC values associated to Control and C-420 thin films are shown in Figure 2b, driving the further investigation of growth mechanism under this VTD and post-selenization preparation scenario. The VTD processed Sb$_2$Se$_3$ thin film is substantially related to a substrate-film interaction, the Mo substrate has been reported to preferentially induce the formation of [hk0] nuclei, leading to the slow growth rate and [hk0] oriented films.[30] However, during post-selenization heat treatment, the energetic ambient Se vapor could have a considerable effect on adatoms behavior and nuclei-substrate interaction, and therefore alter growth kinetics of the thin films with specific morphology and orientation. The top-view SEM images and the atomic Sb/Se ratios of
the Sb$_2$Se$_3$ thin films are also shown in Figure 2c–g. For Control sample, Sb$_2$Se$_3$ thin film with small grains and poor compactness can be observed, along with distinct micro-voids on the surface. SEM-coupled EDS result also revealed that the VTD processed thin film was severely Se poor with a deviated Sb/Se atomic ratio of 0.72, which can be attributed to the weight loss of Sb$_2$Se$_3$ at a threshold temperature ($\approx 423^\circ C$), and the high vapor pressures of Sb$_2$Se$_3$ and Se.[31] After post-selenization heat treatment, the Sb$_2$Se$_3$ thin films exhibited a gradual increase of compactness and uniformity with increasing the temperature from 380 to 420$^\circ C$, accompanied by an synchronous increase of average grain size from $\approx 0.8 \mu m$ (Control) to 1.6 $\mu m$ (C-420). The detailed frequency histograms versus grain size distribution are depicted in Figure S1 (Supporting Information). Moreover, the severe Se deficit can be mitigated upon post-selenization, until achieving an optimal Sb/Se ratio of 0.67 that close to the standard stoichiometric ratio value for Sb$_2$Se$_3$. The post-selenization duration-dependent crystallinity, morphologies, and chemical compositions also showed a similar positive correlation evolution (Figure S2, Supporting Information). Accordingly, a growth model involved with re-crystallization and re-growth of the Control Sb$_2$Se$_3$ thin film upon post-selenization heat treatment can be proposed, which will be clarified later. Finally, once an excessively high post-selenization temperature (e.g., 440$^\circ C$) was applied, the diffraction peak intensities would decrease, the detrimental micro-voids would emerge again, and the chemical composition would deviate reversely, which was possibly caused by a partial decomposition under its high vapor pressure nature (Figure 2a,g).

The investigation of optical properties of the as-prepared thin films is essential for understanding and/or engineering its high efficiency TFPV devices. The reflection spectra of the Sb$_2$Se$_3$ thin films were first obtained via UV/Vis/NIR spectrophotometer on glass substrate, and covering a wavelength range of 300–1500 nm (Figure 3a). Accordingly, the reflectance of the Control thin film was lower than that of the post-selenized thin films in the short wavelength absorption region, and also revealed a gradual increase of the values with increasing the post-selenization temperature. It was closely related to the crystallinity-dependent refractive index, the grainsize and surface compactness evolution, as corroborated by the XRD and SEM results. Moreover, a synchronous blue-shift of the absorption cut-off edge could be observed, implying the variation of band structure. Thus, the important indicator of optical bandgap ($E_g$) was specifically calculated according to the following formulas

$$2\alpha d = \ln \left[ \frac{R_{max} - R_{min}}{R - R_{min}} \right]$$  \hspace{1cm} (2)

$$\alpha h\nu = C(h\nu - E_g)^n$$  \hspace{1cm} (3)

where $\alpha$ is the absorption coefficient, $d$ is thickness, the reflection falls from $R_{max}$ to $R_{min}$ due to the intrinsic absorption of light by the material.[32] The latter is a typical $T_{auc}$ formula, where $C$ is a constant, $h$ is the Planck’s constant, $\nu$ is the photon frequency, $n$ is an index.[33] The calculated $E_g$ values were 1.145, 1.151, 1.158, 1.160, and 1.155 eV for Control, C-380, C-400, C-420, and C-440 thin films, respectively. Such $E_g$ value was close to the optimal value for efficient solar light harvesting, an observable variation also matched well with the real chemical composition, i.e., Sb-rich (Se-poor) or Se-rich (Se-compensation) conditions. To further verify this composition-dependent band structure evolution, DFT calculations with exchange-correlation functional under the generalized gradient approximation (GGA) were...
performed on the representative $\text{Sb}_8\text{Se}_{11}$ (corresponds to Control sample) and $\text{Sb}_8\text{Se}_{12}$ (corresponds to C-420 sample) models (Figure S3 and Note S1, Supporting Information). As shown in Figure 3c,d, by contrast, the latter structure presents an enlarged $E_g$ value, accompanied by more significant direct bandgap characteristic, which is consistent with the experimental observations. The corresponding orbital-resolved projected density of state (DOS) plots further suggest that the VBM is dominated by Se-p orbitals, whereas the CBM consists of Se-p and Sb-p orbitals. Thus, for Sb-rich Control sample, once the Se is replaced by Sb, the new incorporated Sb will bond with the surrounding Sb to induce p–p hybridization, the bonding state will decrease its $E_g$. Moreover, the DFT calculations also shows that the conductivity type can be tuned form n-type to p-type through tuning the Se chemical potential from Sb-rich to Se-rich condition. According to the literature, it is reasonable to occur since the dominant defects change from donor defects to acceptor defects.

Overall, this Sb$_2$Se$_3$ growth engineering can provide opportunity to modify its electronic structures and optical properties, making it an attractive candidate for TFPV applications.

The controllable Sb$_2$Se$_3$ thin films were used to construct substrate structured solar cells with configuration of Mo/$\text{Sb}_2\text{Se}_3$/CdS/ITO/Ag, and the statistical distributions of the key performance parameters are presented in Figure 4a–d. All the five series of devices, each with a set of twenty synchronously fabricated sub-cells demonstrated satisfactory reproducibility. By contrast, the post-selenized devices possessed an obvious increase of open-circuit voltage ($V_{\text{OC}}$) and fill factor (FF), as well as a slight increase of short-circuit current density ($J_{\text{SC}}$) under appropriate treatment of Sb$_2$Se$_3$ thin films. Therefore, PCE as a product of $V_{\text{OC}} \times J_{\text{SC}} \times \text{FF}$ can be improved via this important absorber layer engineering. The detailed champion device performance parameters of each category are summarized in Table 1. $J$–$V$ curves of the representative Control and C-420 devices are shown in Figure 4e. Accordingly, under the simulated AM 1.5G solar irradiation, the Control device offered a $J_{\text{SC}}$ of 22.72 mA cm$^{-2}$, an inferior $V_{\text{OC}}$ of 312 mV, and FF of 34.02%, thus resulting in an unsatisfactory PCE of 2.41%. In comparison, an obvious improvement of PCE to 7.40% was observed for C-420 device, presenting simultaneously increased $J_{\text{SC}}$, $V_{\text{OC}}$, and FF to 24.56 mA cm$^{-2}$, 513 mV, and 58.74%, respectively. Such great breakthrough
matched well with the high-quality C-420 thin film with higher crystallinity, better orientation, larger and void-free grain characteristics, which could effectively reduce bulk and/or interfacial defects, suppress transport and/or recombination loss. Moreover, a further comparison with some previously reported state-of-the-art Sb$_2$Se$_3$ solar cells prepared via VTD or post-selenization involved MSD techniques is shown in Figure S4 (Supporting Information) and Table 2.\textsuperscript{[9,18,21,37]} It is worth mention that this interesting efficiency of 7.40\% is higher than the MSD processed counterpart (highest PCE of 6.84\%),\textsuperscript{[20]} comparable to the VTD processed superstrate structured Sb$_2$Se$_3$ solar cells (champion PCE of 7.6\%, also the record value for planar-type Sb$_2$Se$_3$ solar cells),\textsuperscript{[9]} and is much superior than the VTD processed substrate structured device (PCE of 5.35\%).\textsuperscript{[17]} Furthermore, a maximum $V_{OC}$ of 513 mV undoubtedly represents the highest value for all Sb$_2$Se$_3$ solar cells reported so far. Regarding the aforementioned biggest challenge of $V_{OC}$ deficit, herein, the corresponding values are also calculated and summarized in Table 2. Interestingly, the $V_{OC,def}$ of the champion C-420 device can be reduced to 0.647 V, which is much lower than that of other efficient Sb$_2$Se$_3$ solar cells reported in literature, confirming the effectiveness of this VTD and post-selenization coupled absorber layer growth engineering in breaking through the core bottleneck. As shown in Figure 4e, the forward and reversed biased scanned $J$–$V$ curves of the champion C-420 device could perfectly overlap, reflecting no observable hysteresis loss under its high-quality Sb$_2$Se$_3$ absorber layer, superior heterojunction contact, and benign device interface. Moreover, the champion C-420 device presented an outstanding long-term stability since only a slight performance degradation could be seen with PCE decreased from initial 7.40\%
Figure 4. Statistical distribution of the key performance parameters of the Sb$_2$Se$_3$ thin-film solar cells, including a) $V_{OC}$, b) $J_{SC}$, c) FF, and d) PCE. e) $J$–$V$ curves of the representative Control and C-420 devices under forward scan (FS) and reverse scan (RS). f) EQE and integrated $J_{SC}$ of the devices.

Table 2. A comparison of photovoltaic parameters of Sb$_2$Se$_3$ devices prepared via different methods.

| Method       | Device configuration                     | PCE [%] | $V_{OC}$ [V] | $J_{SC}$ [mA cm$^{-2}$] | FF [%] | $V_{OC}$-dr [V] | $E_g/q-V_{OC}$ | Ref. |
|--------------|------------------------------------------|---------|--------------|--------------------------|--------|----------------|----------------|------|
| VTD$^a$      | ITO/CdS/Sb$_2$Se$_3$/Au                  | 7.60    | 0.420        | 29.90                    | 60.40  | 0.770          | Tang$^9$       |      |
| VTD$^a$      | Mo/Sb$_2$Se$_3$/In$_2$S$_3$/ZnO/ITO      | 5.35    | 0.370        | 28.22                    | 51.90  | 0.820          | Tang$^{37}$    |      |
| MSD-Se$^b$   | Mo/Sb$_2$Se$_3$/CdS/ITO/Ag               | 6.06    | 0.494        | 25.91                    | 47.70  | 0.716          | Liang$^{18}$   |      |
| MSD-Sb-Se$^c$| Mo/Sb$_2$Se$_3$/CdS/ITO/Ag               | 6.84    | 0.504        | 24.91                    | 54.47  | 0.651          | Liang$^{21}$   |      |
| VTD$^d$      | Mo/Sb$_2$Se$_3$/CdS/ITO/Ag               | 2.41    | 0.312        | 22.72                    | 34.02  | 0.833          | This work      |      |
| VTD-Se$^d$   | Mo/Sb$_2$Se$_3$/CdS/ITO/Ag               | 7.40    | 0.513        | 24.56                    | 58.74  | 0.647          | This work      |      |

$^a$Vapor transport deposition  $^b$Sputtering Sb$_2$Se$_3$ and post-seelenization  $^c$Sputtering Sb precursor and post-seelenization  $^d$Vapor transport deposition Sb$_2$Se$_3$ and post-seelenization.

After comparing the device parameters, the mechanism of a high $V_{OC}$ (i.e., minimum $V_{OC}$ deficit) in this work should be emphatically elucidated. The $V_{OC}$ is strongly dependent on the diode ideality factor ($A$) and reverse saturation current ($J_0$) according to Equation (4)$^{39}$:

$$V_{OC} = \frac{AKT}{q} \ln \left( \frac{J_{SC}}{J_0} + 1 \right)$$

The values of $A$, $J_0$, along with the shunt conductance ($G$) and resistance ($R$) were calculated by treating the dark $J$–$V$ characteristic curves. To be illustrative, the current signals of the Control device would fluctuate severely during the measurement in dark under applied bias voltage of $-1$ to $1$ V (Figure S6, Supporting Information). Thus, dark $J$–$V$ curves of the C-400 and C-420 devices with obvious rectifying characteristics were to the final 7.31% after 60 days storage in ambient air without encapsulation (Figure S5, Supporting Information). EQE spectra and the corresponding integrated $J_{SC}$ of the Control and C-420 devices are portrayed in Figure 4f. Both devices exhibited broad photo response range from UV to NIR, echoing the intrinsically narrow bandgap of Sb$_2$Se$_3$ with strong light absorption. The higher EQE values for the C-420 device in the wavelength range of 300–800 nm can be attributed to the reduced non-radiative recombination losses, i.e., the interface recombination and SCR recombination.$^{38}$ A slightly higher photocurrent generation for the Control device in the wavelength greater than 850 nm was possibly due to the absorption of more infrared photons under a smaller bandgap and less reflection loss. The integrated $J_{SC}$ values calculated from EQE data were 22.15 and 24.45 mA cm$^{-2}$ for Control and C-420 devices, respectively, which were close to the corresponding $J_{SC}$ values obtained from $J$–$V$ measurement results.
Figure 5. Electrical behaviors of the representative C-400 and C-420 devices: a) Dark \( J-V \) curves, b) shunt conductance \( G \) characterizations, c) series resistance \( R \) and ideality factor \( A \) characterizations, d) reverse saturation current density \( J_0 \) characterizations. e,f) Logarithmic \( J-V \) curves of the C-400 and C-420 devices, respectively, showing Ohmic, TFL and Child region.

compared and explored according to a single exponential diode equation\[40\]

\[
J = J_0 \exp \left( \frac{q}{AKT} \left( V - JR \right) \right) + GV - J_L \tag{5}
\]

The values of \( G \) were first extracted from the flat portions under reverse bias of plot of \( dJ/dV \) against \( V \) (Figure 5b), and the obtained values were 3.1 and 6.8 mS cm\(^{-2}\) for C-400 and C-420 devices, respectively. Figure 5c shows the plots of \( dV/dJ \) in relation to \( (J + J_{SC})^{-1} \), then the series resistance \( R \) and diode ideality factor \( A \) could be obtained through the intercept of y-axis and a slope of \( AKT/q \), respectively. The \( R \) values were linear fitted as 1.15 and 2.15 Ω cm\(^2\) for C-400 and C-420, respectively. The obtained values of \( A \) were 2.73 and 1.94, implying the coexistence of interface and SCR recombination in both two devices, whereas an obvious decrease of SCR recombination induced by the trap levels in the depletion region can be observed for C-420. Finally, the reverse saturation current \( J_0 \) were extracted from the plot of \( \ln(J + J_{SC} - GV) \) against \( V - RJ \) (Figure 5d), the corresponding intercept yielded an \( J_0 \) of 2.24 × 10\(^{-3}\) mA cm\(^{-2}\) for C-400, and 4.75 × 10\(^{-3}\) mA cm\(^{-2}\) for C-420. Thus, under a more appropriate post-selenization heat treatment of Sb\(_2\)Se\(_3\) absorber layer, a lower reverse saturation current and better diode ideality factor established a much superior Sb\(_2\)Se\(_3\)/CdS junction quality, justifying higher \( V_{OC} \) and PCE for C-420 device. Moreover, these critical electrical parameters were also close to that of highly efficient (e.g., PCE of 7.49%) VTD processed Sb\(_2\)Se\(_3\) solar cell in superstrate configuration.\[41\] In order to further understand the defect states of these engineered Sb\(_2\)Se\(_3\) thin films, the standard space charge limited current (SCLC) model was applied. Figure 5e,f shows the logarithmic \( J-V \) curves of the C-400 and C-420 devices, respectively. The curves can be divided into three regimes: the ohmic region (at low voltages, exponent \( n = 1 \)), the trap-filled limit (TFL) region (at intermediate voltages, \( n > 3 \)), and the trap-free Child region (at high voltages, \( n > 2 \)). In the TFL region, the current snappishly upsurges once the bias voltage exceeds the kink point, signifying that the trap-states are completely filled by the injected carriers. Thus, the determined onset voltages (\( V_{TFL} \)) associated to C-400 and C-420 were 0.33 and 0.25 V, respectively. Then the trap state density \( N_{trap} \) can be calculated according to the following equation\[42\]

\[
N_{trap} = \frac{2\epsilon_0\epsilon_r V_{TFL}}{qL^2} \tag{6}
\]

where \( L \) is the thickness of Sb\(_2\)Se\(_3\) thin film, \( q \) is the elementary charge, \( \epsilon_0 \) is the vacuum permittivity, and \( \epsilon_r \) is the relative permittivity (i.e., 15.1 for Sb\(_2\)Se\(_3\)). The obtained \( N_{trap} \) values fallen from 2.45 × 10\(^{14}\) for C-400 to 1.85 × 10\(^{14}\) cm\(^{-3}\) for C-420, suggesting the latter post-selenized Sb\(_2\)Se\(_3\) thin film possessed less trap sites and defect centers. It also agreed well with its more superior quality with larger grains, benign orientation and accurate composition.

It is known that interface recombination and SCR recombination are two kinds of common recombination mechanisms in...
TFPV devices, and the dominated recombination loss resulted in severe \( V_{OC} \) deficit. Herein, the recombination mechanism referred to where and how the recombination occurred was systematically investigated. First, temperature-dependent open-circuit voltage (\( V_{OC-T} \)) measurement was carried out to obtain the activation energy (\( E_a \)), according to the following equation \[^{[43]}\]

\[
E_a = qV_{OC} + \frac{AkT}{\ln\left(\frac{J_0}{J_L}\right)}
\]  

Hence, \( E_a \) value could be calculated by extrapolating the \( V_{OC} \) to \( y \)-axis (\( T = 0 \) K). In principle, the \( E_a \) of interface recombination is lower than \( E_g \), and \( E_a \) of SCR recombination is equal to \( E_g \).\[^{[10]}\] As shown in Figure 6a, \( E_a \) value of the Control device was 0.51 eV, much lower than its \( E_g \) value (1.145 eV), suggesting a dominated defect-assisted interface recombination, which might be originated from the poor interfacial adhesion between Sb\(_2\)Se\(_3\) absorber layer and other functional layers due to the existence of multiple micro-voids. Both C-400 and C-420 devices possessed an enlarged \( E_a \) value, especially an \( E_a \) of 1.14 eV that was quite close to its \( E_g \) of 1.160 eV (C-420), authenticating suppressed overall recombination loss with mitigatory SCR recombination in dominant. Electrochemical impedance spectroscopy (EIS) analysis was also conducted to examine the charge recombination and charge transfer characteristics of the solar cells. A comparison of the Nyquist plots of the representative Control, C-400, and C-420 devices are shown in Figure 6b, along with an inset equivalent circuit diagram, where \( R_s \), \( R_{rec} \), and \( C_{rec} \) represent series resistance, recombination resistance and chemical capacitance, respectively. Accordingly, the intercept on the \( x \)-axis (\( Z' \)) was assigned to \( R_s \), the extracted \( R_s \) values for Control, C-400 and C-420 were 2.85, 1.92, and 1.79 \( \Omega \) cm\(^2\), respectively, strengthening the post-selenized Sb\(_2\)Se\(_3\) thin films with large grains and benign orientation that would benefit the charge transport. The arc’s diameter length was directly corresponding to the \( R_{rec} \), an obvious suppressed probability of charge recombination at the interface could be observed for C-420 device, which was also consistent with the \( E_a \) analysis. The \( 1/C^2-V \) curves derived from the capacitance-voltage (C–V) measurements were plotted to scrutinize the junction quality. Figure 6c shows the corresponding curves of the representative C-400 and C-420 devices, the results
of Control device were eliminated under its unstable measurement. The heterojunction-dependent built-in voltage (Vbi) could be extracted through linear fitting and extrapolating to x-axis. Vbi of the C-420 device (670 mV) was larger than that of the C-400 device (565 mV), indicating an improved Sb2Se3/CdS heterojunction quality for C-420. Notably, such an interesting Vbi belonging to our champion device was higher than that of mostly reported efficient Sb2Se3 devices, contributing to a record Voc in this work.14,18 Drive-level capacitance profiling (DLCP) characterizations were also performed on C-420 and C-440 devices to examine the interfacial defects. In principle, the C–V determined doping density (N(CV)) symbolizes the responses from free carriers, bulk defects and interfacial defects, whereas the DLCP measured doping density (N(DLCP)) only reveals the responses from free carriers and bulk defects.19 Therefore, the interface defect density (Ni) can be calculated by the discrepancy between C–V and DLCP profiling at zero bias. The plots of NCV and NDLCP against the profiling depth x can be expressed according to the following equations21

\[ N_{C-V} = \frac{-2\varepsilon_{i,r,n} N_D}{\left(\frac{d(V(x)/x)}{dV}\right) qA^2\varepsilon_{i,r,n}^2\varepsilon_{r,p} N_D + 2\varepsilon_{i,r,p}} \] (8)

\[ N_{DLCP} = -\frac{C_0}{2q\varepsilon_{r,p} A^2 C_1} \] (9)

\[ x = \frac{\varepsilon_{r,p} A}{C_0} \] (10)

where \( N_D \) is the doping density of CdS, \( A \) is the device area, \( \varepsilon_{i,r} \), \( \varepsilon_{i,n} \), and \( \varepsilon_{r,p} \) represents the permittivity of free space, the relative permittivity of CdS and Sb2Se3, respectively, \( C_0 \) and \( C_1 \) are two quadratic fitting parameters derived from the C–V curves. As shown in Figure 6d, the difference between NCV and NDLCP of the C-400 device was much larger than that of the C-420 device, the estimated \( N_i \) values for these two devices were \( 1.04 \times 10^{16} \text{ cm}^{-3} \) and \( 2.24 \times 10^{15} \text{ cm}^{-3} \) (at \( n = 0 \text{ V} \)), respectively. Such significant \( N_i \) decr ease with nearly one order of magnitude indicated an improved Sb2Se3/CdS interface, e.g., smooth contact, benign band alignment, alleviated lattice mismatch, and passivated interface defects for C-420 device. Furthermore, the C-420 device possessed a large depletion width (Wd) as compared to C-400 device, which was reasonable to occur since it varied positively with Vbi according to the following equation41

\[ W_d = \sqrt{\frac{2\varepsilon_{r,p} (N_A + N_D)^2}{qN_A N_D (\varepsilon_{p,n} N_A + \varepsilon_{i,n} N_D)}} \] (11)

where \( q \) is the elementary charge, \( \varepsilon_{i,r} \) and \( \varepsilon_{i,n} \) are the permittivity, \( N_A \) and \( N_D \) are the acceptor density and donor density in Sb2Se3 and CdS, respectively. Since the doping concentration of CdS is much higher than that of Sb2Se3, almost the entire \( W_d \) is located within Sb2Se3 absorber layer. An optimal \( W_d \) of 239 nm for C-420 is really beneficial for the light absorption and carrier extraction, corresponding to its much superior device performance. All these heterojunction or interface associated parameters are summarized in Table 3. Notably, an efficient Sb2Se3 absorber layer growth engineering would also influence the junction quality, the interface quality, the recombination mechanism, and therefore improve device Voc and PCE.

Understanding and controlling the bulk defect properties, especially deep-level defects, is also essential for photovoltaic devices to reduce detrimental nonradiative SCR recombination and improve device performance, in particular Voc, which is primarily affected by defect depth, defect density and carrier lifetime. Herein, a well-established deep-level transient spectroscopy (DLTS) was carried out to investigate defect dynamics of the Control and C-420 devices, respectively. According to literature, the positive and negative peaks can be ascribed to majority-carrier and minority-carrier traps, respectively.30 Thus, the energy band information of the light absorbing thin film was first examined by ultraviolet photoelectron spectroscopy (UPS), wherein, the Control and C-420 thin films were determined to be n-type and p-type conductive characteristics, respectively (Figure S7 and Note S2, Supporting Information), agreeing with DFT calculation results. Consequently, as shown in Figure 7a, three electron traps that denoted as E1, E2, and E3 can be well identified in the Control device. By contrast, only two hole traps, which are denoted as H1 and H2, can be observed in the C-420 device. The activation energy \( E_A \) (E1 ~ E2 or E2 ~ E3, where \( E_T \) and \( E_C \) represent the defect energy level, conduction and valance bands edge, respectively) of the defects in these two devices were calculated from the Arrhenius plots by fitting the points near the DLTS peaks (Figure 7b and Note S3 in Supporting Information). The defect characteristics including defect type, defect energy level (\( E_A \)), defect density (\( N_i \)), and capture cross-section (\( \sigma \)) are summarized in Table 4. According to the first-principle theoretical basis and the reported DLTS experimental results in literature,16,45 considering the defects formation energy and defects energy level, regarding the nonequivalent Sb and Se sites (inset in Figure 7b), E1, E2, and E3 can be readily assigned to selenium vacancies (\( V_{Se} \) and \( V_{Se3} \)), antisite defects of Sb\(_{2Se}_2\), respectively. They were reasonable to exist in Sb-rich (Se-poor) Sb2Se3 light absorbing film, the higher concentration (\( \approx 10^{14} \text{ cm}^{-3} \)) of Sb\(_{2Se}_2\) antisite defects were also consistent with their lower formation energy. After an optimal post-selenization heat treatment of the Sb\(_2Se_3\) thin film, the additional Se atoms would preferentially fill the Se vacancies (i.e., Se-compensation), and also increase the formation energy of Sb\(_{2Se_3}\).
Figure 7. a) DLTS signals from the Control and C-420 Sb$_2$Se$_3$ devices, b) The corresponding Arrhenius plots obtained from DLTS signals. c,d) Conduction ($E_C$) and valence ($E_V$) band edges, Fermi level ($E_F$) and defect energy levels of the Control and C-420 samples, respectively. e) Time-resolved absorption spectra obtained at various time delays after photoexcitation for the Control and C-420 Sb$_2$Se$_3$ thin films. f) Transient kinetic traces showing the decay of the PIA peak at 750 nm for both thin films.

Defects, resulting in the vanish of those donor defects. The emergence of Sb vacancies (V$_{Sb}$) in some particular region was caused by the decrease of Sb content, some excessive Se was likely to occupy these Sb vacancies to form Se$_{Sb}$ antisites for maintaining the structure stability. Thus, in C-420 device, two observable hole traps of H1 and H2 with $N_T$ of $6.09 \times 10^{13}$ and $1.46 \times 10^{13}$ cm$^{-3}$ can be assigned to Se$_{Sb}$ and V$_{Sb1}$, respectively. Consequently, the Sb$_2$Se$_3$ absorber engineering could induce an obvious healing effect in defect type and defect concentration, which was highly beneficial for reducing the charge recombination. Figure 7c,d il-
and holes, leading to an enlarged effect, and boost the split of the quasi-Fermi levels of electrons strengthening our effective effort in defect healing.\[46,47\] Bare to those of high efficiency perovskite or CZTSSe solar cells, trapped by these shallow trap states. Moreover, the value is compared to those of high efficiency perovskite or CZTSSe solar cells, strengthening our effective effort in defect healing.\[46,47\]

Table 4. Defect parameters of the representative Control and C-420 \(\text{Sb}_2\text{Se}_3\) solar cells.

| Devices | Defects       | \(E_v\) [eV] | \(\sigma\) [cm²] | \(N_T\) [cm⁻¹] | \(N_x\times\sigma\) [cm⁻¹] |
|---------|---------------|-------------|-----------------|----------------|--------------------------|
| Control | \(E_c\) = 0.288 | \(V_{sd}\) 3.13 \(\times\) 10⁻¹⁰ | 8.17 \(\times\) 10⁻¹⁰| 2.56 \(\times\) 10⁻¹⁴ |
| Control | \(E_c\) = 0.367 | \(V_{sd}\) 1.68 \(\times\) 10⁻¹⁰ | 1.16 \(\times\) 10⁻¹⁰| 1.80 \(\times\) 10⁻¹⁴ |
| Control | \(E_c\) = 0.574 | \(V_{sd}\) 1.68 \(\times\) 10⁻¹⁰ | 1.16 \(\times\) 10⁻¹⁰| 1.80 \(\times\) 10⁻¹⁴ |
| C-420   | \(E_v\) = 0.225 | \(V_{sd}\) 3.35 \(\times\) 10⁻¹⁰ | 6.09 \(\times\) 10⁻¹⁰| 2.04 \(\times\) 10⁻¹⁰ |
| C-420   | \(E_v\) = 0.329 | \(V_{sd}\) 5.97 \(\times\) 10⁻¹⁰ | 1.46 \(\times\) 10⁻¹⁰| 8.72 \(\times\) 10⁻¹⁰ |

Illustrates the band edge positions and defect levels for both devices. For Control device, E1, E2, and E3 electron defects are located at about 0.29, 0.37, and 0.57 eV below \(E_c\), which are close to its intrinsic \(E_F\) position, implying strong pinning effect, resulting in severe trap-assisted recombination and \(V_{OC}\) loss. Especially, E3 defect showed a largest \(N_T \times \sigma\) value (1.95 cm⁻¹), which might act as a major recombination center to diminish carrier lifetime \(\tau_{trap}\), according to the following equation associated with trap-assisted Shockley-Read-Hall (SRH) recombination\[46,47\]

\[
\tau_{trap} = \frac{1}{v_\theta \sigma N_T}
\]

where \(v_\theta\) is the carrier thermal velocity, capture cross-section \(\sigma\) and defect density \(N_T\) are obtained from DLTS. By contrast, for C-420 device, H1 and H2 hole defects are located at 0.23 and 0.33 eV above \(E_v\), which can efficiently alleviate the \(E_F\) pinning effect, and boost the split of the quasi-Fermi levels of electrons and holes, leading to an enlarged \(V_{OC}\) for the device. In addition, it is worth noting that the \(N_T \times \sigma\) of H1 and H2 are markedly decreased with several orders of magnitude, indicating the holes will be emitted again in a very short time even they are captured by these shallow trap states. Moreover, the value is comparable to those of high efficiency perovskite or CZTSSe solar cells, strengthening our effective effort in defect healing.\[46,47\]

To further study the carrier transport dynamics, the transient absorption spectroscopy (TAS) characterizations were performed on the VTD processed (Control) and post-selenized (C-420) \(\text{Sb}_2\text{Se}_3\) thin films that were deposited on soda-lime glass. Under the absence of any electron or hole extraction layer, it can directly reflect the relaxation of the exciton to ground state through charge recombination. As shown in Figure 7e, an obvious photo-induced absorption (PIA) peak located at 750 nm can be observed for both samples, which can be assigned to the trapping of photo-generated minority carriers. Subsequently, the kinetic decay curves were fitted via a biexponential model (Figure 7f), the detailed fitting parameters are summarized in Table S1 (Supporting Information). To be specific, the short decay lifetime \(\tau_J\) and long decay lifetime \(\tau_J\) can be attributed to the interface/surface recombination and the bulk defect recombination, respectively.\[46\] Compared to Control sample, a significant increase of both \(\tau_J\) and \(\tau_J\) for C-420 \(\text{Sb}_2\text{Se}_3\) thin film indicated that both interface/surface recombination and bulk recombination have been suppressed, especially a dominant bulk recombination mitigation mechanism, echoing the aforementioned DLTS results of defects healing. Moreover, it is worth mentioning that the obtained lifetime \(\tau = 14497\) ps also represents one of the top values among all antimony chalcogenides (Table S2, Supporting Information). Overall, benefitting from this efficient \(\text{Sb}_2\text{Se}_3\) layer growth engineering, the improved junction quality, the healed deep defects, and the prolonged carrier lifetime could undoubtedly improve the \(V_{OC}\) and PCE of the \(\text{Sb}_2\text{Se}_3\) thin-film solar cell.

Finally, the morphological and microstructural characteristics of the Mo/\(\text{Sb}_2\text{Se}_3\)/CdS/ITO/Ag thin-film solar cells were investigated to further sightsee the promotion mechanism of the device performance. Figure 8a,b shows the cross-sectional SEM images of the representative Control and C-420 \(\text{Sb}_2\text{Se}_3\) devices, respectively. For Control device, \(\text{Sb}_2\text{Se}_3\) light absorbing thin film consisted of multiple grains that randomly grew on Mo substrate, especially with some small grains that horizontally stacked in parallel with substrate, as marked in black circle in Figure 8a. In contrast, C-420 \(\text{Sb}_2\text{Se}_3\) thin film was composed of large and compact crystal grains that quasi-vertically-oriented across the whole light absorber layer, as marked in red circle in Figure 8b. Such observations matched well with the corresponding XRD results, and revealed an orientation transition from [hk0] to [hk1]. Figure 8e,f illustrate a schematic representation of the \(\text{Sb}_2\text{Se}_3\) microstructure growth under post-selenization. The VTD processed pristine thin film with grains limited, voids existed, [hk0] orientation dominated characteristics was mainly influenced by the nature of Mo substrate and deposition dynamics, i.e., growth rate. Once the post-selenization was introduced, a certain amount of Se atoms would diffuse and react with the Se-poor \(\text{Sb}_2\text{Se}_3\) lattice to form stoichiometric \(\text{Sb}_2\text{Se}_3\) molecules under appropriate temperature. The emerged crystals would occupy the voids first, and then start to grow into large columnar grains under proper duration. Notably, under Se atmosphere, non-van der Waals (vdW) planes (e.g., [hk1].) might have higher growth rates due to the increased adatoms’ absorption energy, and therefore transformed the final thin film orientation to [hk1]. For more clear comparison, two typical crystal planes of (120) and (211) were chosen to clarify the correlation between film orientation and device performance. As shown in Figure 8c,d, the [120]-oriented grain consists of \(\text{Sb}_2\text{Se}_3\) ribbons horizontally stacked, whereas [211]-oriented grain consists of titled \(\text{Sb}_2\text{Se}_3\) ribbons stacked quasi-vertically on the substrate. Obviously, the photo-generated carriers transport in the [211]-oriented grains should be much easier than in the [120]-oriented grains, since the carriers travel efficiently within the covalently bonded \(\text{Sb}_2\text{Se}_3\) ribbons in the former condition, and a more strenuous hop between ribbons held together by vdW forces in the later scenario. Moreover, C-420 thin film with large [211]-oriented grains possessed fewer dangling bonds at grain boundaries, and could reduce the recombination loss.\[12\] Overall, after an efficient post-selenization, the optimized C-420 \(\text{Sb}_2\text{Se}_3\) thin film with large compact grains, accurate chemical composition, and benign growth orientation could be beneficial for carrier transport and recombination mitigation, corresponding to a lower \(R_s\), \(N_T\), higher \(\tau, J_{SC}, V_{OC}\), and PCE in the device.

Figure 9a shows the cross-sectional TEM image of the champion C-420 \(\text{Sb}_2\text{Se}_3\) device, which displays an obvious layered structure with approximate thickness of 1500, 80, and 500 nm for \(\text{Sb}_2\text{Se}_3\), CdS, and ITO layers, respectively. No obvious MoSe₂ interfacial layer can be observed between Mo back contact layer and \(\text{Sb}_2\text{Se}_3\) absorber layer, indicating the elimination of the com-
Figure 8. The surface/cross-sectional morphologies and the growth orientation evolutions of the Sb₂Se₃ absorber layer before and after post-selenization heat treatment. a,b) Cross-sectional SEM image of the Control and C-420 Sb₂Se₃ thin-film devices. c,d) Atomic structures of [120]- and [211]-oriented grains in Sb₂Se₃, charges must hop between ribbons in the [120]-oriented grain (dashed blue traces), but are able to move smoothly along [211]-oriented grains (solid blue traces). e,f) Schematic illustrations of the corresponding Sb₂Se₃ grains on Mo substrate.

3. Conclusions

In summary, Sb₂Se₃ light absorbing thin films were successfully prepared via an effective two-step thermodynamic/kinetic deposition processes, both a temperature-dependent Mo substrate-Sb₂Se₃ film interaction in pre-VTD process, and an energetically favorable reaction between Se vapor and Sb-rich Sb₂Se₃ molecule in post-selenization process can limit the growth of MoSe₂. Such superior back contact can also improve the carrier transport and reduce the interfacial recombination loss, echoing an acceptable $R_s$ value of 1.79 $\Omega \text{ cm}^{-2}$, and FF of 58.74%. According to the high-resolution TEM (HRTEM) images taken at Sb₂Se₃/CdS interface (Figure 9b), it shows compact, well-adherent, and pinhole-free characteristics, which is highly beneficial for reducing carrier recombination and current leakage. The absence of lattice distortion, dislocation, or amorphous layer further validates a high-quality Sb₂Se₃/CdS heterojunction. A lattice fringe with 0.340 nm interplanar d-spacing can be assigned to (111) plane of CdS, whereas the lattice fringe with interplanar d-spacing of 0.319 nm matches well with the (211) plane of the orthorhombic Sb₂Se₃. Figure 9c shows the HRTEM image corresponds to a portion at the middle region of Sb₂Se₃ absorber layer, quite clear lattice fringe with d-spacing of 0.319 nm also agrees well with the (211) lattice plane, indicating an excellent continuity in the vertical direction. The Gaussian blur processed high-angle annular dark field scanning transmission electron microscope (HAADF-STEM) image (Figure 9d) obtained at the bulk region of Sb₂Se₃ clearly revealed its 1D crystal structure with (Sb₂Se₃)ₙ nanoribbons stacked in parallel in the [001] direction. The “bright dots” with strong intensity can be assigned to the Sb-dominated atomic columns ($Z_{Sb}$ of 51), while the “dark dots” with much lower intensity matched well with Se-occupied atomic columns ($Z_{Se}$ of 34). For TEM-coupled EDS results (Figure 9e,f), the marked elements present reasonably compositional distribution, elements of Sb and Se also present good uniformity throughout the whole Sb₂Se₃ absorber layer. Overall, such morphological and microstructural observations really demonstrate the high quality of this engineered Sb₂Se₃ absorber layer, accompanied by excellent interface quality, leading to a remarkable improvement in device performance.
4. Experimental Section

Deposition of Sb₂Se₃ Thin Films: Soda-lime glass (SLG) substrates were first cleaned in an ultrasonic bath using sequential detergent, ethanol and deionized water. Mo as back contact layer was then deposited using direct current (DC) magnetron sputtering based on a dense and high-purity Mo metallic target. A double-pressure sputtering of Mo (≈1 μm thickness) was applied under a first 1.5 Pa pressure of 15 min to improve adhesion, and then 0.5 Pa of 25 min to ensure compactness. In subsequent, Sb₂Se₃ light absorber layer was deposited onto the Mo-coated SLG substrates via VTD method. The schematic diagram of this VTD process is shown in Figure 1a, which was operated in a double-chamber vacuum tubular furnace (Tianjin Zhonghuan Furnace Corp.). 0.5 g Sb₂Se₃ powder (99.999% purity, Jiangxi Ketai) was put into a quartz crucible and placed in the center of the heating zone, while the substrate was immobilized on a graphite support and located at the right end of the tube. Vacuum was pumped by a mechanical pump, and then thoroughly purged with high-purity argon gas. A high working pressure of 5 × 10⁴ Pa was applied to provide sufficient Se partial pressure. Temperature of the Se side was fixed at 400°C, post-selenization of Sb₂Se₃ was carried out at temperatures of 380, 400, 420, and 440°C, respectively.

Adv. Sci. 2022, 9, 2105142 ©2022 The Authors. Advanced Science published by Wiley-VCH GmbH
Preparation of Sb$_2$Se$_3$ Thin-Film Solar Cell: After the deposition of Sb$_2$Se$_3$ thin film, cadmium sulfide (CdS) buffer layer was deposited via chemical bath deposition (CBD) method (Figure 1c). The aqueous solution of cadmium sulfate (0.015 M), thiourea (0.75 M), and ammonium hydroxide aqueous solutions (28%) were sequentially added into deionized water, the substrates were soaked into this mixed solution and then maintained at 80°C water bath under continuous stirring for 9 min. Afterward, the specimens were rinsed with deionized water and dried in an oven. Indium tin oxide (ITO) window layer was subsequently deposited using DC magnetron sputtering under a power of 100 W, a pressure of 0.4 Pa, a flow rate of 30 sccm for argon and 8 sccm for oxygen gas, respectively. Finally, Ag electrodes were thermally evaporated onto the ITO surface to form metallic contact (Figure 1e), and the surface of the device was divided into small squares with identical active device area of 0.14 cm$^2$. Overall, a schematic diagram of the device preparation process and the device configuration (Glass/Mo/Sb$_2$Se$_3$/CdS/ITO/Ag) is illustrated in Figure 1.

Characterizations: The crystal structure of Sb$_2$Se$_3$ thin films were characterized by X-ray diffraction (XRD, Ultima-iv) with Cu K$_\alpha$ radiation under operation conditions of 40 kV and 40 mA. Surface and cross-sectional morphologies of the thin films were observed using scanning electron microscopy (SEM, Zeiss SUPRA 55). SEM-coupled energy dispersive X-ray spectroscopy (EDS, BRUKER QUANTAX 200) could provide the fundamental chemical compositions. Transmission electron microscopy (TEM) characterizations were carried out on a FEI Titan Coubed Themis G2 300 microscope, wherein, the as-measured specimen was prepared by ablating the TFPV device with focused ion beam (FIB, FEI Scios). TEM-coupled EDS was used to further analyze the elemental distribution. Current density–voltage (J–V) characteristics of the Sb$_2$Se$_3$ thin-film solar cells were measured using a multi-meter (Keithley 2400) under AM 1.5G simulated sunlight illumination with intensity calibrated to 100 mW cm$^{-2}$. The external quantum efficiency (EQE) spectra were acquired using a 2olixi SCS 101 system and a Keithley 2400 source meter. Temperature-dependent $V_{oc}$ measurements were performed on a Lakeshore 325 temperature controller, and the temperatures were swept from 340 to 160 K with a step of 20 K. An electrochemical workstation (CHI660e) was employed for the electrical impedance spectroscopy (EIS) measurements, which were conducted with frequency ranging from 1 Hz to 100 kHz. Capacitance–voltage (C–V) measurements were applied at an AC amplitude of 30 mV and a frequency of 10 kHz under dark condition at room temperature. The DC bias voltage during the C–V measurements was scanned from −1 to 0.3 V. Drive-level capacitance profiling (DLCP) measurements were performed with AC amplitude from 20 to 140 mV, and a DC bias voltage from −0.2 to 0.2 V. The deep-level transient spectroscopy (DLTS) information was obtained using an FT-1030 HERA DLTS system configured with a JANIS VFP-800 cryostat controller. Reflection spectra of the Sb$_2$Se$_3$ thin films were obtained through a Shimadzu UV-3600 UV/Vis/NIR spectrophotometer equipped with monochromator. The additional Fermi level and band level information were also analyzed through Ultraviolet photoelectron spectroscopy (UPS) using a Thermo Fisher ESCALAB 250Xi spectrometer. Transient absorption spectroscopy (TAS) characterizations were performed on a transient absorption spectrometer (Newport), which was equipped with a Spectra-Physics Solstice Ace regenerative amplifier (800 nm wavelength, 100 fs pulses with 1 kHz repetition rate). The decay characteristics were fitted according to a biexponential model y = $\sum\alpha_i\exp(-t/\tau_i)$ ($i = 2$). Electronic structure and orbital-resolved projected density of state (DOS) were calculated using first-principles density functional theory (DFT) as implemented in the Vienna Ab-initio Simulation Package (VASP).

Supporting Information

Supporting Information is available from the Wiley Online Library or from the author.

Acknowledgements

This work was supported by National Natural Science Foundation of China (No. 62074102, 62104156, 62104157), Guangdong Basic and Applied Basic Research Foundation (2020A1515010805), China, Key Project of Department of Education of Guangdong Province (No. 2018KZDXM059) China, Science and Technology plan project of Shenzhen (20200812000347001, 20200826143347001, JCYJ20190808153409238) China. The authors wish to acknowledge the assistance on (TEM/STEM/FIB) received from the Electron Microscope Center of the Shenzhen University.

Conflict of Interest

The authors declare no conflict of interest.

Data Availability Statement

The data that support the findings of this study are available from the corresponding author upon reasonable request.

Keywords

absorber layer engineering, crystal growth, defects healing, open-circuit voltage deficit, Sb$_2$Se$_3$ solar cells

Received: November 10, 2021
Revised: January 12, 2022
Published online: January 28, 2022

[1] D. J. Xue, S. C. Liu, C. M. Dai, S. Chen, C. He, L. Zhao, J. S. Hu, L. J. Wan, J. Am. Chem. Soc. 2017, 139, 958.
[2] B. Liu, L. Bai, T. Li, C. Wei, B. Li, Q. Huang, D. Zhang, G. Wang, Y. Zhao, X. Zhang, Energy Environ. Sci. 2017, 10, 1134.
[3] W. Wang, X. Wang, G. Chen, L. Yao, X. Huang, T. Chen, C. Zhu, S. Chen, Z. Huang, Y. Zhang, Adv. Energy Mater. 2019, 5, 1800683.
[4] S. Chen, Z. Zheng, M. Cathelineaud, H. Ma, X. Qiao, Z. Su, P. Fan, G. Liang, X. Fan, X. Zhang, Sol. Energy Mater. Sol. Cells 2019, 203, 110154.
[5] M. A. Green, E. D. Dunlop, J. Hohl-Ebinger, M. Yoshita, N. Kopidakis, X. Hao, Prog. Photovolttaics 2022, 30, 3.
[6] L. H. Wong, A. Zakutayev, J. D. Major, X. Hao, A. Walsh, T. K. Todorov, E. Saucedo, J. Phys. Energy 2019, 1, 032001.
[7] S. Giraldo, Z. Jehl, M. Placidi, V. Izquierdo-Roca, A. Pérez-Rodriguez, E. Saucedo, Adv. Mater. 2019, 31, 1806692.
[8] Z. Li, X. Liang, G. Li, H. Liu, H. Zhang, J. Guo, J. Chen, K. Shen, X. San, W. Yu, R. E. I. Schropp, Y. Mai, Nat. Commun. 2019, 10, 125.
[9] L. Wen, C. Chen, S. Lu, K. Li, R. Kondrotas, Y. Zhao, W. Chen, L. Gao, C. Wang, J. Zhang, G. Niu, J. Tang, Nat. Commun. 2018, 9, 2179.
[10] L. Wang, D.-B. Li, K. Li, C. Chen, H.-X. Deng, L. Gao, Y. Zhao, F. Jiang, L. Li, F. Huang, Y. He, H. Song, G. Niu, J. Tang, Nat. Energy 2017, 2, 17046.
[11] A. A. Volconov, T. Razykov, F. Razaq, I. Gan, J. Chantana, Y. Kawano, T. Nishimura, H. Wei, A. Zakutayev, T. Minemoto, X. Zu, S. Li, L. Qiao, Sol. Energy 2020, 201, 227.
[12] Y. Zhou, L. Wang, S. Chen, S. Qin, X. Liu, J. Chen, D.-J. Xue, M. Luo, Y. Cao, Y. Cheng, E. Sargent, J. Tang, Nat. Photonics 2015, 9, 409.
[13] K. Li, C. Chen, S. Lu, C. Wang, S. Wang, Y. Lu, J. Tang, Adv. Mater. 2019, 31, 1903914.
