SoK: All You Ever Wanted to Know About x86/x64 Binary Disassembly But Were Afraid to Ask

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Abstract—Disassembly of binary code is hard, but necessary for improving the security of binary software. Over the past few decades, research in binary disassembly has produced many tools and frameworks, which have been made available to researchers and security professionals. These tools employ a variety of strategies that grant them different characteristics. The lack of systematization, however, impedes new research in the area and makes selecting the right tool hard, as we do not understand the strengths and weaknesses of existing tools.

In this paper, we systematize binary disassembly through the study of nine popular, open-source tools. We couple the manual examination of their code bases with the most comprehensive experimental evaluation (thus far) using 3,788 binaries. Our examination of their code bases with the most comprehensive study of nine popular, open-source tools shown in Table I at Stevens Institute of Technology.

TABLE I: The group of open-source tools that our study covers and representative works that use those tools.

| Tool (Version) | Source (Release Date) | Public Use |
|----------------|------------------------|------------|
| PSI (1.0)      | Website [63] (Sep 2014) | [50, 88, 111] |
| UROBOROS (0.11) | Github [93] (Nov. 2016) | [103] |
| DYNNINST (9.3.2) | Github [79] (April 2017) | [7, 18, 69, 73, 96] |
| ORIDUMP (2.30) | GNU [47] (Jan. 2018) | [21, 103, 111] |
| GHIDRA (9.0.4) | Github [73] (May 2019) | [24, 45, 91] |
| METASM (2.0.0) | Github [13] (Jan. 2019) | [22, 41, 44] |
| ANGR (8.19.7.25) | Github [8] (Oct. 2019) | [30, 71, 81, 98, 112] |
| BAP (2.1.0) | Github [26] (Mar. 2020) | [10, 16, 64] |
| RADAARE (4.4.0) | Github [89] (April 2020) | [4, 31, 52, 58] |

- Algorithms typically produce results with some correctness guarantees. They mostly leverage knowledge from the binary (e.g., symbols), the machine (e.g., instruction set), and/or the ABI (e.g., calling conventions).
- Heuristics are based on common patterns and typically do not offer assurances of correctness.

Moreover, each tool adopts a different set of strategies, with technical details not always fully documented or publicized. To complicate the matters, the implemented strategies have evolved over time, further deviating from documentation. The above have created a knowledge gap that impedes the users of these tools and, specifically, binary analysis researchers. To bridge the gap, we must answer several questions:

Q1 – What are the algorithms and heuristics used in existing disassembly tools and how do they interact?
Q2 – What is the coverage & accuracy of heuristic methods in comparison to algorithmic ones? Are there trade-offs?
Q3 – What errors do existing disassembly tools make and what are the underlying causes?

To answer these questions, this paper presents a systematization of binary disassembly research, through the study of nine popular open-source tools shown in Table I. Unlike past research [5, 56, 68, 77, 105], we study these tools both qualitatively and quantitatively to understand the tools not only as a whole, but also their individual algorithms and heuristics.

More specifically, our qualitative study of the tools is based on manually inspecting source code. This allows us to answer Q1 by presenting their exact and most recent strategies, avoiding ambiguities and out-of-date information found in documentation and publications. The quantitative study answers questions Q2–Q3 by applying the tools on a corpus of 3,788 benchmark binaries, consisting of utilities, client/server programs, and popular libraries on both Linux

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and Windows systems (see Table IV). To evaluate the tools in terms of coverage and accuracy, we built an analysis framework based on LLVM, GCC, the Gold Linker, and Visual Studio to automatically collect the ground truth while building the corpus. We evaluate the tools by individually measuring different disassembly phases to quantify the effectiveness of the strategies employed. Our evaluation presents the degree of use, precision, and pitfalls of each component of each tool.

By systematically dissecting and evaluating the tools, we were able to make new observations that amend or complement prior knowledge. Our major observations include: (1) For better coverage, mainstream tools incorporate heuristics in nearly every phase of disassembly. These heuristics are heavily used in disassembling real-world binaries and, without them, the tools cannot provide practical utility in many tasks. (2) Heuristics typically cannot provide correctness assurances and lead to various errors, particularly when encountering complex constructs. Moreover, previous works may have overestimated the reliability of those heuristics. For instance, a recent study [5] (unintentionally) overstated the accuracy of linear sweep because many benchmarks containing data-in-code were not considered. (3) Tools may share the same group of algorithms and heuristics, however, they organize and combine them differently, leading to different accuracy-coverage trade-offs. (4) Tools have different strengths across different tasks. For instance, commercial tools are better at recovering instructions but open source tools can better identify cross-references.

**Contributions:** Our main contributions are as follows:

- We present a thorough systematization of binary disassembly from the perspective of algorithms and heuristics. To our knowledge, this is the first research that can answer Q1–Q3.
- We developed a compiler-based framework for automated end-to-end collection of ground truth for binary disassembly. We used it to compose a benchmark data set for assessing binary disassembly tools. The framework and benchmarks are available at https://github.com/junxzm1990/x86-sok.
- We present, to our knowledge, the most comprehensive evaluation of open-source disassembly tools. Our analysis unveils the prevalence of heuristics, their contribution to disassembly, and shortcomings.
- We make new observations and improve the understanding of binary-disassembly strategies and tools. We envision that these insights will facilitate future research in the area of disassembly and drive improvements in existing tools.

**II. SCOPE OF SYSTEMATIZATION**

**A. Functionality**

In general, binary disassembly can involve different tasks based on the context of use. This work focuses on tasks that relate to binary-software security. Table II classifies popular security works and summarizes the information each class needs to recover from binary code. Our study accordingly concentrates on the disassembly aspects of providing them:

**Disassembly** is the process of recovering the assembly instructions of a binary. Perfect disassembly separates data from code regions and correctly identifies the instructions that were emitted by the compiler or introduced by the developer.

**Symbolization** determines cross-references (xrefs for short) or precisely, numeric values in the binary that are references of other code or data objects. Depending on the location of the reference and the location of the target, there are four types of xrefs: code-to-code (c2c), code-to-data (c2d), data-to-code (d2c), and data-to-data (d2d).

**Function Entry Identification** locates the entry points of functions. A special but important case is the main function.

**CFG Reconstruction** re-builds the control flow graph (CFG) of a binary program. We consider direct control transfers, indirect jumps/calls, tail calls, and non-returning functions.

**B. Targeted Binaries**

Similarly to the majority of the works we study, we focus on binaries with the following key properties: (1) They have been produced with mainstream compilers and linkers; (2) Binaries may include hand-written assembly; (3) They have not been obfuscated; (4) We do not assume symbol availability, i.e., binaries are stripped; (5) We only consider X86/X64 binaries. The majority of effort in prior works has focused on such binaries, owing to the popularity of the architectures; (6) They run on Linux or Windows operating systems.

**C. Targeted Tools**

Our systematization is based on study of disassembly tools. We use five criteria to select tools: (1) They are designated for disassembly or have an independent module for disassembly. (2) They can do automated disassembly without user interactions. (3) They are open source tools so that we can study their implemented strategies. (4) They have unique strategies that are not fully covered by other tools. (5) They can run on our targeted binaries to support our quantitative evaluation.

Following the above criteria, we selected 9 tools, as listed in Table I. We also looked at JakStab [59], RetDec [32], and BinCat [12] but excluded them in the study because (1) JakStab cannot run our benchmark binaries due to a parsing error (https://github.com/kinder/jakstab/issues/9); (2) RetDec aims for de-compilation; it uses preliminary strategies for disassembly, which are all covered by other tools we selected; (3) BinCat requires user interactions to do disassembly. The disassembly results vary upon different user interactions.

**TABLE II: Popular solutions of binary security and the information the solutions need from binary disassembly. "Inst", "CFG", "Func", and "Xrefs" respectively mean invalidate instructions, control flow graph, functions, and cross references.**

| Category                  | Solutions & Required Information                                      |
|---------------------------|------------------------------------------------------------------------|
| Vulnerability Finding     | [25, 20, 57] Inst, CFG, Func, Xrefs                                   |
| Control Flow              | [57, 80, 100, 108, 109, 111] Inst, CFG, Func                          |
| Integrity                 | [19, 38, 51, 72, 85, 87, 99] Inst, CFG, Func                          |
| Code Layout               | [25, 53, 60, 104] Inst, CFG, Func, Xrefs                              |
| Randomization             | [61, 65, 78, 106, 109] Inst, CFG, Func, Xrefs                         |
| Execute-only Code         | [21, 110] Inst, CFG                                                    |
| Legacy-code Patching      | [11, 101, 102, 103] Inst, CFG, Func, Xrefs                           |
| Code Similarity           | [14, 17, 40, 42, 55, 57, 82] Inst, CFG, Func                          |
| Measurement               | [15, 29, 30, 43, 62, 70, 83] Inst, CFG, Func                          |
| Software Fault Isolation  | [58, 59, 67, 107] Inst, CFG, Func                                     |
| Software De-Bloating      | [46, 86, 92] Inst, CFG, Func                                          |
To understand the strategies employed in today’s binary disassembly tools, we studied 9 representative examples in Table I. These tools have varying popularity and cover nearly all publicly known techniques in binary disassembly [105]. Our investigation is primarily based on studying source code, instead of solely relying on available documentation and publications. Source code reflects the exact semantics of the strategies applied, protecting us from ambiguities in the documents. Also, many tools have evolved over time and their corresponding documentation and publications are out-of-date. The rest of this section presents our findings, summarized in Table III. We assign a number to each algorithm and heuristic, respectively placing it within a ring (e.g., ①) or disc (e.g., ①).

### III. ANALYSIS OF TOOLS

#### A. Algorithms and Heuristics in Disassembly

The disassembly strategies we study fall into two broad and well-known classes: linear sweep and recursive descent.

**Linear Sweep** [OBJDUMP, PSI, UROBOROS]: Linear sweep continuously scans pre-selected code ranges and identifies valid instructions (①), exploiting the rationale that modern assemblers tend to layout code successively to reduce the binary’s size. In general, a linear sweep strategy can be described by how it selects sweep ranges and how it handles errors during scanning. As such, we summarize algorithms according to these two aspects.

All tools in this class follow OBJDUMP to select code regions for sweep: they process code ranges specified by symbols in the .symtab and .dynsym sections (①), followed by

#### TABLE III: The specifics of existing algorithms (numbered with rings like ①) and heuristics (numbered with discs like ①).

| Alg. | Algorithms & Heuristics | Goals | Tools |
|------|--------------------------|-------|-------|
| Disassembly | Linear Sweep | ① Start from code addresses with symbols | Code accuracy | OBJDUMP, PSI, UROBOROS |
| | Recursive Descent | ② Follow control flow to do disassembly | Code accuracy | DYNINST, GHIDRA, ANGR, BAP, RADARE2 |
| | | ③ Start from program entry, main, and symbols | Code accuracy | DYNINST, GHIDRA, ANGR, BAP, RADARE2 |
| | | ④ Function entry matching | Code accuracy | DYNINST, GHIDRA, ANGR, BAP, RADARE2 |
| | | ⑤ Linear sweep code gaps | Code coverage | ANGR |
| | | ⑥ Disassembly from targets of xrefs | Code coverage | GHIDRA, RADARE2 |
| Symbolization | Xrefs | ⑦ Exclude data units that are floating points | Xref accuracy | ANGR |
| | | ⑧ Brute force operands and data units | Xref coverage | UROBOROS, McSEMA, GHIDRA, ANGR |
| | | ⑨ Pointers in data have machine size | Xref accuracy | UROBOROS, McSEMA, GHIDRA, ANGR |
| | | ⑩ Alignment of pointers in data | Xref accuracy | UROBOROS, McSEMA, GHIDRA, ANGR |
| | | ⑪ Pointers in data or referenced by other xrefs can be non-aligned | Xref coverage | GHIDRA, ANGR |
| | | ⑫ References to code can only point to function entries | Xref accuracy | GHIDRA |
| | | ⑬ Enlarge boundaries of data regions | Xref coverage | GHIDRA, ANGR |
| | | ⑭ Address tables have minimal size of 2 | Xref accuracy | GHIDRA |
| | | ⑮ Exclude pointers that may overlap with a string | Xref accuracy | McSEMA, GHIDRA |
| | | ⑯ While scanning data regions, use step-length based on type inference | Xref accuracy | ANGR |
| Function Entry | MAIN Function | ⑰ Identify main based on arguments to __libc_start_main | Func coverage | ANGR, BAP |
| | | ⑱ Identify main using patterns in _start/__common_main seh | Func coverage | DYNINST, RADARE2 |
| | | ⑲ Identify function entries based on symbols | Func coverage | DYNINST, GHIDRA, ANGR, BAP, RADARE2 |
| | | ⑳ Identify function entries based on exception information | Func coverage | GHIDRA |
| | | ㉑ Identify function entries based on targets of direct calls | Func coverage | DYNINST, GHIDRA, ANGR, BAP, RADARE2 |
| | | ㉒ Identify function entries by resolving indirect calls | Func coverage | GHIDRA, ANGR |
| | | ㉓ Identify function entries based on prologues/decision-tree | Func coverage | DYNINST, GHIDRA, ANGR, BAP, RADARE2 |
| | | ㉔ Consider begins of code discovered by linear scan as function entries | Func coverage | ANGR |
| CFG | Indirect Jump | ㉕ Use VSA to resolve jump table targets | CFG accuracy | DYNINST, GHIDRA, ANGR |
| | | ㉖ Follow patterns to determine jump tables | CFG accuracy | DYNINST, GHIDRA, RADARE2 |
| | | ㉗ Discard jump tables with index bound larger than a threshold | CFG accuracy | GHIDRA, ANGR, RADARE2 |
| | | ㉘ Restrict the depth of slice for VSA | Efficiency | DYNINST, ANGR |
| | Tail Call | ㉙ Consider a jump to the start of another function as a tail call | CFG coverage | GHIDRA, ANGR |
| | | ㉚ Determine tail call based on distance between the jump and its target | CFG accuracy | DYNINST, ANGR |
| | | ㉛ A tail call and its target cross multiple functions | CFG accuracy | GHIDRA |
| | | ㉜ Tail calls cannot be conditional jumps | CFG accuracy | GHIDRA, ANGR |
| | | ㉝ A tail call tears down its stack | CFG accuracy | GHIDRA, ANGR |
| | | ㉞ A tail call does not jump to the middle of a function | CFG accuracy | ANGR |
| | | ㉟ Target of a tail call cannot be target of any conditional jumps | CFG accuracy | ANGR |
| | Non-returning Function | ㊱ Identify system calls or library functions that are known non-returning | CFG accuracy | DYNINST, GHIDRA, ANGR, BAP, RADARE2 |
| | | ㊲ Identify functions with no ret and no tail calls that return | CFG accuracy | DYNINST, ANGR, RADARE2 |
| | | ㊳ Identify functions that always call non-returning functions | CFG accuracy | BAP |
| | | ㊴ Detect non-returning functions based on fall-through after the call-sites | CFG accuracy | GHIDRA |
When encountering direct control transfers, the tools expand the disassembly to the targets. However, to handle indirect control transfers, different tools adopt different approaches. We will cover the details in § III-D. Another challenging part of control flow is to determine non-returning functions. Related details are also discussed in § III-D.

As indirect control flows are (formally and practically) undecidable, recursive descent often leaves behind code gaps. Our evaluation in § IV-B1 shows that recursive disassembly alone can miss 49.35% of the code on average. As such, existing tools incorporate heuristics to enlarge code coverage, which inevitably jeopardizes correctness guarantees. The most common heuristic, used by ANGR, DYINST, RADARE2, BAP, and GHIDRA, searches for function entry points in the code gaps based on common function prologues / epilogues or pre-trained decision-tree models [9] (c). On finding a function entry, the tools will consider it as a new starting point for recursive disassembly. According to our evaluation (§ IV-B3), function-entry matching on average identifies 17.36% of all the functions, leading to a 31.55% increase of code coverage.

Beyond function matching, existing tools also use heuristics that are more aggressive. ANGR performs linear sweep on the code gaps and recursive disassembly on legitimate instructions (c). In case of errors, ANGR skips the current basic block and moves to the next bytes. This linear scan increases the code coverage of ANGR by around 8.20%. However, it will misidentify data as code (e.g., Listing 2 in Appendix G). GHIDRA includes targets of xrefs in recursive descent (b). This strategy discovers 4.33% more code coverage. However, the xrefs are mostly collected with heuristics, which can lead to errors like Listing 3 in Appendix G.

### B. Algorithms and Heuristics in Symbolization

Symbolization identifies numerical values in the binary that are actually references to code or data objects. Tools generally follow the workflow in Fig. 2.

**Constant Operand and Data Unit Extraction** [ANGR, GHIDRA, UROBOROS, McSEMA]: These tools start by identifying numerical values that are potential pointers. They search through all instructions to identify constant operands and scan the non-code regions to find data units (d). As identification of constant operands is trivial, we omit the details and focus on data units. In general, a data unit is composed of consecutive n-bytes at an aligned address. However, different tools have varying choices of n, alignment, and non-code regions:

- All tools assume that a data unit’s size is the same as the machine size, i.e., 4 bytes in 32-bit and 8 bytes in 64-bit binaries (g). This assumption, however, is not always true. Listing 4 in Appendix G shows a jump table with 4-byte entries from a 64-bit binary. In such cases, the assumption about a data unit’s size can mislead symbolization in tools that do not otherwise handle jump tables (e.g., UROBOROS).
Fig. 2: A general workflow of symbolization.

- **UROBOROS** and **MCSEMA** use machine-size alignment (I). **GHIDRA** assumes a 4-byte alignment unless a data unit is the target of another xref. In the latter case, **GHIDRA** has no alignment requirement (I). **ANGR** enforces no alignment requirement (I) due to observations of unaligned pointers [101]. Our evaluation shows that the choice of alignment is a coverage-accuracy trade-off: around 600 pointers are saved at non-aligned addresses while the non-alignment assumption leads to nearly 60% of **ANGR**’s false positives.

- Besides data segments, **GHIDRA** and **ANGR** also search for data units from non-disassembled code regions.

**Data Unit Type Inference** [**ANGR**, **GHIDRA**]: In operand extraction, **ANGR** and **GHIDRA** infer the types of data units when possible. **ANGR** identifies memory loads from data units. If the loaded values flow to floating-point instructions, **ANGR** marks the data units as floating points. This inference is in general reliable as it follows data flow. **GHIDRA** uses a more aggressive strategy: given a constant operand “pointing” to a data unit, **GHIDRA** considers the data unit to be the start of a string if it is followed by a sequence of ASCII/Unicode bytes and a null-byte. Otherwise, **GHIDRA** deems the data unit to be a pointer if the value inside meets the following conditions: (I) the value is at least 4096; (2) the value is an address of an instruction or an address in a non-code region; (3) if the value is an address of an instruction in a known function, the instruction must be the function entry (I); (4) if the value is a data address, the address cannot overlap another typed data unit. **GHIDRA**’s type inference has no correctness assurance.

**Code-to-Code and Code-to-Data Xrefs** [**ANGR**, **GHIDRA**, **UROBOROS**, **MCSEMA**]: For each constant operand, **ANGR**, **UROBOROS**, and **MCSEMA** seek to symbolize it as a code pointer, checking whether the operand refers to a legitimate instruction. Beyond, **GHIDRA** has two extra rules: (I) the operand cannot be a value in {{0-4095}, 0xffffffff, 0x0f0000, 0x0fffffff00, 0x0fffffff0000, 0x0fffffff000000}; (2) the instruction being referred to must be a function entry (if the function was known) (I). We measured heuristic (I) with 3,788 binaries. We discover thousands of pointers to the middle of functions (e.g., pointers for try-catch in exception handling), showing that heuristic (I) is unsound.

For a constant operand that cannot be a code pointer, the tools attempt to symbolize it as a data pointer, checking if the operand refers to a legitimate data location. In the checking process, **ANGR** enlarges the boundaries of a data region by 1,024 bytes and **GHIDRA** adopts a similar idea (I) because many pointers are dereferenced with an offset. This strategy indeed benefits coverage (e.g., Listing 6 in Appendix G). It, however, also introduces errors like Listing 5 in Appendix G. Recall that **GHIDRA** leverages xrefs to aid recursive descent (§ III-A). When a constant operand is symbolized as a xref to a non-disassembled code region, **GHIDRA** recursively disassembles that region. If **GHIDRA** runs into errors like bad opcodes and invalid control transfers, it rolls back the disassembly.

**Address Table** [**ANGR**, **GHIDRA**, **UROBOROS**, **MCSEMA**]: Beyond constant operands, these tools also symbolize the non-code regions by locating address tables: a group of consecutive data units that are pointers. In general, determining address tables depends on the choice of table size and the rules to classify a data unit as a pointer. **GHIDRA** considers 2 as the minimal size of an address table (I) and the others consider 1. While the choice of **GHIDRA** helps more accurately identify grouped pointers like function tables, it misses many individual pointers, leading to false negatives. With regard to determining pointers, all tools follow the approaches as we previously discussed. After the initial generation of address tables, **ANGR**, **MCSEMA** and **GHIDRA** also apply refinements:

- **ANGR** excludes table entries that are floating points (I).
- **MCSEMA** excludes table entries that may overlap with a string (I). When a piece of data can be both a pointer and a string, **MCSEMA** prefers string. As we will discuss below, **ANGR** uses an opposite strategy.
- **GHIDRA** excludes table entries that point to the middle of recovered functions. **GHIDRA** also excludes table entries that overlap with strings or cut into other pointers. Finally, **GHIDRA** splits an address table when adjacent entries have a distance larger than 0xffffffff.
- Given an entry to non-disassembled code, **GHIDRA** expands the recursive descent using the aforementioned approach. **ANGR** uses a special strategy when brute-force searching data regions. Given a location, **ANGR** in turn checks whether the data inside is a pointer, a ASCII/Unicode string, or an arithmetic sequence. If any type matches, **ANGR** jumps over the typed bytes and then resumes the search (I). This strategy incurs many false negatives like Listing 7 in Appendix G.

Overall, there is a lack of algorithmic solutions to symbolization. Today’s tools incorporate a multitude of heuristics, striving for a coverage-correctness balance.

### C. Algorithms and Heuristics in Function Entry Identification

![Figure 2: A general workflow of symbolization.](image)

Most tools use separate strategies to identify the entry of **main** versus the entries of other functions. As such, we first discuss **main**, followed by the other types of functions.

**Main Function** [**DYNINST**, **ANGR**, **BAP**, **RADARE2**]: To locate it, **ANGR** and **BAP** analyze the _start_ function and,

```
1 48 c7 c7 e2 e0 40 00 mov $0x40e0e2, %rdi ; main
2 ff 15 ce 48 05 00 48 callq __libc_start_main
```

**Listing 1: Call to __libc_start_main in _start.** following calling conventions, infer the first argument passed by _start to __libc_start_main (I). Take Listing 1
as an example. ANGR and BAP locate the instruction at line 1 and deem the immediate operand (0x40e0e2) to be the address of main. As __libc_start_main is a standard
interface, ANGR and BAP ensure correctness. RADARE2 and DYINST search architecture-specific patterns near the call to __libc_start_main to get the address of main (\textcopyright). DYINST finds the instruction right before the call and extracts the immediate operand; RADARE2 searches the address after a fixed sequence of raw bytes (e.g., 48 c7 c7 in Fig. 1). For Windows binaries, RADARE2 seeks a pattern in __scrt_common_main_seh as it does for Linux binaries to locate main. Other tools do not particularly find main.

General Functions \[DYNINST, GHIDRA, ANGR, BAP, RADARE2\]: To identify the entries of non-main functions, these tools adopt a hybrid approach that consists of three parts: (1) The tools seek symbols remaining in the \_symtab and \_dynsym sections to determine known-to-be-good functions (\textcopyright). (Only) GHIDRA considers the \_eh_frame section to identify functions that have unwinding information (\textcopyright). As mandated by X86-64 ABI, modern compilers (e.g., LLVM and GCC) keep unwinding information for every function. As we illustrate in \S IV-B3, this way GHIDRA can identify nearly all function entries, but (surprisingly) it does not use exception information when handling Windows binaries.

(2) All tools consider targets of direct calls to be function entries (\textcopyright), while ANGR and GHIDRA additionally resolve certain indirect calls to determine more function entries (\textcopyright). Finally, DYNINST, GHIDRA, ANGR and RADARE2 include targets of tail calls as function entries (see \S III-D).

(3) All tools use pattern-based approaches to further recover functions (\textcopyright). GHIDRA, ANGR and RADARE2 find function entries based on common prologues (or epilogues); DYNINST (by default) and BAP find function entries with pre-trained decision-tree models [9]. As we will present in \S IV-B3, this pattern-based approach is heavily used and indeed identifies many functions. However, it is very sensitive to architectures, optimizations, and compiler specifics.

ANGR adopts an extra aggressive approach: during linear scan over code gaps left by recursive descent, it treats the begin of each identified code piece as a new entry (\textcopyright). This method improves the coverage but incurs many errors (\S IV-B3).

To sum up, the identification of function entry mostly uses hybrid approaches, mixing algorithms and heuristics.

D. Algorithms and Heuristics in CFG Reconstruction

CFG reconstruction consists of many tasks. We focus on the challenging ones: resolving indirect jumps/calls, detecting tail calls, and finding non-returning functions. Direct jumps/calls are not discussed as they are easily derivable after disassembly.

Indirect Jumps \[DYNINST, GHIDRA, ANGR, RADARE2\]: From our benchmark binaries (Table IV), we observed three types of indirect jumps: (1) jump tables [23] (compiled from switch-case and if-else statements); (2) indirect tail calls (indirect calls optimized as tail calls); and (3) handwritten ones (e.g., longjmp and other cases in Glibc [48]).

![Fig. 3: Handling of jump tables by RADARE2.](image)

![Fig. 4: Jump table resolution in DYNINST and GHIDRA. The upper half of Sub-Fig. 4a shows a jump table from Gold Linker. Lower half of Sub-Fig. 4a and Sub-Fig. 4b respectively illustrate how DYNINST and GHIDRA resolve the jump table.](image)
and each can lead to both false positives and false negatives.

ANGR, given an indirect jump, considers the operand as a source and runs backwards slicing. In the sliced area, ANGR uses full-scale VSA to identify possible targets. However, the public version of ANGR restricts the slicing to at most three levels of basic blocks, trading utility for efficiency. ANGR also adopts heuristic 4 with a very large threshold: 100,000.

In summary, tools employ various heuristics to resolve indirect jumps. These heuristics, mostly derived for accuracy, introduce fewer errors but have limited coverage (§ IV-B4).

**Indirect Calls** [GHIDRA, ANGR]: GHIDRA finds targets of indirect calls based on constant propagation. It tracks the intra-procedure propagation of constants from immediate operands, the LEA instructions, and global memory. Once a constant flows to an indirect call, GHIDRA takes the constant as a target. ANGR also uses constant propagation to handle indirect calls but only considers the current basic block.

**Tail Calls** [RADARE2, ANGR, DYINST, GHIDRA]: For efficiency, function calls at the end of procedures are often optimized as jumps (i.e., tail calls). Tools adopt different strategies to detect tail calls.

RADARE2 uses a simple heuristic to determine tail calls: the distance between a jump and its target exceeding a certain threshold. This heuristic exploits the insight that different functions are usually apart. However, it is hard to pick a threshold that is both effective and accurate.

GHIDRA determines a jump as a tail call if the code between the jump and its target spans multiple functions. The heuristic can lead to both false positives due to discontinuous functions and false negatives due to unrecognized functions. GHIDRA further excludes conditional jumps from consideration, preventing the detection of 21.6% of the tail calls.

DYINST takes a sophisticated strategy. It considers a jump as a tail call if the target is the start of a known function. Otherwise, DYINST checks two rules to determine a tail call: (1) the jump’s target cannot be reached by only following false branches; (2) right before the jump, the stack is torn down by [leave; pop $reg] or [add $rsp $const]. While the first rule is hard to reason, the second rule exploits an intrinsic property of tail calls: the current function recycles the stack such that the child re-uses its return address. The above pattern-based approach is less accurate, leading to 97% of DYINST’s false positives and most of its false negatives.

ANGR adopts similar strategies as DYINST. It first identifies jumps whose targets are starts of known functions. Beyond that, ANGR requires four conditions to detect a tail call: (1) the jump is unconditional; (2) the stack at the jump is torn down based on stack height analysis; (3) the target does not belong to any function or it belongs to the current function; (4) all incoming edges of the target are unconditional jumps or direct calls. As we will show in § IV-B4, these heuristics do not provide correctness guarantees and each can lead to both false positives and false negatives.

Tools take different strategies to detect tail calls. These strategies depend critically on function entry detection, inheriting inaccuracies from function identification.

**Non-returning Functions** [ANGR, BAP, RADARE2, DYINST, GHIDRA]: Tools use a similar workflow for detecting non-returning functions. First, they gather the group of library functions or system calls that are known to be non-returning. Second, from this initial group of functions, tools further find other ones.

ANGR, RADARE2, and DYINST use the same idea: they scan each function and, if no ret instruction is found, they consider the function to be non-returning. However, they adopt different solutions when encountering a call to a child function that has unknown returning status: (1) ANGR simply assumes a fall-through after the call; (2) RADARE2 takes a similar strategy but when the child status is later updated, RADARE2 recursively propagates the update to its predecessor functions; (3) DYINST takes a depth-first approach to first handle the successor functions. Only until the status of the child function is determined, DYINST will continue handling the original call. To our understanding, the strategies in RADARE2 and DYINST are equivalent. Both are principled and produce nearly perfect precision.

GHIDRA also follows the idea of DYINST. To handle a call with unknown returning status, GHIDRA checks the fall-through code after the call. If the code runs into abnormal regions (i.e., data, unrecognizable instructions, or another function) or the code is referenced by xrefs, GHIDRA marks the fall-through unsafe. If the child function of that call occurs at least three unsafe fall-throughs, GHIDRA considers the child to be non-returning. Further, instead of doing recursive updates, GHIDRA simply runs the detection process twice.

BAP considers a function to be non-returning if all the paths in the function end at a call to a non-returning function. To handle calls to child functions with unknown status, it takes a similar recursive-updating strategy as RADARE2.

Tools use many principled strategies to detect non-returning functions, ensuring higher correctness.

**IV. Large-scale Evaluation**

**A. Evaluation Setup**

**Benchmarks**: We used the software listed in Table IV to experimentally evaluate the tools we studied. The software includes programs and libraries of diverse functionality and complexity, written in C / C++, and containing hand-written assembly and hard-coded binary code. It also carries a significant amount of complex constructs as listed in Table XVII, fitting the use as a benchmark. To test the effect of different compilers and options, we built each software package for two operating systems (Linux and Windows), using three compilers (GCC-8.1.0 and LLVM-6.0.0 on Linux, and Visual Studio 2015 on Windows), various optimization levels (O0, O2, O3, Os, Ofast on Linux, Od, O1, O2, Ox on Windows), and two architectures (X86 and X64). This resulted in 3,788 binaries.
which are less than the theoretically possible 4484 (177×20 + 59×16), as some cannot be built for certain configurations. Ground Truth: We intercept the compiling and linking process to automatically obtain ground truth about the produced binaries, which includes instructions, functions, control transfers, jump tables, xrefs, and remaining complex constructs.

On Linux, we replicate the approach in CCR [60]. CCR extends the LLVM Machine Code layer to record the needed information in each compiled bitcode/assembly file. It also instruments the GNU Gold Linker to merge the information during linking. Throughout experimentation, we identified several limitation of the CCR linker, which we addressed by extending it to: (1) collect information from previously ignored sections (.text.unlikely, .text.exit, .text.startup, .text.hot); (2) support relocatable objects (compiled with -r); (3) record both the size and the offset of basic blocks; (4) handle linker-inserted functions (e.g. _start) and statically-linked Glbce functions; (5) support X86 targets. We also extend the CCR approach to GCC by instrumenting its RTL pass to label all related constructs when producing assembly code. We also customized the GNU Assembler to record the locations of the constructs and xrefs in the emitted object files. Finally, we re-use the CCR linker to merge object files.

On Windows, we combine compiler options, symbol/debug information, and lightweight manual analysis to build ground truth. Details are covered in Appendix B.

Tools and Configurations: Besides open-source tools, we also test two commercial tools, IDA PRO-7.4 and BINARY NINJA-1.2. We detail the configuration of all tools in Appendix C. It should be noted that we run two versions of GHIDRA and ANGR, including a version of GHIDRA not using exception information, namely GHIDRA-NE, and a version of ANGR without linear scan, namely ANGR-NS.

B. Evaluation Results & Analysis

1) Disassembly: This evaluation measures the recovery of legitimate instructions. We excluded all the padding bytes and linker-inserted functions (e.g., _start). We also inserted a symbol of main so that all recursive tools can find it.
of PSI and UROBROS as they are close to OBJDUMP.

In general, the results of disassembly vary across tool categories. Linear tools and linear-sweep aided tools, like OBJDUMP and ANGR, have very high coverage (99.95%+ recall). Recursive tools have lower recovery rates and some can only recover less than 80% of the instructions (BAP and RADARE2). We also notice that the performance of recursive tools changes across optimization levels and architectures. In particular, nearly all recursive tools (ANGR-NS, GHIDRA-NE, DYNINST, BAP, RADARE2) have reduced recovery as the optimization increases and when analyzing X64 targets. This is because that optimization levels and architectures affect the function matching in recursive tools, further leading to missing of instructions. Such results well comply with previous observations [5].

In the aspect of precision, we have an opposite observation. Recursive tools have high precision (over 99%), regardless of the compiler, architecture, and optimization level. Linear tools are less precise. The precision of OBJDUMP, in the worst case, drops to around 85%. This difference is mainly because recursive tools mostly follow the control flow, ensuring correctness. However, linear tools scan every byte and often run into errors when data appears in code. For instance, OBJDUMP produces the worst result in analyzing Openssl (precision: 85.35%) because Openssl has a lot of data in assembly files and OBJDUMP wrongly identifies the data as code.

Use of Heuristics: To augment the correctness of linear sweep, PSI introduces error detection and handling. In our evaluation, PSI can analyze 971 of the X86 binaries on Linux systems. On the binaries, the linear sweep produces over 10K errors. PSI captures 26% of the errors leading to bad opcode and other 6% resulting in invalid control transfers. However, the public version of PSI has strict requirement of padding patterns, preventing it from fixing the errors. In summary, PSI’s heuristic can capture 32% of the errors in linear sweep. However, we are unable to report the detectable errors due to PSI’s implementation restrictions.

In contrast, heuristics in recursive tools are mostly for coverage enhancement. We measure the effectiveness of each heuristic in turn. We start with pure recursive descent by disabling function matching, linear scan in ANGR, xref aided disassembly in GHIDRA and RADARE2. The results are shown in Table XVIII in Appendix D. Unsurprisingly, all tools have nearly perfect precision. However, without heuristics, the tools have very low recall. ANGR, GHIDRA, DYNINST all have a recall around 51% and RADARE2 recovers no more than 10% of the code. We noted that GHIDRA still produces high recall with Linux binaries. This is because GHIDRA uses exception information to highly accurately identify functions (§ III-C).

On top of pure recursive descent, we in turn enable function matching, linear scan, and the use of xrefs. With function matching, recursive tools recover significantly more functions and code (see Table XIX in Appendix D). In particular, ANGR-NS and DYNINST respectively identify 21.30% and 18.24% more functions, leading to recovery of 36.44% and 26.65% more code (comparing Table V and XVIII). Second, according to the results of ANGR and ANGR-NS in Table V, linear scan aids ANGR to recover 8.20% more instructions. Third, the use of xrefs in GHIDRA leads to a 4.33% increase in code coverage, as shown by the result of GHIDRA on Linux binaries in Table V and XVIII.

Understanding of Errors: In Table VI, we summarize the statistic of false positives. For linear tools (e.g., OBJDUMP), all the false positives are caused by misidentifying padding bytes or data-in-code as code. For recursive tools, the most common reasons of errors include (1) considering illegal locations as function entries; (2) missing non-returning functions and assuming the calls to them fall through; (3) incorrect resolution of jump tables. Beyond that, ANGR’s linear sweep incurs 21% of the errors due to data in code; BAP and GHIDRA have a few implementation flaws, also leading to a group of errors.

The reasons of false negatives are consistent. All the false negatives by linear tools are side-effects of false positives. For recursive tools, most false negatives are caused by undetected function: as shown in Table X, recursive tools averagely miss 25.0% of the functions. The remaining instructions are missed mainly because certain jump tables are not resolved and false positives over-run the legitimate instructions.

2) Symbolization: In this evaluation, we measure the recovery of xrefs. Xrefs in direct jumps/calls are trivial to identify so we excluded them. We also excluded xrefs in wrong instructions since such errors are rooted from incorrect disassembly. Finally, we did not consider jump tables as they are separately measured in § IV-B4.

Overall Performance: We summarize the overall performance of symbolization in Table VII. In general, open-source tools have much higher recovery rates (98.35% on average) than commercial tools (88.63% on average). The difference is even bigger with the worst case. This is because open-source tools brute force all constant operands and data units while the commercial tools adopt more conservative strategies. Our hypothesis can be verified by comparing McSEMA and IDA PRO, since McSEMA just adds a round of brute force on top of IDA PRO.

Somewhat surprisingly, open-source tools also have high precision (99.92% on average). Based on our observation, the high precision is because (1) the heuristic-based checks are generally restrictive and (2) most benchmark programs have less data. On programs that have more data, the tools are more inclined to make mistakes (e.g., Mysql, having plenty of
TABLE VII: Evaluation results of symbolization. UROBOROS cannot run Linux-Of binaries and all Window binaries. We also omitted ANGR’s symbolization on Windows binaries as ANGR’s Reassembler component cannot run them.

|       | Symbolizations | | Symbolizations | |
|-------|----------------|---|----------------|---------|
|       | L              | Avg | Min | W              |         |
|       |                | Pre Rec | Pre Rec |                  | Pre Rec | Pre Rec |
| Urobo*| O0             | 99.99 | 100  | 99.89          | 100     |
|       | O2             | 100  | 100  | 100            | 100     |
|       | O3             | 99.99 | 100  | 99.91          | 100     |
|       | Os             | 99.99 | 100  | 99.88          | 100     |
|       | Ot             | -    | -    | -              | -       |
| Mcsema| O0             | 99.99 | 99.77 | 99.04          | 94.05   |
|       |                | 99.99 | 99.96 | 98.78          | 97.87   |
|       | O3             | 99.99 | 99.86 | 98.88          | 98.79   |
|       | Os             | 99.98 | 99.75 | 98.68          | 90.91   |
|       | Ot             | -    | -    | -              | -       |
| Ghida | O0             | 99.90 | 95.70 | 61.99          | 45.52   |
|       |                | 99.91 | 95.53 | 87.35          | 37.71   |
|       | O3             | 99.92 | 95.99 | 88.36          | 41.60   |
|       | Os             | 99.89 | 95.37 | 61.98          | 41.72   |
|       | Ot             | -    | -    | -              | -       |
| Angr  | O0             | 99.86 | 95.22 | 81.74          | 95.38   |
|       |                | 99.88 | 95.48 | 87.28          | 95.35   |
|       | O3             | 99.90 | 95.59 | 88.29          | 96.21   |
|       | Os             | 99.81 | 99.99 | 86.57          | 98.96   |
|       | Ot             | -    | -    | -              | -       |
| Ida   | O0             | 99.98 | 95.86 | 97.17          | 36.96   |
|       |                | 99.93 | 94.32 | 86.77          | 31.96   |
|       | O3             | 99.94 | 95.06 | 88.38          | 31.96   |
|       | Os             | 99.93 | 93.81 | 85.59          | 25.26   |
|       | Ot             | -    | -    | -              | -       |
| Ninja | O0             | 99.99 | 78.89 | 91.17          | 17.29   |
|       |                | 99.98 | 81.08 | 97.59          | 19.01   |
|       | O3             | 99.91 | 81.38 | 99.40          | 21.15   |
|       | Os             | 99.99 | 77.71 | 98.67          | 17.45   |
|       | Ot             | -    | -    | -              | -       |

TABLE VIII: Statistics of false positives in symbolization. Align, Type, Type-based Sliding, and Extended-data respectively mean no alignment (heuristic 11), ANGR’s moving scheme (heuristic 16), enlarging data regions (heuristic 13).

| Tools  | Percentage of False Positives (%) |
|--------|-----------------------------------|
|        | Align | Type-based Sliding | Extended-data | Collision |
| Urobo* | 0.00  | 0.00               | 0.00          | 0.00       |
| Mcsema | 0.00  | 0.00               | 0.00          | 0.00       |
| Ghidra | 0.29  | 0.00               | 3.91          | 95.80      |
| Angr   | 58.58 | 4.47               | 0.01          | 36.94      |

TABLE IX: Statistics of false negatives in symbolization. Align, Type, Extended-data, Function, Address-table respectively mean assumption of alignment (heuristic 10), preferring strings over pointers (heuristic 15), enlarging data regions (heuristic 13), and size of address table (heuristic 14).

| Tools  | Percentage of False Negative (%) |
|--------|----------------------------------|
|        | Align | Type | Extended-data | Address Table | Func |
| Urobo* | 0.00  | 0.00 | 0.00          | 0.00          | 0.00 |
| Mcsema | 1.14  | 75.22| 23.64         | 0.00          | 0.00 |
| Ghidra | 0.01  | 3.13 | 96.53         | 0.33          |      |
| Angr   | 0.00  | 100 | 0.00          | 0.00          | 0.00 |

data, leads to the lowest precision in many tools.

Use of Heuristics: As shown in Table III, symbolization involves many heuristics, competing for a coverage-correctness trade-off. Our evaluation shows that heuristic 1 (the brute-force based approach), 2 (the no-alignment assumption about pointers), and 3 (the enlargement of data regions) bring full coverage of xrefs in our benchmarks. The other heuristics are instead striving for correctness. In the following, we discuss them in turn.

Heuristic 1 (the brute-force based approach) is necessary for symbolization. This can be verified by comparing IDA PRO and MCSERA. MCSERA adds a round of brute force to IDA PRO, increasing the coverage from 95% to 98%. Heuristic 2 (pointers in data have machine size) is principled if jump tables are not considered. From over 6 million xrefs, we observe no violations. Heuristic 3 (pointers in data are aligned) and 4 (pointers in data may not be aligned) conflict with each other, and neither of them is perfect. Heuristic 5 (pointers in data are aligned) and 6 (pointers in data may not be aligned) conflicts with each other, and neither of them is perfect. Heuristic 6 misses around 600 xrefs while heuristic 7 introduces most false positives in ANGR (over 50K). Heuristic 8 (references to code point to function entries) can reduce false positives but it misses thousands of xrefs to the middle of functions (e.g., pointers for try-catch in exception handling). Heuristic 9 (enlarging boundaries of data regions) helps recover 12K xrefs in data, but leads to 6 and 2K+ false positives in ANGR and GHIDRA. Heuristic 10 introduces most false negatives in GHIDRA, although removing a small set of false positives. Heuristic 11 produces over 3K false negatives in MCSERA because of the inference of strings is not accurate.

Understanding of Errors: Table VIII shows the statistics of false positives. Using the assumption that pointers in data can be non-aligned, ANGR and GHIDRA trigger 59% and 0.3% of their false positives. They also produce false positives because they enlarge the boundaries of data regions when checking the legitimacy of xrefs’ targets. All the other false positives are due to collisions between numeric values and pointers.

As shown in Table IX, most false negatives by ANGR and MCSERA are because they exclude pointers that overlap with the inferred strings. MCSERA also misses 23.64% of the xrefs that point to locations outside of the data regions. GHIDRA, because of its assumption about the address table’s minimal size and that code pointers always point to function entries, respectively produces 96.53% and 0.33% of its false negatives.

3) Function Entry Identification: This evaluation measures the identification of function entries. In this test, we further considered NUCLEUS [6] and BYTEWEIGHT [9]. We retrained BYTEWEIGHT with our benchmarks binaries.

Overall Performance: Table X presents the overall results. The key observation is that function entry identification remains a challenge. 4 of the open source tools can only identify less than 80% of the functions. In particular, RADARE2 has a recall lower than 66%. Such results indicate, even with heuristics, we have yet to develop better function identification. We also observe that the results of function identification varies across optimization levels and architectures. This is because the tools widely use signature-based function matching, which are specific to optimizations and architectures. Besides limited coverage, existing tools also have lower precision in function
commercial tools do better than open source ones. In particular, BINARY NINJA can identify over 97% of the functions with a precision over 95%. Second, NUCLEUS achieves comparable coverage and precision to BINARY NINJA, showing a high promise of its CFG-connectivity based solution. Based on an official blog [84], BINARY NINJA incorporates NUCLEUS for CFG and function detection. Third, BYTEWEIGHT outperforms BAP despite BAP internally runs BYTEWEIGHT. This is because BAP uses pre-trained signatures but BYTEWEIGHT uses signatures trained with our benchmark binaries. Use of Heuristics: In function entry identification, two major heuristics are used. The first heuristic searches function entries using common prologues/data-mining models. We summarize the contribution and accuracy of the heuristic in Table XIX in Appendix D. Without counting functions recursively reachable from the matched ones, this heuristic recovers 17.36% more functions with an average precision of 77.53%. Also observable is that utility of this heuristic varies across optimization levels and architectures. Moreover, existing tools use different patterns or data-mining models, competing for a coverage-accuracy trade-off. Comparing to GHIDRA-NE and RADARE2, ANGR-NS and DYNINST use more aggressive patterns/models, producing higher coverage (21.3%±24.02 vs. 18.24%±7.93%) but lower precision (56.61%±85.37% vs. 98.42%±87.29%).

The other heuristic, used by ANGR, takes the begin of each code region detected by linear scan as a function entry. The heuristic recovers 23% more functions but reduces the precision by 26.96% because it often considers the begin of padding or data-in-code as a function entry. Understanding of Errors: Table XI and XII present the
analysis of false positives and false negative (the analysis of GHIDRA is only done on binaries with exception causes).

For false positives, there are three common causes. First, the signature-based detection wrongly matches function entries (FP ratio - DYNINST: 80.80%, GHIDRA-NE: 28.38%, ANGR-NS: 92.98%, BAP: 99.99%). Second, inaccurate tail call detection takes target of a regular jump as a function entry (FP ratio - DYNINST: 19.20%, GHIDRA-NE: 71.61, ANGR: 11.70%). Third, incorrect disassembly results in erroneous call instructions to incorrect target functions (FP ratio - GHIDRA-NE: 0.01%, ANGR-NS: 0.01%, RADARE2: 0.01%). Beyond the three causes, ANGR produces 78.41% of its false positives because it considers code discovered by its linear scan as a function entry; GHIDRA generates 99.94% of its false positives because exception information also carries pointers to middle of functions; RADARE2, aggressively inferring code pointers based on xrefs, brings 93.17% of its false positives.

False negatives also have a group of similar causes. First, tools can miss targets of jump tables and fail to identify functions called by the target code or their successors (FN ratio - ANGR-NS: 1.18%, BAP: 1.32%, RADARE2: 1.12%). Jump tables have smaller impacts to ANGR and DYNINST because ANGR uses linear scan to compensate jump tables and DYNINST has high coverage of jump tables. Second, tools cannot recognize many tail calls and hence, miss the functions indicated by their targets (FN ratio - ANGR: 13.05%, BAP: 2.42%, RADARE2: 2.78%). Third, missed non-returning functions prevent the detection of many function entries in cases like Listing 8 in Appendix G (FN ratio - GHIDRA-NE: 14.16%, ANGR: 8.25%, ANGR-NS: 4.01%, BAP: 7.99%). Fourth, wrongly identified functions can overlap with true functions, making the true ones un-recognizable (FN ratio - DYNINST: 10.16%, GHIDRA: 12.42%, ANGR: 73.61%, ANGR-NS: 32.78%). All the other functions are missed because they are neither reached by recursive descent nor matched by patterns (FN ratio - ANGR-NS: 60.97%, GHIDRA: 86.03%, RADARE2: 86.94%).

4) CFG Reconstruction: In this part, we measure 5 targets: (1) intra-procedural edges between basic blocks; (2) call graphs for direct calls; (3) indirect jumps and indirect calls; (4) tails calls; (5) non-returning functions. For task (1), we exclude the edge between a call and the fall through code. For jump tables in task (3), we consider a case with no targets resolved as a false negative. All other cases are counted as false positives. For task (4), we exclude recursive calls and we count unique target functions instead of jumps.

Overall Performance: Table XIII and XIV show the results of CFG reconstruction on Linux binaries and Windows binaries.

First, the tools can recover most of the edges with high accuracy. DYNINST, GHIDRA, and ANGR find over 90% of the edges with an accuracy higher than 95%. Moreover, the recovery of edges highly correlates to the recovery of instructions: precision and recall in the two tasks are consistent. The results of call graphs are similar to, so we omit the details.

Second, tools have different capabilities of handling jump tables. On average, GHIDRA and DYNINST can resolve over 93% of the jump tables with an accuracy of around 90%. RADARE2 and ANGR have a similar coverage rate (around 75%) while ANGR has much higher accuracy (96.27% v.s. 90%). In comparison to open-source tools, commercial tools have both higher coverage (96.5% v.s. 84.8%) and accuracy (99% v.s. 92.9%).

Besides jump tables, there are two more types of indirect jumps: handle-written assembly code (336 cases like [48]) and indirect tail calls (35,087 cases). For the first type, GHIDRA, by analysing constant propagation, resolves 96 cases but only report one target in each case. BINARY NINJA generates results for 120 cases, however, with incorrect targets.

Third, ANGR, GHIDRA, IDA PRO, and BINARY NINJA have (limited) supports of indirect calls. ANGR resolves 43 cases but only reports one target in each case. Our manual analysis verified the targets are correct, all following the pattern of `[mov/lea CONST, reg; ...; call reg]`. GHIDRA finds an incomplete set of targets for 88,078 indirect calls (only one target for 87,947 cases). IDA PRO reports results for 15,325 indirect calls (only one target for 15,267 cases). The other 58 cases follow the format of Listing 9 in Appendix G, which are fully solved by IDA PRO. BINARY NINJA reports targets for 3,410 indirect calls (1 target for 92 cases; 2 targets for 1,865 cases; 3 targets for 469 cases; 4 targets for 889 targets; 4+ targets for 95 cases).

Fourth, existing tools are not perfect with detecting tail
calls. The recovery rate ranges from 71.7% (DYNNIST) to 91.28% (BINARY NJINA); the precision varies from 67.39% (DYNNIST) to 90.21% (GHIDRA). We observe that many tools (ANGR, GHIDRA, BINARY NJINA) have the lowest precision with optimization disabled. This is because less tail calls are emitted at lower optimization levels and a few false positives can result in a low precision.

Finally, tools can detect non-returning functions with very high precision (nearly 100% for many tools). However, they have limited coverage, especially on Windows binaries. Based on our analysis, the root cause of lower coverage is incomplete collection of non-returning library functions.

**Use of Heuristics:** The recovery of edges and call graphs share heuristics in disassembly so we omit the details.

The resolving of jump tables uses three heuristics. First, RADARE2, GHIDRA, and DYNNIST use patterns to detect jump tables, identifying 85.53%, 98.01%, and 99.56% of all jump tables. Second, DYNNIST and ANGR restrict the scope of slicing in VSA, missing the index bound in many jump tables. By enlarging the scope of slicing to 500 assignments in DYNNIST and 10 basic blocks in ANGR, the two tools find the index bound in 21.2% and 19.8% more jump tables. Third, GHIDRA and RADARE2 discard jump tables with index over 1,024 and 512, respectively missing 51 and 2,435 jump tables.

To detect tail calls, RADARE2 relies on the distance between jumps and their targets. It is hard to pick a proper threshold: in our benchmark binaries, the distance for regular jumps (min: 0; max: 0x5867) and tail calls (min: 0; max: 0x5707c5) are largely overlapped. GHIDRA uses the heuristic that a tail call and its target spans multiple functions. This heuristic is general to capture tail calls, producing a high coverage of 91.29%. However, it cannot recognize regular jumps between two parts of non-continuous functions, producing over 70K false positives. ANGR and DYNNIST use control-flow and stack-height based heuristics, detecting 4.24% and 6.99% more tail calls. However, the heuristics are neither sound nor complete, producing both false negatives and false positives.

The errors related to jump tables are case specific and tool specific. We randomly sample 10% false negatives and 10% false positives from each tool and do manual analysis to understand the reasons. The false negatives vary across tools. RADARE2, GHIDRA, and DYNNIST rely on patterns to detect jump tables, leading to 64.1%, 31.4%, and 37.72% of their false negatives. Further, RADARE2 incurs 35.9% of its false negatives because it discards jump tables with over 512 entries; GHIDRA produces 62.63%, 5.50%, and 0.47% of its false negatives, respectively because it does not consider sub instructions in VSA analysis, does not capture the correct restrictions to the index, and discards jump tables with more than 1,024 entries; ANGR generates false negatives because of no VSA results (67.86%), wrong VSA results leading to over 100,000 entries (14.29%), and no handling of sbb instructions (17.86%); DYNNIST’s remaining false negatives are caused by its restriction of slicing scope (21%) and no handling of get_pc_thunk (60.18%). Beyond the above reasons, we also observe 6 cases where the index is unrestricted (e.g., Listing 10), leading to false negatives in many tools. The causes of false positives are also diverse. RADARE2’s false positives are mostly because it matches the wrong cmp/sub instruction (54.29%) or the restriction to the index is not by cmp/sub (45.71%). DYNNIST produces false positives because it does not handle special aliases of the index (78.96%), does not consider restrictions by type (17.95%), and restricts the scope of slicing (3.09%). ANGR incurs false positives due to incorrect VSA solving (78.26%) and ignoring certain paths in slicing (21.7%). With GHIDRA, the false positives are because of wrongly considering indirect jumps as indirect calls (55.32%), no consideration of sub instruction in VSA analysis (34.04%), identifying extra/wrong restrictions to the index (4.26%), and

![TABLE XIV: Results of CFG reconstruction on Windows.](image)

| Tools  | **Pre** | **Rec** | **Pre** | **Rec** | **Pre** | **Rec** |
|-------|--------|--------|--------|--------|--------|--------|
| ANGR  | 96.66  | 97.09  | 98.89  | 99.40  | 99.54  | 99.96  |
| GHIDRA| 98.88  | 98.97  | 99.86  | 99.99  | 99.99  | 99.99  |
| RADARE2| 98.88 | 98.97 | 99.86 | 99.99 | 99.99 | 99.99 |
| IDA   | 98.88  | 98.97  | 99.86  | 99.99  | 99.99  | 99.99  |

![TABLE XV: Statistics of false positives in CFG-edge recovery.](image)

| Tools  | **Non-Ret** | **Func** | **T-Call** | **Inst** | **No-split** | **Jump-Tab** |
|-------|-------------|----------|------------|----------|--------------|--------------|
| Dynnist| 23.09       | 12.25    | 23.28      | 4.95     | 35.24        | 1.19         |
| GHIDRA | 8.76        | 1.88     | 13.00      | 0.11     | 75.13        | 1.12         |
| ANGR   | 16.69       | 38.42    | 10.82      | 4.73     | 27.02        | 2.32         |
| Bap    | 37.95       | 22.30    | 1.34       | 0.34     | 38.06        | 0           |
| RADARE2| 73.32       | 3.90     | 15.55      | 0.67     | 4.23         | 2.34         |

The resolving of jump tables uses three heuristics. First, ANGR, GHIDRA, and DYNNIST use patterns to detect jump tables, identifying 85.53%, 98.01%, and 99.56% of all jump tables. Second, DYNNIST and ANGR restrict the scope of slicing in VSA, missing the index bound in many jump tables. By enlarging the scope of slicing to 500 assignments in DYNNIST and 10 basic blocks in ANGR, the two tools find the index bound in 21.2% and 19.8% more jump tables. Third, GHIDRA and RADARE2 discard jump tables with index over 1,024 and 512, respectively missing 51 and 2,435 jump tables.

To detect tail calls, RADARE2 relies on the distance between jumps and their targets. It is hard to pick a proper threshold: in our benchmark binaries, the distance for regular jumps (min: 0; max: 0x5867) and tail calls (min: 0; max: 0x5707c5) are largely overlapped. GHIDRA uses the heuristic that a tail call and its target spans multiple functions. This heuristic is general to capture tail calls, producing a high coverage of 91.29%. However, it cannot recognize regular jumps between two
TABLE XVI: Statistics of false negatives in detection of non-returning functions. \textit{Lib, Cond-NonRet, Exit-Inst, Fake-Ret, Propa} respectively stand for unrecognized non-returning library functions, argument-dependent non-returning functions (e.g., error), instructions that exit (e.g., \texttt{ud2}), functions that contain \texttt{ret} instructions but do not actually return, and propagation of other types of false negatives.

| Tools | Percentage of False Negatives (%) |
|-------|----------------------------------|
|       | Lib | Cond-NonRet | Exit-Inst | Fake-Ret | Propa |
| Dyninst | 12.30 | 43.86 | 0.30 | 27.64 | 15.90 |
| Ghidra | 3.97 | 13.01 | 0.04 | 9.54 | 73.44 |
| Angr | 19.08 | 21.70 | 0.00 | 22.29 | 36.93 |
| Bap | 24.45 | 22.73 | 0.10 | 28.90 | 23.82 |
| Radare2 | 19.89 | 26.04 | 0.08 | 13.07 | 40.92 |

including the default case as an entry (6.38%).

In detecting tail calls, false negatives are largely caused by excluding conditional jumps (FN ratio - \texttt{HIDRA}: 21.6%; \texttt{ANGR}: 17.5%), missing boundaries between adjacent functions (FN ratio - \texttt{HIDRA}: 78.4%), excluding targets reached by both conditional jumps and unconditional jumps (FN ratio - \texttt{ANGR}: 81.6%), incorrect calculation of stack height (FN ratio - \texttt{ANGR}: 0.9%), unrecognized functions and no stack adjustment patterns before the jumps (FN ratio - \texttt{DYINST}: 100%). For false positives, the most common cause is wrong function entries (FP ratio - \texttt{HIDRA}: 5.9%; \texttt{ANGR}: 19.7%; \texttt{DYINST}: 2.4%). Other reasons include non-continuous functions (FP ratio - \texttt{HIDRA}: 94.1%; \texttt{ANGR}: 0.3%; \texttt{DYINST}: 0.6%), instructions follow the format of add esp/rsp, const but do not tear down the stack (FP ratio - \texttt{DYINST}: 97%), unchanged stack height before the jump (FP ratio - \texttt{ANGR}: nearly 90%). We also notice that \texttt{ANGR}’s false positives contain a group of conditional jumps, which are caused by a defect in its code.

Table XVI shows the false negatives in detecting non-returning functions. A special category is functions that have \texttt{ret} instructions but do not return (e.g., \texttt{_Unwind.Resume} in Glibc). Such functions alter the stack to use a self-prepared return address, avoiding returning to the parent. We observe no false positives with \texttt{DYINST}. \texttt{RADARE2} generates false positives because of un-handled jump tables (23.40%), wrong function boundaries (4.26%), and propagation of other false positives (72.34%). \texttt{ANGR} produces false positives due to un-handled jump tables/tail calls (29.73%), wrong function boundaries (54.05%), and propagation of other false positives (16.22%). \texttt{HIDRA} incurs all its false positives due to heuristic \texttt{_Unwind.Resume}. False positives by \texttt{BAP} are due to implementation issues.

C. Threats to Completeness and Validity

Despite our best efforts to carefully evaluate the public versions of the selected tools, there could still be threats to the completeness and validity of our results. Completeness wise, we may have missed reporting the results of some tools in certain tasks due to implementation issues of the tools (other than fundamental limitations). For instance, we could not report \texttt{ANGR}’s results of symbolization with Windows binaries because of two implementation issues in \texttt{ANGR}’s Reassembler module (one is reported at \url{https://github.com/angr/angr/issues/}).

V. FINDINGS

This study uncovered the following major findings.

F-1: Complex constructs are common and heuristics are indispensable to handle them. We observe a large amount of complex constructs in our benchmarks (Table XVII). In particular, we found 295 instances of data-in-code, complementary to previous research [5]. Heuristics are indispensable to handle the constructs: they are responsible for 49% of the instructions, 17% of the function entries, and most xrefs.

F-2: Heuristics inherently introduce coverage-correctness trade-offs. Heuristics significantly increase coverage, but concurrently induce new errors. As a counter-movement, new heuristics are devised to reduce the errors, leading to coverage-correctness trade-offs in nearly every disassembly phase.

F-3: Tools complement each other. As shown in Table III, existing tools use heuristics and algorithms that have overlaps but also many differences. This features tools different strengths, indicating that tool selection should be demand-specific.

F-4: Broader and deeper evaluation is needed for future improvements. We envision that the community may have insufficiently evaluated existing tools. This prevents comprehensive understanding about the limitations of the tools. We hope our study will bring a piece of basis to broaden and deepen the evaluation towards better binary disassembly.

VI. CONCLUSION

We present a systematization of binary disassembly with a thorough study and comprehensive evaluation, centering around the perspective of algorithms and heuristics. Our study, via the comprehension on the source code of 9 disassembly tools, presents in-depth understanding of their strategies. Our evaluation separately measures the tools on each disassembly phase and on individual strategies, which fully unveils how much the heuristics are used, how much the heuristics contribute to disassembly, and what errors the heuristics can introduce. Throughout the study, we derive a group of new observations that can amend/complement previous understandings and also inspire future directions of binary disassembly.

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A. Complex Constructs

Table XVII presents the statistics of complex constructs from our benchmark binaries.

B. Building Ground Truth for Windows Binaries

First, we follow a similar approach as described in [5] to gather instructions. We use the line table in debug information to find the first instruction of each source-code/assembly statement, followed by recursive disassembly (skipping indirect control transfers) to find instructions not covered by the line table. We also found that Visual Studio considers hard-coded bytes as code in the line table. We manually exclude such cases. Second, we rely on symbols to identify functions. The symbol of a function also carries a flag indicating whether it returns, aiding us to gather non-returning functions. Third, we enable the DEBUGTYPE:FIXUP option in Visual Studio such that all xrefs are preserved in the linking information. Finally, we identify jump tables based on xrefs (which contain the base address and entries of a jump table). For each xref, if its target location contains a list of other xrefs pointing to basic blocks in the same function, we consider the xref refers to a jump table and deem the list of xrefs at its target location as entries. For correctness, we further verify if the jump table entries correspond to switch-cases in the source code. Otherwise, we manually verify the correctness (since some jump tables are hand-crafted or compiled from if-else statements).

Our initial ground truth of non-returning functions (for both Linux and Windows binaries) can miss propagated cases. To this end, we expand our ground truth by running algorithms 14 and 15 with recursive updates.

C. Configurations of Disassembly Tools

OBJDUMP, PSI, UROBOROS: We use the recommended options to run them. We parse their outputs for results.

DYNINST: We use its ParseAPI interface to perform recursive disassembly and we further enable IdiomMatching to include the decision-tree based function matching (which is excluded when we do testing without heuristics). We parse the returned structure of ParseAPI to get the disassembly results.

ANGR: We use its CFGFast interface for disassembly. When using this interface, we enable the normalize and detect_tail_calls arguments so that a basic block is split by a control transfer to the middle and tail calls are detected. When testing disassembly without heuristics, we disable the force_complete_scan and function_prologues arguments to CFGFast, preventing linear scan and function matching. To obtain the results, we interpret the CFG returned by CFGFast for results. We also excluded functions marked as "alignment". We use the Reassembler interface for symbolization.

GHIDRA: Besides default settings, we enable Assume Contiguous Functions Only and Allow Conditional Jumps to perform tail call detection. We disable the X86 Constant Reference Analyzer in symbolization as it introduces many dummy xrefs. Finally, to test GHIDRA without heuristics, we disable Function Start Search and xref related options, to prevent the signature-based matching and xref based disassembly.

| TABLE XVII: Results of disassembly without heuristics. |
|---------------------------------------------------------|
| ![Table content](table_content.png)                      |

| TABLE XIX: Results of function-matching. The baseline of Rec is the total number of true functions. |
|-------------------------------------------------------|
| ![Table content](table_content.png)                   |

RADARE2: We run RADARE2 with four options: aa for default recursive disassembly, aann for non-return function detection, aac and aap respectively enable xref based disassembly and signature-based function matching. With aac and
**Disassembly:** Both IDA PRO and BINARY NINJA perform recursive descent to recover instructions. We inferred this based on their correct results of handling Listing 2 and [84]. They also take other approaches to handling code gaps.

BINARY NINJA follows a heuristic as described in [84] to deal with code gaps. It linearly scans non-disassembled code regions and aggregates call targets. Once done, BINARY NINJA sorts all the targets based on the times of being referenced and (in order) hands them off to further recursive descent.

IDA PRO at least uses four strategies to handle code gaps: (1) it searches for common code sequences (e.g., \{push bp; mov bp, sp\}). We inferred this from the kernel option of mark typical code sequences as code [1] and the test-case in Listing 11; (2) it considers addresses in the .eh_frame sections for recursive descent. This is inferred from the enable EH analysis kernel option, confirmed by comparing the results with and without the .eh_frame section; (3) it also performs recursive descent at the targets of d2c xrefs. This is inferred from the create function if data xref data→code32 exists kernel option, verified by comparing the results with and without certain xrefs; (4) it coagulates the remaining bytes in .text as code or data (the make final analysis pass kernel option).

**Symbolization:** It is in general hard to infer how exactly IDA PRO and BINARY NINJA do symbolization. We empirically learned the following strategies: (1) The two tools do not use heuristics \(\circ\), \(\odot\), and \(\boxdot\) BINARY NINJA rarely considers d2c and d2d xrefs. (2) The majority of c2d xrefs identified by BINARY NINJA are constant address operands. (4) IDA PRO will seek to symbolize the data unit at the target of a c2d xref. It will further seek to symbolize the neighbors of the data unit at the target location.

**Function Entry Identification:** Both IDA PRO and BINARY NINJA consider the targets of direct/indirect calls as function entries. They further apply some other approaches.

BINARY NINJA adopts the idea from [6] to facilitate identification of function entries. It traverse the inter-procedural CFG and groups all the connected basic blocks as a function. Similar to many open source tools, BINARY NINJA also considers targets of tail calls as function entries.

IDA PRO leverages at least two other strategies to identify function entries: (1) it considers certain (but not all) addresses in the .eh_frame section as function entries. This is confirmed by comparing the results before and after removing the .eh_frame section; (2) it considers the targets of certain d2c xrefs as function entries. We inferred this by comparing the results before and after we intentionally destroy some d2c xrefs. However, for now we are not fully aware of how IDA PRO selects .eh_frame items and the xrefs.

**Indirect Jumps:** Based on [84], BINARY NINJA implements VSA to handle jump tables. As discussed in § IV-B4, BINARY NINJA also resolves 120 hand-crafted indirect jumps, however, with wrong targets. It remains unclear how BINARY NINJA internally handles the 120 cases.

According to [1], IDA PRO [49] relies on patterns to detect jump tables. We further crafted test-cases (e.g., Listing 12) to demonstrate that IDA PRO does not use VSA analysis.
**Indirect Calls:** We infer how **Binary Ninja** and **IDA PRO** handle indirect calls by examining their results with our benchmark binaries (recall § IV-B3).

All the targets found by **Binary Ninja** are constants propagated to the call sites. According to [76], this is achieved by path-sensitive data-flow analysis to calculate the ranges or disjoint sets of values (or VSA in general). Based on our further communication with the developers, the scope of analysis is intra-procedural.

**IDA PRO** found two types of targets. The first type is propagated from constants in the current function. The second type all follows the format in Listing 9. We envision that **IDA PRO** uses data-flow like constant propagation to handle indirect calls and applies patterns to find function tables.

**Tail Calls:** **Binary Ninja** considers a jump as a tail call if the target is outside of the current function and the stack has a zero offset [84]. **IDA PRO** does not particularly handle tail calls, as clarified by their technical support team.

**Non-returning Functions:** Our analysis shows that **IDA PRO** and **Binary Ninja** are detecting a similar group of non-returning functions as **DYNINST**. The difference is mainly caused by the recognition of non-returning library functions. Further, the three tools have comparable precision and recalls. This indicates that **IDA PRO** and **Binary Ninja** are using similar recursive strategies to **DYNINST**.

---

**G. Interesting Cases and Test Cases**

**Listing 2:** An example of data-in-code. This example comes from OpenSSL, where hand-crafted data are appended after code (left). Both **OBJDUMP** and **ANGR** incur errors in this case.

```
1 popfq
2 .byte 0xf3, 0xc3
3 .align 64
4 .LONG 0xa56363c6
5 ; data; 0x6ab8a0: 69 00 64 00
```

**Listing 3:** A false positive of xref in Xalan_base. **GHIDRA** wrongly identifies the operand 0x6ab8a0 (line 1 left-upper) as a pointer and makes erroneous disassembly.

```
1 ; load base address
2 .db: lea 0x8a(%rip), %r8
3 .w: movzw %cl,%cx
4 .w: movaq (%r8,%rcx,4),%rax
5 .w: add %r8,%rax
6 .w: jmpq +%rax
```

**Listing 4:** Jump table with 4-byte entries in 64-bit Gold Linker.

```
1 004286ab: add 0xfb49eb,%rax
2 ... 00fb49eb: undefined [176081]; invalid address
```

**Listing 5:** A xref error in zeusmp_base. Operand fb49eb points to invalid data but **GHIDRA** symbolizes it.

```
1 mov $0xe,%ebx
2 movzw 0x4e33be(%rbx),%eax; operand = 0x4e33be
```

```
1 0x4e33be: 00 00 00 00; end of .fini
2 0x4e33be: (bad); invalid memory address
3 0x4e33c0: 04 e3 3b 00; begin of .rodata
```

**Listing 7:** Xref missed by **ANGR** in addr2line. At 0x804f16c, **ANGR** detects a Unicode and jumps to 0x804f171, missing the pointer at 0x804f170.

```
1 callq 42ae30; non-returning
2 nopw %cs:0x0(%rax,%rax,1)
3 00 00 00; padding
4 cmpq $0x0,%esi; start of a function
5 je 406f38
```

**Listing 8:** A missed function entry (line 4). The disassembler assumes code after line 1 falls through and includes code at line 4 to the preceding function.

```
1 app_main = {
2 ... test_main, 
3 apcid_main, 
4 ... }
```

**Listing 9:** An indirect call that can be handled by **IDA PRO**. This indirect call uses a target from a function table.

```
1 switch(which){
2 case 't': ...
3 ...
4 default:
5 undefined();
```

**Listing 10:** A jump table with unrestricted index in dwp. The default case in source code transfers to undefined behaviors, which is compiled into jump tables without index restriction.

```
1 pop %ebp
2 jmp 8048430
3 ; new function
4 push %ebp
5 mov %ebp, %esp
```

**Listing 11:** Test-case to infer **IDA PRO**’s disassembly. The code has a function (line 4, left part) that carries a common prologue but is never referenced. **IDA PRO** correctly disassembles the code. After altering the instruction at line 4 (middle part), **IDA PRO** considers the code as data (right part).

```
1 cmp $4, %rax
2 je .default
3 ...
4 cmp $0, %rax
5 jle .default
6 jmp .branch_tbl1(%rax,8)
7 jmp .branch_tbl1(%rax,8)
```

**Listing 12:** Hand-crafted jump table with %rax as the index. Tools with VSA analysis, like **Binary Ninja** and **DYNINST**, figure out %rax ranges from 1 to 4. **IDA PRO** wrongly considers %rax ranges from 0 to 4.