An electrochemical thermal transistor

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The ability to actively regulate heat flow at the nanoscale could be a game changer for applications in thermal management and energy harvesting. Such a breakthrough could also enable the control of heat flow using thermal circuits, in a manner analogous to electronic circuits. Here we demonstrate switchable thermal transistors with an order of magnitude thermal on/off ratio, based on reversible electrochemical lithium intercalation in MoS2 thin films. We use spatially-resolved time-domain thermoreflectance to map the lithium ion distribution during device operation, and atomic force microscopy to show that the lithiated state correlates with increased thickness and surface roughness. First principles calculations reveal that the thermal conductance modulation is due to phonon scattering by lithium rattler modes, c-axis strain, and stacking disorder. This study lays the foundation for electrochemically-driven nanoscale thermal regulators, and establishes thermal metrology as a useful probe of spatio-temporal intercalant dynamics in nanomaterials.
Achiving dynamic control of heat flow at the nanoscale represents an outstanding challenge in engineering energy transport. A thermal transistor, a device whose thermal conductance can be modulated in real-time using an external stimulus, has the potential for transformative applications in dynamic thermal management, energy harvesting, and phonon logic. For decades, there have been numerous studies of fundamental relationships between material microstructure and thermal conductivity. For example, impurities, vacancies, dislocations, grain boundaries, and interfaces can all have a significant impact on the thermal conductivity. Despite progress on the fundamental understanding of these relationships, there are very few demonstrations of tuning nanoscale thermal transport in real-time. Dynamic changes in microstructure have been leveraged to actively modulate the thermal conductivity of a material, e.g., by temperature-induced phase change, electric field, and electrochemical intercalation. However, most prior demonstrations of reversible tuning at room temperature have shown relatively small thermal on/off ratios, below approximately 1.6×, with switching time scales on the order of hours. Practical applications of thermal switches would require devices with significantly larger on/off ratios, and nanoscale dimensions in order to achieve faster operation.

Here we demonstrate reversible thermal conductance modulation in a fully dense medium by a factor of nearly 10×, on time scales of minutes, across a material that is only 10 nm thick. The thermal transistors are based on nanoscale MoS2 films actuated by reversible electrochemical intercalation of Li ions, with the on-state corresponding to the pristine MoS2 and the off-state corresponding to the Li-intercalated MoS2. Thermal conductance measurements are performed in situ using time-domain thermoreflectance (TDTR) while the device (which operates like a particle battery electrode) undergoes reversible electrochemical cycling. Using operando TDTR microscopy, we also probe the spatial distribution of Li ions inside MoS2 at different stages of intercalation within an electrochemical cycle. Correlating atomic force microscopy (AFM) with thermal measurements reveals a strong impact of Li on microstructure in terms of c-axis lattice expansion, and significant mesoscopic disorder. Ab initio density functional theory (DFT) and non-equilibrium molecular dynamics (NEMD) calculations sort out the relative contributions of the different factors leading to the thermal conductance modulation.

Results
Device preparation. Figure 1 shows a schematic of the transparent electrochemical cell used for operando thermal conductance measurements (see Methods for further details of device fabrication and packaging). The working device is a 10 nm thick layered MoS2 crystal, prepared on SiO2 (90 nm) on p-type Si. An 80 nm Al layer patterned on top of the MoS2 serves as an electrical contact and as opto-thermal transducer for TDTR measurements. A solid Li pellet acts as the reference and counter electrodes, and 1.0 M LiPF6 in ethylene carbonate/diethyl carbonate (EC/DEC, 1:1 w/w) is the liquid electrolyte. TDTR is an optical pump-probe technique that is used to measure cross-plane thermal transport in thin-films (see Methods, Supplementary Figs. 1–4, Supplementary Table 1). Because it is an optical method, TDTR is well-suited for non-invasive thermal measurements of such devices during electrochemical operation. We perform real-time measurements by fixing the pump-probe delay time to +100 ps and continuously collecting the in-phase and out-of-phase voltages (V in and V out). This enables measurement of thermal conductance as a function of both time and spatial coordinate. To interpret the measurement, we fit the effective cross-plane thermal conductance of MoS2 (G) such that 1/G represents the combined intrinsic resistance of the film plus the Al/MoS2 and MoS2/SiO2 interfaces acting in series.

Operando thermal conductance microscopy. It is generally known that electrochemical intercalation in single particles can result in an inhomogeneous Li ion distribution. A few recent studies have used in situ optical and spectroscopic techniques to visualize this inhomogeneity at the nanoscale. Here we effectively probe Li ion segregation in single crystal MoS2 films through its impact on the local cross-plane thermal transport. This represents a demonstration of operando thermal conductance microscopy performed during intercalation of a single particle battery electrode. To probe this spatial distribution, we use a modification of the traditional TDTR technique. The sample (see optical image in Fig. 2a) is placed on a two-axis translation stage, allowing it to be raster scanned in the plane perpendicular to the laser’s optical axis. Measurements are performed under equilibrium electrochemical conditions in potentiostatic mode, with the potential of the MoS2 working electrode V WS (relative to Li+/Li) fixed at values ranging between the strongly lithiated and delithiated states at 1.0 V and 3.0 V, respectively. We do not use voltages much lower than 1.0 V to avoid the irreversible formation of products such as Li2S and Mo24. Figure 2b shows thermal conductance maps as the device is sequentially subjected to voltages from 1.8 V to 1.0 V (lithiation), and 1.2 V to 3.0 V (delithiation). A video montage of these images is provided in Supplementary Movie 1.
As the device is gradually lithiated from 1.8 V to 1.4 V, the images reveal regions of low thermal conductance $G$ forming at the top- and bottom-left edges of the device. We suggest that this is due to an increased concentration of Li near the exposed edges of the MoS$_2$. At $V_{WE} = 1.0$ V, we observe the formation of a region of low $G$ at the center of the device. As $V_{WE}$ is raised from 1.2 V to 3.0 V, Li ions leave the film, resulting in a gradual dissolution of the low conductance region at the device center. By the end of the lithiation step at 2.4 V and 3.0 V, we see some spots of low $G$ forming in the lower half of the device. These results reveal that Li intercalation in thin-film MoS$_2$ likely occurs through the formation of Li-rich domains, qualitatively similar to the observations by Lim et al. in single micro-platelets of LiFePO$_4$. In Fig. 2c, we track the evolution of $G$ at two locations (indicated in Fig. 2a) over the full lithiation-delithiation cycle. We find that $G$ decreases with decreasing $V_{WE}$ during the lithiation phase, and increases with increasing $V_{WE}$ during the delithiation phase.

Dynamic thermal conductance modulation. Real-time modulation of thermal conductance is achieved by charging and discharging the cell at constant current (i.e., in the galvanostatic mode), while simultaneously measuring $G$ at a single point on the device, shown in Fig. 3a. We apply a current of $-1.2$ nA and $+1.2$ nA to lithiate and delithiate the thermal transistor, respectively, while fixing the voltage limits for $V_{WE}$ at 1.0 V and 2.9 V. Note that since the Al metal electrode contacts multiple MoS$_2$ flakes, only a portion of the supplied current flows through the device under study. The full cycle time is 14 min, corresponding to a charge and discharge rate of about 8.5 C. Figure 3b shows a plot of $V_{WE}$ vs. time, and Fig. 3c shows the corresponding $G$ measured at the location indicated by the blue circle in Fig. 3a. During the discharge step, a negative current lithiates MoS$_2$ and decreases its thermal conductance from the pre-lithiation value of $15 \pm 2$ MW m$^{-2}$ K$^{-1}$ to $1.6 \pm 0.3$ MW m$^{-2}$ K$^{-1}$ (see Supplementary Fig. 3 for error analysis). When the current is reversed during the charge step, Li ions are removed from the MoS$_2$ and its conductance recovers to its pre-lithiation value. We measure a thermal conductance on/off ratio of 8–10× between the delithiated and lithiated states, which to the best of our knowledge is higher than that observed in previous in situ experiments on nanoscale thermal devices. A schematic of our electrochemically-gated thermal transistor is shown in Fig. 3d.

Figure 3e displays a plot of measured $G$ vs. $V_{WE}$, showing significant hysteresis between the charge and discharge curves. This is due to a hysteresis in the voltage curves themselves, and can be seen when we plot the average lithium composition $\chi$ (in Li$_x$MoS$_2$) vs. $V_{WE}$. Voltage curve hysteresis is a common feature in several battery electrode systems, and it is especially prominent in this case due to the large electrode size and relatively high charge and discharge rates. To estimate $\chi$, we assume that the sample is fully delithiated at $V_{WE} = 2.9$ V ($\chi = 0$) and fully lithiated at $V_{WE} = 1.0$ V ($\chi = 1$) (see Supplementary Fig. 5), varying linearly with intercalation time for intermediate voltages. When we plot thermal conductance vs. the average Li composition determined in this approximate manner (Fig. 3f), we find reduced hysteresis, suggesting a direct physical link between the two quantities.

Ex situ measurements on chemically intercalated MoS$_2$. In addition to operando studies, we also study the impact of chemical lithiation on thermal conductance of MoS$_2$ through ex situ measurements. Layered, crystalline MoS$_2$ films were exfoliated onto SiO$_2$ (90 nm) on p-type Si substrates. The samples were transferred into an Ar-filled glove box, and immersed in a 1.6 M n-Butyllithium in hexane solution (Fisher Scientific) for 2 h at 295 K. They were gently washed with anhydrous hexane to remove organic residues, and allowed to dry. The samples were sealed inside an airtight pouch and transferred to an e-beam evaporator for blanket deposition of the 81 nm thick Al transducer for TDTR measurements. These samples are thicker than
the device used for operando measurements, in the 60–100 nm range.

Figure 4a shows a thermal conductance map of a 72 nm thick sample after chemical intercalation, revealing a strong gradient in G across the device area. Regions closest to the edges are the most resistive as they have a larger local concentration of Li, compared to regions close to the center that are nearly unlithiated. This is consistent with the picture that the intercalants enter the MoS2 crystal from the edges, and not through the basal plane. Figures 4b and c display vertical and horizontal line scans extracted from the conductance image, showing a contrast of up to 7× between the pristine and lithiated regions. To study the effect of lithium intercalation on morphology of MoS2, we perform AFM on the region highlighted in Fig. 4a. The topographical scan shown in Fig. 4d reveals an excellent correspondence with the conductance map. The pristine region remaining after the first recharge (delithiation), even at high electrochemical potentials (2.4 V relative to Li+/Li). They also visualized the coexistence of 2H and 1T phases using high-resolution transmission electron microscopy (HRTEM). The role of stacking disorder has also been suggested by previous ex situ measurements of the thermal conductivity of Li intercalated bulk MoS2. Zhu et al.7 argued that the strong suppression in the cross-plane thermal conductivity was partly a result of a phase mixture present across the thickness of the crystal. As we will show later using NEMD simulations, this hypothesis of stacking disorder and mixed phases is also consistent with our experimental measurements of c-axis strain and thermal conductance modulation.

Theoretical modeling. To understand how Li ions affect the cross-plane thermal conductance of our MoS2 thermal transistors, we perform first principles density functional perturbation theory (DFPT)28 calculations of phonon dispersion relations. We consider the equilibrium crystal structure 2H-MoS2, with stacking sequence ABAB. Upon intercalation, Li atoms occupy the octahedral sites between the MoS2 layers, forming a thermodynamically unstable 2H-Li1MoS2 phase, which eventually transforms into 1T-Li1MoS2 with stacking sequence AA, and lower in-plane symmetry. We find that the c-axis lattice constant expands by 14% upon intercalation in the 2H-Li1MoS2 phase, but in the stable 1T-Li1MoS2 it is only larger by 0.5%, as compared to 2H-MoS2. The lattice parameters from our calculations (see Supp Table 2) for 2H-MoS2 and 1T-Li1MoS2 agree well with recent measurements7, which showed a c-axis expansion in bulk Li0.86MoS2 of 0.5%. However, previous reports on LiMoS2 powders showed larger expansion (2.5–6%)26,29. Our X-ray diffractometry (XRD) measurements of electrochemically intercalated Li1MoS2 powders give a c-axis expansion of 2.3% (see Supplementary Fig. 7), while our AFM measurements on chemically lithiated exfoliated single crystals yield a value up to about 15% (Fig. 4e).

We suggest that the relatively large lattice expansion measured in our samples could be due to an incomplete 2H-1T transition, which engenders a mixture of 2H and 1T phases and creates stacking disorder. Previous reports in literature have found evidence of such phase mixtures in lithiated MoS27,30. For example, using in situ Raman spectroscopy in electrochemically intercalated MoS2, Xia et al.30 observed signatures of the 1T phase remaining after the first recharge (delithiation), even at high electrochemical potentials (2.4 V relative to Li+/Li). They also confirmed the coexistence of 2H and 1T phases using high-resolution transmission electron microscopy (HRTEM). The role of stacking disorder has also been suggested by previous ex situ measurements of the thermal conductivity of Li intercalated bulk MoS2. Zhu et al.7 argued that the strong suppression in the cross-plane thermal conductivity was partly a result of a phase mixture present across the thickness of the crystal. As we will show later using NEMD simulations, this hypothesis of stacking disorder and mixed phases is also consistent with our experimental measurements of c-axis strain and thermal conductance modulation.

Phonon dispersions along the Γ-A direction (Fig. 5a–c) reveal that lithiation gives rise to several flat bands at frequencies above 4 THz in both 2H-Li1MoS2 and 1T-Li1MoS2 (see Supplementary Fig. 8 for a schematic of the Brillouin zone, and dispersions plotted along other high-symmetry directions). The latter phase also displays a large number of MoS2 optical modes, stemming from the larger number of atoms in the unit cell with lower symmetry consisting of four formula units. Phonon modes are color coded to distinguish between those with prevalent MoS2...
performed for 2H-Li$_1$MoS$_2$ is $3.2 \times$ relative to the unlithiated 2H-MoS$_2$. While the highest thermal conductance, while the rough lithiated region is more thermally resistive. The scale bar is 5 µm.

To quantify the impact of Li intercalation on the thermal conductance of thin-film MoS$_2$, we use NEMD. A 10 nm thick film is sandwiched between thermal reservoirs (see Fig. 5f). The cross-plane thermal conductance $G$ is calculated based on the steady-state heat flux, and the temperature gradient developed across the thickness of the film (see Methods, Supplementary Fig. 9, and Supplementary Table 2). Simulations are first performed for 2H-Li$_1$MoS$_2$ with the Li composition $\chi$ varying from 0.75 to 1, and the results are plotted in Fig. 5g as a function of the $c$-axis strain relative to unlithiated 2H-MoS$_2$. The thermal conductance decreases monotonically with increasing $\chi$, consistent with our experimental results. Furthermore, this decrease in $G$ occurs along with a concomitant increase in the $c$-axis lattice constant. This shows that in addition to increasing the phonon scattering rates, intercalation suppresses $G$ by creating tensile strain along the $c$-axis, which softens $\Gamma$-$\Lambda$ phonon modes and reduces group velocities$^{32}$. The maximum reduction in $G$ for 2H-Li$_1$MoS$_2$ is $3.2 \times$ relative to the unlithiated 2H-MoS$_2$. While significant, this reduction is less than experimentally measured, suggesting that the large thermal modulation cannot be explained solely by full lithiation of the 2H phase alone. We also consider thermal transport in the fully lithiated 1T phase. As noted earlier, the 1T-Li$_1$MoS$_2$ system has very little $c$-axis strain (0.5–1%); NEMD calculations of a 10 nm thick film show that the fully intercalated 1T phase shows a comparatively small reduction in $G$ of $1.5 \times$. Furthermore, using ab initio phonon dispersion calculations, we uncover that there is a critical $c$-axis strain of around 8% beyond which the 1T lithiated phase becomes unstable (see Supplementary Fig. 10). These results suggest that a full phase transition to the 1T-Li$_1$MoS$_2$ phase is unlikely to explain our experimental observations.

As noted above, based on previous reports$^{7,30}$, there is a strong likelihood of the existence of mixed 2H and 1T phases and stacking disorder in the lithiated MoS$_2$. In this context, turbostratic disorder has been shown to lead to record-low cross-plane thermal conductivity in layered WSe$_2$ films$^{33}$. To examine the effect of phase mixtures on cross-plane thermal conductance, we construct two systems (each 10 nm thick), which contain stacking disorder along the $c$-axis. They have the following stacking sequences: (1) [6, 5, 6] layers of [2H, 1T, 2H], and (2) [4, 3, 4, 2, 4] layers of [2H, 1T, 2H, 1T, 2H], respectively (see simulation snapshot in Fig. 5f). First, we find that these systems have $c$-axis strains of 12.5 and 13.5% for Li$_1$MoS$_2$ and Li$_{0.9}$MoS$_2$, respectively, close to the calculated value for 2H-Li$_1$MoS$_2$ (14%). Next, the cross-plane thermal conductance is calculated using NEMD. For Li$_1$MoS$_2$, in both mixed phase systems, we calculate a $7.1 \times$ reduction in $G$ compared to the pristine 2H-MoS$_2$. This lowering of $G$ below 2H-Li$_1$MoS$_2$ is likely due to the increased phonon scattering at 2H-1T phase boundaries, qualitatively consistent with the findings of Zhu et al.$^7$ Furthermore, for the Li$_{0.9}$MoS$_2$ system (i.e., with 10% Li vacancies) we calculate a $9-10 \times$ reduction in $G$, i.e., nearly one order of magnitude, comparable to our dynamic TDTR measurements.

**Discussion**

A detailed mechanism thus emerges, as indicated in Fig. 5g: as Li intercalates between the MoS$_2$ layers, increased phonon scattering and $c$-axis tensile strain in the 2H phase lead to a reduction in $G$. With increasing Li concentration, the system transforms partially into the 1T phase, creating 2H-1T phase boundaries along the $c$-axis. Additional phonon scattering due to stacking disorder in the mixed phase causes a further suppression in $G$, by up to $10 \times$ relative to the pristine state. Even in the strongly lithiated state, our calculations and experiments suggest that the thin-film system remains as a phase mixture without undergoing a complete transition to the pure 1T-LiMoS$_2$ state.

We note that an additional source of thermal conductivity suppression is mesoscopic disorder; the high degree of surface

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**Fig. 4** Ex situ chemical lithiation experiments. a Thermal conductance map of a 72 nm thick single crystal MoS$_2$ film lithiated using $n$-Butyllithium for 2 h. Inset shows an optical micrograph of the device, after coating with 81 nm thick Al layer (scale bar is 10 µm). b Vertical line scan taken at $x = 12$ µm, and c horizontal line scan taken at $y = 18$ µm, extracted from the thermal conductance map along the solid lines marked in a. d AFM image of the region enclosed within the dashed box in a, showing a clear correlation between topography and thermal conductance. The smooth pristine region shows the highest thermal conductance, while the rough lithiated region is more thermally resistive. The scale bar is 5 µm. e Height profile extracted from the AFM image along the blue dashed line indicated in d at $x = 12$ µm.
roughness measured by AFM in the chemically lithiated samples provides some evidence of this (Fig. 4e). Such mesoscopic disorder could result from inhomogeneous strains caused by the non-uniform intercalation of Li ions and the possible formation of microdomains. These would be consistent with the strong inhomogeneities seen in the spatially-resolved TDTR measurements (presented in Fig. 2), as also previous in situ optical microscopy studies of MoS₂ flakes undergoing electrochemical intercalation.

Finally, we note that since we measure the total thermal conductance of the devices, it is difficult to experimentally decouple the effect of Li on the thermal conductance of the Al/MoS₂ and MoS₂/SiO₂ interfaces. Our ex situ studies indicate that a significant fraction of exfoliated films that are initially adhered to the substrate become detached when lithiated, suggesting that Li weakens the adhesion between MoS₂ and SiO₂, and thus may reduce the MoS₂/SiO₂ interfacial conductance. This is consistent with prior calculations of the adhesion energy between graphene and SiO₂ with adsorbed interlayers, and with measurements of the direct relationship between bond strength and thermal boundary conductance (TBC) in van der Waals junctions.

In summary, we have demonstrated an electrochemically driven thermal transistor using Li intercalation in thin MoS₂ films. We show large reversible switching of 8–10× in a 10 nm thick
MoS$_2$ device, with an operational time scale of minutes. Using operando scanning thermal conductance microscopy, we reveal inhomogeneities in the distribution of Li within a single MoS$_2$ device, and probe how it evolves over an electrochemical cycle. Correlative AFM imaging provides a direct link between the spatial distribution of Li through its impact on surface topography, and cross-plane thermal transport. Ab initio and NEMD calculations reveal that Li guest atoms act as rattlers within the host MoS$_2$ lattice. The simulations suggest that a combination of multiple factors is likely to lead to such a significant modulation of thermal conductance, including enhanced phonon scattering from Li rattler modes, phonon softening due to lithiation-induced c-axis strain, stacking disorder (2H-1T phase mixtures), and mesoscopic disorder.

These results are of crucial significance to the thermal engineering of Li ion batteries, where heat dissipation is a critical issue.

We have demonstrated that not only does the thermal conductivity of a battery electrode depend on the state-of-charge, it is also spatially inhomogeneous at the microscopic. In the context of battery thermal modeling, we suggest that it may be important to consider dynamics at length scales comparable to the characteristic thermal wavelengths (100's of nm) in thermal conductivity within single electrode particles. In other words, low thermal conductivity regions could be linked to the formation of hot spots, and can accelerate eventual thermal runaway of the battery.

In addition, as an operando technique, we contend that thermal conductance microscopy can provide useful information about changes in the local microstructure of nanomaterials that are undergoing reactions. Our approach leverages the strong sensitivity of phonon scattering rates to local defect density and strain, and represents a fundamentally different mechanism for microscopy of dynamically evolving materials. Furthermore, as an optical technique, it is complementary to existing electron or X-ray based techniques, which can often require specialized sample preparation.

In the context of active thermal management, our thermal transistor results represent an important advance with their nearly one order of magnitude switching ratio. For example, in power electronics where thermal cycling can lead to catastrophic failure due to thermal expansion, the inclusion of a thermal transistor between the device and heat sink can greatly suppress temperature oscillations and improve reliability. Our calculations suggest that to first order, a thermal transistor with 10x on/off ratio can reduce the temperature swing in a device by 10x, and increase its lifetime by almost 3000x. This is related to a modulation of the device’s cooling time constant, $\tau_C \sim \tau_{in} / G$, by switching $G$ to a lower value in the off-state. The temperature swing, $\Delta T$, is proportional to $[1 - \exp(-\tau_{in} / \tau_C)] / \tau_{in}$, where $\tau_{off}$ is the time during which the device is off. Reducing the temperature swing can have a significant effect on device reliability as the relationship between $\Delta T$ and the number of cycles to failure $N_{Ff}$ is strongly non-linear. $N_{Ff} \sim \Delta T^{-3.5}$.

We note that for electrochemical thermal transistors to reach deployment in technological applications, several challenges remain to be overcome. First, it will be important to understand the impact of device thickness on the thermal on/off ratio, and therefore decouple the effect of intrinsic (volumetric) vs. interface thermal switching. From a practical standpoint, in our experiments we found it challenging to perform electrochemical measurements on films thicker than tens of nanometers, likely due to lithiation-induced volume changes and consequent electrode detachment. Furthermore, to ensure robust switching under different operating conditions, it will be necessary to implement a fully solid-state version of this device. This could potentially be achieved using solid polymer electrolytes which have seen widespread adoption in Li ion batteries. Lastly, efforts must be made to increase thermal switching speeds in order to respond better to the fast heat flux transients typically found in electronic devices. Since the time scales for Li ion diffusion scale quadratically with length scale, it could be promising to engineer (arrays of) thermal switches with reduced lateral features.

Besides active thermal management and heat routing, our results also have direct implications for energy harvesting. For example, a pyroelectric device placed in contact with a thermal transistor can be used to harvest electrical energy from a passive, time-invariant heat source. This would be achieved by externally modulating the transistor’s thermal conductance to generate temporal variations in the pyroelectric material’s temperature. Additionally, intercalated layered materials also hold immense promise for thermoelectric applications. Previous studies have shown that Li intercalation enhances the in-plane electrical conductivity of MoS$_2$ and other layered materials by as much as two orders of magnitude. While there have not been, to the best of our knowledge, measurements of cross-plane electrical transport in lithiated MoS$_2$, we anticipate a similar enhancement (due to increased carrier density). Intercalation thus potentially offers a unique mechanism to achieve order of magnitude improvements in the thermoelectric figure of merit $zT$ for atomically-thin energy conversion devices of the future.

Methods

**Fabrication of operando electrochemical platform.** Crystalline, layered MoS$_2$ films were mechanically exfoliated onto SiO$_2$ (90 nm) on p-type Si substrates. Optical microscopy was used to select an appropriate device, and AFM was used to measure its thickness and confirm uniformity. An 80 nm thick Al layer was selectively patterned on top of the MoS$_2$ using electron-beam (e-beam) lithography and e-beam evaporation, while leaving the edges exposed to allow Li ions to intercalate. A 50 nm thick Cu electrode was e-beam evaporated through a shadow mask, patterned approximately 1 cm away from the MoS$_2$ device. The sample was transferred into an Ar-filled glove box, where a small Li metal pellet was placed onto the Cu electrode. A glass cover slip (around 0.2 mm thick) was placed on top of the MoS$_2$ device and Li metal, and sealed on three sides using epoxy. The pocket was filled with electrolyte (1.0 M LiPF$_6$ in EC/DEC, 1:1 w/w) and the fourth side was sealed with epoxy to prevent oxidation. Care was taken to minimize bubble formation in the electrolyte, which could disrupt the optical measurements. Wire bonds were used to make electrical contact to the terminals of the potentialstat (Gamry Instruments Reference 600) for electrochemical measurements. This was done while taking precautions to prevent electrostatic discharge, which was one of the major factors affecting device yield.

**Time-domain thermoreflectance.** Cross-plane thermal transport in MoS$_2$ thin films was measured using TDTR, an optical pump-probe technique. Details of this technique and our setup are provided elsewhere. In these experiments, we used a modulation frequency of 4 MHz, and pump and probe spot diameters of 4.0 µm and 2.7 µm, respectively. The transient temperature variation created by the absorption of modulated pump pulses within the Al transducer was measured by monitoring the reflected probe intensity, which was demodulated using a radio-frequency lock-in amplifier (Stanford Research Systems 844). The signal consists of the ratio of the in-phase ($V_{in}$) to out-of-phase ($V_{out}$) voltage ($V_{in}/V_{out}$) measured by the lock-in as a function of pump-probe delay time, which was varied from 0 to 3.6 ns using a mechanical delay stage. These data were fit to a three-dimensional multilayer heat diffusion model that accounts for bidirectional heat flow, radial heat spreading due to the finite spot size, anisotropic thermal conductivities of MoS$_2$ and TRNB, and scattering between adjacent materials. Cross-plane measurements performed through the transparent electrolyte, the total optical power was limited to ~3 mW to prevent laser-induced degradation of the Al surface. The estimated steady-state temperature rise was <2 K. An integrated dark-field microscope enabled location and imaging of MoS$_2$ thermal transistor devices. Real-time operando measurements were performed by fixing the probe delay time at ~100 ps and collecting the TDTR ratio signal. The temporal resolution is limited by the time-constant of the lock-in amplifier (100 ms), which is significantly faster than intercalation time scales in our device (minutes). For the single-spot time-dependent measurements shown in Fig. 3, $V_{in}$ and $V_{out}$ were recorded every 1 s while the device was subjected to repeated cycles of galvanostatic charge and discharge (see Supplementary Fig. 11 for raw TDTR data). Similar measurements on a control device, i.e., Al/SiO$_2$/Si, showed no changes in TDTR signal with electrochemical potential, ruling out intercalation-induced changes in the properties of the Al transducer and electrolyte/Al interface. Measured ratio ($V_{in}/V_{out}$) at the ~100 ps delay time was converted to conductance $G$ by comparing with a correlation curve calculated from the multilayer thermal model (see Supplementary Fig. 3, Supplementary Table 1). Thermophysical
Spatial thermal conductance imaging using TDTR was performed by mounting the sample on a motorized stage (Melles Griot Nanomotion II), which has a resolution of 10 nm and a bidirectional repeatability of 100 nm. After holding the MoS2 device at a constant $V_{in}$ (relative to Li/$Li$) for about 10 min, the sample was raster scanned over the plane normal to the laser beam while recording $V_{out}$ and $V_{in}$ at a fixed delay time of ±100 ps, with a step size of 500 nm and dwell time of 300 ms per pixel (see Fig. 2b). A typical 40 × 40 µm scan takes around 30 min. Pixels near the edges of the device give unphysical thermal conductance values due to optical edge effects (and for ex situ samples, also due to oxidation), and are not plotted on the colormap. To define valid data points, we used the following criterion: $0.9 \times V_{in} < V_{out} < 1.1 \times V_{in}$, where $V_{in}$ is the mean $V_{out}$ value inside the device. This is based on the fact that at short delay times, $V_{in}$ is not significantly sensitive to $G$, so that large variations in $V_{in}$ indicate abrupt changes in device reflectivity. Measured TDTR ratio values at each pixel were converted to $G$ using the procedure mentioned above.

In the operando measurements, we observed that optical propagation through the liquid electrolyte causes an asymmetry in the $V_{out}$ signal as a function of distance $z$ on either side of the focal plane. This causes the TDTR ratio signal to be asymmetric as well, which could introduce an error in the measurements if the sample is not at the correct $z$ plane. We posit that this occurs due to a thermo-optic modulation of the refractive index of a thin layer of liquid due to heat conducted away from the Al transducer. This effect (which occurs at relatively long time scales due to the low thermal conductivity of the electrolyte) likely leads to a modification of the effective thermoreflectance coefficient of the metal by a scaling factor. To correct for this, we scale the TDTR ratio data by a small factor (1.05), calibrating such that the thermal conductance of the device in the unlithiated state under liquid be equal to that of the pristine device measured prior to liquid encapsulation (Supplementary Fig. 1). This correction factor does not significantly change the measured thermal on/off ratio between the unlithiated and lithiated states.

**Ab initio and molecular dynamics calculations.** The first principles DFT calculations were performed in local density approximation (LDA) of the exchange and correlation functional. Core electrons were approximated using norm-conserving pseudopotentials, and the Kohn–Sham wavefunctions were expanded on a plane wave basis set with a cutoff of 1360 eV. Integration of the electronic wave functions were performed using the Quantum-ESPRESSO package. From the physical properties computed by DFT (lattice parameters, phonon dispersion relations, speed of sound) were fitted the parameters of a two-body empirical potential for NEMD calculations. The functional form of the potential consists of a combination of Lennard–Jones (LJ) interactions and electrostatics: $V(r_{ij}) = 4\epsilon \left( \frac{r_{0}}{r_{ij}} \right)^{12} - 2\frac{\epsilon}{r_{ij}}$. Partial charges (Q) and LJ parameters ($r_{0}$ and $\epsilon$) are provided in Supplementary Table 2. This potential was used to perform NEMD simulations of thermal transport employing the reverse-NEMD approach, in a nano-device of the same thickness as the experimental one. Supercells consisting of a total of 34 MoS$_2$ layers were used, in which two equally spaced two-layer slabs operate as hot and cold thermal baths. The system has a lateral size of 2.76 nm by 2.55 nm, and periodic boundary conditions are applied in all directions. The systems were first equilibrated in the weak-coupling NPT ensemble for 200 ps, and eventually in the NVT ensemble for further 200 ps. In reverse-NEMD, a stationary heat current ($Q$) is set by exchanging the momentum of a particle with high kinetic energy in the cold thermal bath, with the momentum of a particle with low kinetic energy in the hot thermal bath. The stationary value of the heat current is determined by the momentum exchange rate: our simulations were carried out for 4 ns with an exchange rate of 1000 time steps (each MD time step is 1 fs). To compute thermal conductance, one could directly use Kapitza’s definition $G = \rho/\alpha T^3$ under stationary conditions. However, to avoid spurious contact effects, we instead computed the thermal conductivity ($\kappa$) of the finite system using Fourier’s law $\kappa = \kappa_{bulk}$. After all, it is the distance between the thermal bath. Temperature profiles and heat flux as a function of time and are shown in Supplementary Fig. 9.

**Data availability:**

The data that support the findings of this study are available from the corresponding authors upon reasonable request.

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Author contributions

F.X., Y.C., and E.P. proposed the idea of measuring the impact of Li on MoS$_2$ thermal transport. A.S. and K.E.G. conceived the in situ spatio-temporal thermal transport experiments. F.X. fabricated the devices with assistance from H.W., J.Z., C.M., and J.S. A. S. developed the thermal conductance microscopy technique and performed TDTR experiments, electrochemical measurements, correlative AFM characterization, and analyzed all experimental data, with advice and input from K.E.G. D.D. designed modeling, S.C. performed DFT calculations, D.D. and D.S. performed MD simulations. A.S. synthesized the experimental and theoretical results and wrote the manuscript draft, with inputs from D.D. and E.P. Y.C., E.P., and K.E.G. supervised the project. All authors discussed the results, commented on the manuscript, and approved the final version.

Additional information

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