Detailed Simulink implementation for induction motor control based on space vector pulse width modulation SVPWM

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ABSTRACT
Space vector pulse width modulation (SVPWM) generates less harmonic distortion in the output voltage or currents, provides more efficient use of supply voltage and better voltage utilization compared with sine pulse width modulation (PWM). In this work, a detailed Simulink implementation for SVPWM for the open loop control of permanent magnet synchronous motor (PMSM) is presented. Results show the output of the blocks that assembles SVPWM besides the PMSM voltages and currents when exposing the motor to different load torques. The technique of SVPWM enables the load to respond to the change in external load torque. This technique also results in lower total harmonic distortion and better utilization of the direct current (DC) supply compared with traditional sinusoidal pulse width modulation sinusoidal pulse width modulation (SPWM).

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1. INTRODUCTION
Harmonic reduction in inverters and other electrical systems is important which results in higher operating efficiency. Increasing the switching frequency reduces harmonics in the output in pulse width modulation (PWM) inverters. Induction motors are mostly controlled using PWM inverters. Sinusoidal pulse width modulation (SPWM) is considered as efficient technique but limited to 0.78 modulation index. To work with modulation index up to 0.9 space vector pulse width modulation space vector pulse width modulation (SVPWM) is appropriate solution besides it reduces the total harmonic distortion (THD) and increases the fundamental component of the output inverter voltage [1]-[3]. Induction motors have poor dynamic response in correlation to the direct current (DC) motor. The SVPWM technique is widely used because of their better DC utilization and practical digital realization [4]-[7]. Comparing the performance of SPWM and SVPWM based inverter has been analyzed in MATLAB/Simulink from the analysis of speed regulation, torque ripple and total harmonic distortion [8]-[10]. In alternating current (AC) systems we are sensing AC signals to be used as control variables for finding the performance of the systems. If these ac control signals are replaced by DC quantities (called AC to DC frame transformation) a much better performance can be achieved because dc signals is simpler and having a better bandwidth. This transformation principle is based on converting ac signals to DC and then operate on these DC values and then back transform it to AC domain where they became AC signals.
2. DETERMINATION OF SVPWM SIMULINK BLOCK

2.1. Calculate $V_{\text{ref}}$ and its position $\alpha$

Figure 1 shows a three phase resultant vector $V_{\text{ref}}$ at certain instant of time. The reference vector is the control variable in space vector technique and is shifted by an angle $\alpha$ from the $a$ axis. This will transform three phase system to two phase system. This vector is first decomposed into two phase ac components resulting in $V_a$ and $V_b$. This is known as three phases to $\alpha - \beta$ transformation and will simplify the analysis. In order to transform these two-phase ac control variables into dc components another two axis are inserted and known as direct and quadrature axis ($d-q$ axis) which is shifted by a time varying angle ($\rho = \omega t$) and have the same frequency as $V_{\text{ref}}$ [11], [12]. This makes the $d-q$ axis to be synchronized with three phase supply frequency. Now if $V_{\text{ref}}$ is projected on $d-q$ axis at any instant of time then we obtain two constant values control variables (DC values), namely $V_d$ and $V_q$.

We could relate the components $V_d$ and $V_q$ in terms of the three phase input signals $V_a$, $V_b$ and $V_c$ in terms of the transformation matrix:

$$\begin{bmatrix} V_d \\ V_q \end{bmatrix} = \frac{2}{3} \begin{bmatrix} 1 & -\frac{1}{2} & -\frac{1}{2} \\ 0 & \sqrt{3}/2 & -\sqrt{3}/2 \end{bmatrix} \begin{bmatrix} V_a \\ V_b \\ V_c \end{bmatrix}$$

(1)

The reference vector $V_{\text{ref}}$ and its position inside the hexagon ($\alpha$) in Figure 1 can be found as (2).

$$V_{\text{ref}} = \sqrt{V_d^2 + V_q^2} \text{ where } \alpha_\rho = \tan^{-1} \frac{V_q}{V_d}$$

(2)

The reference vector will rotate with frequency equal to the system frequency ($f=50\text{Hz}$) with radian frequency $\omega = 2\pi f$ and its projection on the $d-q$ axis is constant. The Simulink box for step1 uses $f=50\text{Hz}$ and the input signal rms value =230Volt). The angle $\alpha$ which is varied from $-\pi$ to $\pi$ for one complete cycle. In order to avoid negative values a constant value of $\pi$ is added in the Simulink model so the variation in $\alpha$ is considered between 0 to 2$\pi$. Figure 2 shows the implementation of $V_{\text{ref}}$ and its position $\alpha$.

2.2. Calculation of the modulation index and identifying the hexagon sector

The modulation index controls the average output voltage of the three-phase inverter. It is the relation between the reference vector and the dc supply voltage $V_{dc}$ of the inverter. The modulation index ($m$) for SVPWM can be found from $m = V_{\text{ref}}/(2/3 V_{dc})$. The Hexagon has six sectors each of them house $\pi/3$ radians. For example, sector 1 is the area between vectors $V_1$ and $V_2$ (i.e. $0 \leq \text{angle} \leq 1.0472\text{ rad}$). Figure 3 (see Appendix) shows the Simulink implementation of step2.
Detailed Simulink implementation for induction motor control based on space vector … (Taha A. Hussein)

2.3. Operating times for insulated gate bipolar junction transistors (IGBJT’s) switches

To get the desired output voltage and frequency the status of each IGBJT transistor switch must be changed between on and off conditions. In three phase inverter topology there are six switches, three of them are connected to the upper leg while the other three are connected to the lower leg as shown previously in Figure 4. In order to reduce switching losses at the output the pattern of switching states is controlled in such a way that the upper leg switches are allowed to transit to the next operating state with only one change in states of the switches. The statues of the three switches on the lower leg are complementary to the switches at the upper leg. There are eight possible combination states for the three upper leg switches, six of them are called the active state i.e. 100,110,010,001,011 and 101 where 1 is considered as ON while 0 is considered as OFF and the remaining two states are 000 and 111 which are added to enable the switches to transit their states as mentioned before. These two states are called passive states because the output voltage is zero. Therefore, the corresponding passive state vectors are called the zero vectors and are located at the origin of the hexagon as shown in Figure 4 (b). The eight states are tabulated in Table 1 with their phase voltage and the corresponding space vector values for the case of three-phase inverter with 180° conduction [13]-[16].

The area enclosed between two adjacent vectors form one sector. For example, sector 1 is the area between Vectors $V_1$ and $V_2$. The time taken by sector 1 and sector 2 for example are collected in Table 2 and shown in Figure 5. The procedure is repeated for the other remaining sectors. It is allowed to turn only one of the switches ON or OFF on the upper leg to reduce switching losses. The period of the operating frequency is defined as $T_Z$ and if we consider sector 1 the on time for Vectors $V_1$ and $V_2$ and the null vector (V₀ or V₇) are $T_1$, $T_2$ and $T_{0/2}$ respectively. The on time for the upper leg switches calculated from null state (000) toward the centre of Figure 4 which is called right aligned is the same as the time calculated from the null state (111) from the center of Figure 4 to the end of the Figure 4 which is called left aligned. The on time for $S_1$ is $T_1 + T_2 + T_{0/2}$ while in sector 2 is $T_2 + T_{0/2}$. Following the above constrains on the six switches the time pattern is shown in Table 3.

Figure 2. Calculation of $V_{ref}$ and its position $\alpha$; (a) sim. block (b) $V_{ref}$, $V_dV_q$, and (c) angle $\alpha$ for $V_{ref}$
Figure 4. Switches states ($S_1$ to $S_6$ vs $T_1$, $T_2$ and $T_o$); (a) for sector 1 and (b) for sector 2

Table 1. Eight hexagon states with phase voltages and space vectors

| Variable | Phase voltages | Space vector |
|----------|----------------|--------------|
| A        | b              | Van          | Vbn          | $S = V_d + jW_q$ | $\psi$          |
| 1        | 0              | 0            | 2/3          | 1/3            | 0/0            |
| 1        | 0              | 1/3          | -1/3         | 2/3            | 2/3 or 60°     |
| 0        | 1              | 0            | 2/3          | 1/3            | 2/3 or 60°     |
| 0        | 1              | -1/3         | 1/3          | 2/3            | 2/3 or 60°     |
| 1        | 0              | -1/3         | 1/3          | 2/3            | 2/3 or 60°     |
| 1        | 0              | -2/3         | 3/3          | 2/3            | 2/3 or 60°     |
| 0        | 1              | -1/3         | -1/3         | 1/3            | 2/3 or 60°     |
| 1        | 0              | 1/3          | -1/3         | 1/3            | 2/3 or 60°     |

Table 2. Time taken by vectors $V_o$ to $V_v$ in sectors 1 and sector 2

| Sector 1 | Sector 2 |
|----------|----------|
| Vector   | State    | Time     | Vector   | State    | Time     |
| $V_o$    | 000      | To/2     | $V_o$    | 000      | To/2     |
| $V_1$    | 100      | T1       | $V_1$    | 010      | T2       |
| $V_2$    | 110      | T2       | $V_2$    | 110      | T1       |
| $V_v$    | 111      | To/2     | $V_v$    | 111      | To/2     |

Table 3. The complete table for the all-timing switches in the six sectors

| Sector 1 | Sector 2 | Sector 3 | Sector 4 | Sector 5 | Sector 6 |
|----------|----------|----------|----------|----------|----------|
| $S_1$    | $T_1 + T_2 + T_o/2$ | $S_2$    | $T_1 + T_2 + T_o/2$ | $S_3$    | $T_1 + T_2 + T_o/2$ | $S_5$    | $T_1 + T_2 + T_o/2$ |
| $S_3$    | $T_1 + T_o/2$      | $S_4$    | $T_1 + T_o/2$      | $S_5$    | $T_1 + T_o/2$      | $S_6$    | $T_1 + T_o/2$      | $S_2$    | $T_1 + T_o/2$      |

To calculate $T_1$, $T_2$ and $T_o/2$ we refer to Figure 4. In sector 1 the area enclosed by vectors $V_1$ and $V_2$ can be found as:

$$
\int_{0}^{T_2} V_{ref} \, dt = \int_{0}^{T_1} V_1 \, dt + \int_{T_1}^{T_2} V_2 \, dt + \int_{T_1+T_2}^{T_2} V_o \, dt
$$

$$
V_{ref} T_x = V_1 T_1 + V_2 T_2 + V_o (T_2 - T_1)
$$
\[ V_{\text{ref}} T_2 \sin \alpha = \frac{2}{3} V_s T_1 \sin (0) + \frac{2}{3} V_s T_2 \sin \left( \frac{\pi}{3} \right) \] (5)

\[ T_2 = a \frac{\sin a}{\sin (\frac{\pi}{3})} T_z \] (6)

Where \( a = \frac{V_{\text{ref}}}{V_s} \) is the modulation index

The \( d \)-axis component is:

\[ V_{\text{ref}} T_x \cos \alpha = \frac{2}{3} V_s T_1 \cos (0) + \frac{2}{3} V_s T_2 \cos \left( \frac{\pi}{3} \right) \] (8)

\[ T_1 = a \frac{\sin \left( \frac{\pi}{3} - a \right)}{\sin (\frac{\pi}{3})} T_z \] (9)

\[ T_0 = T_z - T_1 - T_2 \] (10)

The general form of \( T_1 \) and \( T_2 \) for all the six sectors can be written as

\[ T_1 = a \frac{\sin \left( \frac{k \pi}{3} - a \right)}{\sin (\frac{\pi}{3})} T_z \] (11)

\[ T_2 = a \frac{\sin \left( a - \left( \frac{k-1}{3} \right) \pi \right)}{\sin (\frac{\pi}{3})} T_z \] (12)

where \( k \) is the sector number. Figure 5 (a) shows the Simulink model for calculating \( T_1 \), \( T_2 \) and \( T_3 \). The output of the model (i.e. \( T_1 \), \( T_2 \) and \( T_0 \)) is shown in Figure 5 (b).

Figure 5. Operating times for IGBT’s switches; (a) simulink block and (b) \( T_1 \), \( T_2 \) and \( T_0 \)

2.4. Determination of the control signals

The time segments for switches S1, S2 and S3 are handed to a multi-port switch as shown in Figure 6 (a) for identifying the sector in the corresponding time instant. The output is the three controlled signals as shown in Figure 6 (b). These signals are then compared with the triangular carrier signal to form the space vector pulse width modulated pulses as shown in Figure 6 (c). These pulses are the triggering signals to the IGBTs in the three-phase inverter. The Simulink blocks obtaining SVPWM three phase inverter is shown in Figure 6 (d).

Detailed Simulink implementation for induction motor control based on space vector ... (Taha A. Hussein)
Figure 6. (a) Identifying the sector, (b) Controlled signals, (c) IGBT’S signals, and (d) Simulink blocks
2.5. Determination of the control signals

The voltage source inverter is best tested when loaded with induction motor because of its dynamic characteristics [17-20]. A three-phase inverter with six IGBT transistors is controlled by the pulses shown in Figure 6 (c). These pulses are PWM signals results when comparing the three phase-controlled signals in Figure 6 (b) with repeating signal (carrier) with frequency \( = 1 \) kHz or more to ensure the elimination of more harmonics. It is worth noting that the amplitude of the carrier must be equal to the amplitude of the controlled signals \( = 0.02 \) in this work. The Simulink block diagram with the three phase output voltages are shown in Figures 7 (a) and (b) respectively.

Figure 7. Complete SVPWM with induction motor load; (a) Simulink model and (b) Three-phase inverter output voltages

The load is asynchronous machine squirrel cage type and has the parameters 5.4HP (4KW), 400V, 50Hz, 1430 RPM and a slip\( = 1430/1500 = 0.9534 \). The load is subjected to a change of Torque equal to 20Nm at step time \( = 0.5 \) sec, then this external torque is removed at step time\( = 1 \) sec. Figures 8 (a), (b), and (c) show speed, electrical torque and the three phase currents. The effect of external torque is decreasing the speed, increasing the electrical torque and increasing the three phase currents.

The technique of SVPWM results in lower total harmonic distortion and better utilization of the DC supply compared with traditional sinusoidal pulse width modulation SPWM as seen in Figures 9 (a) and (b) and tabulated in Table 4. Beside the application of SVPWM on the traditional three phase inverter, SVPWM find efficient application in the area of z-source inverter which widen the application of SVPWM on modern electrical systems [21], [22]. The technique of SVPWM can also be implemented without using zero vectors [23]. For high power applications and reduced harmonics elimination multilevel inverters can be implemented with SVPWM [24], [25]. For the purpose of long-time operation and reliability the SVPWM Simulink model is subjected to multiple change in load torque at the instants of 2 and 4 seconds of time, the system responds correctly as expected and shown in Figure 10.

Detailed Simulink implementation for induction motor control based on space vector ... (Taha A. Hussein)
Figure 8. Effect of external torque on induction motor; (a) motor speed, (b) electrical torque, and (c) three-phase load current

Figure 9. THD and fundamental components values; (a) SPWM and (b) SVPWM
Detailed Simulink implementation for induction motor control based on space vector ... (Taha A. Hussein)
Figure 3. Modulation index and hexagon: (b) sector determination, (c) sim. block for sector and m index (continue)
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