Harmonious Coexistence of Structured Weight Pruning and Ternarization for Deep Neural Networks

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Abstract

Deep convolutional neural network (DNN) has demonstrated phenomenal success and been widely used in many computer vision tasks. However, its enormous model size and high computing complexity prohibits its wide deployment into resource limited embedded system, such as FPGA and mGPU. As the two most widely adopted model compression techniques, weight pruning and quantization compress DNN model through introducing weight sparsity (i.e., forcing partial weights as zeros) and quantizing weights into limited bit-width values, respectively. Although there are works attempting to combine the weight pruning and quantization, we still observe disharmony between weight pruning and quantization, especially when more aggressive compression schemes (e.g., Structured pruning and low bit-width quantization) are used. In this work, taking FPGA as the test computing platform and Processing Elements (PE) as the basic parallel computing unit, we first propose a PE-wise structured pruning scheme, which introduces weight sparsification with considering of the architecture of PE. In addition, we integrate it with an optimized weight ternarization approach which quantizes weights into ternary values ($\{-1, 0, +1\}$), thus converting the dominant convolution operations in DNN from multiplication-and-accumulation (MAC) to addition-only, as well as compressing the original model (from 32-bit floating point to 2-bit ternary representation) by at least 16 times. Then, we investigate and solve the coexistence issue between PE-wise Structured pruning and ternarization, through proposing a Weight Penalty Clipping (WPC) technique with self-adapting threshold. Our experiment shows that the fusion of our proposed techniques can achieve the best state-of-the-art $\sim 21 \times$ PE-wise structured compression rate with merely $1.74\%/0.94\%$ (top-1/top-5) accuracy degradation of ResNet-18 on ImageNet dataset.

Table 1: Comparison of DNN compression techniques. Comp. and Acc. are abbreviation of Compression and Accuracy. Specific values are reported in Table 6.

| Technique               | Structured sparsity | Quantized weight | Comp. rate | Acc. drop |
|-------------------------|---------------------|------------------|------------|-----------|
| Group-Lasso pruning     | ✓                   | ✓                | Low        | Low       |
| Weight ternarization     | ✓                   | ✓                | Moderate   | Low       |
| Naive combination       | ✓                   | ✓                | Moderate   | Moderate  |
| This work               | ✓                   | ✓                | High       | Low       |

Weight pruning reduces model size via making partial weights as zero-value. Previous studies of weight pruning can be generally put into two categories: non-structured pruning (Han et al. 2015; Han, Mao, and Dally 2015) and structured pruning (Lebedev and Lempitsky 2016; Liu et al. 2015; Wen et al. 2016). The main difference between these two counterparts is the regularity of sparse weight pattern after pruning. The non-structured pruning attempts to generate highly irregular sparse weight pattern, targeting to achieve highest sparsity, which makes it extremely difficult to encode sparse weight efficiently due to the sparse indexing. Even though the non-structured pruning normally shows less compression accuracy degradation owing to its relatively higher pruning flexibility, its performance improvement on hardware deployment is not quite tempting. In contrast to that, the structured pruning introduces weight sparsification in a regular fashion (e.g., kernel-wise/channel-wise (Wen et al. 2016)), which makes it more hardware friendly for hardware deployment.
In addition, weights quantization is to quantize the weights of DNN into multiple discrete levels, where those quantized levels can be represented by the bit-width limited binary string. Different from weight pruning which requires crafting hardware for efficient DNN inference, quantization of weight is more straight-forward and easy to implement. In this work, we adopt the ternary value \((-1, 0, +1)\) as the quantization scheme, to simplify the convolution computation complexity from MAC operations to \(add/sub\)-only and reduce the model size. In order to enjoy the benefits of both pruning and quantization, there are related works (Han, Mao, and Dally 2015) have shown the combination of them can achieve negligible accuracy degradation, but with moderate compression rate (e.g., 8-bit quantization bit-width) on highly redundant DNN architectures (e.g., AlexNet). Thus, those previous works fail to provide detailed insight for more aggressive compression method on the state-of-the-art DNN architectures.

In order to achieve a ternarized DNN with structured sparse weight pattern, the straightforward method is to combine the pruning and ternary quantization. However, when using the popular Group Lasso based pruning method, we found that a naive combination causes non-negligible accuracy loss. From weight pruning side, the group lasso regularization iteratively shrinks the weight distribution, meaning decrease of mean and variance of weights during training (Wen et al. 2016). However, weight ternarization prefers to loose the weight distribution (He 2019). This mismatch between weight ternarization and pruning makes network training difficult to reach optimum.

In this work, our objective is to effectively integrate the structured weight pruning and ternarization to boost the performance of DNN inference on hardware platform, with ultra-small accuracy degradation. Since we expect the improvement is maximized and valid both theoretically and applicable on practical hardware implementation, we conduct the hardware/software co-design to address various technical issues. For clarification, we summarize our main contributions in a Problem&Solution format as follows:

- **Problem 1: The structured pruning is decoupled from the hardware architecture of DNN accelerator.** Most of the previous works perform the structured pruning in the kernel-wise/channel-wise fashion (Wen et al. 2016). In our work, in order to maximize the efficiency on hardware-based DNN accelerator, we propose the processing element-wise (PE-wise) structured pruning (i.e., length of a group of weights to be pruned is identical to the capacity of PE), which is the computation core in hardware platform (i.e. FPGA, ASIC). Thus, some PEs can be easily skipped without internal modification.

- **Problem 2: There is disharmony between structured pruning and ternarization on weights of DNN, where the naive combination shows severe accuracy degradation.** For structured pruning, we adopt PE-wise group-Lasso as the weight penalty in the loss function, which tends to make entire group of weights approach to zero. For weight ternarization, the quantizer will make the close-to-zero weights tend to zero, while pushing the other weights close to positive/negative one (-1/+1) against zero. Such opposite weight evolution trend leads to a convergence dilemma. As the countermeasure, we propose the Self-Adjustable Weight Penalty Clipping (SA-WPC) which dynamically suspends penalty for weights with large absolute values. Therefore, we achieve a harmonious coexistence of structured pruning and ternarization, achieving best state-of-the-art performance. Moreover, we evaluate the performance of our proposed algorithm on both software and hardware.

### Background and Related Works

**Pruning**

Pruning is one of the most well-known neural network compression techniques, which enforces partial of weights be zero for both model size reduction and computation simplification (Han et al. 2015). According to the shape of weight sparsity pattern, the existing pruning technique can be generally categorized into two branches: non-structured (Han et al. 2015; Han, Mao, and Dally 2015; Srinivas and Babu 2015; Molchanov, Ashukha, and Vetrov 2017; Louizos, Welling, and Kingma 2017) and structured (Li et al. 2016; Liu et al. 2017; Lebedev and Lempitsky 2016; Wen et al. 2016; Alvarez and Salzmann 2016; He, Zhang, and Sun 2017). As the example shown in Fig. 1, structured pruning method leads to sparsity patterns (color in grey) with highly regular shapes. The division of aforementioned pruning methods are mainly hardware-oriented, where the DNN inference hardware (e.g., GPU/FPGA accelerators) can barely benefit from the random sparse pattern (Fig. 1 left) of structured pruned DNN. On the contrary, the structure pruning is a more feasible solution for hardware inference acceleration, through leveraging the regular sparsity pattern (Fig. 1 right).

**Non-structured pruning.** The study of non-structured pruning is originated from optimal brain damage/surgeon (LeCun, Denker, and Solla 1990; Hassibi and Stork 1993), which further explored by Han et al. in (Han et al. 2015; Han, Mao, and Dally 2015) on DNN. A simple pruning
where this equation uses the identical notations as in Eq. (1). The threshold \( \alpha_l \) can be expressed as:

\[
\hat{w}_{l,i} = \text{Tern}(w_{l,i}, \Delta_l) = \begin{cases} 
+1 & w_{l,i} > \Delta_l \\
0 & -\Delta_l \leq w_{l,i} \leq \Delta_l \\
-1 & w_{l,i} < -\Delta_l 
\end{cases}
\]

where \( \hat{w}_{l,i} \) is the \( i \)-th ternarized weight element in \( l \)-th layer. \( \alpha_l \) is the layer-wise scaling coefficient (i.e., quantized value). The threshold \( \Delta_l = 0.05 \cdot \max(\|W_l\|) \). As other quantization work, the Straight-Through Estimator (STE) (Bengio, Léonard, and Courville 2013) method is used as the countermeasure against non-differential issue of staircase quantization function (Eq. (2)). Thus, with the given vectorized input \( x \) and target \( t \), the optimization of ternarized DNN can be written as

\[
\min_{\{W_l\}_{l=1}^L} \mathcal{L}(f(x; \{\hat{W}_l\}_{l=1}^L), t) \\
\text{s.t.} \{W_l\}_{l=1}^L = \text{Tern}(\{W_l\}_{l=1}^L)
\]

where this equation uses the identical notations as in Eq. (1).
Methodology

Problem formalization

As described in the related work section, both group-lasso based structured pruning and weight ternarization show great performance in compressing the model while maintaining the inference accuracy w.r.t the full-precision baseline. For maximizing the model compression rate and enjoying the benefits from both methods, we first naively combine them together, where the DNN training process can be described as minimizing the following loss function:

\[
\hat{\mathcal{L}} = \mathcal{L}(f(x; \text{Tern}(\{W_l\}_{l=1}^L), t) + \lambda \sum_{l=1}^{L} \sum_{i=1}^{G_l} P(W_{l,i})
\]

Nevertheless, such naive combination leads to severe accuracy degradation without significantly improving the compression rate.

In order to identify the problem, we examine the weight distribution of different training configurations, where the corresponding histograms are depicted in Fig. 2. The results show that the group Lasso based structure pruning still acts like a normal regularization term which forces the entire weights toward smaller values (i.e., small standard deviation \(\sigma\) with zero mean \(\mu\)), in comparison to the full-precision baseline. On the contrary, weight ternarization makes negligible change on \(\sigma\) w.r.t the baseline. We also observe that, naively combining the structured pruning and ternarization method leads the \(\sigma\) converge to some value between Group-Lasso and weight ternarization counterparts. Such observation inspires us about the potential cause of accuracy degradation when naively combining two methods:

- Is that applying the group Lasso (i.e., weight penalty) upon entire weights contradictory to the weight ternarization method?
- Whether maintaining the weight distribution close to the original ternary counterpart helpful to mitigate the accuracy degradation?

Based on those inspirations, we propose a Weight Penalty Clipping (WPC) technique with self-adapting threshold as the countermeasure to address the coexistence issue between group Lasso based structure pruning and ternarization, which is specified in the following subsection.

Weight Penalty Clipping with self-adapting threshold

To address above discussed issue, further adjustment is made on the intra-group \(L_2\)-norm term in Eq. (1), thus the Eq. (5) can be rewritten as:

\[
\hat{\mathcal{L}} = \mathcal{L}(f(x; \text{Tern}(\{W_l\}_{l=1}^L), t)
+ \lambda \sum_{l=1}^{L} \sum_{i=1}^{G_l} \min(||W_{l,i}||_2; \delta_l)
\]

where \(\delta_l\) denotes the layer-wise self-adapting clipping threshold, which is utilized to mitigate the intra-group \(L_2\)-norm penalty on large weights, and \(a\) is scaling coefficient. Note that, once the intra-group \(L_2\)-norm penalty of \(W_{l,i}\) is clipped, the inter-group \(L_2\)-norm penalty is clipped as well. We name such method as the weight penalty clipping. Fig. 3 shows the processing of WPC. In each training iteration, the updated weights are used to the loss function as shown in Eq. (7). Then after intra-group \(L_2\)-norm calculation, by comparing with a threshold \(\delta_l\), WPC will decide whether the corresponding \(||W_{l,i}||_2\) will be used on loss function and go backward. Considering two cases:

- When \(||W_{l,i}||_2 \geq \delta_l\), it indicates that weights in \(W_{l,i}\) are relatively large (i.e., important) which are not supposed to be pruned by the group-Lasso term in 6. Then, the weight penalty clipping is performed which replaces the weight penalty of \(||W_{l,i}||_2\) in \(\hat{\mathcal{L}}\) with \(\delta_l\). Hereby, we have to highlight that \(\delta_l\) is treated as a constant, where its calculation is removed from the backward computation graph.
- When \(||W_{l,i}||_2 < \delta_l\), we keep the weight penalty of \(||W_{l,i}||_2\) in its original value, thus the group Lasso term can continuously affect \(W_{l,i}\) and prune the weights in group-wise fashion.

Our simulation in the later sections will show that the proposed WPC with self-adapting threshold is helpful to mitigate the disharmony between structured pruning (i.e., group Lasso) and weight ternarization.

PE-wise structured pruning

The performance (e.g., accuracy) of DNN pruned by group Lasso based method highly relies on the defined
group shape, where various group shape (e.g., channel/filter/depth/etc.) has been explored in (Wen et al. 2016). Normally, using large group capacity (i.e., number of weights per group) leads to either low group sparsity or low inference accuracy. Meanwhile, using small group capacity will be beneficial to both group sparsity and accuracy, however the pruned DNN may hardly be accelerated by targeted hardware. For balancing the DNN performance after structured pruning and inference efficiency on targeted hardware, we propose to make the group shape identical to the Processing Element (PE) of target hardware. PE is defined as the basic computing unit in modern DNN accelerator (FPGA, ASIC or GPU). Through proper hardware design, DNN accelerator can efficiently utilize the PE-wise sparsity pattern, and further speed up is observed by our later experiments.

Due to the limited hardware resource, fully paralleled computation can hardly be achieved. For PE-wise structured pruning, we define the group size equals to the capacity of one PE. In a standard convolution layer, weights are stored in a 4-D tensor \( W ∈ \mathbb{R}^{N × C × K_h × K_w} \), where \( N, C, K_h, K_w \) denotes the output channel, input channel, kernel height and width in current layer, respectively. As shown in Fig. 4, the size of the PE-wise pruning is between the filter \((C × K_h × K_w)\) pruning and channel \((N)\) pruning. In addition, in the terms of hardware deployment, the high dimensional convolution operation is often reduced to matrix multiplication, which reshapes the 4D weight tensor to 2D matrix with the size of \((N, C × K_h × K_w)\). Thus, defining structured sparsity groups in channel-wise \((C)\) and shape-wise \((K_h × K_w)\) does not match well with the practical hardware implementation. We choose the capacity of PE as the group to perform group-wise pruning, with size of \( C_g × K_h × K_w \). \( C_g \) can be divided by \( C \). In this case, one PE could do computation between one group of weight and corresponding partial feature maps vector with size of \( C_g × H × W \).

**Experiment and Discussion**

**Experiment setup**

**Dataset and network structure**  In this work, we take the classic image classification task as example to examine the performance of our proposed technique. Two dataset are used in this work, which are CIFAR-10 (Krizhevsky and Hinton 2009) and ImageNet (Deng et al. 2009). CIFAR-10 contains 50K training samples and 10K test samples with \( 32 × 32 \) image size. For CIFAR-10, we adopt the ResNet-20/32/44/56 (He et al. 2016). We train the network using momentum SGD optimizer, where the initial learning rate is 0.1, which scaled by 0.1 at epoch 80 and 120 respectively. The data augmentation is identical as the configuration adopted in (He et al. 2016).

In addition, ImageNet contains 1.2 million training images and 50 thousands validation images, which are labeled with 1000 categories. For the data pre-processing, we choose the scheme adopted by ResNet (He et al. 2016). We adopt the ResNet-18 and AlexNet. We train the network using Adam optimizer, where the initial learning rate is 0.0001, which scaled by 0.2 at epoch 30, 40, 45 respectively. Similar with other quantization works, such as (Lin, Zhao, and Pan 2019), we keep the first and last layer in full precision (i.e. 32-bit float).

**Compression rate definition**  To fully utilize structured pruning in hardware perspective, we could use a binary indexer to index which PE group are all zeros values. By doing this, we just need to store the particular weight groups that have non-zeros values in memory. Also, the size of the binary indexer is negligible comparing with the size of weights. Thus, we formulate the compression rate of weight \( C \) as:

\[
C = \frac{32}{(1 - G_s)} \cdot n
\]

where \( G_s \) means the group sparsity which is the fraction of number of groups with all zero values over layers and \( n \) is the bit-width of model which should be 2 in here since we encode weights in binary format. 32 represents the bit-width of the full precision. It is worthy noting that for calculating the compression rate, we only consider group sparsity, since this matrix that we can real implementation in the hardware. In addition, all the compression rate of our method shown below are defined by the Eq. (8).

**Results**

**CIFAR-10 Experiment**  The proposed method is tested on ResNet-20/32/44/56. The size of PE group is set to be \( 16 × 3 × 3 \) for all layers of all kinds of networks. From the result of inference accuracy which are listed in Table 2, it can be seen that all these four kinds of ResNet models achieves less than 1% accuracy loss comparing with floating point baseline. Also, it shows that a more compact neural network, like ResNet-20, is more likely to encounter accuracy loss, owing to the network capacity is hampered by this aggressive model compression.

**ImageNet Experiment**  Our proposed method is evaluated on ResNet18 and AlexNet. The size of PE group is setted to be \( 64 × 3 × 3 \) for all convolutional layers.

**AlexNet**  To better show the effectiveness of our method, besides comparing with non-structured ternarization method as mentioned above, we also compare with related structured pruning work (Wen et al. 2016). However, the enhancement...
Table 3: Simulation result of structured pruning for AlexNet (Krizhevsky, Sutskever, and Hinton 2012) on ImageNet dataset. FP indicates full-precision (32-bit floating-point) and Tern. indicates ternary weights. Note that, Acc. and Comp. are abbreviation of accuracy and compression.

| Method       | Weight format    | Top-1 Acc. gap (%) | Sparsity metrics       | Layer index | Comp. rate |
|--------------|------------------|--------------------|------------------------|-------------|------------|
| SSL (Wen et al. 2016) | Floating-point (32-bit) | 2.06               | Column (%) Row (%) Layer (%) | Conv1 Conv2 Conv3 Conv4 Conv5 Conv2-5 |
|               |                  |                    | 0.0 63.2 76.9 84.7 80.7 -  | ~ 6.4× |
| This work    | Ternary (2-bit)  | 3.23               | Group (%) In-group (%) Layer (%) | 24.3 36.5 47.3 45.0 24.2 35.46  | ~ 24.7× |

Table 4: Inference accuracy (%) of ResNet18 on ImageNet

| Quan scheme | First layer | Last layer | Accuracy (top1/top5) | Comp. rate |
|-------------|-------------|------------|-----------------------|------------|
| Baseline    | FP          | FP         | 69.75/89.07           | 1×         |
| BWN         | Bin.        | FP         | 60.8/83.0 (∼ 32×)     |            |
| ABC-Net     | Bin.        | FP         | 68.3/87.9 (∼ 6.4×)    |            |
| ADMM        |             | FP         | 64.8/86.2 (∼ 32×)     |            |
| TWN         | Tern.       | FP         | 61.8/84.2 (∼ 16×)     |            |
| TTN         | Tern.       | FP         | 66.6/87.2 (∼ 16×)     |            |
| ADMM        | Tern.       | FP         | 67.0/87.5 (∼ 16×)     |            |
| (He 2019)   | Tern.       | FP         | 67.95/88.0 (∼ 16×)    |            |
| **Ours**    | Tern.       | FP         | **68.01/88.13** (∼ 21.3×) |            |

Table 5: Ablation study on CIFAR-10

| Method       | Pruning | Naive combine | Ours |
|--------------|---------|---------------|------|
| **ResNet-20** | 91.62   | 91.1          | 90.01 | 90.89 |
| Overall sparsity | 49     | 61            | 70   | 50   |
| Group sparsity   | -      | 44            | 18   | 25   |
| Comp. rate       | ~ 16×  | ~ 1.78       | ~ 19.5× | ~ 19.5 |
| **ResNet-32** | 92.48   | 91.88         | 90.68 | 91.64 |
| Overall sparsity | 48     | 40            | 74   | 58   |
| Group sparsity   | 34     | 28            | 43   |      |
| Comp. rate       | ~ 16×  | ~ 1.5        | ~ 22.2× | ~ 28.1 |
| **ResNet-44** | 92.78   | 92.29         | 91.15 | 91.98 |
| Overall sparsity | 55     | 58            | 80   | 64   |
| Group sparsity   | -      | 46            | 21   | 44   |
| Comp. rate       | ~ 16×  | ~ 1.85       | ~ 20.2× | ~ 28.6 |
| **ResNet-56** | 93.1    | 92.86         | 92.01 | 92.85 |
| Overall sparsity | 52     | 67            | 82   | 67   |
| Group sparsity   | -      | 28            | 42   | 55   |
| Comp. rate       | ~ 16×  | ~ 1.39       | ~ 27.6× | ~ 35.6 |

Ablation study and discussion

As proposed method is a combination of these two different compression techniques, to better evaluate the effectiveness, we do comparison with three different cases: weight ternarization, Group-Lasso pruning and directly combining these two techniques (naive combine) on both Cifar-10 and ImageNet dataset respectively. The results are shown in Table 5 and Table 6. Overall sparsity means the ratio of the individual zero values within the whole weight tensors. Differently, group sparsity represents the ratio of the number of PE-wise groups that are all zeros. We observe that when comparing with naive combine, our method achieves smaller overall sparsity, but larger useful group sparsity. This result demonstrates the effectiveness of the proposed method. By using weight penalty clipping with self-adapting threshold, only part of the weight groups which has smaller norm values will be regularized and rest of the weight groups keeps unchanged, focuses on the prune of unimportant weight groups and keeps rest of the weight groups unchanged.

Evolution of clipping threshold

In this work, the value of clipping threshold and sparsity are two important factors which both are dynamic during the whole training processing. Fig. 5 shows the dynamics of sparsity and clipping threshold during training. It can be seen that the sparsity increases greatly at the first 80 epochs and then becomes covered. Meanwhile, the dynamic of clipping threshold has...
the similar pattern, the only difference is its value becomes smaller. It is because that the initial learning rate is large, which makes the Group Lasso regularizer have a strong effect on the weights. Moreover, we observe that later layers have larger sparsity than front layers.

PE capacity vs. structured sparsity ratio  To evaluate the effectiveness of PE-group size, we select 5 different PE sizes on ResNet-20 (eg., PE-8 means the PE-size is $8 \times K_h \times K_w$), which are trained with the same hyper-parameter setting. As shown in Table 7, it can be seen that smaller PE-size is easier to be regularized which leads to larger sparsity and more accuracy loss under the same hyper-parameter setting.

Table 7: PE capacity versus structured sparsity ratio of ResNet-20 trained on CIFAR-10 dataset.

| PE | Accuracy (%) | Overall sparsity (%) | Group sparsity (%) |
|----|--------------|----------------------|--------------------|
| PE-16 | 90.89 | 90.56 | 90.29 | 89.93 | 89.66 |
| PE-8 | 90.89 | 90.56 | 90.29 | 89.93 | 89.66 |
| PE-4 | 90.89 | 90.56 | 90.29 | 89.93 | 89.66 |
| PE-2 | 90.89 | 90.56 | 90.29 | 89.93 | 89.66 |
| PE-1 | 90.89 | 90.56 | 90.29 | 89.93 | 89.66 |

Relation to the norm based criterion  (He et al. 2019) mentioned there are two requirements should be met for pruning: (1) the norm deviation of the filters should be large; (2) the minimum norm of the filters should be small. As shown in Fig.6, comparing with naive combine, norm distribution of our method has larger norm deviation, which means the norm distribution becomes suitable for pruning gradually during training. Obviously, it’s easier to separate the important and unimportant weight groups according the absolute value of the norm for our method.

FPGA implementation  To evaluate the performance of our proposed method in the real-word inference hardware accelerator, we deploy three representative convolutional layers of ResNet20 into a FPGA board. We compare our method with weight-ternarization-only method which also replaces MAC to add/sub operations, but without PE-wise pruning. To fairly compare, the bit-width of feature map is 16-bit fixed point number for both methods. The detailed setup and speedup is shown in Table 8. It is obvious that great speed-up is measured in real FPGA hardware implementation with our proposed PE-wise structured ternary network compared with non-structured ternary network.
Table 8: Convolutional layer implementation setup and speed up on FPGA

| Layer | Weight size | PE size | Non-sparsity groups | Speedup |
|-------|-------------|---------|---------------------|---------|
| Stage 1 | (16, 16, 3, 3) | 16 × 3 × 3 | 9 | 1.56x |
| Stage 2 | (32, 32, 3, 3) | 16 × 3 × 3 | 32 | 1.90x |
| Stage 3 | (64, 64, 3, 3) | 16 × 3 × 3 | 119 | 1.74x |

**Conclusions**

In this paper, we aim to integrate the Group Lasso based pruning and ternarization to maximum the efficiency of DNN hardware deployment. We firstly define the PE-wise sparsity, then based on the observation of the disharmony between these two methods, a new method is proposed, named weight penalty clipping with adjustable threshold to solve this problem. Moreover, comparing with related works in weight pruning and ternarization, we could achieve best state-of-the-art accuracy with a much higher compression rate.

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