Effect of Low Pressure on Surface Roughness and Morphological Defects of 4H-SiC Epitaxial Layers

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Abstract: In this work, 4H-SiC epilayers are performed on 4° off-axis substrates under low pressure condition by horizontal hot wall chemical vapor deposition (HWCVD) with a standard chemistry of silane-propane-hydrogen, which focuses on the effects of growth pressure on morphology, basal plane dislocations (BPDs) and crystalline quality. It is found that morphological defects reduce with the decreasing of growth pressure, since the surface diffusion length of absorbed adatoms increases under low growth pressure, which suppresses the nucleation of adatoms on terraces and the formation of morphological defects. However, as the surface diffusion length increases under low growth pressure, the difference of growth velocity at steps is enhanced, which leads to the extension of the steps’ width and the formation of step-bunching. Besides variation of surface diffusion length, the phenomenon described above can be correlated with different dominate modes for the minimization of surface energy at varied growth pressure. Because of the contrary influence of increased C/Si ratio and enhanced step-flow growth on the propagation of BPDs, the dislocation densities of BPDs and threading edge dislocations (TEDs) in epilayers grown at varied pressures remain basically unchanged. The crystalline quality is almost independent of growth pressure based on high resolution X-ray diffraction (HRXRD) measurements.

Keywords: low pressure growth; 4H-SiC homoepitaxy; surface diffusion length; minimization mode of surface energy

1. Introduction

Because of higher power conversion efficiency, 4H Silicon carbide (4H-SiC) devices have benefits in wind turbine power converters [1–3], photovoltaic inverters [4,5] and hybrid electric vehicles [6–8]. Among 4H-SiC devices, 4H-SiC metal-oxide-semiconductor field effect transistors (MOSFETs) and Schottky barrier diodes (SBDs) with voltage ratings of 1.2 kV and 1.7 kV are the most widely used in power applications. Homoepitaxial growth of 4H-SiC layers is a key step in the fabrication of 4H-SiC power devices since high-quality 4H-SiC epilayers with low defects density, low doping concentration and good morphology are necessary [9]. The most established technique for growth of epitaxial layers of SiC is chemical vapor deposition (CVD) using silane (SiH₄), and propane (C₃H₈) as precursors. A significant topic is the defects in epilayers, which impede devices’ performance. Epitaxial layers grown at high growth rate on 4° off-axis substrates suffer from step-bunching and triangular defects (TDs) [10–12]. It is reported that step-bunching causes an increase in the leakage current of SBDs [13] and has a negative impact on the channel mobility or the oxide breakdown characteristics in MOSFETs [14]. Triangular defects are thought to be device killers [15,16]. The formation of triangular defects may be attributed to the larger terrace widths resulting in two-dimensional nucleation [17]. Leone et al. [10] have studied the effect of the growth temperature on the formation of step-bunching.
and triangular defects in standard process. They found that, with temperature increase, a reduction of defects was seen but a significant increase of step-bunching was observed. In the work by Dong et al. [18], the effect of Cl/Si ratio on triangular defects was investigated. They found that triangular defects can be effectively suppressed at high Cl/Si ratio condition. In our previous work, we preliminarily studied the impacts of growth pressure on surface roughness and morphological defects of 4H-SiC epitaxial layers [19]. Besides morphological defects, basal plane dislocations (BPDs) which lead to increasing the forward voltage drift in SiC bipolar devices [20] or the reverse leakage current in unipolar devices [21] is another problem for epitaxial layers performed on off-axis substrates. Because of considerably higher elastic energy of dislocation per unit growth length, BPDs in the substrate deflect to threading edge dislocations (TEDs), which are less harmful to SiC devices in the epilayer during SiC homoepitaxial growth [22]. The conversion of BPDs to TEDs is correlated with growth condition [23,24], substrate pretreatment [25,26], etc. Even though several groups have investigated formation of morphological and crystal defects on off-axis 4H-SiC substrates using CVD, no consensus has been reached, since the approaches and the parameters of processes are different in each of the cases. Moreover, there are few reports about the effect of the growth pressure on the formation of morphological defects and the conversion of BPDs to TEDs. Therefore, it is necessary to study how to increase growth rate and improve morphology and crystallographic defects of 4H-SiC epilayers grown on the substrates with low off-axis angle at low pressure.

In this paper, we investigate 4H-SiC epilayers grown on 4° off-axis substrates in horizontal CVD under low growth pressure (<100 mbar), which emphasizes on the impact of growth pressure on surface morphology and the correlations between the surface roughness and morphological defects. Based on analysis of present results, the effect of growth pressure on conversion of BPDs to TEDs is discussed.

2. Results and Discussion

2.1. Effect of Growth Pressure on Growth Rate

In order to systematically investigate the dependence of process pressure on the growth rate in low pressure range, the growth pressures were arranged between 40 and 100 mbar, and the other process parameters, such as silane flow rate (42 sccm), the process temperature (1580 °C) and C/Si ratio (1.0) are kept constants. As shown in Figure 1, the growth rate remains relatively constant between 40 and 100 mbar, approximately 10 µm/h, which indicates that the growth pressure has little impact on epilayer growth rate in low pressure range (<100 mbar). The relation between growth pressure and growth rate in lower pressure range is different from that in higher pressure range, for which the growth rate decreases rapidly as growth pressure increases in the 100–500 mbar range [27].

![Figure 1. Dependence of 4H-SiC epitaxial growth rate on process pressure.](image-url)
The impact of silane flow rate on the growth rate at low pressure was investigated. The silane flow rate varied between 21 and 50 sccm (the maximum silane flow rate of this CVD reactor). Figure 2 presents the results of the growth rate as a function of silane flow rate. The silane flow rate of 21 sccm produced a growth rate of 5 µm/h, while the maximum silane flow rate of 50 sccm produced a growth rate of approximately 13 µm/h, indicating a linear increase in growth rate with the silane flow rate. The epilayer surface grown with maximum silane flow rate of 50 sccm is specular with few morphological defects in optical microscope, which demonstrates that gas phase homogeneous nucleation that arises from supersaturation of silicon can be effectively suppressed at low growth pressure. It can be concluded that the growth rate will be elevated further with the increase of precursors flow rates to a certain extent at low pressure. Meanwhile, the surface morphology and structural quality of films grown at high growth rate under low pressure will not be degraded.

To verify the stability and reproducibility of the thick epitaxial layers growth process, long growth runs were conducted to fabricate epitaxial films. The process parameters in this experimental section are the same as described above. Growth rates of 4H-SiC epitaxial layers for various growth time are shown in Figure 3. As the figure shows, the average growth rates remained constant (~10 µm/h) for various growth times. Films up to 50 µm were specular with occasional morphological defects, which indicates that the growth process at low pressure was of good stability and reproducibility.

![Figure 2](image-url). Growth rate of 4H-SiC epitaxial layers for various silane flow rate.

![Figure 3](image-url). Growth rate of 4H-SiC epitaxial layers for various growth time.
2.2. Effect of Growth Pressure on Surface Roughness and Morphological Defects

Surface roughness and morphological defects are key issues as the growth rate is effectively elevated. The impacts of process pressure on the surface roughness and morphological defects of epilayers are investigated in detail. The epilayers are grown on substrates cut from a wafer with homogenous surface condition and distributions of substrate defects under the same growth processes except for the growth pressure to avoid the impacts of other factors on surface roughness and morphological defects. All the epilayers have smooth and mirror-like surfaces viewed under an optical microscope. Figure 4 shows the atomic force microscopy (AFM) images of epilayers grown at different pressures: 40 mbar, 60 mbar, 80 mbar and 100 mbar. At growth pressure of 100 mbar, epilayer surface presents protuberances without step structures, and these protuberances are discontinuous as shown in Figure 4a. The roughness root mean square (RMS) of epilayers grown at 100 mbar is 0.331 nm. As the growth pressure reduced to 80 mbar, the protuberances elongate to discontinuous crossovers on the surface, indicating the anisotropy of the elongated protuberances (or steps) which caused by the difference in growth rates along the [10-10] and [11–20] directions [28] (Figure 4b). The RMS of epilayer grown at 80 mbar is 0.193 nm. Even though the epilayer exhibits a step-bunching free surface, it is reasonable to believe that 80 mbar is the initial stage of bunched steps. At the growth pressure of 60 mbar, the undulations are much more continuous across the surface (Figure 4c). Macrosteps perpendicular to the [11–20] direction appear on the surface, which causes the higher surface roughness (1.3018 nm). The width and height of macrosteps are 130–250 nm and 3–5 nm, respectively. At the growth pressure of 40 mbar, the width and height of macrosteps keep almost unchanged, adopting values of 150–300 nm and 3–5 nm, respectively. The RMS of epilayers grown at 40 mbar is 1.4154 nm. It is apparent that the variations of growth pressure influence surface roughness of the grown epilayers.

![Figure 4](https://example.com/figure4.jpg)

**Figure 4.** Atomic force microscopy (AFM) images (10 μm × 10 μm) of approximately 20-μm-thick 4H-SiC epitaxial layers grown under different pressures: (a) 100 mbar; (b) 80 mbar; (c) 60 mbar; (d) 40 mbar.

The increase of surface roughness may attribut to the following reasons: firstly, due to the difference in energy cost for deposition, there is a different step velocity between different basal planes of the stacking sequence [29]. It has been calculated that in 4H-SiC, the A and B planes are growing...
faster than the C plane [30,31]. In the early days, Kimoto et al. gave an expression of the growth rate of the step for chemical vapor deposition of 6H-SiC by the following equation [32]:

$$\nu_{\text{step}} = \frac{4J\lambda_sh_0}{n_0h_0}$$  (1)

where $J$, $n_0$, $h_0$, $h$ and $\lambda_s$ are the flux of reactants, the density of adatom sites, the spacing of the 6H-SiC \{0001\} face, the height of macrostep and surface diffusion length, respectively. This equation indicates that the growth rate of the step is proportional to the flux of reactants and surface diffusion length. Based on the stagnant layer model reported in references [33,34], mass transport from gas phase to wafer surface through the stagnant layer is increased under low pressure. To crystal growth, the surface diffusion length is a critical parameter since it determines whether crystal growth proceeds through step-flow or two-dimensional nucleation [32]. Increase of surfaces diffusion length at reduced growth pressure may be ascribed to the decreased supersaturation, which leads to the restraint of nucleation of adatoms on the terraces and reduction of nucleus density. Step velocity increases as the flux of reactants and surface diffusion length increase at low pressure. As a consequence, step-bunching may form due to the enhancement of difference of growth velocity at steps. On the other hand, it is demonstrated that the relative mole fraction of C-containing gaseous species increases with reduced growth pressure in a CVD reactor using a standard chemistry of silane-propane-hydrogen [35–37]. In other words, the effective C/Si ratio on the surface increases at lower pressure even though the inlet C/Si ratio is kept constant. It is reported that surface morphology is affected by the C/Si ratio [10,29,33,38]. Step-bouncing or surface roughness tend to enhance with increasing C/Si ratio, i.e., C-rich condition. High C/Si ratio may change surface migration of absorbed adatoms and incorporation probability at steps [32]. Besides, from a viewpoint of surface energy, the origin of step-bunching may be correlated with the surface equilibrium process in which the free energy is minimized during crystal growth [39–41]. According to the report of Kojima et al., the surface energy in Si-rich condition and in C-rich condition is different [26]. Therefore, variation tendency of step-bunching at varied growth pressure may be due to the difference of surface energy caused by variation of effective C/Si ratio.

The optical microscope images of 20 µm thick samples performed at pressure varying between 40 and 100 mbar are shown in Figure 5. downfall defects are observed on all samples. The densities of triangular defects for epitaxial layers grown at different pressures are shown in Table 1. It can be concluded that the amount of triangular defects reduces with the growth pressure decrease, which could be attributed to the increased surface diffusion length at lower pressure. With reduced growth pressure, surface migration of adsorbed species is enhanced, which means adsorbed species are more likely to reach the step and contribute to the step flow growth [32]. Therefore, 2D nucleation of adatoms on the terraces is suppressed. Meanwhile, the reduction of triangular defects shows that the step-flow growth is enhanced under lower pressure condition. It suggests that morphological defects tend to appear more frequently on the epilayer surfaces without step-bunching than on the surfaces with step-bunching. Similar results have been reported by Leone et al. [10] and Dong et al. [18] in studies with other growth processes and parameters. Apart from surface diffusion length, the phenomenon observed above relates to the minimization of surface energy at varied pressures. Besides step-bunching, surface energy can also be minimized by the formation of morphological defects. At lower pressure, the surface energy is mainly released by step-bunching. In contrast, the formation of morphological defects is the dominating mode in the minimization of surface energy at relatively higher pressure.
Figure 5. Optical microscopy images of approximately 20 μm thick 4H-SiC epitaxial layers grown under different pressures: (a) 100 mbar; (b) 80 mbar; (c) 60 mbar; (d) 40 mbar.

Table 1. Triangular defect densities and results of Gauss fit of full width at half maximum (FWHM) for epitaxial layers grown at pressure from 40 to 100 mbar.

| Growth Pressure (mbar) | 100  | 80   | 60   | 40   |
|------------------------|------|------|------|------|
| Densities of TDs (cm⁻²) | 80   | 90   | 25   | 7    |
| FWHM (arcsec)          | 23.544 | 50.79 | 25.452 | 23.256 |

2.3. Effect of Growth Pressure on Crystal Defects and Crystal Quality

According to the discussion in Section 2.2, the effective C/Si ratio on the surface is increased at lower pressure. Ohno et al. [24] reported that higher C/Si ratios could promote the conversion of BPDs to TEDs. Therefore, we speculated that, previously, the BPDs density decreased with the reduction of growth pressure. However, it seems that the dislocation densities of epilayers are not influenced by growth pressure as shown in Figure 6. Step-flow growth mode is enhanced by lowering growth pressure. The propagation of BPDs could be promoted under lower growth pressure condition. Thus, the reason that dislocation densities of BPDs and TEDs in epilayers grown at varied pressures are nearly unchanged may be attributed to the contrary effect of increased C/Si ratio and enhanced step-flow growth on the propagation of BPDs. However, further investigations of the relationship between growth pressure and the conversion of BPDs are still required.

High-resolution X-ray diffraction (HRXRD) rocking curve measurements are employed to study the crystalline quality of the 4H-SiC films performed at different pressures. The reflection peaks from the (0004) plane of 20 μm thick epilayers with full width at half maximum (FWHM) of 50–80 arcsec are shown in Figure 7. It can be seen that the reflection peaks are broadened as more than one domain on samples is probed by the X-ray beam. Therefore, the peaks have been fitted with Gaussians, and each with a FWHM of approximately 20 arcsec, except for the epilayers grown at 80 mbar of which the FWHM is approximately 50 arcsec, as shown in Table 1. This result implies that crystal quality is almost independent of growth pressure. This is consistent with the results shown in Figure 6, since
the factors such as crystal defect density and strain could influence FWHM value of diffraction peak theoretically. Compared to the substrate of which the FWHM is more than 40 arcsec, the epilayers with FWHM values of approximately 20 arcsec are of high-quality.

![Figure 6. Dislocation densities of epitaxial layers grown at different pressures.](image)

3. Materials and Methods

3.1. Sample Preparation

Homoepitaxial growth was performed in a commercial Hot-wall chemical vapor deposition (HWCVD) reactor (Epigress VP508, Lund, Sweden) [19,42]. The substrates used for homoepitaxial
growth were 1.7 cm × 1.7 cm samples cut from a chemical-mechanical polished n-type, Si-face 4H-SiC wafer oriented 4° off-axis towards the [11–20] direction with a doping concentration of ~10^{18} cm^{-3}. The samples were cleaned following the Radio Corporation of America (RCA) cleaning process prior to deposition. Silane (SiH₄) and propane (C₃H₈) were used as precursors during growth and introduced with a hydrogen carrier gas. In situ etching took place during the temperature ramp from 1400 °C to 1580 °C in pure H₂ atmosphere with pressure of 100 mbar. Afterwards, the growth pressure was adjusted to a reduced pressure of less than 100 mbar and then the precursors were taken into the chamber. Consequently, the epilayer deposition lasted for a selected period of time. No intentional dopant was introduced during the epitaxial growth. In this paper, the growth temperature and C/Si ratio were maintained at 1580 °C and 1.0 for all the experiments, respectively. The growth pressure was varied between 40 mbar and 100 mbar.

3.2. Characterization

The growth rate was calculated from the thickness of the epitaxial layers which was measured by Fourier Transform Infrared Spectroscopy (FTIR) (Spectrum 100, PerkinElmer, Waltham, MA, USA). The epilayer morphology was evaluated with an optical microscope (DM4000M, Leica, Wetzlar, Germany) equipped with Nomarski differential interference contrast optics (NDIC). Atomic force microscopy (Agilent 5500, Agilent, Santa Clara, CA, USA) was used to analyze the surface roughness of the epilayers. The epilayers were etched by molten KOH at 500 °C for 5 min. Dislocation types and densities were also measured using optical microscope. The crystalline quality of the epitaxial layers was studied using X-ray diffraction (XRD) (Bruker D8 Advance, Bruker, Karlsruhe, Germany).

4. Conclusions

4H-SiC epilayers were performed in a horizontal hot-wall CVD reactor at low pressure on 4° off-axis substrates. The effects of growth pressure on growth rate, surface roughness and morphological defects were studied. Good stability and reproducibility of the growth processes was verified. At the higher growth pressures of 100 mbar and 80 mbar, the epitaxial films were smooth without step-bunching, but more morphological defects were observed on the films. At lower growth pressures of 60 mbar and 40 mbar, macrosteps formed on the surface increased the surface roughness. However, it can be seen that densities of morphological defects decrease at lower pressure, which indicates that morphological defects tend to appear on the epilayer surfaces without step-bunching rather than on the surfaces with step-bunching. It is suggested that the effect of growth pressure on surface diffusion length is the main reason for the variation of surface morphology. At reduced growth pressure, the increase of surface diffusion length of absorbed adatoms leads to the formation of step-bunching. Meanwhile, morphological defects originating from nucleation on terraces are suppressed as surface diffusion length increases. Besides, the surface energy is mainly released by step-bunching at relatively lower pressure and by morphological defects at relatively higher pressure, respectively. Thus, different dominate modes for minimization of surface energy at varied growth pressure could be another reason for the phenomenon described above. Due to the contrary effect of increased C/Si ratio and enhanced step-flow growth on the propagation of BPDs, the dislocation densities of BPDs and TEDs in epilayers grown at varied pressures are nearly unchanged. High resolution X-ray diffraction measurements indicate that the epilayers performed in this work are of high quality.

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References

1. He, J.; Zhao, T.; Jing, X.; Demerdash, N.A.O. Application of wide bandgap devices in renewable energy systems—Benefits and challenges. In Proceedings of the International Conference on Renewable Energy Research and Application (ICRERA), Milwaukee, WI, USA, 19–22 October 2014; pp. 749–754.

2. Zhang, H.; Tolbert, L.M. Efficiency Impact of Silicon Carbide Power Electronics for Modern Wind Turbine Full Scale Frequency Converter. IEEE Trans. Ind. Electron. 2011, 58, 21–28. [CrossRef]

3. Adamowicz, M.; Gizioewski, S.; Pietryka, J.; Rutkowski, M.; Krzeminski, Z. Evaluation of SiC JFETs and SiC Schottky diodes for wind generation systems. In Proceedings of the IEEE International Symposium on Industrial Electronics, Gdansk, Poland, 27–30 June 2011; pp. 269–276.

4. Nashida, N.; Hinata, Y.; Horio, M.; Yamada, R.; Ikeda, Y. All-SiC power module for photovoltaic Power Conditioner System. In Proceedings of the IEEE 26th International Symposium on Power Semiconductor Devices & ICs (ISPSD), Waikoloa, HI, USA, 15–19 June 2014; pp. 342–345.

5. Kim, T.; Jang, M.; Agelidis, V.G. Comparative thermal performance evaluation of SiC MOSFETs and Si MOSFET for 1.2 kW 300 kHz DC-DC boost converter as a solar PV pre-regulator. In Proceedings of the International Power Electronics Conference, Hiroshima, Japan, 18–21 May 2014; pp. 3933–3937.

6. Zhang, H.; Tolbert, L.M.; Ozpineci, B. Impact of SiC Devices on Hybrid Electric and Plug-In Hybrid Electric Vehicles. IEEE Trans. Ind. Appl. 2014, 47, 912–921. [CrossRef]

7. Su, M.; Chen, C.; Sharma, S.; Kikuchi, J. Performance and cost considerations for SiC-based HEV traction inverter systems. In Proceedings of the IEEE 3rd Workshop on Wide Bandgap Power Devices and Applications (WiPDA), Blacksburg, VA, USA, 2–4 November 2015; pp. 347–350.

8. Ishikawa, T.; Tanaka, Y.; Yatsuo, T.; Yano, K. SiC power devices for HEV/EV and a novel SiC vertical JFET. In Proceedings of the IEEE International Electron Devices Meeting, San Francisco, CA, USA, 15–17 December 2014.

9. Cooper, J.A.; Agarwal, A. SiC PowerP-switching devices—the second electronics revolution? Proc. IEEE 2002, 90, 956–968. [CrossRef]

10. Leone, S.; Pedersen, H.; Henry, A.; Kordina, O.; Janzén, E. Improved morphology for epitaxial growth on 4° off-axis 4H-SiC substrates. J. Cryst. Growth 2009, 311, 3265–3272. [CrossRef]

11. Leone, S.; Beyer, F.C.; Pedersen, H.; Kordina, O.; Henry, A.; Janzén, E. Growth of smooth 4H-SiC epilayers on 4° off-axis substrates with chloride-based CVD at very high growth rate. Mater. Res. Bull. 2011, 46, 1272–1275. [CrossRef]

12. Aigo, T.; Ito, W.; Tsuge, H.; Yashiro, H.; Katsuno, M.; Fujimoto, T.; Ohashi, W. 4H-SiC Epitaxial Growth on 2° Off-Axis Substrates using Trichlorosilane (TCS). Mater. Sci. Forum 2012, 717–720, 101–104. [CrossRef]

13. Kato, T.; Kinoshita, A.; Wada, K.; Nishi, T.; Hozomi, E.; Taniguchi, H.; Fukuda, K.; Okumura, H. Morphology Improvement of Step-Bunching on 4H-SiC Wafers by Polishing Technique. Mater. Sci. Forum 2010, 645–648, 763–765.

14. Chung, J.E.; Chen, J.; Ko, P.K.; Hu, C.; Levi, M. The effects of low-angle off-axis substrate orientation on MOSFET performance and reliability. IEEE Trans. Electron Devices 1991, 38, 627–633. [CrossRef]

15. Shrivastava, A.; Muzykov, P.G.; Pearman, B.; Angel, S.M.; Sudarshan, T.S. Investigation of Triangular Defects in 4H-SiC 4° off Cu (0001) Si Face Epilayers Grown by CVD. Mater. Sci. Forum 2009, 600–603, 139–142. [CrossRef]

16. Kimoto, T.; Miyamoto, N.; Matsunami, H. Performance limiting surface defects in SiC epitaxial p-n junction diodes. IEEE Trans. Electron Devices 1999, 46, 471–477. [CrossRef]

17. Matsunami, H.; Kimoto, T. Step-controlled epitaxial growth of SiC: High quality homoepitaxy. Mater. Sci. Eng. R 1997, 20, 125–166. [CrossRef]

18. Dong, L.; Sun, G.; Yu, J.; Zheng, L.; Liu, X.; Zhang, F.; Yan, G.; Li, X.; Wang, Z. Growth of 4H-SiC epilayers with low surface roughness and morphological defects density on 4° off-axis substrates. Appl. Surf. Sci. 2013, 270, 301–306. [CrossRef]

19. Hu, J.; Zhang, Y.; Jia, R.; Wang, Y.; Xin, B. Low Pressure Homoepitaxial Growth of 4H-SiC on 4° off-axis axis substrates. Mater. Sci. Forum 2015, 821–823, 181–184. [CrossRef]

20. Jacobson, H.; Bergman, J.P.; Hallin, C.; Janzen, E.; Tuomi, T.; Lendenmann, H. Properties and origins of different stacking faults that cause degradation in SiC PiN diodes. J. Appl. Phys. 2004, 95, 1485. [CrossRef]
21. Agarwal, A.; Fatima, H.; Haney, S.; Ryu, S.-H. A new degradation mechanism in high-voltage SiC power MOSFETs. *IEEE Electron Device Lett.* 2007, 28, 587–589. [CrossRef]

22. Ohno, T.; Yamaguchi, H.; Kuroda, S.; Kojima, K.; Suzuki, T.; Arai, K. Direct observation of dislocations propagated from 4H-SiC substrate to epitaxial layer by X-ray topography. *J. Cryst. Growth* 2004, 260, 209–216. [CrossRef]

23. Stahlbush, R.E.; VanMil, B.L.; Myers-Ward, R.L.; Lew, K.-K.; Gaskill, D.K.; Eddy, C.R., Jr. Basal plane dislocation reduction in 4H-SiC epitaxy by growth interruptions. *Appl. Phys. Lett.* 2009, 94, 041916. [CrossRef]

24. Ohno, T.; Yamaguchi, H.; Kuroda, S.; Kojima, K.; Suzuki, T.; Arai, K. Influence of growth conditions on basal plane dislocation in 4H-SiC epitaxial layer. *J. Cryst. Growth* 2004, 271, 1–7. [CrossRef]

25. Zhang, Z.; Sudarshan, T.S. Basal plane dislocation-free epitaxy of silicon carbide. *Appl. Phys. Lett.* 2005, 87, 151913. [CrossRef]

26. Sumakeris, J.J.; Hull, B.A.; O’Loughlin, M.J.; Skowronski, M.; Balakrishna, V. Developing an effective and robust process for manufacturing bipolar SiC power devices. *Mater. Sci. Forum* 2007, 556–557, 77–80. [CrossRef]

27. Myers-Ward, R.L.; Shishkin, Y.; Kordina, O.; Haselbarth, I.; Saddow, S.E. High Epitaxial Growth Rate of 4H-SiC Using Horizontal Hot-Wall CVD. *Mater. Sci. Forum* 2006, 527–529, 187–190. [CrossRef]

28. Syväjärvi, M.; Yakimova, R.; Janzén, E. Step-bunching in SiC epitaxy: Anisotropy and influence of growth temperature. *J. Cryst. Growth* 2002, 236, 297–304. [CrossRef]

29. Kimoto, T.; Itoh, A.; Matsunami, H.; Okano, T. Step bunching mechanism in chemical vapor deposition of 6H- and 4H-SiC [0001]. *J. Appl. Phys.* 1997, 81, 3494–3500. [CrossRef]

30. Heine, V.; Cheng, C.; Needs, R.J. The Preference of Silicon Carbide for Growth in the Metastable Cubic Form. *J. Am. Ceram. Soc.* 1991, 74, 2630–2633. [CrossRef]

31. Chien, F.R.; Nutt, S.R.; Yoo, W.S.; Kimoto, T.; Matsunami, H. Terrace growth and polytype development in epitaxial β-SiC films on α-SiC (6H and 15R) substrates. *J. Mater. Res.* 1994, 9, 940–954. [CrossRef]

32. Kimoto, T.; Matsunami, H. Surface diffusion lengths of adatoms on 6H-SiC{0001} faces in chemical vapor deposition of SiC. *J. Appl. Phys.* 1995, 78, 3132–3137. [CrossRef]

33. Kimoto, T.; Nishino, H.; Yoo, W.S.; Matsunami, H. Growth mechanism of 6H-SiC in step-controlled epitaxy. *J. Appl. Phys.* 1993, 73, 726–732. [CrossRef]

34. Eversteyn, F.C.; Severin, P.J.W.; Brekel, C.H.J.v.d.; Peek, H.L. A Stagnant Layer Model for the Epitaxial Growth of Silicon from Silane in a Horizontal Reactor. *J. Electrochem. Sot.* 1970, 117, 925–931. [CrossRef]

35. Kojima, K.; Nishizawa, S.; Kuroda, S.; Okumura, H.; Arai, K. Effect of growth condition on micropipe filling of 4H-SiC epitaxial layer. *J. Cryst. Growth* 2005, 275, 549–554. [CrossRef]

36. Forsberg, U.; Danielsson, Ö.; Henry, A.; Linnarsson, M.K.; Janzén, E. Nitrogen doping of epitaxial silicon carbide. *J. Cryst. Growth* 2002, 236, 101–112. [CrossRef]

37. Nishizawa, S.; Pons, M. Numerical modeling of SiC–CVD in a horizontal hot-wall reactor. *Microelectron. Eng.* 2006, 83, 100–103. [CrossRef]

38. Omar, S.U.; Chandrashekhar, M.V.S.; Chowdhury, I.A.; Rana, T.A.; Sudarshan, T.S. Step dynamics in the heteroepitaxial growth of 6H-SiC by chemical vapor deposition on 1° offcut substrate using dichlorosilane as Si precursor. *J. Appl. Phys.* 2013, 110, 184904. [CrossRef]

39. Herring, C. Correlation Energy and the Heat of Sublimation of Lithium. *Phys. Rev.* 1951, 82, 87. [CrossRef]

40. Williams, E.D.; Phaneuf, R.J.; Wei, J.; Bartelt, N.C.; Einstein, T.L. Thermodynamics and statistical mechanics of the faceting of stepped Si(111). *Surf. Sci.* 1993, 294, 219–242. [CrossRef]

41. Makoto, K.; Naoki, K. Equilibrium multiatomic step structure of GaAs(001) vicinal surfaces grown by metalorganic chemical vapor deposition. *Appl. Phys. Lett.* 1993, 62, 1262–1264.

42. Xin, B.; Jia, R.; Hu, J.; Tsai, C.; Lin, H.; Zhang, Y. A step-by-step experiment of 3C-SiC hetero-epitaxial growth on 4H-SiC by CVD. *Appl. Surf. Sci.* 2015, 357, 985–993. [CrossRef]