Evaluation of phase and thermoelectric properties of thin film SrSi2

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We firstly prepared SrSi2 thin films on insulating substrates and measured their thermoelectric properties. Thin films of Sr–Si system were deposited on (0001) Al2O3 substrates by radio frequency magnetron sputtering method at various deposition temperatures and under various total deposition pressure. Constituent phases primary depend on the deposition temperature. The films deposited below 600°C consisted of amorphous or the metastable CaSi2 structure phase. CaSi2 structure phase was obtained at 600°C irrespective of the pressure and finally stable α-SrSi2 (α-phase) above 700°C. The films with CaSi2 structure phase had low power factor below 10 μW m⁻¹ K⁻² for the temperature range between 100 and 400°C. On the other hand, the film with α-phase showed p-type conduction and good thermoelectric power factor beyond 700 μW m⁻¹ K⁻² at room temperature. This value is larger than the reported value of (111) one-axis-oriented Mg2Si films prepared by the same deposition process, maximum 130 μW m⁻¹ K⁻² at 300°C. The present result shows that α-phase is one of the promising candidates as thermoelectric materials.

Key-words : SrSi2 thin films, Crystal structure, Thermoelectric

1. Introduction

Recently, thermoelectric materials have been widely investigated.1–6 Thermoelectric materials are capable of converting waste heat into usage electricity.7 The technology of energy conversions from waste heat into electrical energy is very important for reduction of generating energy from fossil fuels.8 A dimensionless figure of merit ZT (S²ρ⁻¹κ⁻¹T) is widely used for indicating the thermoelectric performance of the material. The term concerning electronic properties of figure of merit is called power factor (S²ρ⁻¹). It is very important to discover the novel materials with high power factor because κ has been demonstrated to be lowered by the introduction of nanostructure etc.9 without changing material such as heavy element substitution.10

Thermoelectric thin films are also potential cooling applications.11 For cooling application, thickness of thermoelectric materials should be as small as possible, according to the formula of the maximum cooling density.12 However, when materials are made into a thin film, the electrical resistivity often increases13 and the performance is degraded from that of the bulk.14 Furthermore, it is not always possible to produce the same materials as bulk because the thin film fabrication process is a non-equilibrium process.15,16 Therefore, it is necessary to study thin film with good thermoelectric performance, as well as bulk material.

In recent years, silicide-based materials attract attention as promising candidate for thermoelectric materials.17 Silicide compounds have advantages such as low environmental load, abundant constituent elements in the earth’s crust, high compatibility with the Si process. The famous thermoelectric silicides are Mg2Si and Mn–Si systems14,18–20 because of good thermoelectric property. They have been widely investigated with not only bulk form but also thin film form. The α-SrSi221–23 (described as α-phase) is also a promising candidate as a thermoelectric material because it is environmentally friendly and a good thermoelectric power factor near the room temperature.24–26 Furthermore, SrSi2 has other phases showing interesting electrical properties. Although this α-phase is the most stable phase, several polymorphs are known. For example, β-phase23,27 has been recognized as high pressure and high temperature phase, which is produced by compression in a belt-type apparatus at a pressure of 4 GPa with heating at 1000–1500°C and decomposition after quenching to room temperature.28 This phase is known as a superconductor.29 SrSi2 thin films prepared at 500°C...
by a Molecular Beam Epitaxy (MBE) method is reported to have the phase with CaSi2 structure (hereinafter described as CaSi2 structure phase), which has not been reported in bulk, and showed very low electrical resistivity on the order of $10^{-5} \Omega \cdot \text{cm}$. This structure has "silicene layers", which is the analogue of graphene consisting of Si atoms, in its unit cell, attracting much attentions. Thus, it is essential to investigate the conditions comprehensively to synthesis SrSi2 thin films with desired phases.

In this study, SrSi2 thin films were deposited on (001) Al2O3 substrates by radio frequency (RF) magnetron sputtering method at various deposition temperature and sputter pressure. The deposition at low temperature resulted in CaSi2 structure phase, while the deposition at high temperature provided $\alpha$-phase. The temperature-dependent electrical resistivity and Seebeck coefficient were measured, leading to resultant power factor of 700 $\mu$W m$^{-1}$ K$^{-2}$ at 100°C.

2. Experimental

SrSi2 films with 2 $\mu$m in thickness were prepared on (001) Al2O3 substrates by RF magnetron sputtering method under Ar with 5% H2. A sintered body of SrSi2 with 1.3 inch in diameter was used as a sputtering target. The deposition temperature was varied from 200 to 700°C, while the deposition total pressure was changed from 20 to 200 mTorr. The input power for sputtering and target-substrate distance were maintained at 20 W and 60 mm, respectively.

The Si/(Sr + Si) ratio, Si content, of the deposited film was measured by the X-ray fluorescence spectrometry (XRF) (PANalytical PW4400). Constituent phase of the films was characterized by X-ray diffraction (XRD) method (Bruker AXS D8 discover). XRD $2\theta$-$\theta$ scans were repeated by changing the sample inclination angle ($\psi$) under the sample rotation condition. Integrated $2\theta$-$\theta$ scans was obtained by integrated along $\psi$ direction.

The temperature dependences of the electrical resistivity and Seebeck coefficient were measured from room temperature to 400°C in He atmosphere and reduced pressure (ULVAC ZEM-3).

3. Results and discussion

3.1 Film composition and crystal structure

Figure 1 shows the results obtained using XRF. Figures 1(A) and 1(B) show the deposition amount of Si and Sr respectively, revealing that the higher deposition pressure decreases deposition amount. This trend is more prominent, especially for Si. Therefore, the Si content of the films decreased with increasing the pressure. The reason for this is considered that Si is more easily scattered because of its small atomic weight. From this result, it was possible to adjust the composition by controlling the deposition pressure. Regarding to deposition temperature, the films deposited at 600°C (hereinafter described as 600°C films) had larger Si content in the films than the films deposited at 700°C (hereinafter described as 700°C films).

Figures 2(A) and 2(B) show the integrated XRD results for the 600°C films and 700°C films deposited under various deposition pressure. These XRD patterns indicate that the majority phase was CaSi2 structure phase for all 600°C films regardless of the deposition pressure, as confirmed by 100, 101, and 102 diffraction peaks. It must be noted that a peak at around 28° is Si III. This Si peak intensity increased with decreasing deposition pressure. This indicates that the volume fraction of Si secondary phase increased with decreasing deposition pressure. The presence of Si secondary phase is due to Si-excess composition against SrSi2 that was confirmed by XRF measurement as shown in Fig. 1. In contrast to 600°C films, $\alpha$-phase was the majority phase for all deposition pressures at 700°C. This can be confirmed by the 111, 210, 211, and 220 peaks of $\alpha$-phase. The Si III peak was also observed for the films deposited at 700°C. At 100 and 200 mTorr, the films with stoichiometric ratio were obtained.

Figure 3 displays the constituent phase as a function of deposition temperature and Si content of the film, showing the growth of $\alpha$-phase at 700°C and CaSi2 structure phase at 600°C as confirmed in Fig. 2. The constituent phase below 600°C, show CaSi2 structure phase or amorphous (no detectable peak on XRD pattern). Boundary between
CaSi$_2$ structure phase and amorphous depended on not only deposition temperature but also Si contents of the films. These results differ from the information from the phase diagram, but are similar to the film preparation condition especially in temperature reported in the present study. In this study, we had grown the both $\alpha$-phase and CaSi$_2$ structure phase, on the other hand, had not the $\beta$-phase probably due to low deposition temperature and pressure. The confirmation of CaSi$_2$ structure phase is consistent with the previous result using MBE at 500°C.

These results indicate that the relatively lower deposition temperature might lead to the CaSi$_2$ structure phase. It should be mentioned that CaSi$_2$ structure phase have not been observed in bulk because 600°C is too low to obtain significant diffusion for fabrication of the bulk.

3.2 Thermoelectric properties

Figure 4 summarizes the electric resistivity at room temperature as a function of deposition temperature. The increase in resistivity between deposition temperatures from 200 to 600°C might be due to the increase in the size of crystallite, which reduce the carrier scattering at the grain boundary. The high resistivity of 700°C films compared to 600°C films accords the previous studies. These studies showed the $\alpha$-phase is semimetal, while the CaSi$_2$ structure phase exhibits normal metallic characteristics. Thus, the change in the formed phase would provide the decrease in the resistivity with increasing deposition temperature.

Figure 5(A) shows the temperature dependence of electrical resistivity of CaSi$_2$ structure phase and $\alpha$-phase. The films consisting of CaSi$_2$ structure phase showed lower electrical resistivity than that with $\alpha$-phase. In addition, the resistivity of the present film of CaSi$_2$ structure phase is about 100 times larger than that of epitaxial films in previous study. This difference is probably due to the difference in crystallinities; the present film is polycrystalline one, whereas the reported film was epitaxial one. The films consisting of $\alpha$-phase showed similar trend to the reported value for polycrystalline bulk consist of $\alpha$-phase by Imai et al., but their absolute value is slightly larger. When a film is prepared by sputtering, films consist of the columnar grains. There might be a lot of grain boundaries along the in-plane direction due to the small grain size on several tens to hundreds nm. Since electrical resistivity was measured along the in-plane direction, the carrier tend to be scattered at the grain boundary, leading to the higher resistivity than that of bulk.

Figure 5(B) shows temperature dependence of Seebeck coefficients of CaSi$_2$ structure phase and $\alpha$-phase. The
Seebeck coefficients of CaSi$_2$ structure phase is about $-6 \mu$V K$^{-1}$ over the entire temperature range. This small value shows the metallic characteristics. On the other hand, relatively large value of 120 and 80 $\mu$V K$^{-1}$ at 27 and 300°C, respectively were observed for the film consist of $\alpha$-phase. This large value is also in good agreement with the reported one for the bulk polycrystalline sample$^{24)}$.

Figure 5(C) shows temperature dependence of power factor of CaSi$_2$ structure phase and $\alpha$-phase. The power factor of CaSi$_2$ phase was less than 10 $\mu$W m$^{-1}$ K$^{-2}$ for all temperature range due to small Seebeck coefficients. On the other hand, $\alpha$-phase had 700 $\mu$W m$^{-1}$ K$^{-2}$ at 100°C. This value is smaller than that of $\alpha$-SrSi$_2$ bulk, but larger than those of our previously reported Mg$_2$Si and Ca$_3$Si$_5$ films deposited by the same process$^{14),33)}$. The famous thermoelectric materials of Bi$_2$Te$_3$ showed over 1000 $\mu$W m$^{-1}$ K$^{-2}$ at 27°C in a film form$^{39)}$, which is much lower than that of bulk. In contrast, $\alpha$-phase thin film showed less degraded properties, compared to the bulk. Therefore $\alpha$-phase is one of the promising candidates for the thermoelectric application with the materials with film form.

4. Conclusions

We investigated the formation condition of SrSi$_2$ films and measured thermoelectric properties of the films. It was found that the crystal structure of the SrSi$_2$ films varies depending on the deposition temperature and that Si content can be controlled by the deposition pressure. For the deposition at 600°C, films of CaSi$_2$ structure phase were formed. Polycrystalline CaSi$_2$ structure phase has low electric resistivity regardless of the temperature range and metallic behavior. By the 700°C deposition, films of $\alpha$-phase were formed. It showed the power factor of 700 $\mu$W m$^{-1}$ K$^{-2}$ at 100°C and might show larger value at room temperature. This power factor is larger than other silicide films like Mg$_2$Si and Ca$_3$Si$_5$ prepared by the same method. Moreover, the less degraded properties from the bulk of $\alpha$-phase films enables itself to be one of proposing thermoelectric materials for thin film application at room temperature.

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