Test Beam Characterizations of 3D Silicon Pixel Detectors

M. Mathes, M. Cristinziani, C. Da Via', M. Garcia-Sciveres, K. Einsweiler, J. Hasi, C. Kenney, Sherwood Parker, L. Reuen, M. Ruspa, J. Velthuis, S. Watts, N. Wermes

Abstract—3D silicon detectors are characterized by cylindrical electrodes perpendicular to the surface and penetrating into the bulk material in contrast to standard Si detectors with planar electrodes on its top and bottom. This geometry renders them particularly interesting to be used in environments where standard silicon detectors have limitations, such as for example the radiation environment expected in an LHC upgrade. For the first time, several 3D sensors were assembled as hybrid pixel detectors using the ATLAS-pixel front-end chip and readout electronics. Devices with different electrode configurations have been characterized in a 100 GeV pion beam at the CERN SPS. Here we report results on unirradiated devices with 3D electrodes per 50 × 400 µm² pixel area. Full charge collection is obtained already with comparatively low bias voltages around 10 V. Spatial resolution with binary readout is obtained as expected from the cell dimensions. Efficiencies of 95.9% ± 0.1% for tracks parallel to the electrodes and of 99.9% ± 0.1% at 15° are measured. The homogeneity of the efficiency over the pixel area and charge sharing are characterized.

I. INTRODUCTION

The silicon tracking devices at the LHC detectors ATLAS and CMS have to cope with very intense radiation levels reaching fluence levels of up to 10^{15} particles per cm² in the innermost pixel layer during a ten year LHC projected life time [1]. This large particle fluence causes damage to silicon pixel sensors, most dominantly by: (a) the trapping of moving charges which reduces the signal, (b) an increase in the negative space charge concentration \( N_{\text{eff}} \) of the depleted silicon bulk which needs a larger external bias voltage to reach full depletion, and (c) an increase of leakage currents due to new energy states inside the band gap. The semiconductor tracking detectors of the large LHC experiments ATLAS and CMS have been designed and built to stand these challenges for the LHC life time [2]–[5]. As plans for an upgrade of the LHC which may eventually lead to an increase in luminosity by a factor of 10 (sLHC, Super-LHC) are discussed [6], new types of sensors for pixel detectors, either by material or by design, are being investigated to be able to cope with the concurrent increase also in radiation fluence of up to 10^{16} particles per cm². For such radiation levels the present detectors are not suited. So-called 3D-silicon sensors have been proposed and developed ([7], [8] and references therein). Due to a different electrode configuration 3D sensors are more radiation hard and have faster charge collection than standard planar silicon sensors. They are thus a prime candidate for pixel sensors at the sLHC.

II. THE TESTED STRUCTURES

The basic concept of 3D silicon sensors is shown in fig. 1. A 3-dimensional structure is obtained by processing the n⁺ and p⁺ electrodes into the substrate bulk by combining VLSI and MEMS (Micro Electro Mechanical Systems) technologies [9]. Charge carriers drift inside the bulk parallel to the surface over a typical drift distance of 50 – 100 µm. Typical depletion voltages are of the order of 10 V.

In this paper we have particularly studied the response of 3D pixel devices to high energy particles in terms of response homogeneity and spatial resolution. A wafer with three different types of 3D structures geometrically adapted to fit the pixel pattern of the ATLAS pixel frontend chip FE-I3 [10] has been produced at the Stanford Nanofabrication Facility [11]. The thickness of the p-type sensors is 208 ± 5 µm. The nominal etched diameter of the 3D-columns is 17 µm. PbSn underbump deposition and flip-chipping to bumped FE-I3 chips was done at IZM-Berlin [12], [13]. The pixel area of the FE-I3 chip is 50µm × 400µm. Different structures with different distances between the 3D electrodes were produced to fit the FE-I3 pixel dimensions with two (2E), three (3E), or four (4E) equally-spaced electrodes in the area of one electronics pixel. In this paper we report on the study of the configuration with three electrodes (3E, Fig. 1). An evaluation of the 2E, 3E, and 4E structures against each other will be the subject of a future publication. The tested devices were still unirradiated. The ATLAS pixel electronics provides zero suppression in the readout with a threshold level for this study of about 3000e−.
The electronics noise is about 380e⁻. Analog information is obtained via the in-pixel measurement of time-over-threshold (ToT) with an approximate resolution of 7 bits [10]. The calibration is approximately linear for signal charges above 5000 e⁻ with one ToT unit corresponding to about 400 e⁻.

### III. TEST BEAM SETUP AND ENVIRONMENT

The 3D silicon devices have been tested in a 100 GeV beam of pions at the CERN SPS. The setup is shown schematically in fig. 2. The device under test (DUT, 3D silicon pixels) was placed in between two pairs of silicon microstrip detectors [14]. The telescope was developed for ATLAS and consists of double sided silicon microstrip detectors with 50 µm pitched strips on both sides rotated by 90° with respect to each other. The S/N ratio of about 37 for the p-side, measuring the DUT x-direction, is better than for the n-side (S/N ≈ 22), measuring the DUT y-direction. Hits are read out zero suppressed and events waiting for read out can be buffered for some time [14]. The latter option was, however, not used. The setup is triggered by the coincidence signal of two scintillators in front of and behind the setup. The precision obtained in the plane of the DUT is better than 0.2 mrad. The beam incidence angle with respect to the DUT plane is at most 1°. The average data taking rate was limited by the readout system to around 50-60 Hz. The results presented in this paper concentrate on 3D devices with three electrodes. Bias voltages between 6V and 25V in steps of 4V were applied. Two angles of inclination with respect to the beam, 0° and 15°, were studied. Only tracks from events with a single hit in each of the telescope planes were selected for characterization measurements. Hits in the 3D-device were accepted if the seed position of the cluster is found in the predicted or in a neighboring pixel. Otherwise they are counted as misses.

### IV. RESULTS

**Operation characteristics:** 3D devices can be fully depleted with low bias voltages compared to planar silicon sensors. The theoretically expected voltage for cluster signal saturation is about 8 V. In fig. 3(a) the measured charge distribution with a Landau type shape is shown as obtained by operating the DUT with a bias voltage of 10 V. The insert shows the 59.6 keV and 26.3 keV γ-lines from 241Am, indicating that the ToT calibration uncertainty lies between 5-9%. From the fit, the values for peak, mean and sigma for the fully depleted device are 15700 e⁻, 19200 e⁻ and 2100 e⁻, respectively, the latter being mostly due to Landau fluctuations. The population of entries at small charges on the left hand side of fig. 3(a) indicate charge losses when tracks pass directly through the 3D-electrodes. This will be studied in more detail below. For data taking the 3D devices were operated with several different bias voltages. We show in fig. 3(b) the hit efficiency, obtained by integrating all hits and misses over the area of a complete pixel cell, as a function of the applied bias voltage. The efficiency is almost unchanged even down to voltages of ~6 V. Data using even lower bias voltage were not taken during the test beam. The slight increase with bias voltage seen in fig. 3(b) we interpret as coming from effects due to the higher electric fields inside the sensor rather than being due to a change in the depletion volume.

**Hit efficiency:** In order to investigate the spatial dependence of the hit efficiency, the sensors with three electrodes per pixel have been scanned with the beam to obtain a hit efficiency map. Scans with straight tracks (perpendicular incidence) and with tracks under an inclination angle of about 15° were carried out. For straight track, the obtained efficiency map is shown in fig. 4(a). Tracks from all illuminated pixels enter the efficiency map. Scans with straight tracks (perpendicular incidence) and with tracks under an inclination angle of about 15° were carried out. For straight track, the obtained efficiency map is shown in fig. 4(a). Tracks from all illuminated pixels enter the efficiency map. The spot-resolution with which the map is drawn is given by the 2-dimensional bin size, smoothed by a gaussian, folded with the telescope extrapolation error. All combined this corresponds to ~6.5 µm in both coordinates. The position of the 3D electrodes are clearly identified. The overall efficiency for perpendicularly incident tracks, using a threshold setting of 3000 e⁻ and obtained by integrating over the pixel area,
(A) CHARGE DISTRIBUTION OF PERPENDICULARLY INCIDENT PARTICLES MEASURED USING A BIAS VOLTAGE OF 25 V. THE SOLID LINE IS A LANDAU FIT. THE QUOTED ERRORS ON MEAN AND SIGMA ARE OBTAINED BY THE FIT. THE INSERT SHOWS THE $^{241}$AM LINES OF 59.6 keV AND 26.3 keV, RESPECTIVELY. THE CALIBRATION UNCERTAINTY IS BETWEEN 5% AND 9%. (B) HIT EFFICIENCY AS A FUNCTION OF APPLIED BIAS VOLTAGE FROM 6.5 V TO 31.5 V.

corresponds to

$$\epsilon_0 = 95.9\% \pm 0.1\%$$  \hspace{1cm} (1)
additional spreading we hence interpret as being due to effects like charge diffusion or other mechanisms of signal induction of tracks entering the undepleted column regions.

The mean charge of hits belonging to the region outside of the electrode is about 20 – 25 ke. Hits assigned to the electrode center, on the contrary, show a mean charge below 5 ke. Thus, with some uncertainty and caution introduced by the existing pixel thresholds of ∼3 ke, we estimate the charge collection efficiency inside the 3D-electrode in the order of at most 25%. If, alternatively, one assumes 0% efficiency inside the electrode and 100% outside, the effective column radius is calculated from eq. (1) to be 6.5 µm.

In a pixel tracking detector, the pixel modules are usually tilted in the azimuthal direction to optimize charge sharing between neighboring readout cells. For 3D sensors this produces the extra beneficial effect that tracks originating from the interaction point would not run exactly parallel through the center of the 3D electrodes. In fig. 5 Landau distributions for perpendicular tracks (0°) and for tracks under an inclination angle of 15° are compared. As expected, for inclined tracks in fig 5(b) the entries with small charge on the left of fig 5(a) disappear. The width of the Landau distribution now broadens by about 20% and shows the lowest detected charge at about 5 ke. While this is beneficial for track detection at collider detectors where tracks always impinge the detector under an angle, the Landau broadening with entries as low as 5 ke does not constitute a comfortable distance to the threshold, in particular when a decrease of the signal charge due to radiation must be expected. The hit efficiency for 15° inclined tracks is measured to be

$$\epsilon_{15°} = 99.9\% \pm 0.1\%$$

**Spatial resolution:** The spatial resolution is obtained by plotting the difference between the track position predicted by the telescope on the plane of the DUT (the 3D pixel sensor) and the reconstructed hit of the DUT device. Figure 6 shows this distribution for normal incidence of tracks for both directions of the pixel. X corresponds to the direction of the long pixel side (400 µm), Y corresponds to the short direction (50 µm). For the hit reconstruction first only the digital information is used, i.e. the pixel with the largest signal above threshold collected in a cone around the extrapolated track position is taken as the hit pixel and its center is assumed to be the reconstructed position. The digital resolutions of pixel pitch divided by $\sqrt{2}$ in both directions, i.e. 14.4 µm and 114.5 µm are smeared by a Gaussian spread in the order of 5 µm in y and 6 µm in x, which is attributed to the resolution of the track extrapolation, the detector noise (∼380 e−) and the charge sharing behavior of the pixel cells. The structure visible especially in fig. 6(b), results from position dependent response efficiencies which were subject to the investigations above. As will be shown below, charge sharing in 3D sensors is restricted to a very narrow region of about ∼4 µm (threshold dependent) at the edge of a pixel. The improvement by using a charge-weighting algorithm (e.g. the so-called $\eta$-algorithm [15]) compared to a purely digital readout is therefore limited for normally incident tracks. In real vertex detectors charge sharing is usually purposely introduced by tilting the detectors to improve spatial resolution through charge interpolation [16].

**Charge sharing:** Next we investigate the sharing of charge between pixel cells as a function of the impact point of the track. This is studied by plotting the mean value of the cluster size distribution, where a cluster is a number of nearby hits above threshold. Also the fraction of charge appearing in the pixel with the largest charge of the cluster (seed pixel) is of interest. These quantities are displayed as pixel maps in fig. 7. The plots show most clearly the overall expected behavior. On average the cluster size increases for inclined
tracks. Correspondingly the seed charge fraction decreases, modulated by the position of the 3D electrodes and the resulting influence on the charge collection efficiency.

V. CONCLUSIONS

3D active silicon sensors, assembled as pixel devices with the ATLAS front end readout electronics have been tested and characterized for the first time in a high energy (100 GeV) test beam using a 4-plane microstrip beam telescope as reference. Devices with three 3D electrodes under the area of a readout pixel (50×400 μm²) were characterized. Hit detection efficiencies of 95.9% ± 0.1% (99.9% ± 0.1%) for normal (15° inclined) tracks have been obtained. The inefficiencies for normally incident tracks have been identified as being due the presence of the columnar 3D electrodes. For inclined tracks the hit efficiency approaches 100%. The spatial distribution of efficiencies and the mean cluster size as well as the fraction of charge seen by the seed pixel, which characterize the charge sharing, have been measured.

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