Derandomization from Algebraic Hardness*

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Abstract

A hitting-set generator (HSG) is a polynomial map Gen : \( \mathbb{F}^k \rightarrow \mathbb{F}^n \) such that for all \( n \)-variate polynomials \( C \) of small enough circuit size and degree, if \( C \) is nonzero, then \( C \circ \text{Gen} \) is nonzero. In this paper, we give a new construction of such an HSG assuming that we have an explicit polynomial of sufficient hardness. Formally, we prove the following result over any field \( \mathbb{F} \) of characteristic zero:

Let \( k \in \mathbb{N} \) and \( \delta > 0 \) be arbitrary constants. Suppose \( \{P_d\}_{d \in \mathbb{N}} \) is an explicit family of \( k \)-variate polynomials such that \( \deg P_d = d \) and \( P_d \) requires algebraic circuits of size \( d^\delta \). Then, there are explicit hitting sets of polynomial size for the class \( VP \).

This is the first HSG in the algebraic setting that yields a complete derandomization of polynomial identity testing (PIT) for general circuits from a suitable algebraic hardness assumption. Unlike the prior constructions of such maps [NW94, KI04, AGS19, KST19], our construction is purely algebraic and does not rely on the notion of combinatorial designs.

As a direct consequence, we show that even saving a single point from the “trivial” explicit, exponential sized hitting sets for constant-variates polynomials of low individual-degree which are computable by small circuits, implies a deterministic polynomial time algorithm for PIT. More precisely, we show the following:

Let \( k \in \mathbb{N} \) and \( \delta > 0 \) be arbitrary constants. Suppose for every \( s \) large enough, there is an explicit hitting set of size at most \( ((s + 1)^k - 1) \) for the class of \( k \)-variate polynomials of individual degree \( s \) that are computable by size \( s^\delta \) circuits. Then there is an explicit hitting set of size \( \text{poly}(s) \) for the class of \( s \)-variates polynomials, of degree \( s \), that are computable by size \( s \) circuits.

As a consequence, we give a deterministic polynomial time construction of hitting sets for algebraic circuits, if a strengthening of the \( \tau \)-Conjecture of Shub and Smale [SS95, Sma98] is true.

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1 Introduction

The interaction of hardness and randomness is one of the most well studied themes in computational complexity theory, and in this work we focus on exploring this interaction further in the realm of algebraic computation. To set the stage, we start with a brief introduction to algebraic complexity.

The field of algebraic complexity primarily focuses on studying multivariate polynomials and their complexity in terms of the number of basic operations (additions and multiplications) required to compute them. Algebraic circuits (which are just directed acyclic graphs with leaves labelled by variables or field constant, and internal gates labelled by $+$ or $\times$) form a very natural model of computation in this setting, and the size (number of gates or wires) of the smallest algebraic circuit computing a polynomial gives a robust measure of its complexity.

The main protagonists in the hardness-randomness interaction in algebraic complexity are the hardness component, which is the question of proving superpolynomial lower bounds for algebraic circuits for any explicit polynomial family, and the randomness component, which is the question of designing efficient deterministic algorithms for polynomial identity testing (PIT) — the algorithmic task of checking if a given circuit computes the zero polynomial. Both these questions are of fundamental importance in computational complexity and are algebraic analogues of their more well known Boolean counterparts: the $P$ vs $NP$ question and the $P$ vs $BPP$ question respectively. These seemingly different problems are closely related to each other, and in this work we focus on one direction of this relationship; namely, the use of explicit hard polynomial families for derandomization of PIT.

It is known from an influential work of Kabanets and Impagliazzo [KI04] that lower bounds on the algebraic circuit complexity of explicit polynomial families lead to non-trivial deterministic algorithms for polynomial identity testing (PIT) of algebraic circuits. Moreover, the results in [KI04] show that stronger lower bounds give faster deterministic algorithms for polynomial identity testing. For instance, from truly exponential (or $2^\Omega(n)$) lower bounds, we get quasipolynomial (or $n^{O(\log n)}$) time deterministic algorithms for PIT. From weaker superpolynomial (or $n^{\omega(1)}$) lower bounds, we only seem to get a subexponential (or $2^{n^{o(1)}}$) time PIT algorithm.

However, no matter how good the lower bounds for algebraic circuits are, this connection between lower bounds and derandomization does not seem to give truly polynomial time deterministic algorithms for PIT. This is different from the Boolean setting, where it is known that strong enough boolean circuit lower bounds imply that $\text{BPP} = \text{P}$ [IW97]. The difference stems from the fact that, in the worst case, an $n$-variate degree $d$ polynomial $P$ needs to be queried on as many as $(n+d \choose d) \gg 2^n$ points to be sure of its nonzeroness. A key player in this interaction of hardness and randomness, in the context of algebraic complexity, is the notion of a hitting-set generator (HSG), which we now define.

**Definition 1.1** (Hitting-set generators). A polynomial map $G : \mathbb{F}^k \to \mathbb{F}^m$ given by $G(z_1, z_2, \ldots, z_k) = (g_1(z), g_2(z), \ldots, g_n(z))$ is said to be a hitting-set generator (HSG) for a class $C \subseteq \mathbb{F}[x_1, x_2, \ldots, x_n]$ of
polynomials if for every nonzero $Q \in C$, we have that $Q \circ G = Q(g_1, g_2, \ldots, g_n)$ is also nonzero.

We shall say that $G$ is $t(n)$-explicit if, for any $a \in \mathbb{F}^k$ of bit-complexity at most $n$, we can compute $G(a)$ in deterministic time $t(n)$. Here $k$ is called the seed length of the HSG and $n$ is called the stretch of the HSG. The maximum of the degrees of $g_1, g_2, \ldots, g_n$ is called the degree of the HSG.

If a polynomial map $G$ is an HSG for a class $C$ of circuits, we say $G$ fools the class $C$.

Informally, an HSG $G$ gives a polynomial map which reduces the number of variables in the polynomials in $C$ from $n$ to $k$ while preserving their nonzeroness. It is not hard to see that such polynomial maps are helpful for deterministic PIT for $C$. To test if a given $n$-variate polynomial $Q \in C$ is nonzero, it is sufficient to check that $Q \circ G$, a $k$-variate polynomial, is nonzero. If the degree of each $g_i$ is not-too-large, then a “brute-force” check (via the Ore-DeMillo-Lipton-Schwartz-Zippel Lemma [Ore22, DL78, Sch80, Zip79]) can be used to test if $Q \circ G$ is zero in at most $\text{poly}(t(n)) \cdot (\deg(G) \cdot \deg(Q))^{O(k)}$ time, if $G$ is $t(n)$-explicit. Thus, it is desirable to have HSGs that are very explicit (small $t(n)$), low degree and large stretch ($k \ll n$).

**PIT from the Boolean and algebraic perspective:** Typically, in algebraic complexity, the model of algebraic circuits are allowed to introduce arbitrary field constants on the wires, for free, to scale the value passed on. The size of an algebraic circuit usually just denotes the number of gates in the circuit (and not the complexity of intermediate constants used). In contrast, when PIT is interpreted as a Boolean computational task, the circuit is expected to be provided as input which includes the descriptions of any constants used by the input circuit. Hence, the size of the input circuit also includes the complexity of contants on edges of the circuit.

Often, for PIT, it is convenient to construct hitting sets for polynomials that are computable by algebraic circuits of a certain size $s$, which is the number of gates in the circuit and does not depend on the constants used on the edges. As long as the the hitting set produced is efficiently enumerable by a Turing machine, this is sufficient to solve the Boolean task of PIT as the size of the input is no more than the algebraic size of the circuit.

Throughout this paper, we always deal with this algebraic notion of size which refers to the number of gates in circuit (regardless of the constants used). Therefore, the notion of lower bounds will also refer to the number of gates necessary, and not related to the constants on the wires.

### 1.1 Prior construction of generators

We shall use $C(n, d, s)$ to denote the class of $n$-variate polynomials of degree at most $d$ that are computable by size $s$ circuits, and $C(n, \text{i-deg: } d, s)$ to denote the class of $n$-variate polynomials of individual degree\(^1\) at most $d$ that are computable by size $s$ circuits.

**Generators from combinatorial designs:** One of the earliest (and most well-known) applications of lower bounds to derandomization is the construction of pseudorandom generators (PRG) from

\(^1\)maximum degree of any variable; for example, a multilinear polynomial has individual degree 1.
hard explicit Boolean functions by Nisan and Wigderson [NW94]. In the algebraic setting, an analogous construction was shown to produce HSGs by Kabanets and Impagliazzo [KI04]. These constructions are based on the notion of a combinatorial design, which is a family of subsets that have small pairwise intersection. Given an explicit construction of such a combinatorial design (e.g. a family $\mathcal{F} = \{S_1, S_2, \ldots, S_n\}$ of subsets of $[k]$ of size $t$ each), the PRG/HSG in [NW94, KI04] is then constructed by just taking a hard polynomial $P(x_1, \ldots, x_t)$ and defining the map as $G(y_1, y_2, \ldots, y_k) = (P(y|S_1), P(y|S_2), \ldots, P(y|S_n))$. The proof of correctness for this HSG goes via a hybrid argument and a result of Kaltofen [Kal89].

**Bootstrapping hitting sets and HSGs with large stretch:** In a recent line of work [AGS19, KST19] the following surprising bootstrapping phenomenon was shown to be true for hitting sets for algebraic circuits. The following is the statement from [KST19]:

**Theorem 1.2 ([KST19]).** Let $\delta > 0$ and $n \geq 2$ be constants. Suppose that, for all large enough $s$, there is an explicit hitting set of size $s^{n-\delta}$ for all degree $s$, size $s$ algebraic formulas (or algebraic branching programs, or circuits respectively) over $n$ variables. Then, there is an explicit hitting set of size $s^{\exp(\exp(O(\log^* s)))}$ for the class of degree $s$, size $s$ algebraic formulas (or algebraic branching programs, or circuits respectively) over $s$ variables.

In other words, a slightly non-trivial explicit construction of hitting sets even for constant-variate algebraic circuits implies an almost complete derandomization of PIT for algebraic circuits. A natural question in this direction which has remained open is the following.

**Question 1.3 ([KST19]).** Can slightly non-trivial hitting sets for constant-variate algebraic circuits can be bootstrapped to get polynomial size (and not just almost polynomial size as in Theorem 1.2) hitting sets for all circuits?

The proof of Theorem 1.2 can also be interpreted as a different HSG for algebraic computation. This HSG, given the hypothesis of Theorem 1.2, stretches $k$ bits to $n$ bits (for arbitrarily large $n$), but the degree and explicitness of the generator grows as $n^{\exp(\exp(O(\log^* n)))}$. Thus, this construction comes very close to answering Question 1.3 without completely answering it. This HSG is essentially constructed via a repeated composition of the HSG in [KI04, NW94] where, for each step, it uses a different hard polynomial with gradually increasing hardness. Due to this inherent iterative nature of the construction, it seems difficult to reduce the degree and explicitness of such HSG constructions to $\text{poly}(n)$.

**The need to go beyond design-based HSGs:** In the set up of boolean computation, observe that we cannot expect to have any PRG (or even HSG) of seed length $k$ to fool circuits of size much larger than $n^{2^k}$ since we can construct a circuit of size $O(n^{2^k})$ to identify the range of the generator.

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2 Even though the construction of the generator is the same in [KI04] and [NW94], there are crucial differences in the analysis. In particular, the analysis for the HSG in [KI04] relies on a deep result of Kaltofen [Kal89] about low degree algebraic circuits being closed under polynomial factorization.
(consisting of $2^k$ strings of length $n$ each). A similar argument gives an upper bound of $(dD)^{O(k)}$ on the size of degree $D$ algebraic circuits which can be fooled by a HSG with seed length $k$ and degree $d$. Thus, while the stretch of any boolean PRG constructed via hardness of a boolean function is upper bounded by $n2^k$, in the algebraic setting, one could hope for a construction of hitting-set generators of stretch as large as $d^k$ from sufficiently hard explicit polynomial families. However, till recently, there were no known constructions of such HSGs with stretch larger than $2^k$ (even for non-constant $k$). Note that obtaining a stretch of $d^k$ in the algebraic world be meaningful even when $k = O(1)$ if $d$ is a growing parameter (as we could conceivably have $d^k \gg n \cdot 2^k$). An HSG with strong enough parameters would answer the following very natural question.

**Question 1.4.** Let $k(d) : \mathbb{N} \rightarrow \mathbb{N}$ be a non-decreasing function (a slow-growing function, or constant). If there is an explicit (in the dense representation$^4$) polynomial family $\{P_d\}_{d \in \mathbb{N}}$, where $P_d$ is a $k(d)$-variate polynomial of degree $d$ such that any algebraic circuit computing it has size $d^{O(k)}$, then is PIT in $\text{DTIME}(n^{\text{poly}(k(n))})$ ?

In particular, if $k = O(1)$, does an explicit $k$-variate polynomial family $\{P_d\}_{d \in \mathbb{N}}$, with $\deg(P_d) = d$, that requires size $d^{\Omega(1)}$ imply that PIT is in $\mathbb{P}$?

Note that the hitting set generators by Kabanets and Impagliazzo [KI04] already answer the above question for the specific case when $k = \text{poly}(\log n)$. The above question asks if a strengthening of their hypothesis (to ask for lower bounds for polynomial families on fewer variables) yields a stronger conclusion. In particular, is there a suitable lower bound for algebraic circuits that would yield a complete derandomization of PIT?

Another reason for looking beyond the design-based HSGs in the algebraic setting is that by definition, a design-based HSG is combinatorial. Aesthetically, it seems desirable to have a route from algebraic lower bounds to algebraic pseudorandomness which does not rely on clever combinatorial constructions.

**PRGs of Shaltiel & Umans [SU05] and Umans [Uma03]:** An alternative to the design-based PRGs in the boolean setting is the generator of Shaltiel and Umans [SU05], and a related follow up work of Umans [Uma03]. These generators are quite different from the design-based generators of Nisan and Wigderson [NW94] and, in particular, appear to be more algebraic in their definition and analysis. We refer the interested reader to the original papers [SU05, Uma03] for the formal definitions of these generators and further details.

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$^3$Indeed, we know from elementary counting (or dimension counting) arguments that there exist degree $d$ polynomials in $k$ variables which require algebraic circuits of size nearly $\binom{d+k}{k}$, which can be approximated by $d^{O(k)}$ when $k$ is much smaller than $d$, which is the range of parameters we work with in this paper.

$^4$When there are implications to Boolean classes involved, we ought to be careful about the notion of explicitness. For instance, we may have a polynomial $P$ that is explicit in the sense that there is a small algebraic circuit but the circuit involves arbitrary constants. In this instance, we mean that there is deterministic $\text{poly}(d)$-time algorithm that, on input $1^d$, outputs $P_d$ as a sum of monomials.
The algebraic nature of these PRGs makes them good candidates for potential HSGs in the algebraic setting and, indeed, this work was partially motivated by this goal. However, as far as we understand, it remains unclear whether there is an easy adaptation of these PRGs which works for algebraic circuits. In particular, the hardness required for the analysis of the PRGs in [SU05, Uma03] appears to be inherently functional, i.e. they assume that it is hard to evaluate the polynomial over some finite field. In the context of algebraic complexity, the more natural notion of hardness is that it is hard to compute the polynomial syntactically as a formal polynomial via a small algebraic circuit.

1.2 Our Results

Our main result is the construction of a hitting-set generator that answers Question 1.4, for characteristic zero fields. In this paper, we work with slow-growing functions such as $k(d) = o(\sqrt{\log d})$ but we state our results in the introduction for the setting where $k = O(1)$. This is to present our main results without any notational clutter; the general statements can be found in the subsequent sections.

Theorem 1.5 (Main theorem; simpler form of Theorem 3.3). Assume that the underlying field $\mathbb{F}$ has characteristic zero. Let $k \in \mathbb{N}$ and $\delta > 0$ be arbitrary constants. Suppose $\{P_{k,d}\}_{d \in \mathbb{N}}$ is an explicit family of $k$-variate polynomials such that $\deg(P_{k,d}) = d$ and $P_{k,d}$ requires circuits of size at least $d^\delta$. Then, there are explicit hitting sets of size $\text{poly}_{d, k}(s)$ for the class $C(s, s, s)$.

As mentioned earlier, the above theorem can also be extended to slow-growing functions $k : \mathbb{N} \to \mathbb{N}$ such as $k(d) = o(\sqrt{\log d})$, which would yield non-trivial (albeit superpolynomial sized) hitting sets. The formal statement can be seen in Theorem 3.3. This theorem answers Question 1.4 affirmatively. In particular, if $k = O(1)$ and if we have an explicit $k$-variate hard polynomial family $\{P_d\}_{d \in \mathbb{N}}$ with $\deg P_d = d$ that requires circuits of size $d^{\Omega(1)}$, then PIT $\in \mathbb{P}$. As alluded to in the introduction, we do not know of prior constructions of HSGs with these properties.

In addition to being interesting on its own, Theorem 1.5 leads to the following result which shows that bootstrapping of hitting sets can be done. We recall the following simpler version of the Polynomial Identity Lemma\(^5\) [Ore22, DL78, Sch80, Zip79].

Lemma (Folklore). Let $f$ be a nonzero $n$-variate polynomial of individual degree at most $d$. Then, for any set $S \subseteq \mathbb{F}$ with $|S| > d$, there is a point $a \in S^n$ such that $f(a) \neq 0$.

This implies that the class of $k$-variate polynomials of individual degree $d$ have hitting sets of size $(d+1)^k$, irrespective of the circuit size. On the other hand, a simple counting argument shows that a random set of size $O(s^2)$ is a hitting set for the class of size $s$ circuits, with high probability. The following theorem shows that even improving on this bound of $(d+1)^k$ by even one point for small circuits would yield to a complete derandomization of PIT.

\(^5\)This version below is an immediate consequence of the Combinatorial Nullstellensatz [Alo99] but a simpler proof is to just use the fact that a univariate degree $d$ polynomial can have at most $d$ roots, one variable at a time (Lemma 2.3).
Theorem 1.6 (Bootstrapping hitting sets). Assume that the underlying field $\mathbb{F}$ has characteristic zero. Let $k \in \mathbb{N}$ and $\delta > 0$ be arbitrary constants. Suppose that, for all large enough $s$, there is an explicit hitting set of size $(s + 1)^k - 1$ for the class $\mathcal{C}(k, i\text{-deg: } s, s^\delta)$. Then, there is an explicit hitting set of size $\text{poly}_{\delta,k}(s)$ for $\mathcal{C}(s, s, s)$.

The above theorem answers Question 1.3 in a strong sense over characteristic zero fields. However, it is crucial that we work with the class of algebraic circuits and not subclasses such as algebraic branching programs or algebraic formulas.

A connection to Shub-Smale’s $\tau$-conjecture

The following conjecture, called the $\tau$-Conjecture, attributed to Shub and Smale [SS95, Sma98] is a well known conjecture in algebraic complexity.

Definition 1.7 ($\tau(f)$). For a univariate polynomial $f(x)$, let $\tau(f)$ denote the number of additions, subtractions and multiplications required to compute $f(x)$ starting with two leaves labelled with 1 and $x$ respectively (all intermediate constants are expected to be computed from the leaf labelled 1).

Let $\text{size}(f)$ refer to its algebraic circuit size, which is the smallest number of gates (with arbitrary constants on the wires) required to compute $f(x)$.

Conjecture 1.8 (Smale’s 4th Problem [Sma98], or Shub and Smale’s $\tau$-conjecture [SS95]). There is an absolute constant $c$ such that the number of distinct integer zeroes for a univariate polynomial $f(x)$ is bounded by $(\tau(f))^c$.

Consider the following strengthened conjecture that uses the algebraic size, $\text{size}(f)$ instead.

Conjecture 1.9 (Strengthened $\tau$-conjecture). There is an absolute constant $c$ such that the number of distinct integer zeroes for a univariate polynomial $f(x)$ is bounded by $(\text{size}(f))^c$.

If the above conjecture is true for a constant $c$, then this immediately implies that the polynomial $P_s(x) = (x - 1) \cdots (x - s)$, which has $s$ distinct integer roots, requires algebraic circuits of size $s^{1/c}$. In other words, the conjecture immediately implies a lower bound on the size of algebraic circuits computing the polynomial family $\{P_s\}$. Thus, if we invoke this lower bound together with Theorem 1.5, and using the fact that the coefficients of $P_s$ are not too large, we have the following corollary.

Corollary 1.10. If Conjecture 1.9 is true then there exist hitting sets, that can be enumerated by a deterministic Turing machine in time $\text{poly}(s)$, for algebraic circuits of size and degree at most $s$.

Thus, if the strengthened $\tau$-conjecture Conjecture 1.9 is true, then PIT is in $P$.

Note that it is important that the coefficients of $P_s$ are bounded by $s! = 2^{O(s \log s)}$. Strassen [Str74] showed (amongst other things) an unconditional lower bound of $\Omega(d^{1/3})$ on the circuit complexity of the polynomial $Q_d = \sum_{i=0}^{d} 2^i x^i$. When invoked with the polynomial $Q_d$, Theorem 1.5 would yield a construction of hitting set consisting of $\text{poly}(s)$ points for general algebraic circuits of size $d$. 
However, this hitting set is not efficiently enumerable by a Turing machine in polynomial time since the coefficients of $Q_d$ are too large. In contrast to this, Corollary 1.10 gives a deterministic construction of hitting sets of polynomial size and polynomial bit complexity, although the correctness of the construction is conditional on Conjecture 1.9.

1.3 An overview of the proof

All the theorems are a consequence of properties of our hitting-set generator:

**Definition 1.11 (The generator).** For any $k$-variate polynomial $P(z)$, define the map $Gen_P : \mathbb{F}^k \times \mathbb{F}^k \rightarrow \mathbb{F}^{n+1}$ as follows:

$$Gen_P(z, y) = (\Delta_0(P)(z, y), \Delta_1(P)(z, y), \ldots, \Delta_n(P)(z, y)),$$

where $\Delta_i(P)$ is the homogeneous degree $i$ (in $y$) component in the Taylor expansion of $P(z + y)$, i.e.

$$\Delta_i(P)(z, y) = \sum_{e \in \mathbb{N}^k, |e| = i} \frac{y^e}{e!} \cdot \frac{\partial P}{\partial z^e}. \quad \text{(here, } |e| := e_1 + \cdots + e_k \text{ and } e! := e_1! \cdots e_k!)$$

It is clear that the above definition is $d^{O(k)}$-explicit, where $d = \deg(P)$, as we can express $P$ as a sum of $d^k$ monomials and compute each component of $Gen_P(z, y)$ with a small additional cost. The proof proceeds by showing that the above generator $Gen_P$ is an HSG provided $P(z)$ is hard enough.

**Theorem 1.12 (Gen$_P$ is an HSG).** Assume that the underlying field $\mathbb{F}$ has characteristic zero. Let $P$ be a $k$-variate polynomial of degree $d$, for arbitrary integers $k, d > 0$. Suppose $P$ cannot be computed by algebraic circuits of size $\tilde{s} = (s \cdot D \cdot d^3 \cdot n^{10k})$ for parameters $n, D, s$. Then, for any $(n + 1)$-variate polynomial $C(x_0, \ldots, x_n) \in C(n + 1, D, s)$, we have

$$C \neq 0 \iff C \circ Gen_P(z, y) \neq 0.$$

The proof of Theorem 1.5 is relatively straightforward from Theorem 1.12 by setting some parameters appropriately and using a Kronecker-ing trick (see Section 3.2). To show that the HSG in Definition 1.11 is indeed a hitting-set generator for $C(n + 1, D, s)$, we focus our attention on a purported nonzero polynomial $C(x_0, \ldots, x_n) \in C(n + 1, D, s)$, for parameters $n, D, s$. Then, for any $(n + 1)$-variate polynomial $C(x_0, \ldots, x_n) \in C(n + 1, D, s)$, we have

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$$C \neq 0 \iff C \circ Gen_P(z, y) \neq 0.$$
Reducing to the non-degenerate case: In the non-degenerate case we insist that, in addition to having $C \circ \text{Gen}_P = 0$, we have $(\partial_{x_n} C) \circ \text{Gen}_P \neq 0$; i.e. the derivative of $C$ with respect to the last variable $x_n$ is fooled by the generator.

Given a nonzero circuit $C$ such that $C \circ \text{Gen}_P = 0$, and we wish to construct a circuit $C'$ with the above stronger condition. We may assume without loss of generality that the circuit $C$ is minimal in the sense that all circuits of the same size depending on fewer variables are fooled by the generator; in particular, $C$ depends non-trivially on the last variable $x_n$.

We consider the circuit $\tilde{C}$ obtained by substituting the generator for all coordinates except $x_n$. Interpreting this as a univariate polynomial in $x_n$, with coefficients in $\mathbb{F}(z, y)$, $\tilde{C}(x_n) = C(g_1, \ldots, g_{n-1}, x_n)$

where $\text{Gen}_P = (g_1, \ldots, g_{n-1}, g_n)$. The minimality of $C$ allows us to argue $\tilde{C}(x_n)$ is a nonzero polynomial (for otherwise, $\tilde{C} = C(x_1, \ldots, x_{n-1}, a) \neq 0$, for a random $a \in \mathbb{F}$, is also not fooled by the generator since $\tilde{C} \circ \text{Gen}_P = \tilde{C}(a) = 0$, contradicting the minimality of $C$). Using the Remainder theorem, this then implies that there must be some $0 \leq i \leq \deg_{x_n}(\tilde{C})$ such that

$$
\left(\partial_{x_n} \tilde{C}\right)(g_n) = 0,
$$

$$
\left(\partial_{x_n+1} \tilde{C}\right)(g_n) \neq 0.
$$

Hence, the circuit $C' = \partial_{x_n} (C)$ satisfies the non-degeneracy condition. Standard interpolation arguments shows that the circuit complexity of $C'$ is at most $O(sD)$ and we work with this circuit.

The proof of Lemma 3.2: The proof of the lemma can be viewed as a variant of the standard Newton Iteration (or Hensel lifting) based argument often used in the context of root finding, although there are some crucial differences. We iteratively construct the polynomial $P(z)$ one homogeneous component at a time (recall that $P(z)$ is a $k$-variate polynomial of degree $d$). In fact, our induction hypothesis needs to be a bit stronger than this. For our proof, we maintain the invariant that at the end of the $i$th iteration, we have a multi-output circuit which computes all the partial derivatives of order at most $n$ of all the homogeneous components of $P(z)$ of degree at most $i + n$. However, for this overview, we ignore this technicality and pretend that we are directly working with the homogeneous components of $P(z)$.

For the base case, we assume that we have access to all the homogeneous components of $P$ of degree at most $n$, which are homogeneous polynomials of degree at most $n$ on $k$ variables and are trivially computable by a circuit of size at most $n^{5k}$. the presumed hardness of $P$ for $d \gg n$. Thus, we have $n$ homogeneous components of $P(z)$, and the goal is to use them and the non-

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7For an infinite field $\mathbb{F}$, by choosing a “random” $a \in \mathbb{F}$ we mean choosing $a$ uniformly from a suitably large but finite subset of $\mathbb{F}$.

8This is also where the dependency of the hardness of $P$ on the degree $D$ appears. We suspect that this dependency can be removed.
degeneracy assumption to reconstruct all of $P$. Let us assume that we have already computed $P_0, \ldots, P_i$, where $P_j$ is the homogeneous component of $P(z)$ of degree equal to $j$. We now focus on recovering the homogeneous component $P_{i+1}$ of degree equal to $i + 1$. Observe that $\Delta_n(P_{i+1})$ is a homogeneous (in $z$) polynomial of degree $(i - n + 1)$. We show that given the non-degeneracy condition in the hypothesis of the lemma, there is a small circuit such that, modulo the ideal $\langle z \rangle^{i-n+2}$, it computes $\Delta_n(P_{i+1})(z, y)$. Since $\Delta_n(P_{i+1})(z, y)$ is essentially a generic linear combination of $n$-th order derivatives of $P_{i+1}(z)$, it is not hard to show\footnote{In particular, this part of the argument relies on the stronger hypothesis that we have access to each of the order $n$ partial derivatives of $P_0, P_1, \ldots, P_i$.} that we can obtain a small circuit that outputs each of the $n$-th order partial derivatives of $P_{i+1}(z)$, modulo higher degree monomials. Then we would be able to reconstruct $P_{i+1}(z)$ mod $\langle z \rangle^{i+2}$ via repeated applications of the Euler’s differentiation formula for homogeneous polynomials:

**Fact 1.13** (Euler’s formula for differentiation of homogeneous polynomials). If $A(x_1, \ldots, x_k)$ is a homogeneous polynomial of degree $t$, then $\sum_{i=1}^{k} x_i \cdot \partial_{x_i} A = t \cdot A(x_1, \ldots, x_k)$.

One crucial point in this entire reconstruction is that each step of the reconstruction only incurs an additive blow-up in size and hence can be repeated for polynomially many steps to recover each homogeneous part of $P$ (Figure 1 in Section 3 contains a pictorial description of the inductive step).

However, we are still left with the task of getting rid of the higher order terms as we have only constructed a circuit computing $P_{i+1}$ modulo the ideal $\langle z \rangle^{i+2}$. We need to extract the lowest degree homogeneous parts from the outputs. The standard way to proceed here is to perform a homogenisation but this needs to be done carefully. Typically, extracting a certain homogeneous part incurs a multiplicative blow-up in size which is unaffordable in this setting as this needs to be performed for $d$ steps! Fortunately, the structure of the circuit built has the property that each inductive step adds more gates atop the outputs of the previous steps and hence it suffices to only homogenise the newly added gates. This allows us to argue that each inductive step incurs only an additive blow-up of $d^2 \cdot n^{O(k)} \cdot sD$. By performing the reconstruction step to extract all homogeneous components of $P$, we can construct a small enough circuit for $P$, contradicting the hardness of $P$. That would complete the proof of Theorem 1.12.

**Similarities with PRGs of Shaltiel and Umans [SU05, Uma03] and list decoding algorithms of Kopparty [Kop15]:** We remark that at a high level, our construction of the HSG was inspired by the constructions by Shaltiel and Umans [SU05, Uma03], although the precise form of our generator seems different from those in [SU05, Uma03]. We also note that the set up of induction we have in the proof of Lemma 3.2 is very similar to the set up used by Kopparty [Kop15] in the context of list decoding Multiplicity codes. More precisely, our induction is similar to what is used in constructing a power series expansion of a non-degenerate solution of the univariate Cauchy-Kovalevskii differential equations, which are used in [Kop15]. The key difference is that although we work with a multivariate setting (and hence deal with a partial differential equation of high order and high degree), the iterative proof of Lemma 3.2 resembles the list decoding algorithm
for univariate multiplicity codes in [Kop15] (which deals with an ordinary differential equation of high order and high degree). It appears to be of interest to understand this analogy further.

Relating Theorem 1.6 and results of Jansen and Santhanam [JS12]: The hypothesis of Theorem 1.6, for $k = 1$, bears some similarities with a result of Jansen and Santhanam [JS12]. Jansen and Santhanam showed, roughly speaking$^{10}$, that if the class $C(1, d, \text{polylog}(d))$ has a hitting set of size $d$ (one point fewer than the trivial hitting set of size $d + 1$) that can be enumerated by small, succinctly encodable $\text{TC}^0$ circuits, then Perm does not have polynomial sized constant-free algebraic circuits.

Our hypothesis, for $k = 1$, is similar in the sense that it requires a saving of one point from the trivial hitting set for the appropriate class, but we only need the standard notion of explicitness for the hitting set. However, the two results work for different ranges of parameters — their hypothesis requires the hitting set for $C(1, d, \text{polylog}(d))$ whereas ours requires the same for $C(1, d, d^\delta)$ for some constant $\delta > 0$. Furthermore, our conclusion is also in terms of derandomizing PIT, and not a lower bound. It would be interesting to see if our hypothesis is also sufficient to obtain a similar conclusion as Jansen and Santhanam.

2 Notation and preliminaries

We follow the following notations.

- We use $\mathbb{N}$ to denote the set of natural numbers $\{1, 2, 3, \ldots \}$.
- Throughout the paper, we think of $\mathbb{F}$ as a field of characteristic zero (or large enough).
- We use boldface letters such as $\mathbf{z}$ to denote tuples: $\mathbf{z} = (z_1, z_2, \ldots, z_k)$; in almost all instances, the length of the tuple will be clear from context. For an exponent vector $e$, we shall use $\mathbf{z}^e$ to denote the monomial $z_1^{e_1} \cdots z_k^{e_k}$. Let $|e| := \sum e_i$.
- We use $\partial_{\mathbf{z}^e} (P(z))$ to denote the partial derivative $\frac{\partial^{|e|}(P)}{\partial \mathbf{z}^e}$.
- We use $\langle \mathbf{z} \rangle^i$ to denote the ideal in $\mathbb{F}[\mathbf{z}]$ generated by all degree $i$ monomials in $\mathbf{z}$.
- We use $\mathcal{P}(k, d)$ to denote the class of $k$-variate polynomials of degree at most $d$.

2.1 Algebraic circuits

Definition 2.1 (Algebraic circuits). An algebraic circuit is a directed acyclic graph whose internal nodes are labelled by $+$ and $\times$, with leaves (in-degree zero nodes) labelled by variables or field constants. The addition and multiplication gates naturally compute the sum and product of its children respectively. Edges may carry arbitrary field constants whose semantic behaviour is to scale the value passing through it.

$^{10}$The exact theorem statement is more complicated, and also handles a larger range of parameters, but the above description contains the essence of their main statement.
The size of an algebraic circuit refers to the number of gates in the circuit (and is independent of the constants that may appear on the edges/ wires).

We use the notation $C(n,d,s)$ to denote the class of $n$-variate polynomials of degree bounded by $d$ that are computable by circuits of size at most $s$. Similarly, $C(n,\text{i-deg: }d,s)$ refers to the class of $n$-variate polynomials of individual degree bounded by $d$ computable by circuits of size $s$.

### 2.2 PIT preliminaries

The following well-known lemma gives an exponential (in the number of variables) sized hitting set for low degree polynomials.

**Lemma 2.2** ([Ore22, DL78, Sch80, Zip79]). Let $f$ be a nonzero $n$-variate polynomial of degree at most $d$. Then for any set $S \subseteq \mathbb{F}$ with $|S| > d$, there is a point $a \in S^n$ such that $f(a) \neq 0$.

The following is a variant for the case of polynomials of individual degree as opposed to total degree.

**Lemma 2.3.** Let $f$ be a nonzero $n$-variate polynomial of individual degree at most $d$. Then, for any set $S \subseteq \mathbb{F}$ with $|S| > d$, there is a point $a \in S^n$ such that $f(a) \neq 0$.

**Proof.** Let $S \subseteq \mathbb{F}$ with $|S| \geq d + 1$. We may interpret $f$ as a univariate polynomial in $x_1$ of degree at most $d$ with coefficients from $\mathbb{F}(x_2, \ldots, x_n)$. Since a nonzero univariate polynomial of degree $d$ can have at most $d$ roots, there must be some $a_1 \in S$ such that $f(a_1, x_2, \ldots, x_n) \neq 0$. Repeating this argument for variables $x_2, \ldots, x_n$, the lemma follows.

A more general statement is the following Combinatorial Nullstellensatz due to Alon [Alo99].

**Theorem 2.4** (Combinatorial Nullstellensatz [Alo99]). Let $f(x_1, \ldots, x_n)$ be a nonzero polynomial with $\deg(f) \leq t_1 + \cdots + t_n$, and suppose the coefficient of $x_1^{t_1} \cdots x_n^{t_n}$ is nonzero in $f$. If $S_1, \ldots, S_n \subseteq \mathbb{F}$ with $|S_i| \geq (t_i + 1)$, then there is a point $a = (a_1, \ldots, a_n) \in S_1 \times \cdots \times S_n$ such that $f(a) \neq 0$.

It is also known that existence of non-trivial hitting sets for a class $C$ can be used to construct hard polynomials. We state the version that corresponds to the individual degree as opposed to total degree.

**Theorem 2.5** (Heintz and Schnorr [HS80], Agrawal [Agr05]). Let $H(n, \text{i-deg: }d,s)$ be an explicit hitting set for the class $C(n,\text{i-deg: }d,s)$. Then, for every $k \leq n$ and $d'$ such that $d' \leq d$ and $(d' + 1)^k > |H(n,\text{i-deg: }d,s)|$, there is a nonzero polynomial on $k$ variables and individual degree $d'$ that vanishes on the hitting set $H(n,\text{i-deg: }d,s)$, and hence cannot be computed by a circuit of size $s$.

Finally, we need the following notion of interpolating sets for a class of polynomials.

**Definition 2.6** (Interpolating sets for $P(k,d)$). Let $M_{k,d}$ denote the number of $k$-variate monomials of degree at most $d$. That is, $M_{k,d} = \binom{k+d}{d}$.

A set of points $a_1, \ldots, a_r \in \mathbb{F}_k$ is said to be an interpolating set for $P(k,d)$ if the vectors

$$\left\{ \left( a_i^e : e \in \mathbb{Z}_{\geq 0}^k, |e| \leq d \right) : i \in [r] \right\} \subseteq \mathbb{F}^{M_{k,d}}$$
form a spanning set for \( \mathbb{F}^{M_{k,d}} \).

Equivalently, for every \( \mathbf{e} \in \mathbb{Z}_{\geq 0}^k \) with \( |\mathbf{e}| \leq d \), there exists field constants \( \beta_1, \ldots, \beta_r \) such that for every \( f(z) \in \mathcal{P}(k,d) \), we have

\[
\text{coeff}_\mathbf{e}(f) = \sum_{i=1}^r \beta_i \cdot f(a_i).
\]

A canonical example of an interpolating set for \( \mathcal{P}(k,d) \) is \( S^k = \{(s_1, \ldots, s_k) : s_i \in S \ \forall i\} \) where \( S \subseteq \mathbb{F} \) is a set of at least \((d + 1)\) distinct field elements. The following well-known proposition says that we can find interpolating sets inside a suitably large grid, even in the presence of additional polynomial constraints.

**Proposition 2.7** (Interpolating sets with additional polynomial constraints). Let \( d, k \) be arbitrary positive integers, and any polynomial \( Q(x) \) of degree \( D \). If \( S \subseteq \mathbb{F} \) with \( |S| \geq (D + d + 1) \) there is a set of points \( a_1, \ldots, a_{M_{k,d}} \in S^k \) such that \( Q(a_i) \neq 0 \) for all \( i \in [M_{k,d}] \) and \( \{a_1, \ldots, a_{M_{k,d}}\} \) is an interpolating set for \( \mathcal{P}(k,d) \).

**Proof.** Consider the following \( M_{k,d} \times M_{k,d} \) matrix \( \Lambda \):

The rows of \( \Lambda \) are indexed by \([M_{k,d}]\) and the columns indexed by monomials \( x^e \) of degree at most \( d \), and the entry at \((i, x^e)\) is \( a_i^e \) (the monomial \( x^e \) evaluated at \( a_i \)).

The condition that \( \{a_1, \ldots, a_{M_{k,d}}\} \) is an interpolating set can be characterised by the nonzeroness of the determinant of \( \Lambda \). Hence, the condition that \( a_1, \ldots, a_{M_{k,d}} \) forms an interpolating set and \( Q(a_i) \neq 0 \) for all \( i \) can be characterised by the nonzeroness of the polynomial

\[
\Gamma(a_1, \ldots, a_{M_{k,d}}) := \Lambda(a_1, \ldots, a_{M_{k,d}}) \cdot \prod_{i=1}^{M_{k,d}} Q(a_i).
\]

Let \( \{a_{ij} : i \in [M_{k,d}], j \in [k]\} \) be indeterminates, corresponding to the coordinates of the points \( a_1, \ldots, a_{M_{k,d}} \). Although \( \Gamma \) is a \( k \cdot M_{k,d} \)-variate polynomial of total degree \( O((d + D)M_{k,d}) \), the individual degree is at most \((D + d)\) (since each row of \( \Gamma \) is indexed by a disjoint set of variables).

Thus, by Theorem 2.4, there are points \( a_1, \ldots, a_{M_{k,d}} \in [D + d + 1]^k \) that forms an interpolating set for \( \mathcal{P}(k,d) \) and also keeps \( Q \) nonzero. \( \square \)

### 2.3 Homogenisation

**Definition 2.8** (Homogeneous circuits). A circuit \( C \) is said to be homogeneous if every gate of the circuit computes a homogeneous polynomial.

For a non-homogeneous polynomial \( f \), we shall say that a homogeneous (multi-output) circuit \( C \) computes \( f \) if the outputs of \( C \) are the homogeneous parts of \( f \). \( \diamond \)

**Lemma 2.9** (Strassen’s homogenisation). Let \( C \) be a circuit of size \( s \) computing a homogeneous polynomial of degree \( d \). Then, there is a homogeneous circuit \( C' \) of size at most \( O(sd^2) \) computing the same polynomial. \( \square \)
Lemma 2.10 (Partial homogenisation). Let $C$ be a multi-output homogeneous circuit of size $s$, with $m$ outputs computing homogeneous polynomials $f_1, \ldots, f_m$. Suppose $C'$ is a multi-output, $m$-input circuit of size $s'$. Then, there is a homogeneous circuit $D$ of size at most $s + O(s' \cdot d^2)$ computing $C' \circ C = C'(f_1, \ldots, f_m)$, where $d$ is the maximum degree of the outputs of $C'(f_1, \ldots, f_m)$.

Proof. We follow the standard homogenisation procedure, but applied only to the circuit $C'$ — we replace each gate $g \in C'$ by copies $g_0, \ldots, g_d$ and add the following connections:

$$g_a = \begin{cases} h_a^{(L)} + h_a^{(R)} & \text{if } g = h^{(L)} + h^{(R)} \\ \sum_{b=0}^a h_b^{(L)} \times h_{a-b}^{(R)} & \text{if } g = h^{(L)} \times h^{(R)} \end{cases}$$

and any leaf $\ell$ of $C'$ labeled with the $i$-th variable is renamed as $\ell_{\deg(f_i)}$. Since each of the $f_i$ is a homogeneous polynomial, it is immediate to see that the above transformation yields a homogeneous circuit for $C' \circ C$ of size at most $s + O(s' d^2)$.

2.4 The Generator

For a $k$-variate polynomial $P$, let $\Delta(P)(z, y) \in \mathbb{F}[z, y]$ be defined as

$$\Delta_i(P) = \sum_{e : |e| = n} \left( \frac{y^e}{e!} \right) \cdot \partial_{x^e}(P)$$

where $e! = e_1! \cdots e_k!$. The generator with respect to $P$ is defined as follows:

$$\text{Gen}_P(z, y) = (\Delta_0(P)(z, y), \ldots, \Delta_n(P)(z, y)).$$

The following are some simple observations about the operator $\Delta$.

Observation 2.11. For any $a \in \mathbb{F}^k$, if $P'(z) = P(z + a)$, then for all $i$ we have $\Delta_i(P')(z, y) = \Delta_i(P)(z + a, y)$. Hence, $\text{Gen}_{P'}(z, y) = \text{Gen}_P(z + a, y)$.

Observation 2.12. Let $P(z)$ and $Q(z)$ be polynomials such that $P = Q \mod \langle z \rangle^j$. Then, for any $i \leq j$, we have $\Delta_i(P) = \Delta_i(Q) \mod \langle z \rangle^{j-i}$.

3 The Main Theorem

We start by recalling the theorem that states that our generator is indeed a hitting-set generator.

Theorem 1.12 (Gen$_P$ is an HSG). Assume that the underlying field $\mathbb{F}$ has characteristic zero. Let $P$ be a $k$-variate polynomial of degree $d$, for arbitrary integers $k, d > 0$. Suppose $P$ cannot be computed by algebraic circuits of size $\bar{s} = (s \cdot D \cdot d^3 \cdot n^{10k})$ for parameters $n, D, s$. Then, for any $(n + 1)$-variate

\footnote{Note that this statement is meaningful only when $d \gg n^3$ as $P$ always has an algebraic circuit of size at most $d^k$.}
polynomial \( C(x_0, \ldots, x_n) \in C(n + 1, D, s) \), we have

\[
C \neq 0 \iff C \circ \text{Gen}_P(z, y) \neq 0.
\]

The rest of this section would be devoted to the proof of this theorem.

Let us assume the contrary. That is, there is a circuit \( C(x) \) of size \( s \) and degree \( D \) such that \( C \neq 0 \) but \( C \circ \text{Gen}_P(z, y) = 0 \). We shall assume, without loss of generality, that \( C \) depends nontrivially on the variable \( x_n \) and that no circuit \( C'(x) \) of size \( s \) and degree \( D \) with \( C' \) depending on fewer variables satisfy \( C' \neq 0 \) and \( C' \circ \text{Gen}_P(z, y) = 0 \).

The proof will proceed by inductively building a circuit that computes each homogeneous part of \( P \) but we would need the following preprocessing step.

Preprocessing the circuit: Let \( C(x_0, \ldots, x_n) \) be the minimal (in terms of number of variables) size \( s \) circuit that is not fooled by \( \text{Gen}_P(z, y) \). That is, \( C \circ \text{Gen}_P(z, y) = 0 \).

Claim 3.1. There is some \( i \geq 0 \) such that

\[
\partial_{x_i} C \circ \text{Gen}_P(z, y) = 0,
\]

\[
\partial_{x_{i+1}} C \circ \text{Gen}_P(z, y) \neq 0.
\]

Proof. Let \( \text{Gen}_P(z, y) = (g_0, \ldots, g_n) \). Consider the polynomial \( C(g_0, \ldots, g_{n-1}, x_n) \) (which is the application of the generator \( \text{Gen}_P \) to all coordinates except one) as an element \( \hat{C}(x_n) \in \mathbb{F}(y, z)[x_n] \).

Case 1: \( \hat{C} = 0 \)

In this case, using Lemma 2.3, let \( a \in \{0, 1, 2, \ldots, D\} \) such that \( C(x_0, \ldots, x_{n-1}, a) \neq 0 \). Then, \( C(x_0, \ldots, x_{n-1}, a) \) is also a nonzero polynomial computable by a size \( s \) circuit, depending on fewer than \( n + 1 \) variables, that also vanishes on \( \text{Gen}_P(z, y) \). This contradicts the minimality of \( C \).

Case 2: \( \hat{C} \neq 0 \)

Let \( r = \deg_{x_n}(\hat{C}) \). Note that if \( (\partial_{x_i} (\hat{C}))(g_n) = 0 \) for all \( 0 \leq i \leq t \), then \( (x_n - g_n)^{t+1} \) divides \( \hat{C} \).

Since \( \deg_{x_n}(\hat{C}) \) is \( r \), the largest \( e \) such that \( (x_n - g_n)^e \) divides \( \hat{C} \) is at most \( r \). Hence, there must be some \( t \leq r - 1 \) such that \( \partial_{x_i} (\hat{C})(g_n) \neq 0 \). Since \( \hat{C}(g_n) = C \circ \text{Gen}_P(z, y) = 0 \) and \( \partial_{x_i} (\hat{C})(g_n) \neq 0 \), there must be an intermediate derivative where a switch from zero to nonzero occurs. Hence, there must be some \( i < r \) such that

\[
\partial_{x_i} C \circ \text{Gen}_P(z, y) = \partial_{x_i} (\hat{C})(g_n) = 0,
\]

\[
\partial_{x_{i+1}} C \circ \text{Gen}_P(z, y) = \partial_{x_{i+1}} (\hat{C})(g_n) \neq 0.
\]

Let \( C' = \partial_{x_i}(C) \). In what follows, we will work with \( C' \) instead of \( C \). By interpolation, its size \( s' \leq s \cdot D \) (where, recall, \( D \geq \deg(C) \)) and we now have

\[
C' \circ \text{Gen}_P(z, y) = 0,
\]
We will defer the proof of this lemma to the end of the section and finish the rest of the proof.

Without loss of generality (by translating $z$ by a point in $\{0, 1, \ldots, dD\}^k$ if necessary, via Observation 2.11), we may assume that

$$\left( \partial_{x_n} (C') \circ \text{Gen}_{P} (z, y) \right) =: \Psi (y) \neq 0.$$  

Let $P = P_0 + P_1 + \cdots + P_d$ be the decomposition into homogeneous parts, with $P_i$ being the homogeneous part of degree $i$, and let $P_{\leq r} := \sum_{i \leq r} P_i$.

**The reconstruction:** We now proceed to describe the set up of the inductive reconstruction of a *small* circuit for $P$. The induction is on a parameter $j$ which takes values from 0 up to $d - n$. At the end of the $j^{th}$ step, we would have a circuit which computes all partial derivatives of order at most $j$ of homogeneous components of $P$ of degree at most $j + n$. We now describe the steps of the induction argument more formally.

**Base case** ($j = 0$): Each $\partial_{x^e} P_\ell$ for $|e| \leq n$ and $\ell \leq n$ can be explicitly written as a sum of at most $N := \binom{n+k}{n}$ monomials. Hence, there is a circuit $B_0$ of size $s_0 = N^2$ that computes \{ $\partial_{x^e} (P_\ell)$ : $0 \leq \ell \leq n$, $|e| \leq n$ \}.

**Induction hypothesis:** There is a circuit $B_{j-1}(z)$ of size at most $s_{j-1}$, with $N(n+j-1)$ outputs that computes $\partial_{x^e} P_\ell$ for each $e$ with $|e| \leq n$ and $\ell \leq n+j-1$.

**Induction step:** To construct a circuit $B_j(z)$ of size at most $s_j$ (to be defined shortly) that computes $\partial_{x^e} P_\ell$ for each $e$ with $|e| \leq n$ and $\ell \leq n+j$. Let $\Gamma_{j-1,a}$ be defined as

$$\Gamma_{j-1,a} := (\Delta_0 (P_{\leq n+j-1})(z, a), \ldots, \Delta_n (P_{\leq n+j-1})(z, a)).$$

**Lemma 3.2.** Let $a \in \mathbb{F}^k$ be such that $0 \neq \Psi (a) = (\partial_{x_n} C') \circ \text{Gen}_{P} (0, a)$. Then,

$$-1 \cdot C'(\Gamma_{j-1,a}) = \Delta_n (P_{n+j})(z, a) \mod (z)^{j+1}.$$  

We will defer the proof of this lemma to the end of the section and finish the rest of the proof.

We can begin with the circuit $B_{j-1}(z)$ that computes every $\partial_{x^e} (P_\ell)$ for $|e| \leq n$ and $\ell \leq n+j-1$. By taking suitable linear combinations of the output gates, we can create a new circuit $B_j$ of size at most $s_{j-1} + N^5$, that computes \{ $\Gamma_{j-1,a_t} : t \in [N]$ \}. Using Lemma 3.2 for each $a_t$, we then obtain
\[
\{ \partial_z^e(P_{n+j}) + \text{(monomials of degree } \geq n+j-|e|+1) : |e| \leq n \}
\]

Figure 1: Pictorial representation of \( B'_j \)

a circuit of size \( s_{j-1} + N^5 + s' \cdot N \) that computes \( \{ \Delta_n(P_{n+j})(z, a_t) : t \in [N] \} \) modulo the ideal \( \langle z \rangle^{j+1} \).

By definition, \( \Delta_n(P_{n+j})(z, a) \) is a suitable linear combination of the \( n \)-th order partial derivatives of \( P_{n+j}(z) \). As \( \{ a_1, \ldots, a_N \} \) was chosen to be an interpolating set, each \( \partial_z^e(P_{n+j}) \) with \( |e| = n \) can be written as a suitable linear combination of \( \{ \Delta_n(P_{n+j})(z, a_t) : t \in [N] \} \). Furthermore, since \( P_{n+j} \) is a homogeneous polynomial, we can also compute all its lower order derivatives via repeated applications of Euler’s formula (Fact 1.13). Overall, combined with the outputs of \( B_{j-1}(z) \), we have a circuit \( B'_j(z) \) (shown in Figure 1) of size \( s_{j-1} + N^{10} + s'N \) that computes

\[
\{ \partial_z^e(P_{\ell}) : |e| \leq n, \, \ell \leq n+j-1 \} \cup \{ \partial_z^e(P_{n+j}) : |e| \leq n \},
\]

where \( \partial_z^e(P_{n+j}) \mod \langle z \rangle^{n+j-|e|+1} = \partial_z^e(P_{n+j}) \) for every \( |e| \leq n \). At this point, the only task left to do is extracting the lowest degree homogeneous components of these outputs.

From Figure 1, the circuit \( B'_j \) is a composition of a circuit of size \( N^{10} + s'N \) over the homogeneous circuit \( B_{j-1} \) of size \( s_{j-1} \). By Lemma 2.10, we extract the lowest degree homogeneous parts of the outputs of \( B'_j \) by constructing an equivalent homogeneous circuit of size at most \( s_{j-1} + (N^{10} + s'N) \cdot d^2 \) that computes

\[
\{ \partial_z^e(P_{\ell}) : |e| \leq n, \, \ell \leq n+j \}.
\]
This completes the induction step.

Unraveling the induction for \( d - n \) steps, we eventually obtain a circuit of size at most \( s_{d-n} = O(s' \cdot d^3 \cdot N^{10}) = O(s \cdot D \cdot d^3 \cdot n^{O(k)}) \) that computes all the partial derivatives of order at most \( n \) of \( P_0, \ldots, P_d \), which in particular includes \( P_t, \ldots, P_d \) that can be summed to produce a circuit for \( P \) of size \( O(s \cdot D \cdot d^3 \cdot n^{O(k)}) \). However, this contradicts the hardness assumption of \( P \). Hence, it must be the case that \( C \circ \text{Gen}_P(z, y) \neq 0 \). This completes the proof of the main theorem barring the proof of Lemma 3.2; we address this next.

\( \square \) (Theorem 1.12)

### 3.1 Proof of Lemma 3.2

We are given \( \Gamma_{j-1, a} = (\Delta_0(P_{\leq u+j-1})(z, a), \ldots, \Delta_n(P_{\leq n+j-1})(z, a)) \). From the assumption on \( C' \), we have

\[
0 = C'(\Delta_0(P)(z, a), \ldots, \Delta_n(P)(z, a))
\]

which implies

\[
0 = C'(\Delta_0(P)(z, a), \ldots, \Delta_n(P)(z, a)) \mod \langle z \rangle^{j+1}.
\]

By Observation 2.12, we have that \( \Delta_i(P)(z, a) = \Delta_i(P_{\leq n+j-1})(z, a) \mod \langle z \rangle^{j+1} \) for all \( i \leq n - 1 \), and \( \Delta_n(P)(z, a) = \Delta_n(P_{\leq n+j-1})(z, a) + \Delta_n(P_{n+j})(z, a) \mod \langle z \rangle^{j+1} \). For the sake of brevity, let \( R_i = \Delta_i(P_{\leq n+j-1})(z, a) \) for \( 0 \leq i \leq n \) and \( A = \Delta_n(P_{n+j})(z, a) \). Therefore,

\[
0 = C'(R_0, R_1, \ldots, R_{n-1}, R_n + A) \mod \langle z \rangle^{j+1}.
\]

We now expand the above expression as a univariate in \( z \) (or in other words, perform a Taylor expansion of the polynomial \( C' \) around the point \( (R_0, R_1, \ldots, R_n) \)). Let \( d' = \deg_{x_n}(C') \). Then,

\[
0 = C'(R_0, \ldots, R_n) + \sum_{i=1}^{d'} A^i \cdot \left( \frac{\partial x_n^i(C')(R_0, \ldots, R_n)}{i!} \right) \mod \langle z \rangle^{j+1}.
\]

Moreover, since \( A = \Delta_n(P_{n+j})(z, a) \) is a homogeneous polynomial of degree \( j \geq 1 \), we have \( A^2 = 0 \mod \langle z \rangle^{j+1} \). Therefore,

\[
0 = C'(R_0, \ldots, R_n) + \sum_{i=1}^{d'} A^i \cdot \left( \frac{\partial x_n^i(C')(R_0, \ldots, R_n)}{i!} \right) \mod \langle z \rangle^{j+1}
\]

\[
= C'(R_0, \ldots, R_n) + A \cdot \left( \partial x_n(C')(R_0, \ldots, R_n) \right) \mod \langle z \rangle^{j+1}
\]

\[
= C'(R_0, \ldots, R_n) + A \cdot a \mod \langle z \rangle^{j+1}
\]

where \( a = \partial x_n(C')(R_0, \ldots, R_n)(0) \), the constant term of \( \partial x_n(C')(R_0, \ldots, R_n)(z) \). Note

\[
a = \partial x_n(C')(R_0, \ldots, R_n)(0) = \partial x_n(C')(\Delta_0(P_{\leq u+j-1})(0, a), \ldots, \Delta_n(P_{\leq n+j-1})(0, a))
\]

\[
= \partial x_n(C')(\Delta_0(P_{\leq n+j-1})(z, a), \ldots, \Delta_n(P_{\leq n+j-1})(z, a))(0)
\]
= \partial_{x_n}(C') (\Lambda_0(P)(z, a), \ldots, \Lambda_n(P)(z, a))(0) \\
= (\partial_{x_n}(C') \circ \text{Gen}(P, a))(0) \\
= \Psi(a) \neq 0.

Combining this with the previous equation, we get

\[ 0 = C'(R_0, \ldots, R_n) + A \cdot \Psi(a) \mod \langle z \rangle^{j+1} \]

\[ \implies A = \Delta_n(P_{n+j})(z, a) = \left(\frac{-1}{\Psi(a)}\right) \cdot C'(R_0, \ldots, R_n) \mod \langle z \rangle^{j+1}. \]

\[ \square\] (Lemma 3.2)

### 3.2 Derandomization from hard polynomial families

In this section we will prove the more general form of our main result, Theorem 1.5. It would be instructive to think of the case when \( k \) is a constant, as it would retain the main message but simplify the theorem statement (for instance, the hitting set in the conclusion will be of size \( s^O(k^2/\delta^3) \) instead of the more complicated expression in the statement below).

**Theorem 3.3.** Let \( \delta > 0 \) be an arbitrary constant, and \( k : \mathbb{N} \to \mathbb{N} \) be any non-decreasing function with \( k(d) = o \left( \sqrt{\log d} \right) \); let \( \bar{k}(d) = k \left( \log d \right) \).

Suppose \( \{ P_{k,d} \}_{d \in \mathbb{N}} \) is an explicit family\(^{12}\) of polynomials such that \( P_{k,d} \) is \( k \)-variate, \( \deg(P_{k,d}) = d \) and \( P_{k,d} \) requires circuits of size at least \( d^\delta \) (that is, \( P_{k,d} \notin C(k, d, d^\delta) \)). Then, there are explicit hitting sets of size \( s^O(k(s)^3) \) for the class of \( C(s, s, s) \).

**Proof.** We wish to construct a generator to fool the class of \( s \)-variate, degree \( s \) polynomials computable by circuits of size \( s \). Let \( t = \lceil \frac{s}{\delta} \rceil \). Choose the smallest \( d \) such that \( d > s^{\left(10\bar{k}(d)+2\right)/t} \).

Note that this is always possible for some \( d \leq s^{O(\sqrt{\log s})} \) since \( k(d) = o\left( \sqrt{\log d} \right) \) (furthermore, if \( k = O(1) \), then we can in fact find a \( d \leq s^{O(1)} \) satisfying the above constraint). We shall just use \( k \) to denote \( k(d) \).

To the polynomial \( P_{k,d}(z_1, \ldots, z_k) \) in the family, we associate the natural \( kt \)-variate polynomial \( Q_{k,d,t}\left( (z_{i,j} : i \in [k], j \in [t]) \right) \), of degree at most \( d' = (kt) \cdot d^{1/l} \), such that \( Q_{k,d,t}(z_{1,1}, \ldots, z_{k,t}) \) under the substitution

\[ z_{i,j} \rightarrow z_i^{d(i-1)/l} \]

yields the polynomial \( P_{k,d}(z_1, \ldots, z_k) \). This is achieved by replacing each monomial \( z_i^{e_1} \cdots z_k^{e_k} \) in \( P_{k,d} \) by \( z_{1,\langle e_1 \rangle} \cdots z_{k,\langle e_k \rangle} \) where \( \langle e_i \rangle \) is the tuple corresponding to the integer \( e_i \) expressed in base \( d^{1/l} \).

\(^{12}\)We are assuming that the polynomial family contains a degree \( d \) polynomial, \( P_{k,d} \), for every positive integer \( d \). For the purposes of this theorem, it suffices to assume that the family is sufficiently often in the following sense: There are absolute constants \( a, b \) such that for any \( t \in \mathbb{N} \), there is some \( P_{k,d} \) in the family such that \( t^a \leq d \leq t^b \).
Note that if $Q_{k,d,t}$ has a circuit of size $d'^4 < d^{\delta/2+o(1)}$ (recall that $k = o(\sqrt{\log d})$ and $t = O_{d}(1)$), then (by employing repeated squaring to perform the substitution described above) $P_{k,d}$ has a circuit of size at most
\[ d^{\delta/2+o(1)} + O(kt \log d) \ll d^{\delta}, \]
which is a contradiction to the hardness of $P_{k,d}$. Hence we have that $Q_{k,d,t}$ is a $k$-variate polynomial of degree $d'$ that requires circuits of size at least $d'^4$. Since, by the choice of $d$, we have $d' > d^{1/10} > s^{10kt+2}$, we have
\[ d'^4 > s^{10kt} \cdot s^{2} \cdot d^{3}. \]

By Theorem 1.12, we hence have that $\text{Gen}_{Q_{k,d,t}}$ is a hitting-set generator for the class $C(s,s,s)$. Therefore, from Lemma 2.2, this yields an explicit hitting set of size at most
\[
(sd' + 1)^{2t \cdot k} = (sd' + 1)^{2t \cdot k(d)} \\
\leq s^{O(k(d^2))} \\
\leq s^{O(k(s^{O(\log s)}))}^2 \\
\leq s^{O(k(s))}^2 \\
\leq s^{O(k(s))}^2 \quad \text{(since $k(s) = o(\sqrt{\log s}) \Rightarrow k(s^{O(\log s)}) = O(k(s^{\log s}))$)}
\]
as claimed by the theorem.

\[ \blacksquare \]

**Comparison with the derandomization of Kabanets and Impagliazzo [KI04]:** The work of Kabanets and Impagliazzo [KI04] shows that if there is an explicit family of multilinear polynomials $\{Q_m\}$ that requires circuits of size $2^\Omega(m)$, then we can construct explicit hitting sets of $s^{O(\log s)}$ size for $C(s,s,s)$.

From the above proof, it is easy to see that if $P_{k,d}$ requires circuits of size $d^x$, then the above proof shows that the polynomial $Q_{k,d,t}$ in the above proof, for $t = \log d$, is an $m = O(k \log d)$-variate multilinear polynomial that requires circuits of size roughly $d^x = \exp(\Omega(m))$ to compute it. Therefore, the hypothesis of Theorem 1.5 implies the hypothesis used in the result of Kabanets and Impagliazzo.

However, the other direction seems unclear as it is conceivable that the circuit complexity of $P_{k,d}$ is significantly smaller than the complexity of $Q_{k,d,t}$. Hence, Theorem 1.5, when compared with the hardness-randomness trade-off of Kabanets and Impagliazzo [KI04], can be interpreted as a potentially stronger hypothesis leading to the better derandomization of PIT (a complete derandomization of PIT in the case when $k = O(1)$).
3.3 Application to bootstrapping phenomenon for hitting sets

We now use Theorem 3.3 to prove the following theorem about bootstrapping hitting sets for algebraic circuits. The following theorem shows that, for any constants $\delta, k > 0$, any hitting set for $C(k, i\text{-deg}: s, s^\delta)$, that saves even one point from the trivial hitting set of size $(s + 1)^k$ guaranteed by Lemma 2.3, is sufficient to completely derandomize PIT for $k = O(1)$.

**Theorem 1.6** (Bootstrapping hitting sets). Assume that the underlying field $\mathbb{F}$ has characteristic zero. Let $k \in \mathbb{N}$ and $\delta > 0$ be arbitrary constants. Suppose that, for all large enough $s$, there is an explicit hitting set of size $(s + 1)^k - 1$ for the class $C(k, i\text{-deg}: s, s^\delta)$. Then, there is an explicit hitting set of size $\text{poly}_{\delta, k}(s)$ for $C(s, s, s)$.

**Proof.** Consider an arbitrary, large enough $s$ and let $H_s$ be the hitting set for $C(k, i\text{-deg}: s, s^\delta)$; the hypothesis guarantees that $|H_s| \leq (s + 1)^k - 1 < (s + 1)^k$. From Theorem 2.5, we can then obtain a polynomial $P_s(z_1, \ldots, z_k)$ of individual degree at most $s$ that cannot be computed by circuits of size $s^\delta$. Expressing this in terms of its total degree $d \leq ks$, we get that $P_s$ is a degree $d$ polynomial that requires circuits of size more than

$$\left(\frac{d}{k}\right)^{\delta} \gg d^{\delta/2}.$$  

Hence, $\{P_s\}$ is an explicit family of $k$-variate, degree-$d$ polynomials that require circuits of size $d^{\delta/2}$. Therefore, Theorem 3.3 yields an explicit $\text{poly}_{\delta, k}(s)$-sized hitting set for the class of $s$-variate, degree $s$ polynomials that can be computed by size $s$ circuits.

Analogous statements also hold for slow-growing functions $k(d) : \mathbb{N} \to \mathbb{N}$ yielding non-trivial derandomizations. However, the resulting hitting set and the time required to construct it would require super-polynomial (but subexponential) time. We state the above simpler settings as we believe they presents the main message without any notational clutter.

3.4 The strengthened Shub-Smale $\tau$-conjecture implies PIT is in $P$

In this section, we prove that the strengthened $\tau$-conjecture (Conjecture 1.9) implies an efficiently enumeratable polynomial sized hitting set for algebraic circuits.

**Corollary 1.10.** If Conjecture 1.9 is true then there exist hitting sets, that can be enumerated by a deterministic Turing machine in time $\text{poly}(s)$, for algebraic circuits of size and degree at most $s$.

**Proof.** Let $P_d(x) = (x - 1) \cdots (x - d)$. If Conjecture 1.9 is true with exponent $c$ as stated, then $P_d$ requires algebraic circuits of size $d^{1/c}$ to compute it. Let $f$ be a nonzero polynomial in $C(s, s, s)$. Following the proof of Theorem 3.3, for $t = O(c)$ and $d = \text{poly}(s)$ chosen large enough, if $Q_{1,d,t}$ is the un-Kroneckered version of $P_d$ used in the proof of Theorem 3.3, then $\text{Gen}_{Q_{1,d,t}} \in (\mathbb{F}[y_1, \ldots, y_t, z_1, \ldots, z_t])^s$ is a hitting-set generator for $C(s, s, s)$.
Then, since \( f \circ \text{Gen}_{Q_{1,d,t}} \) is a \( 2t \)-variate, nonzero polynomial of degree at most \( sd \), by Lemma 2.2 we have that the set

\[
\mathcal{H} = \{ \text{Gen}_{Q_{1,d,t}}(a) : a \in [sd + 1]^{2t} \}
\]

is a hitting set for \( C(s,s,s) \). As the coefficients of \( P_d \) are bounded by \( d^d \cdot d! = 2^{O(d \log d)} \), we have the same bound for the coefficients of \( Q_{1,d,t} \). Using the definition of the generator (Definition 1.11), all the coordinates of points in \( \mathcal{H} \) are rational numbers with numerator and denominators being integers of \( \text{poly}(d,s) = \text{poly}(s) \) bits and can certainly be computed by a Turing machine in \( \text{poly}(s) \) time. Thus, the above set \( \mathcal{H} \) is a hitting set for \( C(s,s,s) \) that can be enumerated by a deterministic Turing machine in \( \text{poly}(s) \) time.

\[\square\]

4 Open Problems

We end with some open problems.

- The construction in this paper takes a \( d^\Omega(k) \)-hard, \( k \)-variate polynomial of degree \( d \) to construct a generator for \( C(s,s,s) \) with \( s \lesssim d \). A \( k \)-variate degree \( d \) polynomial has \( d^{\Theta(k)} \) coefficients and if \( P \) is \( d^\Omega(k) \)-hard then, intuitively, we have “enough entropy” to be able to fool \( C(s,s,s) \) with \( s \approx d^{O(k)} \).

On the other hand, the HSG of Kabanets and Impagliazzo [KI04], in certain ranges of parameters, is “lossless” in the above sense it uses a \( k \)-variate, multilinear, hard polynomial which has \( \tilde{s} = 2^k \) coefficients to build a generator for \( C(s,s,s) \) with \( s = \tilde{s}^{O(1)} \).

In principle, it seems conceivable that there is a construction that, starting from a \( d^\Omega(k) \)-hard \( k \)-variate degree \( d \) polynomial, gives an HSG for \( C(s,s,s) \) for \( s = d^\Omega(k) \). Constructing such an explicit HSG is perhaps the most natural open question here.

- The construction of the HSG in this paper needs the characteristic of the field to be large enough or zero. Constructing a HSG with similar properties (seed length, stretch, running time, degree) over fields of small positive characteristic would be quite interesting.

- In the current statement of Theorem 1.12, the hardness required for \( P \) for the HSG to fool circuits of size \( s \), depends also on the degree of this circuit. We suspect that this dependence on the degree can be avoided, if the hardness assumption is in terms of border-complexity; in that case, this HSG should fool all circuits of small size regardless of their degree.

- Lastly, it would be interesting to understand if this new HSG and the ideas in its analysis have any other applications in complexity theory.
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