Nickel Oxide–Based Heterostructures with Large Band Offsets

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The authors present research on the electronic transport in heterostructures based on p-type nickel oxide (NiO) with the n-type oxide semiconductors zinc oxide (ZnO) and cadmium oxide (CdO). NiO is a desirable candidate for application in (opto)electronic devices. Because of its small electron affinity, heterojunctions with most n-type oxide semiconductors exhibit conduction and valence band offsets at the heterointerface in excess of 1 eV. ZnO/NiO junctions exhibit a so-called type-II band alignment, making interfacial electron–hole recombination the only process enabling vertical current flow through the structure. These heterojunctions are nevertheless shown to be of practical use in optoelectronic devices, as exemplified here by UV-converting transparent solar cells. These devices, although exhibiting high conversion efficiencies, suffer from two light-activated recombination channels connected to the type-II interface, one of which the authors analyze in more detail here. Furthermore, CdO/NiO contacts are studied—a heterostructure with even larger band offsets, achieving a type-III band alignment which theoretically enables the development of a 2D electronic system consisting of topologically protected states. The authors present experiments demonstrating that CdO/NiO heterostructures indeed host a conductive layer absent in both materials when studied separately.

1. Introduction

Due to the abundance of n-type oxide semiconductors with wide bandgaps on the one hand, but the lack of p-type variants of the same materials, the fabrication of all-oxide pn homojunctions for use in transparent electronics is out of reach up to now. In this regard, nickel oxide (NiO) offers a promising alternative: it has a bandgap of around 3.8 eV and is therefore transparent for the human eye; it forms rectifying contacts with a variety of wide-gap n-type materials such as zinc oxide (ZnO)[1–3] In2O3,[4] Ga2O3,[5] and TiO2[6,7]; and it can be doped, both extrinsically (by group-I elements like Li) and intrinsically (by oxygen excess). Consequently, it has been widely used in heterojunction devices where a certain degree of transparency is needed, most prominently as a p-type electrode in transparent UV detectors and solar cells based on ZnO[13] and as a hole transport layer in perovskite solar cells.[9–12] Most recently, it has also found application in gas-sensing devices, both as a standalone component and in combination with other oxide materials.[11–16]

Because of its low electron affinity of 1.46 eV,[17] NiO has the tendency to form contacts with other materials exhibiting large band offsets in both valence and conduction bands. In most cases, this will lead to the so-called type-II band alignment where two materials share a gap of reduced size at the interface (“staggered gap”). Such a situation is sometimes considered undesirable because it invokes electron–hole recombination at the heterointerface. For that reason, there have been only a few attempts to develop a theory that describes the current transport mechanism in a type-II system.[18,19] Based on the aforementioned results, however, it can be stated that this configuration is suitable for efficient bipolar devices. In particular, for solar cells, the high band offsets act as barriers for minority carrier recombination, which overall benefits the device performance. In this article, we will therefore make an attempt to develop a general framework that addresses interface-recombination-limited transport in type-II heterojunctions. Although the work of Dolega[18] focuses on the special case of infinitely fast interface recombination, the theory previously published by us[19] fails to account for ideality factors of well below 2 encountered in the experiments described in this publication. We also show, using the example of
the ZnO/NiO contact, that the defect properties of the interface have a pivotal role in determining the parameters of the current-voltage relationship.

The second part of the publication is devoted to an electronically even more extreme situation. The conductive transparent material cadmium oxide (CdO) possesses such a high electron affinity that, when in contact with NiO, no interface gap remains—the band alignment is said to be of “broken-gap” or type-III character. This configuration holds the promise of enabling the formation of a topological phase at the interface, as will be described further subsequently. Results in the literature on this subject are limited to narrow-gap materials where liquid-He temperatures are necessary to observe the desired effect. Using wide-gap materials instead facilitates its stability also at higher temperatures. Herein, we report for the first time investigations of the lateral electrical transport in CdO/NiO heterocontacts, and we show indirect evidence for a conductive channel forming within the otherwise insulating NiO that is induced by the type-III contact, and that is stable up to room temperature.

2. Experimental Section

2.1. ZnO/NiO Heterostructures

For the investigation of transport properties in type-II structures, a 180 nm thick Al-doped ZnO was grown by pulsed laser deposition (PLD) on top of a-plane-oriented corundum substrates, serving as a back contact layer. On top of this, a nominally undoped, 1 μm thick ZnO film was deposited. For both layers, the substrate temperature amounted to 670 °C, and an oxygen partial pressure of 0.016 mbar was used. Individual pillar-shaped NiO contacts were then fabricated on top by PLD, employing conventional UV photolithography and the lift-off technique for patterning. Before removing the photoresist, the NiO was capped by a 3–4 nm thin Au layer, fabricated by DC magnetron sputtering, which is needed to ensure uniform current extraction across the contact area. Due to the use of the temperature-sensitive photoresist, NiO growth was conducted at room temperature (no intentional substrate heating). A comparably high oxygen partial pressure of 0.1 mbar was chosen, which is known to lead to high acceptor (Ni vacancy) densities in NiO, therefore rendering the film semiconducting. Semitransparent ZnO/PIO₂ Schottky diodes were fabricated by reactive DC magnetron sputtering from a metallic Pt target in O₂ atmosphere, and patterning of these structures was done in the same way as for the NiO contact. Further details on the PLD system and the fabrication processes can be found in previous studies [3,20–22]. A cross-sectional schematic view of the ZnO/NiO diodes is shown in the inset of Figure 1.

Characterization of the structures by current density–voltage (i–V) measurements was conducted using a SÜSS wafer prober with tungsten needles and an Agilent 4155C precision semiconductor parameter analyzer. On the basis of these measurements, individual contacts were selected for further characterization. Those samples were then mounted onto transistor sockets, and the selected pillars were contacted by Au wire bonding using silver epoxy resin. Capacitance–voltage (C–V) measurements were conducted with the help of an AGILENT Precision Semiconductor Parameter Analyzer 4294 A. For characterization under UV illumination, a high-power UV light-emitting diode (LED, emission centered around 365 nm, total optical power density 160 mW cm⁻²) was used. Measurements of the optical transmittance were conducted on a Perkin Elmer Lambda 19 UV/Vis/NIR spectrophotometer with tungsten and deuterium lamps.

2.2. CdO/NiO Heterostructures

The CdO (100) layers reported here were grown by plasma-assisted molecular beam epitaxy on top of (100)-oriented MgO substrates at a Cd effusion cell temperature of 400 °C and an oxygen flow of 0.4 cm³ min⁻¹, giving rise to a residual pressure during growth of about 10⁻³ Torr. For the CdO/NiO heterostructures, uniform 50 nm thick NiO thin films were grown by PLD on top of the CdO, using a substrate temperature of 320 °C and an oxygen partial pressure of 10⁻² mbar. This was done to obtain insulating NiO films so as to eliminate as many conduction channels in the structures as possible, facilitating the identification of (potentially weak) additional conductivities induced by making the CdO/NiO contact.

The samples were characterized by Hall effect and magneto-conductivity measurements in a Physical Properties Measurement System (Quantum Design).

3. Type-II Band Alignment: ZnO/NiO

The most thoroughly investigated heterostructure including NiO is with ZnO as the n-type partner. ZnO has a wide bandgap of around 3.3 eV, can be deposited and patterned easily by technologically relevant methods, and is earth-abundant and nontoxic. Because of its low electron affinity, NiO forms a type-II band alignment with ZnO (electron affinity above 4 eV) [23]. In such an arrangement, valence and conduction bands exhibit offsets ΔEᵥ, with the same sign (Figure 2), sometimes also termed staggered gap alignment. The magnitudes of the respective offsets have been determined by various researchers and are summarized in Table 1. Although these values vary significantly, the

![Figure 1. Optical transmittance of a ZnO/NiO heterostructure, including Au capping. A cross-sectional view is shown schematically in the inset.](Image)
strong type-II alignment with $\Delta E_{C,V} > 1$ eV is supported by all available experimental results.

Because both semiconductors have wide bandgaps, and the Au capping is only a few nanometers thin, our ZnO/NiO heterostructures are semitransparent in the visible spectral range, with an average transmittance of 49% between 1.77 and 3.27 eV, as shown in Figure 1.

3.1. Characterization of the Electrical Transport: The Role of Interface Defects

One can easily imagine that strong band offsets impose injection barriers for charge carriers: in the case discussed in this article, electron currents from ZnO into NiO, and hole currents from NiO to ZnO, respectively, will be strongly suppressed. A current can only flow by means of electron–hole recombination at the interface, which is usually supported by interfacial electronic defect species acting as recombination centers. The behavior of this contact, e.g., its characteristics under electric biases or illumination, and therefore also its performance in active electronic devices, is strongly dependent on the detailed interface properties. More specifically, the voltage dependence of the current flowing in such a situation (the current density–voltage or $j$–$V$ characteristic of the contact) can be expected to depend strongly on the recombination rate associated with the interfacial defect states, i.e., their density, carrier capture and emission rates, etc. Finding an analytical expression describing the $j$–$V$ curve of such a contact that captures all these parameters under generalized conditions is tremendously difficult. Dolega\[18\] has calculated an expression under the assumption of infinitely fast recombination at the interface, implying that it is the flow of free carriers to the interface, not the interface recombination itself, that limits the total current. This is, however, not necessarily applicable to many relevant cases, where differences in interface properties are reflected strongly in measured $j$–$V$ characteristics. This is exemplified by the following experiment. Two thin-film heterostructures were produced consisting of NiO on top of ZnO. For the first sample, the initial 5000 pulses of the NiO deposition were conducted with a circular shadow mask inserted into the center of the plasma plume. This led to significantly reduced growth rates on the one hand, but prevented high-energy particles from directly reaching and damaging the surface of the growing film on the other hand, thereby suppressing the formation of impact-induced defects.[24] This technique is also referred to as \textit{eclipse PLD}. After the deposition of a 2 nm thin layer using this method, the rest of the NiO film (approximately 100 nm, corresponding to 30 000 laser pulses) was deposited without the aperture. For the second sample, a NiO layer of the same thickness as in the first sample was deposited directly on top of the ZnO without modifications (“standard NiO”), serving as a reference.

The characteristics are shown in Figure 3a as absolute current densities in a semilogarithmic plot. The figure also includes fits.
to the curves in the forward voltage region according to the general diode relation

\[ j(V) = j_s \exp \left( \frac{\beta V}{n_{id}} \right) - 1 \]  

(1)

with \( n_{id} \) and \( j_s \) being the ideality factor and saturation current density of the diode, respectively, and \( \beta = \frac{kT}{q} \). Values for \( j_s \) and \( n_{id} \) are given in the figure. It can be seen that the interface quality clearly has an impact on the diode behavior. The eclipse-PLD sample exhibits a \( j_s \) two orders of magnitude lower than the reference sample and \( n_{id} \) much closer to 1 than in the reference. In our previous attempt to develop a theory for recombination-limited transport in type-II structures,\(^{19}\) we derived the ideality factor of such a contact to be always around 2, only with minor variations depending on the doping symmetry and the interface recombination rate. The result here is in clear contradiction to that prediction.

What follows is a more crude and general derivation, lacking quantitative prediction power on the one hand, but being able to reproduce the results presented here in a qualitative manner on the other, in contrast to Grundmann et al.\(^ {18}\) To develop a model for the current–voltage relationship that considers the interface recombination itself to be the limiting process, and not the transport to the interface, as done by Dolega,\(^ {19}\) the following expressions for the current densities on both sides of the junction can be used

\[ j_p = eS_p \delta \rho_f, \quad j_n = eS_n \delta n_f \]  

(2)

with interfacial excess carrier densities \( \delta \rho \) and \( \delta n \) and interface recombination velocities \( S_{p,n} \) on the p-side and electrons on the n-side, respectively. Using the Boltzmann approximation, assuming that the donor density \( N_D \) on the n-side is considerably lower than the acceptor density \( N_A \) on the p-side (a situation that applies to these diodes, as will be shown further below), one can show that the \( V \)–relation takes the form

\[ j(V) = eS_nN_D \exp[-\beta V_{bi}](\exp[\beta V] - 1) \]  

(3)

\( V_{bi} \) being the built-in voltage of the contact. Comparing this with Equation (1), we identify the parameters \( j_s \) and \( n_{id} \) as

\[ j_s = eS_nN_D \exp[-\beta V_{bi}] \quad \text{and} \quad n_{id} = 1 \]  

(4)

The recombination velocity \( S_n \) on the other hand, can be described by \( S_n = \sigma_{c,n}v_{th,n}N_{id} \) with \( \sigma_{c,n} \) being the defects’ capture cross section for electrons, \( v_{th,n} \) the electron thermal velocity in the n-type semiconductor, and \( N_{id} \) the interface defect density. Collecting all factors, we have

\[ j_s = eN_D\sigma_{c,n}v_{th,n}N_{id} \exp[-\beta V_{bi}] \]  

(5)

\( v_{th,n} \) and \( \sigma_{c,n} \) are considered here to be independent of the interface quality. The changes of \( j_s \) observed in the aforementioned experiment must therefore be related to either the ZnO doping, \( N_D \), the interface defect density \( N_{id} \), or the built-in voltage \( V_{bi} \). To clarify this, capacitance–voltage (C–V) measurements were taken at room temperature and an AC frequency of 10 kHz. The results are shown in Figure 3b in the form of doping profiles. Both samples have comparable net doping levels of some \( 10^{16} \text{ cm}^{-3} \). This value is two orders of magnitude lower than the NiO doping, which we determined to be \( 4 \times 10^{18} \text{ cm}^{-3} \).\(^ {25}\) Therefore, the assumption of asymmetric doping levels made earlier is justified. It should be added that the eclipse-PLD technique is known to cause slightly less compact thin films due to the reduced particle surface energies,\(^ {24}\) but since the NiO doping is determined mostly by the oxygen partial pressure during growth, the doping concentration of the thin eclipse layer can be considered to be similar to the one of the conventionally deposited film.

The inset of Figure 3b displays Mott–Schottky plots (\( C^{-2} \) vs \( V \)) from the same measurements, which have been extrapolated to the total band bending. As a consequence, a saturation current density of the eclipse-NiO sample reduced by about a factor of 350 in comparison to the reference sample can only be related to an interface defect density reduced by the same extent. Apart from defect states that may form in the mere presence of a heterointerface (which, in itself, is nothing but a 2D structural defect naturally producing a certain population of in-gap states), structural defects may also be introduced during growth of the heterostructure. The surface of one semiconductor (more specifically: the uppermost few atomic layers) exposed to an incoming particle flux during deposition of a second semiconductor may be damaged upon impact of these particles. Eclipse PLD is a method specifically designed to suppress such damaging, and it has been shown to be successful in doing so, e.g., for the deposition of a transparent conductive oxide on top of organic semiconductors.\(^ {24}\) In the present case, it seems to be appropriate to prevent damaging of the ZnO surface as well. Furthermore, this experiment unambiguously demonstrates that the interface recombination itself, and not the transport of charge carriers to the interface, is the critical process in this particular heterostructure. Moreover, ideality factors deviating strongly from 1 are, in many cases, indicative of laterally varying diode properties within the contact area.\(^ {26}\) The fact that \( n_{id} \) is closer to 1 for the eclipse-PLD sample can therefore be interpreted as being due to better homogeneity—another indication for the critical role of the interface in determining the parameters of the electronic transport in these particular type-II heterostructures.

3.2. Transparent UV-Active Solar Cells Based on ZnO/NiO

Based on ZnO/NiO heterojunctions, we already demonstrated a visible light–transparent, UV-converting solar cell with a conversion efficiency of 3.1% with respect to the UV part of the solar spectrum.\(^ {3}\) The functionality of these oxide interface devices relies on the absorption of UV light by ZnO (photon energies >3.2eV) and the subsequent separation of the generated electron–hole pairs at the interface toward the NiO layer. The high hole density in the NiO ensures that the ZnO layer with its lower doping accommodates the main part of the space charge width and band bending, such that as many generated carriers as possible can be collected. The band offsets between the two materials produce additional potential barriers for back-diffusion of photogenerated carriers once they have crossed the interface. The critical contribution to the photocurrent is represented by the holes (minorities) in the space charge region flowing...
toward the interface; their lifetime limits the collection efficiency. We have analyzed the electric loss processes in these solar cells and concluded that white light (i.e., solar light) illumination increases the interface recombination current, which reduces the photovoltaic performance in terms of all three parameters open-circuit voltage $V_{oc}$, short-circuit current density $j_{sc}$, and fill factor $ff$.[22] This effect that, so far, could not be tied to a specific photon threshold energy increases the probability for an interface-crossing hole to recombine on the one hand and enables higher interface recombination velocities for injected carriers on the other hand. The latter is expressed in the form of a so-called crossover, i.e., an intersection of the $j$–$V$ characteristics in darkness and under illumination with simulated sunlight. This intersection occurs close to $V_{oc}$, implying that the open-circuit state is adversely affected.

This white-light-induced process is, however, not the only loss mechanism in ZnO/NiO solar cells that is triggered by illumination. We have tested our devices under monochromatic UV excitation, using a high-power UV LED and employing different neutral density filters to adjust the illumination intensity between 5% and 4700% of the UV power density contained in terrestrial sunlight (AM1.5G). For this study we investigated the eclipse-NiO/ZnO device described in the previous paragraph, note that the photocurrent density is much weaker. The loss process responsible for the strong voltage dependence must therefore be connected to the usage of NiO as a front contact. It is also apparent from the data that crossover of the dark and illuminated characteristics is absent for the ZnO/NiO cell, which indicates that there is no connection to the white-light-enhanced interfacial recombination current identified in Karsthof et al.[22] For the Schottky reference structure, the forward currents are slightly enhanced for voltage sweeps from negative to positive bias, leading to a hysteresis of the characteristics between the two sweep directions. Such an effect has been found in (Mg, Zn)O-based structures[27,28] and has been attributed to the trapping and detrapping of photogenerated holes at the metal–semiconductor interface. It is likely that the situation is similar in the present case. No hysteresis has been found in our ZnO/NiO devices. Note that the $j$–$V$ characteristic of the PtO$_x$ structure measured under dark conditions shows a nonzero current at zero voltage. This effect is caused by the combination of a low saturation current density and high shunt resistance on this diode such that the current at low voltage becomes dominated by slow electrostatic (dis-)charge of defect states, such as the interfacial defects responsible for the hysteresis under UV illumination.

To analyze the UV-enhanced loss process in NiO-based cells further, we have employed the method described in Karsthof et al.[22] to calculate and fit the voltage dependence of the carrier collection efficiency under UV illumination, $\eta_{CC,UV}$, using the $j$–$V$ characteristics shown in Figure 4. The procedure has been applied to three pairs of characteristics, pairwise similar with respect to $p_{UV}$, to obtain $\eta_{CC,UV}$ for weak, medium, and strong UV illumination conditions (Figure 5). A significant decrease

![Figure 4](image.png)

**Figure 4.** Current–density voltage relations of a ZnO/eclipse-NiO (top panel) and ZnO/PtO$_x$, Schottky (bottom panel) solar cell under UV illumination for different intensities, given relative to the solar terrestrial UV power density. Dark characteristics are also shown.

![Figure 5](image.png)

**Figure 5.** Voltage dependence of the carrier collection efficiency of a ZnO/eclipse-NiO solar cell under monochromatic UV excitation for different illumination intensities, and fits according to Equation (6).
of \( \eta_{\text{CC}, \text{UV}} \) and increasingly convex curvature of the collection function in the photovoltaic quadrant with intensifying UV is observed. For negative voltages, the data can be well fitted with a model considering recombination within the space charge region\(^{[29]} \)

\[
\eta_{\text{CC}}(V) = \frac{\tau_r}{\tau_d} \left( 1 - \frac{V}{V_0} \right) \left( 1 - \exp\left[ -\frac{\tau_d}{\tau_r (1 - \frac{V}{V_0})} \right] \right) \tag{6}
\]

with \( \tau_r \) and \( \tau_d \) the recombination lifetime and drift time for minority carriers in the depletion region, respectively, and \( V_0 \) a characteristic voltage for which the collection probability goes to zero. The inset in Figure 5 shows how the ratio of recombination and drift time decreases with intensifying UV illumination, underlining the light-activated character of the process. Recombination within the space charge region is usually caused by point defects acting as recombination centers. Because the loss mechanism is not present for the Schottky reference structure, the defect in question must be specifically related to the usage of NiO. Ni as an impurity in ZnO has been investigated, e.g., by Schmidt et al.\(^{[30]} \) where the authors show Ni incorporation to lead to the formation of an electronic defect having two detectable charge transitions within the ZnO bandgap. The deeper of these two can be (de-)populated by UV photons. Based on other available works on Ni in Zn chalcogenides, the substitutional defect Ni\(_{\text{Zn}}\) seems most plausible as a candidate.

Figure 6 shows doping profiles, obtained by C–V measurements, from an NiO-based and a Schottky contact solar cell in the dark state and illuminated by the UV LED (a neutral density filter was used to reduce the power density to 16 mW cm\(^{-2}\)). Whereas for the Ni-free cell the apparent net doping density increases only moderately without significant changes to the overall profile shape, the NiO-based structure exhibits a pronounced peak in \( N_{\text{net}} \) close to the interface toward the NiO. Because this feature is absent in the PtO\(_x\) control sample, we attribute it to the aforementioned defect Ni\(_{\text{Zn}}\) acting as a UV-activated recombination center. Ni incorporation is possible by indiffusion during NiO growth, although it should be mentioned that i) the growth itself was conducted at room temperature and only the sample contacting involved a slightly elevated temperature of 90 °C and ii) an implantation of Ni can probably be excluded because the sample investigated here has an eclipse-NiO contact. Ni therefore seems to have high diffusivity in ZnO, also at moderate temperatures.

Although the loss process identified here dominates only at comparatively high power densities that are most likely not relevant for real applications, it also limits the cell performance under normal AM1.5G conditions to a considerable extent. Using the data in the inset of Figure 5, it is evident that for \( \rho_{\text{UV}}/\rho_{\text{UV, sol}} = 1 \) the parameter \( \tau_r/\tau_d \) is around 0.5. With the aid of Equation (6) and considering short-circuit conditions (\( V = 0 \)), this translates to a fraction of 0.57 of the photogenerated minorities being lost to recombination in the space charge layer. This demonstrates that the impact of the UV-activated process should not be underestimated.

4. Type-III Band Alignment: CdO/NiO

CdO belongs to the materials showing high electrical conductivity and optical transparency simultaneously (transparent conductive material, TCM), and it was one material system amongst a few others for which this property was reported for the very first time, namely, by Karl Bädeker in 1907\(^{[31, 32]} \). Its strong propensity for n-type conductivity stems from its low-lying, highly dispersive valence band (electron affinity 5.86 eV\(^{[34]} \)). Combined with the high-lying valence band of NiO, a CdO/NiO heterostructure opens the possibility of the formation of a type-III (or “broken-gap”) band alignment. The valence band states of NiO and the conduction band states of CdO are similar in energy, with an expected overlap of the two bands of the order of 1 eV.\(^{[35]} \) This can enable coupling between these states by tunneling at the interface. Because the CdO conduction band is mostly Cd 5s derived, and the NiO valence band is a hybrid mixture of Ni 3d and O 2p states, a set of bands with mixed parity is expected to evolve at the interface, separated by an energy gap. Similar effects have already been demonstrated to occur in CdTe/HgCdTe heterostructure quantum wells\(^{[34]} \) and InAs/GaSb heterostructures,\(^{[35]} \) with evidence supporting transport through so-called helical edge states with quantized conductivity, indicative of the opening of a hybrid gap of the order of a few millivolts in both cases. In comparison to the materials previously investigated, both CdO and NiO have larger bandgaps (2.3 and 3.8 eV, respectively) which, on the one hand, may give rise to a larger hybridization gap and thereby stability of helical edge state transport at much higher temperatures. On the other hand, the transparency of both materials for a wide range of photon energies may enable the study of interface properties by optical methods.

We have used the tool SCAPS-1D\(^{[36, 37]} \) to numerically calculate the band diagram of the NiO/CdO heterostructure and the charge carrier density distribution within it in thermodynamic equilibrium. The relevant material parameters used for the calculation are given in Table 2. The film thicknesses were set to 15 nm each. The doping level values used for the calculation were chosen as typically measured for our CdO films produced for this study; for NiO we assumed a doping level in a range where the NiO film is
usually not conductive anymore, like it is realized in the films presented later. For both materials we assumed a spatially constant charge carrier density. The result of the simulation is shown in Figure 7. The broken gap-alignment is clearly demonstrated by a negative offset between the CdO conduction band and the NiO valence band being as large as $-0.75 \text{ eV}$. It is also shown that the carrier densities on both sides of the interface are significantly enhanced within a few nanometers and that CdO and NiO show electron and hole enhancement, respectively. It should be noted that the presence of an intrinsic accumulation layer on the CdO surface, as will be shown to exist later, is not accounted for in this simulation.

An anticrossing behavior of extended Cd, O, and Ni bands and localized Ni 3d levels has already been demonstrated within the bulk of Ni$_x$Cd$_{1-x}$O alloys.[33,38,39] This can be viewed as a first proof-of-principle for the idea of making a type-III heterostructure with the same materials exhibiting topologically protected states at the interface. In this work, we fabricated such structures for the first time and investigated their electrical transport properties via a temperature-dependent Hall effect and magnetoresistance (MR) measurements. To unambiguously detect conductive 2D states in a CdO/NiO heterostructure by such methods, the behavior of the individual layers must be known first. On the one hand, the NiO films fabricated for this section exhibit electrical conductivity that is orders of magnitude lower than that of CdO, and moreover they do not produce a Hall voltage when exposed to a magnetic field. On the other hand, CdO is highly conductive. Therefore, we first focus on its electrical characterization in the next section before analyzing properties of CdO/NiO heterostructures.

### 4.1. Electronic Transport in Single CdO Layers

It is well established that CdO, like several other n-type oxide semiconductors, shows electron accumulation on its surface,[40] which can be attributed to the fact that all defects with low formation energy in this material system, intrinsic bulk and surface as well as H-derived defects, are donors.[41] The result is a downward band bending toward the surface, which can amount to up to $0.6 \text{ eV}$. It is therefore important to distinguish electrical transport associated with this “intrinsic” surface accumulation from one that arises due to the possible formation of a type-III heterointerface, which by itself should accommodate enhancement layers.

Figure 8 shows the temperature dependence of the electron concentration and the Hall mobility obtained on four CdO samples, the details of which will be given subsequently. Their qualitative behavior is similar: after an initial decrease (increase) of $n_{\text{Hall}}$ ($\mu_{\text{Hall}}$) while $T$ decreases from room temperature, it reaches a minimum (maximum) at $T \approx 100 \text{ K}$, then increases (decreases) again, and finally saturates at low temperatures. This behavior is typical of samples accommodating several charge carrier systems, of which (at least) one is degenerate and one is not. At low enough temperatures, only the degenerate system can contribute to conductivity, giving temperature-independent $n_{\text{Hall}}$ and $\mu_{\text{Hall}}$. As the temperature increases, the nondegenerate system begins to contribute until at sufficiently high temperatures it dominates the conduction process. In addition, if the degenerate electrons are not a bulk (3D) but a surface or interface (2D) system, this can lead to extremal values in Hall concentration and mobility,[42] like they are observed in this

| Parameter | NiO | CdO |
|-----------|-----|-----|
| $E_g$ [eV] | 3.8$^{[47]}$ | 2.3$^{[46]}$ |
| $\chi$ [eV] | 1.46$^{[47]}$ | 6.0$^{[58]}$ |
| $N_{A/D}$ [cm$^{-3}$] | $5 \times 10^{16}$ | $5 \times 10^{18}$ |

Table 2. Relevant material parameters, bandgap energy, electron affinity, and acceptor/donor densities, used for the SCAPS-1D simulation.

Figure 7. Simulated band diagram of a thin NiO/CdO heterostructure in thermodynamical equilibrium. The enhancement of the carrier densities in the interfacial region is shown in the inset.

Figure 8. Temperature-dependent electron concentrations and (Hall) mobilities for three CdO films on MgO substrates.
experiment. The temperature at which these extremal values occur depends on the ratio of the conductances of the two systems.

To determine the location of the 2D electronic system present in our CdO films, we can compare the four samples with respect to their specifications, which we will now lay out. CdO-1 was grown under the standard conditions given earlier and serves as a reference. CdO-2 consists of a standard layer grown on top of a low-temperature buffer layer which is employed to reduce the possible influence of electrically active defects at the substrate–film interface. These are known to affect the transport properties of, e.g., GaN layers\cite{43} and ZnO\cite{44} layers on sapphire substrates through the indiffusion of Al into the film. As can be seen, the inclusion of such a buffer layer does not lead to significantly different electrical properties. Therefore, the presence of a possible degenerate layer at the film–substrate interface can be excluded.

Sample CdO-3 is nominally identical to CdO-1, except it was deposited in a longer growth process, which led to larger film thickness (270 instead of 115 nm). CdO-3 shows better crystalline quality than CdO-1 in terms of the (200) reflex rocking curve broadening (full width at half-maximum, FWHM; see Table 3). This is consistent with earlier observations on CdO (100) layers grown by metalorganic chemical vapour deposition on r-plane sapphire, where a continuous crystal quality improvement with deposited in a longer growth process, which led to larger crystallites, which is re-

tected by its higher rms surface roughness value. In CdO-3, at room temperature, \( n_{\text{Hall}} \) is reduced by a factor of 3, and \( \mu_{\text{Hall}} \) at low temperatures is twice as large with respect to CdO-1. The temperature at which the extremum in \( n_{\text{Hall}} \) and \( \mu_{\text{Hall}} \) appears is also shifting to lower temperatures, which indicates a stronger dominance of bulk over surface conductivity, which may be attributed to the increased thickness of CdO-3. The fourth sample, CdO-4, is comparable to CdO-3 with respect to the CdO layer, but it has additionally been capped with a 10 nm thick MgO film in situ, i.e., without breaking the vacuum after CdO deposition. It can be seen that its carrier density, including the temperature behavior, is similar to that of CdO-3 whereas the electron mobility at low \( T \) is strongly reduced.

The presence of a quasi 2D electronic system at the free surface of CdO has already been unambiguously demonstrated.\cite{46,47} Electrons are strongly confined to a region in the range of \( \approx 2.5 \text{ nm} \) below the CdO surface, which leads to the quantization of the conductive states into two nonparabolic subbands. This surface system determines \( \mu_{\text{Hall}} \) and \( n_{\text{Hall}} \) at low temperatures.

### Table 3. CdO sample details: film thickness, rms roughness, FWHM of (200) reflex rocking curve, and sheet carrier concentration at low temperatures.

| Sample       | Remarks                  | \( d_{\text{film}} \) [nm] | \( r_{\text{rms}} \) [nm] | \( \Delta \omega_{200} \) [°] | \( n_{\text{Hall}}/(4 \text{ K}) \) [\( 10^{14} \text{ cm}^{-2} \)] |
|--------------|--------------------------|-----------------------------|-----------------------------|-------------------------------|---------------------------------------------------------------|
| CdO-1        | Reference                | 115                         | 0.8                         | 0.45                          | 2.19                                                          |
| CdO-2        | w/ buffer layer          | 109                         | 1.1                         | 1                             | 2.17                                                          |
| CdO-3        | Long growth run          | 269                         | 1.6                         | 0.37                          | 1.58                                                          |
| CdO-4        | w/ MgO capping           | 249                         | 0.9                         | 0.37                          | 1.43                                                          |

The differences and similarities between the samples with lower crystalline quality (i.e., CdO-1 and 2) and the one with higher quality (CdO-3) can be understood in the following way: the Fermi level at the CdO surface is pinned to a rather universal value of \( \approx 0.4 \text{ eV} \) above the conduction band minimum.\cite{46} This produces a (more or less) fixed surface electron density, as measured by the Hall effect at low temperatures (compare the sheet densities \( n_{\text{Hall}} \) in Table 2). The position of the bulk Fermi level depends on the doping, and the lower the bulk electron density, the larger is the difference between the bulk and surface Fermi levels—and, therefore, the deeper is the “surface quantum well.” This means that a lower bulk doping level, as seen in sample CdO-3, leads to a stronger electronic confinement at the surface, which explains the higher mobility exhibited by the surface system in CdO-3. Apparently, depositing a thin passivating MgO layer does not alter the sheet density of the surface system, as exemplified by the comparable \( n_{\text{Hall}} \) of CdO-3 and 4. This implies either that the formation of virtual gap states at the surface is complete before MgO deposition and the states are not affected by the passivation layer or that the contact with MgO induces these states in the same way vacuum (or air) does. Both arguments seem plausible since there is no energetic overlap of extended electronic states of any MgO bands with the bandgap region of CdO (type-I band alignment) due to the low electron affinity and large bandgap of MgO (conduction band at \( \approx E_{\text{c}} \approx 0.8 \text{ eV} \) and valence band at \( E_{\text{v}} \approx 8.6 \text{ eV} \)). The passivation layer does, however, significantly modify the electronic mobility of the surface system, which may be attributed to scattering by localized states lying in the MgO/CdO interface gap.

It must be noted that the electron densities of the CdO layers, in particular CdO-3 and 4, are, to our knowledge, the lowest values ever reported in the literature, with the room temperature value of \( n_{\text{Hall}} = 5 \times 10^{18} \text{ cm}^{-2} \) being almost in the nondegenerate regime.

### 4.2. Lateral Transport Properties of CdO/NiO Heterostructures

We next turn to CdO/NiO heterostructures. Figure 9 shows the temperature dependence of carrier mobility and density, as obtained from Hall effect measurements, of the sample CdO-3 before and after a 50 nm thick NiO layer was deposited on top of it. In contrast to the MgO passivation, coating with NiO seems to radically alter the transport behavior of the CdO surface (the NiO film itself is not conductive and therefore does not contribute any conductivity or Hall voltage). The carrier density is enhanced by a factor of 6 and the mobility decreases by 60% at 4 K as compared to bare CdO. This can be interpreted as indirect evidence of conductive interfacial states induced by the contact with NiO.

Under certain conditions, Hall effect measurements on samples exhibiting parallel conduction by two electronic systems can be corrected in such a way that the two contributions can be distinguished from each other.\cite{42} An important prerequisite is that the 2D electron gas observed at low temperatures possesses constant carrier density and mobility across the whole temperature range investigated. Attempts to use this procedure on the data presented in this article fail: after correction for the low-temperature degenerate system, the corrected carrier densities...
and mobilities show unphysical behavior over a wide temperature range. The assumption of a temperature-independent conductivity of the 2D system must therefore be discarded. A reasonable explanation is that the electrons in the surface or interface layer are subject to scattering processes with temperature-dependent scattering time constants such that the carriers’ mobility changes with $T$. To study this, measurements of the magnetocconductivity were taken in the temperature range between 4 and 100 K using magnetic fields between 0 and 9 T, oriented perpendicular to the sample surface. The results are shown in Figure 10 for the bare and the NiO-coated CdO films. The most striking features of these data are the pronounced increase of conductivity for magnetic fields between 0 and $\approx 2$ T (negative MR). For both the bare and the NiO-coated CdO films, this effect can be observed to up to 100 K. To make the low-field behavior better visible, the decrease of $\sigma_{xx}$ at higher fields due to the Lorentz force was fitted with the standard formula

$$\Delta \sigma_{\text{lf}}(B) = \frac{\sigma_0}{1 + (\mu B)^2} = \frac{\sigma_{\text{lf}}}{1 + (\mu B)^2}$$

(7)

which agrees very well with the data above 3 T; the fits for selected temperatures are shown as red dashed lines in Figure 10. After subtracting the Lorentz force contribution, the remaining conductivity $\delta \sigma_{xx} = \sigma_{xx} - \Delta \sigma_{\text{lf}}$ is plotted in Figure 11. It is clearly visible that the low-field contribution is the same for the bare and the NiO-coated CdO.

Negative MR in CdO has been reported by Wang et al. and has therein been attributed to the weak localization (WL) effect. This phenomenon is related to constructive quantum interference of charge carrier wave functions that are localized around scattering centers in two dimensions. The effect lowers the conductivity of the material at small magnetic fields and temperatures. However, the present authors are convinced that the magnetic fields ($B > 2$ T) and temperatures ($T > 100$ K) needed to destroy the effect in CdO—both in our data and in that of Wang et al.—are too high to be due to WL. Goldstein et al. have observed negative MR in surface accumulation layers on ZnO single crystals. A central observation of these authors was that the magnitude of the negative MR depended on the surface

Figure 9. Hall carrier mobility and density for a CdO thin film before and after deposition of NiO on top.

Figure 10. Diagonal elements $\sigma_{xx}$ of the magnetoconductivity tensor of a) a bare CdO layer and b) for a CdO/NiO heterostructure, for various temperatures. Red dashed lines are fits according to Equation (7).

Figure 11. Magnetoconductivity $\sigma_{xx}$ for bare and NiO-coated CdO for selected temperatures, after correction for positive Lorentz-force contribution. Inset: plot of MR versus $B/T$ of both samples for all temperatures investigated.
accumulation density and increased together with it. These authors have also developed a theory based on spin scattering, i.e., interaction of electrons with ionized impurities via their respective magnetic moments. Since the cross section of inelastic spin-dependent scattering is lowest when the spins of the scatterer and the charge carrier are parallel, this mechanism becomes less dominant with an increasing magnetic field. The theory in Goldstein et al. also shows that the MR

\[
\frac{\Delta \rho_{\text{xx}}}{\rho_{\text{xx},0}} = \frac{\rho_{\text{xx}}(B) - \rho_{\text{xx}}(0)}{\rho_{\text{xx}}(0)}
\]

is a universal function of \(B/T\). The inset in Figure 11 displays the MR data of the bare and NiO-coated CdO films as a function of \(B/T\), and it can be seen that the curves indeed follow a common line (for one specific sample) at low reduced fields before departing from it due to the Lorentz force contribution. Because spin scattering only depends, in the first approximation, on the relative orientation of the spins, and not on the direction of current with respect to the magnetic field, the negative MR should be present also when the magnetic field is oriented in-plane (longitudinal MR). The Lorentz force contribution, in contrast, is zero when the field is oriented parallel to the current flow. We have checked this on the sample CdO-2 with an in-plane-oriented magnetic field and found both facts to be fulfilled (data not included here) such that the interpretation of both contributions to the magnetococonductivity as presented here is well supported. It is probable that the temperature dependence of the spin-dependent scattering is what is causing a decreasing mobility of the surface system with rising temperature, causing the two-layer correction of the Hall data to fail.

We now turn back to the main assertion of the data in Figure 11. The negative MR at low fields is a fingerprint of the CdO surface accumulation layer, and the MR magnitude is directly proportional to its sheet charge density. The fact that the negative MR magnitude does not change upon deposition of an NiO layer therefore implies that the CdO surface system remains unaltered. The sample’s total sheet density, however, increases dramatically by a factor of 7 (see Figure 9). The CdO/NiO contact must therefore possess a second conductive channel with a sheet density of approximately \(9.7 \times 10^{14} \text{cm}^{-2}\). Because the negative character of the Hall effect is enhanced, this channel must be of n-type. Based on the simulations presented in Figure 7, p-type behavior is expected for an enhancement layer on the NiO side; if such a layer exists, it is not detected in this experiment. The high surface electron density of the CdO layer can be assumed to efficiently screen the effects of charge redistribution, such that a contact-induced enhancement layer in the CdO bulk (which would be n-type) is excluded. It is therefore concluded here that the CdO/NiO contact induces a conductive channel directly at the interface, as proposed by theory. Our results can be seen as a proof of concept for the type-III nature of the CdO/NiO heterointerface. It can be stated that the conductive interface channel seems to be stable up to room temperature, as \(n_{\text{Hall}}\) retains its enhanced value across the whole temperature range investigated here. It should also be noted that on the samples investigated in this work, we could not detect a chirality of the magnetotransport which would be indicative of helical edge states. We believe that to observe this, samples with well-defined edges have to be used (micro-Hall bar structures, e.g.) because this would eliminate the influence of (trivially behaving) uncoated CdO at the edges of the sample. This issue must be addressed in future work.

5. Conclusions

We have investigated two types of heterojunctions comprising p-type NiO, both characterized by large band offsets toward the second oxide. In the first case, ZnO was the n-type partner, forming a type-II band alignment with NiO. The electrical current through such a configuration is determined by the rate at which electrons and holes recombine at the interface. We have developed a simplified framework to model the parameters of the current–voltage relationship and have demonstrated the critical impact of the interface quality in terms of defect population. A method to reduce the amount of interface defects introduced during NiO growth was given. The ZnO/NiO contacts can be employed as UV-converting solar cells with record efficiency; however, we presented evidence for a recombination process triggered by UV light, which we attribute to the previously identified recombination center \(\text{Ni}_2\), that forms by indiffusion of Ni into the topmost layers of ZnO.

The second structure investigated here is the CdO/NiO contact. We have shown that the interface (which is expected to have type-III character) harbors a conductive layer that is present neither in CdO nor in NiO alone, and that is detectable at least up to room temperature. We believe this to be a 2D carrier enhancement layer directly at the CdO/NiO interface that is apparently an n-type one, in accordance with theoretical expectations for such a type-III junction. This work can therefore serve as a basis for the next step, which is proving the topological nature of the interfacial electronic system.

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Conflict of Interest

The authors declare no conflict of interest.

Keywords

cadmium oxide, electronic transport, heterojunctions, nickel oxide, oxides, zinc oxide

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