Optimal Fractional-Order Active Disturbance Rejection Controller Design for PMSM Speed Servo System

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Abstract: In this paper, a fractional-order active disturbance rejection controller (FOADRC), combining a fractional-order proportional derivative (FOPD) controller and an extended state observer (ESO), is proposed for a permanent magnet synchronous motor (PMSM) speed servo system. The global stable region in the parameter \((K_p, K_d, \mu)\)-space corresponding to the observer bandwidth \(\omega_o\) can be obtained by D-decomposition method. To achieve a satisfied tracking and anti-load disturbance performance, an optimal ADRC tuning strategy is proposed. This tuning strategy is applicable to both FOADRC and integer-order active disturbance rejection controller (IOADRC). The tuning method not only meets user-specified frequency-domain indicators but also achieves a time-domain performance index. Simulation and experimental results demonstrate that the proposed FOADRC achieves better speed tracking, and more robustness to external disturbance performances than traditional IOADRC and typical Proportional-Integral-Derivative (PID) controller. For example, the \(J_{ITAE}\) for speed tracking of the designed FOADRC are less than 52.59% and 55.36% of the \(J_{ITAE}\) of IOADRC and PID controller, respectively. Besides, the \(J_{ITAE}\) for anti-load disturbance of the designed FOADRC are less than 17.11% and 52.50% of the \(J_{ITAE}\) of IOADRC and PID controller, respectively.

Keywords: PMSM speed servo; ADRC design; fractional-order control; frequency-domain specifications; time-domain performance

1. Introduction

Permanent magnet synchronous motor (PMSM) is mostly accepted in motion control applications due to its advantages, such as compact structure, high power density, high air-gap flux density, and high efficiency [1]. Currently, PMSM is widely applied in industries. Proportional-Integral-Derivative (PID) controller can get reasonable transient and steady-state response with three adjustable parameters. However, the satisfied tracking and anti-load disturbance performances are difficult to be obtained simultaneously [2].

Active disturbance rejection control (ADRC), proposed by Prof. Jingqing Han, is a solution to the problem of internal and external disturbances raised by Tsien and Horowitz [3–5]. The core of ADRC is the extended state observer (ESO), which treats the external disturbances and internal uncertainties as the total disturbance and rejects them actively. However, due to the complexity of the structure of the ADRC and the difficulty of tuning parameters, its applications in the industrial fields were still limited until the linear ADRC (LADRC) proposed in Reference [6]. The research and application of ADRC have increased significantly in recent years and have proved that ADRC can achieve satisfactory anti-load performance [7–10]. To simplify the tuning parameters of ADRC, the observer bandwidth \(\omega_o\) and controller bandwidth \(\omega_c\) are the only tuning parameters, which is the basis of most of the studies. For example, Caifen Fu proposed a generalized ADRC (GADRC) method with known plant information and a tuning method to enhance control performance [11]. Xiangyang Zhou proposed a parameter tuning strategy using the genetic algorithm (GA)
for ADRC to increase the stabilization accuracy and robustness to disturbance [12]. However, this method is only approximate, and its application is limited by the complexity of the plant. Thus, to achieve a better control performance in terms of tracking and anti-load disturbance and be widely used in the PMSM field, a fractional order ADRC based on fractional-order proportional derivative (FOPD) controller and the design method based on frequency-domain are proposed.

Since fractional-order calculus was created in 1695 [13,14], there has been a great development as a purely theoretical subject of mathematics. Recently, the utilization of fractional-order calculus continues to increase, not only in mathematics but also in biology, physics, electromagnetic, engineering, and other areas of science [15–19]. Particularly in the field of control engineering, various forms of fractional-order (FO) controllers have been demonstrated that can achieve better control performance, such as the fractional-order proportional-integral (FOPI) controller, FOPD controller, fractional-order (proportional-derivative) (FO(PD)) controller, fractional-order proportional-integral-derivative (FOPID) controller, and so on [20–24]. The tuning methods for FO-type controllers mainly include minimizing a performance index in time-domain [25,26] and satisfying pre-specified frequency-domain specifications [22,27]. The popular time-domain indexes and frequency-domain specifications are the integral square error (ISE), the integral time absolute error (ITAE), phase margin, gain margin, and gain crossover frequency.

Due to the fact that, fractional-order PD controller has been proven to achieve better tracking performance with less overshoot and faster response than traditional integer-order PD controller [28,29]. Fractional active disturbance rejection control (FADRC) combined with fractional-order ESO (FOESO) and fractional-order proportional derivative (FOPD) controller was proposed by Reference [30] to enhance the control performance for a fractional-order system (FOS), where the FADRC stability and frequency-domain characteristics were analyzed. However, the proposed FADRC strictly constraints the orders of FOPD and FOESO corresponding to the order of the FOS. A fractional-order active disturbance rejection control (FOADRC) strategy including a nonlinear ESO to achieve precise trajectory tracking performance for a newly designed linear motor was presented in Reference [31]. However, the parameters tuning rule was not presented in References [30,31]. Pengchong Chen [32] proposed a fractional-order ADRC strategy combined with the FOESO and a simple proportional controller. The tuning approach based on frequency-domain specifications was presented for the proposed fractional-order ADRC and traditional integer-order ADRC. However, the analytical design method based on frequency-domain specifications for ADRC-type has not been studied, which is crucial for industrial applications.

In this paper, aiming to realize satisfied tracking and anti-load disturbance performance for the PMSM speed servo system, a FOADRC combining fractional-order proportional derivative (FOPD) controller and ESO is proposed. The total disturbances are estimated and compensated by the ESO; the FOPD controller achieves optimal tracking performance. A FOADRC design strategy is proposed for satisfying user-specified frequency-domain indexes and achieving a time-domain performance index. The main contributions in this paper can be stated as follows: (1) A FOADRC structure for the PMSM speed servo system is proposed combining a FOPD controller and an ESO. The stability boundaries for controller parameters have been clearly analyzed. (2) An optimal FOADRC design scheme for satisfying time-domain and frequency-domain specifications simultaneously is proposed, which is also applicable to IOADRC. (3) PMSM speed servo simulation and experimental results are presented to show the control performance advantages of the designed optimal FOADRC over the traditional IOADRC and the typical PID controller.

The next sections of the paper are organized as follows: Section 2 gives the background of fractional-order calculus and the PMSM speed servo system. The structure of FOADRC for the PMSM speed servo system is proposed. In Section 3, the stability boundary analysis of FOADRC is given. The design scheme is presented with an example in Section 4. In Sections 5 and 6, simulation and experimental results are presented to demonstrate
the performance of the PMSM servo system using the designed optimal FOADRC. The conclusion is indicated in Section 7.

2. Background and Preliminaries

2.1. The Tuning Methods for ADRC

In this subsection, some of the existing methods are summarized chronologically: In Reference [33], an auto-tuning method for $\omega_o$ and $\omega_c$ based on noise level in control signal was proposed. Behzad et al. [34] proposed a systematic procedure for tuning ADRC parameter based on the desired settling time. A tuning procedure was proposed in Reference [10] for modified ADRC by systematically varying $b_0$, $\omega_c$, and $\omega_o$. All the above methods have the element of trial and error, although they are effective. The operator should have a thorough understanding; otherwise, it is difficult to apply in practice. To solve this issue, the quantitative tuning rule based on frequency-domain specifications was proposed in Reference [35] to satisfy the phase and gain margin for first-order plus time delay system was proposed. However, this method is based on the FOPTD systems and only applicable for lag-dominated plants.

2.2. Fractional-Order Calculus

Fractional-order calculus means generalizing integral and differential operators to the fractional operators. The continuous integral-differential operator is defined as follows:

$$t_0 D_t^\alpha f(t) = \begin{cases} \frac{d^\alpha}{dt^\alpha}, & \text{Re}(\alpha) > 0; \\ 1, & \text{Re}(\alpha) = 0; \\ \int_{t_0}^t f'(\tau)(-\tau)^{-\alpha} d\tau, & \text{Re}(\alpha) < 0; \end{cases}$$

where $t_0$ and $t$ are the start time and the end time of the integration, respectively. The term $\alpha$ is the fractional-order. $\text{Re}(\alpha)$ is the real part of $\alpha$.

In this paper, the following Caputo definition of fractional derivative is utilized to realize the FOPD controller [36],

$$0D_t^\alpha f(t) = \frac{1}{\Gamma(n-\alpha)} \int_{t_0}^t f^{(n)}(\tau) d\tau \frac{(t-\tau)^{\alpha-n+1}}{(t-\tau)}$$

where $n$ is an integer which satisfied the case $n-1 < \alpha < n$, $f^{(n)}(\tau)$ is the $n$th derivative of the $f(\tau)$, and the $\Gamma(x)$ is the Gamma function with the definition,

$$\Gamma(x) = \int_0^\infty t^{x-1} e^{-t} dt.$$

The Laplace transform of the Caputo derivative is:

$$L_0 D_t^\alpha f(t); s = s^n F(s) - \sum_{k=0}^{n-1} s^{n-k-1} f^k(0) \quad n-1 < \alpha \leq n$$

2.3. Problem Formulation

The PMSM speed closed-loop control system is shown in Figure 1, which can be equivalent to Figure 2.

Figure 1. The permanent magnet synchronous motor (PMSM) speed closed-loop control system.
Where \( n_r \) and \( n \) are the reference speed and actual speed, respectively, and the unit is rpm, \( i_q \) and \( u_q \) are the \( q \)-axis current and voltage, respectively, \( C_e \) is the induced electro motive force coefficient, \( K_0 \) and \( K_1 \) are the voltage and current conversion factors, respectively, \( T_f \) is the filter time constant, \( R \) and \( L \) are the stator equivalent resistance and inductance, respectively, \( C_m \) is the torque constant, \( GD^2 \) is the flywheel inertia, and the unit is N\( \cdot \)m\(^2\); \( u_0 \) is the output of the FOPD controller, \( z_3 \) is the output of the ESO, and \( u \) is the control law.

Assume that hysteresis and eddy current loss, saturation nonlinear factor of magnetization curve and friction are ignored, and there is no damper winding on the rotor. The transfer functions of the electromagnetic and mechanical parts, which are needed to be identified, are shown with zero initial conditions,

\[
G_1(s) = \frac{1}{L} \frac{1}{s + \frac{R}{L}}, \tag{5}
\]

\[
G_2(s) = \frac{375C_m}{GD^2s}. \tag{6}
\]

A nonlinear identification method based on output-error is adopted to obtain the model parameters \([37,38]\). Thus, the electromagnetic and mechanical models can be identified as:

\[
G_1(s) = \frac{154.8568}{s + 446.188}, \tag{7}
\]

\[
G_2(s) = \frac{367.6516}{s}. \tag{8}
\]

The comparison between simulation results based on the obtained models of the electromagnetic and mechanical parts and experimental results are presented in Figures 3 and 4. For more details on the system identification scheme, the reader may refer to Reference [38].

A PI controller with \( T_i \) is applied to control the current-loop system,

\[
C_i(s) = K_{pi}(1 + \frac{1}{T_is}). \tag{9}
\]

Based on the above identification results, the speed servo plant of PMSM can be obtained:

\[
P(s) = \frac{Y(s)}{U(s)} = \frac{b}{s(s + a)}, \tag{10}
\]

where \( a = 26.08, b = 383.635, U \) is the input reference current, and \( Y \) is the actual speed.
2.4. The Structure of the FOADRC

In this paper, a FOADRC with a FOPD controller is proposed. The structure of the FOADRC is shown in Figure 2.

The load disturbance $d$ is considered when the PMSM is running. Thus, Equation (10) can be rewritten as (11), considering the external disturbance $d$:

$$
\ddot{y} = bu - a\dot{y} + d = bu + f,
$$

where $u$ and $y$ are the input and output, respectively, $d$ is the external disturbance, and $f$ is the total disturbance, which contains the internal dynamics and external disturbance $d$.

The FOADRC includes a FOPD controller and an ESO. The ESO of the FOADRC is used to estimate the total disturbance $f$. Suppose that $f$ is differentiable, $h = f$, then the state space form of (11) is:

$$
\begin{cases}
\dot{x} = Ax + Bu + Eh, \\
y = Cx,
\end{cases}
$$

where

$$
A = \begin{bmatrix} 0 & 1 & 0 \\ 0 & 0 & 1 \\ 0 & 0 & 0 \end{bmatrix}, \quad B = \begin{bmatrix} b \\ 0 \\ 0 \end{bmatrix}, \quad E = \begin{bmatrix} 0 \\ 0 \\ 1 \end{bmatrix},
$$

$$
C = \begin{bmatrix} 1 & 0 & 0 \end{bmatrix}, \quad x = \begin{bmatrix} x_1 \\ x_2 \\ x_3 \end{bmatrix}.
$$
Then, the ESO is designed for (12),
\[
\begin{align*}
\dot{z} &= Az + Bu + L(y - \hat{y}), \\
\hat{y} &= Cz,
\end{align*}
\] (13)
where \( L = [\beta_1 \, \beta_2 \, \beta_3]^T \) is the gain of the ESO; \( z = [z_1 \, z_2 \, z_3]^T \), \( z_1, z_2, \) and \( z_3 \) are the outputs of the ESO; \( z_1 \) and \( z_2 \) will estimate \( y \) and its derivative, and \( z_3 \) will estimate total disturbance \( f \). The estimated total disturbance will be rejected as
\[
u = u_0 - \frac{z_3}{b},
\] (14)
where \( u_0 \) is the output of the FOPD controller.

The FOPD controller is:
\[
C_{FOPD}(s) = K_p + K_d s^\mu,
\] (15)
where \( K_p \) and \( K_d \) are the proportional and derivative gains, and \( \mu \in (0, 2) \) is the derivative order.

3. Stability Boundary Analysis

Because the stability is the minimal requirement for the control system, it is desirable to determine the complete stabilizing FOPD parameters before controller design.

From (13), with Laplace transform,
\[
Z_3(s) = \frac{\beta_3 s^2}{s^3 + \beta_1 s^2 + \beta_2 s + \beta_3} Y(s) - \frac{b \beta_3}{s^3 + \beta_1 s^2 + \beta_2 s + \beta_3} U(s),
\] (16)
where
\[
\beta_1 = 3\omega_o, \quad \beta_2 = 3\omega_o^2, \quad \beta_3 = \omega_o^3.
\] (17)
In this way, all three of the observer poles will be placed at \(-\omega_o\), which is the observer bandwidth [6].

According to (10), (14) and (16), the plant with disturbance compensation is
\[
P_c(s) = \frac{Y(s)}{U_0(s)} = \frac{s^3 + \beta_1 s^2 + \beta_2 s + \beta_3}{s^3 + (a + \beta_1)s^4 + (a \beta_1 + \beta_2)s^3 + (a \beta_2 + \beta_3)s^2},
\] (18)
where \( U_0(s) \) is the Laplace transform of \( u_0 \).

In the considered feedback control system as shown in Figure 5, \( P_c(s) \) is the controlled plant, and \( C_{FOPD}(s) \) is the designed FOPD controller. There are four tuning parameters: \( K_p, K_d, \mu, \) and \( \omega_o \).

![Figure 5. The PMSM speed closed-loop control system based on FOADC.](image)

In Figure 5, \( M_T \) is a Gain-Phase Margin Tester,
\[
M_T(A, \phi) = Ae^{-j\phi}, \quad (19)
\]
where \( A \) and \( \phi \) are the gain margin and phase margin, respectively.

The closed-loop transfer function of the control system in Figure 5 can be obtained as:
\[
G(s) = \frac{M_T(A, \phi)C_{FOPD}(s)P_c(s)}{1 + M_T(A, \phi)C_{FOPD}(s)P_c(s)}, \quad (20)
\]
Hence, the characteristic equation of $G(s)$ is:

$$
D(K_p, K_d, \mu, A, \phi; s) = (s^5 + (a + \beta_1)s^4 + (a\beta_1 + \beta_2)s^3 +
(a\beta_2 + \beta_3)s^2) + Ae^{-j\phi}(K_p + K_d\phi^\mu)(s^3 + \beta_1s^2 + \beta_2s + \beta_3).
$$

The range of the fractional-order $\mu$ is defined as $\mu \in (0, 2)$. The lower limit of $\omega_o$ depends on the pre-specified gain crossover frequency $\omega_{gc}$, and the upper limit of $\omega_o$ is set to 800 rad/s in this paper. The boundaries of the stable region which are determined by real root boundary (RRB), infinite root boundary (IRB) and complex root boundary (CRB) can be obtained by the D-decomposition method [39,40].

- **Real root boundary (RRB):** The RRB is defined by the equation $D(K_p, K_d, \mu, A, \phi; \omega_o; s = 0) = 0$, so the boundary of $K_p$ is:

$$K_p = 0.
$$

- **Infinite root boundary (IRB):** Due to the relative order of $P_1(s)$ is 2, $K_d$ has no boundary restrictions.

- **Complex root boundary (CRB):** Putting $j\omega$ for $s$ into (21), the CRB can be described by $D(K_p, K_d, \mu, A, \phi; \omega_o; s = j\omega) = 0$, yielding

$$
D(K_p, K_d, \mu, A, \phi, \omega_o; j\omega) = ((j\omega)^5 + (a + \beta_1)(j\omega)^4 + (a\beta_1 + \beta_2)(j\omega)^3 + (a\beta_2 + \beta_3)(j\omega)^2 + Ae^{-j\phi}(K_p + K_d(j\omega)^\mu)((j\omega)^3 + \beta_1(j\omega)^2 + \beta_2(j\omega) + \beta_3) = 0.
$$

The real part and the imaginary part of (23) can be obtained,

$$
F_1 + A(K_pE_5 + K_dE_6) = 0,
$$

$$
F_2 + A(K_pE_7 + K_dE_8) = 0,
$$

where

$$
F_1 = (a + \beta_1)\omega^4 - (a\beta_2 + \beta_3)\omega^2,
$$

$$
F_2 = \omega^5 - (a\beta_1 + \beta_2)\omega^3,
$$

$$
E_3 = -\omega^3 + \beta_2\omega, E_4 = -\beta_1\omega^2 + \beta_3,
$$

$$
E_5 = \cos(\phi)E_4 + \sin(\phi)E_3,
$$

$$
E_6 = \omega^\mu\cos(\frac{\mu\phi}{2} - \phi)E_4 - \omega^\mu\sin(\frac{\mu\pi}{2} - \phi)E_3,
$$

$$
E_7 = \cos(\phi)E_3 - \sin(\phi)E_4,
$$

$$
E_8 = \omega^\mu\cos(\frac{\mu\phi}{2} - \phi)E_3 - \omega^\mu\sin(\frac{\mu\pi}{2} - \phi)E_4.
$$

From (24) and (25),

$$K_d = \frac{F_2E_5 - F_1E_7}{A(E_6E_7 - E_5E_8)},
$$

$$K_p = -\frac{F_2 + AK_dE_8}{E_7}.
$$

So, given a fixed fractional-order $\mu$ and a fixed $\omega_o$, the stable and unstable regions in the parameter plane can be separated according to the RRB and CRB with $A = 1$, $\phi = 0^\circ$.

One example is given as: Choosing $\mu = 0.9$ and $\omega_o = 200$ rad/s, draw the curve of $K_p$ w.r.t. $K_d$ and the line $K_p = 0$ according to CRB and RRB, respectively. Detect the stable region by testing a random point [41] as shown in Figure 6. Sweeping all the $\mu \in (0, 2)$, the
global stable region can be obtained as shown in Figure 7. Thus, with different $\omega_o$, different global stable regions can be determined.

![Figure 6](image)

**Figure 6.** Stable region with $\mu = 0.9$, $\omega_o = 200$ rad/s and the designed $K_p$ and $K_d$ with $\phi_m = 80^\circ$, $\omega_{gc} = 50$ rad/s.

![Figure 7](image)

**Figure 7.** Global stable region with $\omega_o = 200$ rad/s.

### 4. FOADRC/IOADRC Design Strategy

#### 4.1. Controller Design Specifications

In this paper, two frequency-domain specifications, gain crossover frequency ($\omega_{gc}$) and phase margin (PM), are applied to tune FOADRC:

- **Gain crossover frequency**

  $$|C_{FOPD}(j\omega_{gc})P_c(j\omega_{gc})|_{dB} = 0. \quad (28)$$

- **Phase margin**

  $$\arg[C_{FOPD}(j\omega_{gc})P_c(j\omega_{gc})] = -\pi + PM. \quad (29)$$

To achieve an optimal dynamic control performance, a time-domain specification, ITAE, is also applied for the FOADRC design:

- **ITAE**

  $$J_{ITAE} = \int_0^\infty |e(t)|t \, dt, \quad (30)$$

where $e(t)$ is the deviation between the reference input and the actual output.
4.2. The Optimal FOADRC Design for PMSM Speed Servo Plant

4.2.1. The FOADRC Satisfying the Frequency-Domain Specifications

The Gain-Phase Margin Tester can provide information for satisfying the given gain margin or phase margin [40]. Setting \( A = 1 \) and \( \phi = \phi_m \), plot the curve according to Equations (26) and (27) with \( \omega \) increasing from 0. Every point on the above curve fulfills the given phase margin \( \phi_m \).

The characteristic equation of Equation (20) is,

\[
1 + M_T(A, \phi)C_{FOPD}(s)P_c(s)|_{s=j\omega} = 0,
\]

which denotes the open-loop transfer function \( T(s) \) equals to \(-1\),

\[
T(s)|_{s=j\omega} = M_T(A, \phi)C_{FOPD}(s)P_c(s)|_{s=j\omega} = -1,
\]

so one can get the magnitude and the phase equations,

\[
|M_T(A, \phi)C_{FOPD}(s)P_c(s)|_{s=j\omega} = 1,
\]

\[
\arg[M_T(A, \phi)C_{FOPD}(s)P_c(s)|_{s=j\omega}] = -\pi.
\]

Thus, all the \( \omega \) satisfying (23) can be treated as the gain crossover frequencies \( \omega_{gc} \) with \( A = 1 \) and \( \phi = \phi_m \) for the system (18) in Figure 5. Because equations (23) and (32) are equivalent, every frequency \( \omega \) corresponding to the point on the curve of \( K_p \) versus \( K_d \) in parameter plane \((K_p, K_d)\) can also be treated as \( \omega_{gc} \).

Thus, with the specified \( \omega_c, \phi_m \), a fixed \( \mu \) and a fixed \( \omega_o \), the other two FOADRC parameters \( K_p \) and \( K_d \) can be determined at a point. Besides, the point should be tested whether it is in the stable region. Then, sweeping over the \( \mu \in (0, 2) \), a curve in the \((K_p, K_d, \mu)\)-space can be determined. All the points on this curve can be guaranteed to satisfy the two specifications \( \omega_c \) and \( \phi_m \). Then, Sweeping all the \( \omega_o \in (\omega_{gc}, \omega_{max}) \), one can obtain a three-dimensional graph about \( K_p, K_d \) and \( \mu \). Every point on this graph corresponding to the parameters \((K_p, K_d, \mu)\) of the FOADRC satisfying the pre-specified \( \omega_{gc} \) and \( \phi_m \).

One example is given as: With the pre-specified \( \phi_m = 80^\circ \), \( \omega_{gc} = 50 \text{ rad/s} \) and fixed \( \mu = 0.9 \), \( \omega_o = 200 \text{ rad/s} \), the point corresponding to the parameters \((K_p, K_d)\) is determined as a red star ‘A’ shown in Figure 6. Sweeping all the \( \mu \) in \((0, 2)\), a series red stars can be obtained shown in Figure 8 and visualized in three-dimensional parameter space which is shown in Figure 9. Every point on this curve meets \( \omega_{gc} = 50 \text{ rad/s} \) and \( \phi_m = 80^\circ \) according to Figure 10 with the \( \omega_o = 200 \text{ rad/s} \), simultaneously. Sweeping all the \( \omega_o \) in \((\omega_{gc}, 800)\), one can obtain a three-dimensional scatter plot of \( K_p - K_d - \mu \) as shown in Figure 11. Every point corresponding to the parameters satisfies the frequency-domain specifications \( \omega_{gc} = 50 \text{ rad/s} \) and \( \phi_m = 80^\circ \).

![Figure 8](image_url)

Figure 8. The designed \( K_p \) and \( K_d \) with sweeping all the optional \( \mu \in (0, 2) \).
Figure 9. The designed $K_p$ and $K_d$ with all the $\mu \in (0, 2)$.

Figure 10. Bode plots with different $\mu$.

Figure 11. All the $K_p$, $K_d$, and $\mu$ with $\phi_m = 80^\circ$ and $\omega_{gc} = 50$ rad/s.

4.2.2. The Optimal FOADRC Satisfying Time-Domain Specifications

Given all the parameters of the FOADRC corresponding to the point on the above curve in Figure 9, the step and load-disturbance response simulation are implemented and the ITAE of the trial is calculated in MATLAB/Simulink. The ITAE is calculated using (33), which is the sampling form of (30)

$$J_{ITAE} = \sum_{n=1}^{\infty} t[n] |e[n]| \Delta t.$$  

(33)
Thus, one can obtain the correspondence plot between $\mu$ and $J_{ITAE}$ as shown in Figure 12. And the smallest $J_0$ corresponding to the $\mu = 1.0$ is shown as a red star in Figure 12. Sweeping all $\omega$ the in $(\omega_{gc}, 800)$, the three-dimensional scatter plot of $\mu - \omega_o - J_{ITAE}$ is shown in Figure 13. According to Figure 13, the smallest $J_{min}$ can be determined, and the parameters $(K_p, K_d, \omega_o, \mu)$ of the optimal FOADRC corresponding to the $J_{min}$ can be obtained.

![Figure 12. $J_{ITAE}$ with different $\mu$.](image1)

![Figure 13. All the $\mu$, $\omega_o$, and $J_{ITAE}$ with $\phi_m = 80^\circ$ and $\omega_{gc} = 50$ rad/s.](image2)

In summary, the design guidelines are shown in Figure 14.

![Figure 14. The design guidelines summary.](image3)
4.3. IOADRC Design Strategy

The design strategy for IOADRC satisfying the user-specified frequency specifications \( \omega_{gc} \) and PM is also given with a short explanation. For a fair comparison with FOADRC, set the same \( \omega_o \) with FOADRC. Just as analyzed above, the parameters \( K_p \) and \( K_d \) with \( \mu = 1 \) can be determined according to Equations (26) and (27), with \( \omega = \omega_{gc}, \phi = \phi_m \).

5. Simulation Results

In this section, simulation studies are carried out on PMSM speed servo system compared with IOADRC and typical PID controller \([42]\). The tuning strategy for typical PID controller is based on the frequency-domain specifications (gain crossover frequency, phase margin and flat phase constraint).

5.1. Tracking Performance

Given the frequency-domain specifications \( \omega_{gc} = 10 \text{ rad/s} \) and \( \phi_m = 60^\circ \), the optimal FOADRC is obtained applying the above proposed method in Section 4,

\[
K_{p1} = 123.59, \quad K_{d1} = 36.248, \quad \mu = 0.74, \quad \omega_o = 40 \text{ rad/s}.
\]  

(34)

For fair comparison, the IOADRC is designed with the same frequency-domain specifications \( \omega_{gc} = 10 \text{ rad/s} \) and \( \phi_m = 60^\circ \) and the same \( \omega_o = 40 \text{ rad/s} \) according to the Section 4.3, so that one can get

\[
K_{p2} = 202.703, \quad K_{d2} = 18.282.
\]  

(35)

Similarly, the typical PID controller can be designed with the same frequency-domain specifications as

\[
C_{PID}(s) = 0.719 + \frac{1.7416}{s} + 0.006s.
\]  

(36)

The open-loop Bode plots of the three control systems are presented in Figure 15. Three control systems have the same \( \omega_{gc} \) and \( \phi_m \). The closed-loop Bode plots of three control systems are shown in Figure 16. Figure 16 shows that the designed IOADRC control system has the biggest resonance peak, which means the IOADRC control system has the biggest overshoot than other control systems. The designed PID control system has the smallest overshoot with the smallest resonance.

Given the reference speed as 600 rpm, PMSM speed responses are performed, using the designed FOADRC, IOADRC, and PID controller. The simulation results are shown in Figures 17 and 18. The comparison results are shown in Table 1.

**Figure 15.** Open-loop Bode diagrams of three control systems.
Obviously, the designed PID control system has the smallest overshoot and the designed FOADRC control system has the biggest overshoot. The overshoots of three control systems are consistent with the analysis in Figure 16. However, comparing with the IOADRC control system, the designed FOADRC control system has shorter settling time.
(0.7369 s) and smaller overshoot (20.5%); comparing with PID control system, the designed FOADRC control system has a shorter settling time, although the overshoot is bigger. Overall, the designed FOADRC control system achieves the best tracking performance by comparison with $J_{ITAE}$ and can achieve a compromise between overshoot and settling time.

Table 1. Comparison results of three control systems (simulation).

| Controller | Speed Tracking | Anti-Load Disturbance |
|------------|----------------|-----------------------|
|            | Overshoot (%) | Settling Time (s) | $J_{ITAE}$ | Speed Drop (%) | $J_{ITAE}$ |
| FOADRC     | 20.5          | 0.7369               | 19.2       | 12.07          | 110.32     |
| IOADRC     | 26.4          | 0.9831               | 27.18      | 11.08          | 124.3      |
| PID        | 19            | 0.985                | 28.05      | 19.47          | 301.45     |

5.2. Robustness to External Disturbance

The process sensitivity Bode plots ($P(s)/(1+C(s)P(s))$) for three control systems are shown in Figure 19. Figure 19 shows that the designed FOADRC and IOADRC control systems have similar anti-load performance, and both outperform PID control system. Injecting the load disturbance when the motor speed is running, the anti-load disturbance responses of three control systems are performed as shown in Figure 17, and the comparison results are shown in Table 1.

As shown in Figure 19, the designed FOADRC and IOADRC control systems have the similar speed drop, and both are less than the speed drop of the PID control system. According to Figure 17 and Table 1, the designed PID control system has the biggest speed drop (19.47%) and recovery time; the speed drop shows that the anti-load disturbance performances of FOADRC and IOADRC control systems are basically the same and both significantly smaller than the PID control system, which also can be seen clearly from $J_{ITAE}$. These results are consistent with the analysis from Figure 19.

6. Experiment Results and Discussion

In this section, the real PMSM speed control experiments are carried out to verify the effectiveness of the proposed tuning strategy for FOADRC and IOADRC. The laboratory PMSM platform is shown in Figure 20. The platform consists of a PMSM, which is the model is Sanyo-P50B08075HXS, a DC generator, a DC power, a servo driver, and a PC. The servo drive is based on the TMS320F28335 DSP and control algorithm implementation based on C-program. The sampling frequency of the current-loop is 8 kHz and the velocity-loop is 1.6 kHz. The fractional-order operator $s^{0.74}$ is implemented by the impulse-response-invariant-discretization (IRID) method [36] and has the following form (Equation (37)), with the sampling period $T_s = 0.00625$ s, and the comparison of approximated Bode plot and true Bode plot is shown in Figure 21.
\[ s^{0.74} \approx \frac{N}{D}, \quad (37) \]

\[ N = z^5 - 3.27103z^4 + 4.00345z^3 - 2.22461z^2 + 0.529238z - 0.0370365, \]

\[ D = 0.003855z^5 - 0.009175z^4 + 0.007067z^3 - 0.001626z^2 - 0.0001659z + 5.35515 \times 10^{-5}. \]

Figure 20. The PMSM speed control platform

Figure 21. Comparison of approximated Bode plot and true Bode plot.

6.1. Tracking Performance

Setting the reference speed as 600 rpm, PMSM speed step response experiments are performed, using the designed FOADRC, IOADRC, and PID controller. The experimental results are presented in Figure 22, and the comparison results are shown in Table 2.
Figure 22. The speed and anti-load disturbance responses of three control systems (experiment).

Table 2. Comparison results of three control systems (experiment).

| Controller | Speed Tracking | Anti-Load Disturbance |
|------------|----------------|-----------------------|
|            | Overshoot (%)  | Settling Time (s)     | I\textsubscript{ITAE} | Speed Drop (%) | I\textsubscript{ITAE} |
| FOADRC     | 22.1           | 0.985                 | 28.72                   | 17.6           | 174.814               |
| IOADRC     | 32.3           | 1.47                  | 60.58                   | 16.6           | 210.8931              |
| PID        | 19             | 1.94                  | 64.3414                 | 19.47          | 368.0378              |

Figure 22 shows that the designed PID control system has the smallest overshoot, and the designed IOADRC control system achieves the biggest overshoot, which are consistent with the analysis from Figure 16 and the simulation results in Figure 17. Comparing with the IOADRC control system, the designed FOADRC control system has shorter settling time (0.985 s) and smaller overshoot (22.1%); the designed PID control system has the smallest overshoot but the longest settling time. In summary, the FOADRC control system achieves the best tracking performance according to the I\textsubscript{ITAE} and can achieve a compromised performance in terms of overshoot and settling time between IOADRC and PID control systems.

6.2. Robustness to External Disturbance

Rejecting the same disturbance, the anti-load disturbance responses of three control systems are performed in Figure 22. As can be seen visually from Figure 22, IOADRC and FOADRC control systems achieve the similar speed drop and both less than the speed drop of the PID control system. There result are consistent with the analysis from Figure 19 and the simulation results in Figure 17. The designed PID control system has the biggest speed drop (19.47%) and recovery time. The IOADRC and FOADRC control systems have basically similar speed drop (16.6% and 17.6%), and the anti-load disturbance performance of IOADRC has a little oscillation. The detailed comparison results are shown in Table 2. In summary, the proposed FOADRC can achieve the best control performance in terms of anti-load over IOADRC and PID controller.

6.3. Discussion

Comparing with the traditional IOADRC, the proposed FOADRC can achieve better tracking performance with a smaller overshoot, and the value of the I\textsubscript{ITAE} is smaller. On
the other hand, the proposed FOADRC has a little bigger speed drop when inputs load comparing with IOADRC. However, recover time is less than IOADRC and has a smaller value of $J_{\text{ITAE}}$. Comparing with the traditional PID controller, the proposed FOADRC can achieve better disturbance rejection performance with less speed drop when inputs load. On the other hand, the proposed FOADRC has a bigger overshoot comparing with PID controller. However, the settling time of the proposed FOADRC is less than the one of PID controller and has less $J_{\text{ITAE}}$.

7. Conclusions

The paper proposes a FOADRC with a FOPD controller for the PMSM speed servo system, which is identified through a nonlinear identification method. A stabilization method is presented to obtain the stabilizing FOPD controller for the PMSM speed servo plant via D-decomposition. Furthermore, a synthesis is proposed for the FOADRC design to meet user-specified frequency-domain specifications, i.e., phase margin and gain crossover frequency, and the time-domain specification, i.e., ITAE, simultaneously. The design method is also applicable to IOADRC. The designed optimal FOADRC/IOADRC can get desired control performance as satisfying two given frequency-domain specifications and the optimal $J_{\text{ITAE}}$. The simulation and experiment are carried out for the PMSM speed servo, comprised of the traditional IOADRC and PID controller. Based on the simulation and experiment results, the proposed FOADRC can achieve better tracking performance and more robustness to external disturbance. The $J_{\text{ITAE}}$ for speed tracking of the designed FOADRC are 52.59% and 55.36% better than the $J_{\text{ITAE}}$ of IOADRC and PID controller, respectively. Obviously, the settling time of the designed FOADRC is the shortest (0.7369 s), which is 32.99% and 49.22% less than the settling time of the designed IOADRC and PID controller. Besides, the $J_{\text{ITAE}}$ for anti-load disturbance of the designed FOADRC is 17.11% and 52.50% better than the $J_{\text{ITAE}}$ of IOADRC and PID controller, respectively.

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Abbreviations

FOADRC fractional-order active disturbance rejection control
FOPD fractional-order proportional-derivative
ESO extended state observer
PMSM permanent magnet synchronous motor
ADRC active disturbance rejection control
IOADRC integer-order active disturbance rejection control
PID proportional-integral-derivative
FO fractional-order
FOPI fractional-order proportional-integral
FO(PD) fractional-order (proportional-derivative)
FOPID fractional-order proportional-integral-derivative
ISE integral square error
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