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CAPILLARY-LIMITED EVAPORATION FROM WELL-DEFINED MICROSTRUCTURED SURFACES

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ABSTRACT

Thermal management is increasingly becoming a bottleneck for a variety of high power density applications such as integrated circuits, solar cells, microprocessors, and energy conversion devices. The performance and reliability of these devices are usually limited by the rate at which heat can be removed from the device footprint, which averages well above 100 W/cm² (locally this heat flux can exceed 1000 W/cm²). State-of-the-art air cooling strategies which utilize the sensible heat are insufficient at these large heat fluxes. As a result, novel thermal management solutions such as via thin-film evaporation that utilize the latent heat of vaporization of a fluid are needed. The high latent heat of vaporization associated with typical liquid-vapor phase change phenomena allows significant heat transfer with small temperature rise.

In this work, we demonstrate a promising thermal management approach where square arrays of cylindrical micropillar arrays are used for thin-film evaporation. The microstructures control the liquid film thickness and the associated thermal resistance in addition to maintaining a continuous liquid supply via the capillary pumping mechanism. When the capillary-induced liquid supply mechanism cannot deliver sufficient liquid for phase change heat transfer, the critical heat flux is reached and dryout occurs. This capillary limitation on thin-film evaporation was experimentally investigated by fabricating well-defined silicon micropillar arrays using standard contact photolithography and deep reactive ion etching. A thin film resistive heater and thermal sensors were integrated on the back side of the test sample using e-beam evaporation and acetone lift-off. The experiments were carried out in a controlled environmental chamber maintained at the water saturation pressure of ≈3.5 kPa and ≈25 °C. We demonstrated significantly higher heat dissipation capability in excess of 100 W/cm². These preliminary results suggest the potential of thin-film evaporation from microstructured surfaces for advanced thermal management applications.

NOMENCLATURE

Symbols

| Symbol | Description |
|--------|-------------|
| D      | diameter (m) |
| H      | height (m)  |
| I      | current (A) |
| L      | pitch (m)   |
| P      | pressure (N/m²) |
| Q      | heating power (W) |
| qʰ    | heat flux (W/m²) |
| R      | resistance (Ω) |
| T      | temperature (K) |
| t      | time (s)    |
| ΔT    | superheat (ΔT = T_{wall} - T_{sat}), (K) |
| V      | voltage (v) |
| W      | channel width (m) |

Subscripts

| Subscript | Description |
|-----------|-------------|
| amb       | ambient     |
| back      | back side   |
| ps        | power source|
| sat       | saturation  |
| htr       | heater      |
| tot       | total       |
| wall      | solid-liquid contact wall |

INTRODUCTION

Advances in many cutting-edge-technologies are becoming increasingly dependent on the capability of removing concentrated heat loads from small areas. For example, early Intel Pentium CPUs utilized forced air convection to dissipate
≈30 W/cm² waste heat. In the last few decades, this heat flux has increased by more than threefold due to the ever-increasing miniaturization and faster processing speed [1-3] of electronic devices, which has motivated the need to develop more efficient thermal management schemes.

Two-phase cooling strategies have received significant interest due to the large latent heat of vaporization associated with phase change [4-6]. While various approaches such as pool boiling [7-9], flow boiling [10-13], spray cooling [14-16], and heat pipes [17-21] have been pursued by utilizing the latent heat of vaporization to improve thermal performance, thin-film evaporation [22-25] with liquid film thicknesses of few microns promises to be an efficient strategy for dissipating high heat fluxes. Schonberg and Wayner [26] developed an analytical model for predicting the maximum heat load that can be dissipated from the thin-film region. Wang et al. [24, 27] developed a simplified model based on the augmented Young-Laplace equation and obtained an analytical solution for the total heat transfer in the thin-film region. Their model showed that the thin-film region constitutes more than 50% of the total heat flux during evaporation. Yan et al. [28] showed that the heat transfer efficiency of thin-film evaporation is much higher than nucleate boiling heat transfer. In addition to the high heat fluxes, the superheat that is required for phase change in thin-film evaporation is expected to be smaller than that for a bubble growth during nucleate boiling, especially in the initial stages of bubble nucleation [28]. Ma and Peterson [29] numerically studied the thin film profile, heat transfer coefficient, and temperature profile along the axial direction of a triangular groove. Hanlon and Ma [30] showed that when the particles size decreases, the thin-film region can be significantly increased. While all of these theoretical studies suggest the potential of thin-film evaporation, past experimental studies including using sintered metal powders [21, 31, 32], micro post arrays [33, 34], and carbon nanotubes [35, 36] for implementing thin-film evaporation indicate that limitations associated with transporting the coolant to the heated area exist. Recent studies have used micro/nanostructured surfaces to overcome this limitation by generating capillary pressure for passive liquid transport [37-40]. Nam et al. [41] have numerically characterized the capillary limitation of dense hydrophilic copper micropillar posts. Coso et al. [42] showed an enhancement in heat flux of ≈120 W/cm² by using biporous wicks (micronscale pin fins separated by microchannels) to overcome the capillary limitation. Microscale fin pins are used to generate high capillary pressure while larger microchannels are used to reduce the overall flow resistance.

In this work, we used a two-tier strategy to overcome the capillary limitation. In the first-tier of surface texturing, we used closely spaced micropillar arrays to generate high capillary pressure, while a second-tier of surface texturing with sparse micropillars was used to create channels with minimal viscous loss for liquid transport. Such a strategy improves the liquid transport for thin-film evaporation by decoupling the capillary pressure from the viscous loss.

**EXPERIMENT**

We fabricated cylindrical silicon micropillar arrays in a square pattern with diameter $D$, height $H$ and pitch $L$. Scanning electron micrographs (SEMs) of the textured area of two samples used in this investigation are shown in Fig. 1a and 1b.

![Figure 1](https://proceedings.asmedigitalcollection.asme.org/doi/10.1115/DETC2013-15239)

**Figure 1:** (a) Scanning electron micrograph (SEM) of one level porosity sample # 1 with dimensions $D$, $H$, and $L$ of 4.6 μm, 21.7 μm, and 20.0 μm, respectively. (b) Scanning electron micrograph of biporous sample # 2 with dimensions $D$, $H$, and $L$ of 7.7 μm, 24.2 μm, and 20.0 μm, respectively. The microchannel (second level of porosity) with width $W=52.0$ μm (edge-to-edge) was added to assist liquid transport by reducing the overall viscous loss. The arrows show the longitudinal direction of liquid transport. The SEM images were acquired at a 10˚ inclination. Side (c) and top (d) view of the schematic (not to scale) of a typical test sample used in the experiment. Degassed water was supplied to the water reservoir via a water hose. The capillary pressure that is generated as a result of microstructuring the surface transports the water from the reservoir to the 1x1 cm² heated area.
The test samples were fabricated using projection photolithography and deep reactive ion etching (DRIE). For sample #1 in Fig. 1a, $D$, $H$, and $L$ were 4.6 μm, 21.7 μm, and 20.0 μm, respectively. Biporous wicks with four longitudinal water channels of widths $W$=52.0 μm (edge-to-edge) were incorporated in sample #2 where $D$, $H$, and $L$ were 7.7 μm, 24.2 μm, and 20.0 μm, respectively (Fig. 1b). The biporous wicks were incorporated to assist liquid transport by reducing the overall flow resistance.

The overall dimensions of the test sample including the liquid reservoirs along the four edges was 3x3 cm$^2$ while the center 1x1 cm$^2$ area was microstructured for thin-film evaporation (Fig. 1c and 1d). The microstructured area was surrounded by a 8 mm wide water reservoir which was also created alongside the micropillars during the DRIE process. The depth of the water reservoir was assumed to be the same as the height of the micropillars as shown by the schematic in Fig. 1c. The reservoir was covered from the top using a 500 μm thick borosilicate pyrex glass which was bonded anodically to the silicon substrate by heating the silicon wafer and the pyrex glass to 400 °C. Prior to the anodic bonding, four 2.5 mm diameter holes were laser cut through the pyrex glass (Fig. 1d) for supplying water through a water hose. The back side of the test sample was patterned to create a resistive heater and three temperature sensors using e-beam evaporation and acetone lift-off. The heater covers the center 1x1 cm$^2$ structured area. The heater and sensors were fabricated using 100 nm thick platinum layer on top of a 20 nm thick titanium seed layer. They were annealed at 300 °C for 4 hours using a conventional oven. The sensors were calibrated before use and their resistance was confirmed to depend linearly on temperature with no hysteresis.

![Figure 2: Schematic (not to scale) of the experimental setup.](image)

The water in the canister is kept at the saturation temperature and pressure after degassing using a PID controller. The canister is degassed in priori and kept at constant temperature and pressure using a PID (proportional-integral-derivative) controller. The heater was turned on once a continuous liquid supply was established between the external water canister and the internal water reservoirs next to the micropillars. The capillary pressure then takes over as the mechanism to transport the liquid from the reservoir to the microstructured area which was heated using a platinum thin-film heater. The back side temperature is also measured using the temperature sensors which are integrated with the heater during the microfabrication step.

**RESULTS**

The result of a typical experimental run on sample # 1 with one level of micro-porosity and $D$=4.6 μm, $H$=21.7 μm, and $L$=20.0 μm is shown in Fig. 3. The input supply voltage to the back side heaters was increased in steps until dryout. The sample was maintained at each input power for a sufficiently large time confirming the steady state behavior at each step. The input voltage ($V_{in}$) as a function of elapsed time is shown in Fig. 3a while the heater electrical resistance ($R_{heater}$) is shown in Fig. 3b. The total heating power ($Q_{tot}$) was then calculated as follows,
where $I_{ps}$ is the power supply current. The total heating power as a function of elapsed time is shown in Fig. 3c and the corresponding back side temperature $T_{back}$ along with the saturation chamber temperature $T_{sat}$ is shown in Fig. 3d.

Dryout is observed at a critical power of $\approx 78$ W ($t \approx 28$ min., Fig. 3c) beyond which the capillary-induced liquid supply mechanism was not sufficient to provide enough liquid for phase change heat transfer via evaporation. Dryout was also visually confirmed where a dry patch was observed to develop at the center of the heated area. The thermal runaway phenomenon where the sudden decrease in heat removal capability due to dryout resulted in a significant increase in the back side temperature was observed at $t \approx 28$ min. in Fig. 3d. As a result of the increase in temperature, the heater resistance rapidly increased (Fig. 3b) decreasing the current flow and hence the input power to the heater as shown in Fig. 3c. In order to avoid heater burnout and sample failure, the input power to the sample was cut off once the back side temperature exceeded 150 °C. The results presented in Fig. 3 were highly repeatable and similar values of dryout power were consistently observed during all experimental test runs. The onset of isolated nucleate boiling with localized regions of cyclic bubble nucleation, growth, departure, and rewetting processes were observed at larger superheats ($\Delta T > 45$ °C) which resulted in significant increase in the back side temperature fluctuation at higher superheats as shown in Fig. 3d. As a result, the error bars at higher superheats in Fig. 4 were significantly larger than those at lower superheats where the sample was undergoing predominantly thin-film evaporation based heat dissipation mechanism.

A dryout power of 78 W translates approximately to dryout heat flux of 78 W/cm$^2$ for the 1x1 cm$^2$ heated textured area in the current case. The heat flux is plotted as a function of superheat in Fig. 4. A value of 78 W/cm$^2$ is a significantly high heat flux and demonstrates the potential of thin-film evaporation for advanced thermal management strategies using a completely passive capillary pumping scheme. We performed additional experiments with another sample (#2) where the pillar diameter was increased to 7.7 μm while maintaining the height and pitch approximately the same. An increase in pillar diameter for the same pitch implies higher capillarity based pumping pressure as demonstrated in our previous work [40].
Alongside the increase in diameter, we also introduced longitudinal microchannels (second level of porosity) on sample #2. As a result, viscous losses in the micropillar array where the liquid had to travel uniformly through the micropillar array structure to reach the center of the sample further away from the reservoir were minimized due to these bypass liquid lines. The heat flux versus superheat for this sample is also shown in Fig. 4. As expected, significant increase (≈30%) in dryout heat flux (≈102 W/cm²) was observed on this sample when compared to sample #1 (≈78 W/cm²). Moreover, the heat flux for a given superheat, i.e., the effective heat transfer coefficient was also increased in sample #2 when compared to sample #1. This result can be explained by the reduction in the thermal resistance in the porous layer where the pillar diameter was increased from 4.6 μm to 7.7 μm. Isolated nucleate boiling was also observed at higher superheats (ΔT > 45 °C) for both samples which made it difficult to identify the contribution of thin-film evaporation to the heat dissipation mechanism. Moreover, due to the large test sample size (3×3 cm²) compared to the heated area (1×1 cm²), the conduction and convection losses were not accounted for in this calculation. We are currently working to improve the experimental setup to quantify the heat losses and more systematically investigate the parametric effect of the micropillar array geometry and microchannel width on the dryout heat flux. Nonetheless, the results obtained thus far are encouraging and show that thin-film evaporation is a promising approach to address thermal management needs of the next generation electronic devices.

CONCLUSIONS

We investigated thin-film evaporation using well-defined two-tier microstructured surfaces. The first tier contains closely spaced micropillar arrays to generate high capillary pressure, while the second tier contains sparse micropillar arrays to create biporous wicks with microchannels for reducing the overall flow resistance. The microchannels improve liquid transport by providing a less-viscous bypass path for fluid flow. Preliminary experimental results indicate that thin-film evaporation is a promising thermal management strategy to address high heat fluxes in excess of 100 W/cm². We dissipated a heat flux of ≈78 W/cm² by using micropillar array structures only. By adding a second tier of biporous wicks into the design, we improved the dissipated heat flux to ≈102 W/cm² (≈30% increase). The biporous wick enhanced the thermal performance by improving liquid transport to sustain evaporation and effectively delay dryout. The results presented in this work are preliminary and neither the micropillar array geometry nor the widths of the microchannels have been optimized to demonstrate the true potential of thin-film evaporation. However, we believe that higher heat flux can be dissipated by optimizing the geometry of the pillar array structure and the microchannel width.

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