On-chip Spectrometer Formed by a Multi-stage Structure

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**ABSTRACT**
With apparent size and weight advantages, on-chip spectrometer could be a good choice for the spectrum analysis application which has been widely used in numerous areas such as optical network performance monitoring, materials analysis and medical research. In order to realize the broadband and the high resolution simultaneously, we propose a new on-chip spectrometer structure, which is a two-stage structure. The coarse wavelength division is realized by the cascaded Mach-Zehnder interferometers, which is the first stage of the spectrometer. The output of the Mach-Zehnder interferometers are further dispersed by the second stage structure, which can be realized either by arrayed waveguide gratings or by digital Fourier transform spectrometer structure. We further implemented the thermo-optic modulation for the arrayed waveguide gratings to achieve a higher spectral resolution. The output channel wavelengths of the spectrometer are modulated by the embedded heater to obtain the first order derivative spectra of the input optical signal to obtain a 2nm resolution. With respect to the computer simulation and device characterization results, the 400nm spectral range and the nanoscale resolution have been demonstrated.

**1. Introduction**

The application of optical spectrometers ranges in many areas, such as biochemical sensing, food and drug testing, medical treatment and environmental monitoring \cite{1-4}. However, due to the size, high power consumption and price, difficult for secondary development, the application of traditional spectrometers has been greatly limited. Compared to the traditional spectrometers, miniaturized spectrometers have the advantages of low cost, small volume, low power consumption and easy secondary development, which expands the application. Nevertheless, miniaturized spectrometer which is usually based on discrete optical components, doesn’t have high integration and flexibility. As more and more high to the requirement of portability, further miniaturization and integration have become a trend of spectrometer. On-chip spectrometers, with apparent Size, Weight, and Power (SWaP) advantages, have unprecedented impact on applications ranging from unmanned devices to intelligent platform. During the last few years, on-chip spectrometers have become an enormously active area, resulting in significant progress \cite{5-12}. Among the methods to realize on-chip spectrometers, silicon photonics offers an approach to realize an integrated and cheap spectroscopic system because...

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of its mature processing and integration [13]. The silicon-based on-chip spectrometer can be realized by either dispersive components, or interferometers. Dispersive components like Echelle diffraction gratings (or planar concave grating) and arrayed waveguide gratings (AWG) can achieve a high resolution, but suffer serious SNR penalties when designed for high spectral resolution, which precludes high performance spectrometers with a large bandwidth. The spectrometer based on interference can obtain a large bandwidth with a high SNR benefiting from the multiplex advantage. However, compared to the dispersive components, it’s difficult to realize a high resolution. In application, compact, broadband, and high-resolution spectrometers are appealing for sensing application.

In this work, we propose and experimentally demonstrate a novel spectrometer architecture. The spectrometer is realized by a multi-stage structure, which combines the high resolution advantage of dispersive components and the broadband advantage of interference structure. We further implemented the thermos-optic modulation to achieve a higher spectral resolution. Compared with other existing on-chip spectrometers, the multi-stage waveguide spectrometer can achieve both a broadband spectral range and a relatively high resolution.

2. Methods

In this work, there are two methods to realize the spectrometer. As shown in Figure 1, the spectrometer is a multi-stage structure. The first stage of the structure is a cascaded Mach-Zehnder Interferometer (MZI) structure based coarse wavelength division de-multiplexer to separate the signal of 1150-1550 nm into four channels with a channel spacing of 100 nm. Then, the second stage can be realized by two ways, one way is the AWG structures, and the other is digital Fourier transform structures. The signal in each of the four outputs of the cascaded MZIs can be individually dispersed into 8 channels further with a channel spacing of 12.5 nm. Then, the second stage can be realized by two ways, one way is the AWG structures, and the other is digital Fourier transform structures. The signal in each of the four outputs of the cascaded MZIs can be individually dispersed into 8 channels further with a channel spacing of 12.5 nm by the AWG structures. Finally, the output channel wavelengths of AWGs are modulated (with a wavelength shift 2 nm) by the embedded heater to obtain the first order derivative spectra of the input optical signal. In order to improve the resolution, a digital Fourier transform (dFT) spectrometer can be also developed as the alternative for the second stage, which is formed by a reconfigurable MZI (more details in Discussion part). The two stage structure with a cascaded MZI and AWGs will be firstly introduced as follows.

![Figure 1. On-chip spectrometer principle block diagram](image)

**2.1 The Cascaded Mach-Zehnder Interferometer**

The cascaded MZI shown in Figure 2 was employed for the first stage of the spectrometer and was used to disperse the 1150-1550 nm signal into four channels with a channel spacing of 100 nm. By cascading multiple MZIs, the pass band can be engineered while keeping the device quite compact [14]. The MZI is constructed by connecting two directional couplers with an optical delay line. The free spectral range (Δλ) of the individual MZI is inversely proportional to the delay length (ΔL)

\[
\Delta \lambda = \frac{\lambda_i}{n_g \cdot \Delta L}.
\]

Where \(\lambda_i\) is the center wavelength, \(\Delta L\) is the physical path length difference between longer and shorter arm of the MZI and \(n_g\) is group index of the designed waveguide which is defined as:

\[
n_g = n_e - \frac{dn_e}{d\lambda}.
\]

Where \(n_e\) is the effective index. The operating wavelength of the cascaded structure is centered at 1350 nm and each MZI at this stage is designed for a free spectral range (Δλ) of 200 nm and 400 nm, respectively. The ΔL was selected to be 2×1.33μm and 2× 0.67μm for first and second MZI structures, respectively. The performance of this MZI cascaded structure was studied by the 2.5D FDTD method. As shown in Figure 3, according to the simulation results, the peak transmission efficiency of the MZI structure was determined to be ranged from 55 – 65% (-2.5 dB) and the bandwidth for each channel were 100 nm which were sufficient for our application.
2.2 Arrayed Waveguide Grating

An AWG consists of two star-couplers and an array of waveguides with constant successive increment of length \((\Delta L)\), shown as Figure 4. The input and output apertures of the phased array are in the Rowland circle configuration \([15]\) such that the image plane follows a circle with radius \(f\). The working principle of an AWG is based on the optical phased array, which has identical elements and constant phase difference between successive elements. The light enters into input star-coupler and diverges. This diverging light beam is then coupled in an array of waveguides and propagates through these waveguides to a second star-coupler. Hence, the AWG equation can be written as:

\[
\lambda_c = \frac{n_s \cdot \Delta L}{m}
\]

\[
d\theta = \frac{mn_g}{n_s n_a d_a}
\]

\[
\Delta \lambda = \frac{\lambda_c \cdot n_a}{mn_g}
\]

in which \(n_a\) is the group index of the arrayed waveguides.

Although the above equations show that there are many degrees of freedom for the AWG design, its design parameters are also limited by the design rule of specific fabrication process. For example, the effective index \(n_a\) is dependent on the width (w) and height (h) of the waveguide but the h in the actual device fabrication is limited to be the thickness of the Si layer in the SOI wafer. In this work, the w and h of the arrayed waveguide were fixed to be 0.5 \(\mu m\) and 220 nm, respectively.

In this work, the design was based on the modeling result of 2.5D FDTD method by Lumerical. We have designed and simulate 4 AWGs with \(\lambda_c = 1200nm, 1300nm, 1400nm\) and \(1500nm\), each AWG has a free spectral range of 100nm and a channel space of 12.5nm. One of the computer simulation results for AWG structures (\(\lambda_c\) of 1500 nm) were shown in the Figure 5. The free spectral range and channel spacing of the AWG structure (\(\lambda_c\) of 1500 nm) was determined to 100 nm and 12.5 nm. The transmission efficiency for all channels was determined to be > 20% (-6 dB). Hence, the total loss of this on-chip spectrometer design was determined to be 14.5 dB including a 2.5 dB loss from MZI, 6 dB loss from AWG structure and 2×3dB loss from the input and output coupling.
2.3 Thermo-optic Modulation

In order to improve the spectral resolution, the spectrum derivation was performed by introducing the thermo-optic modulation. Derivative spectrometry utilizes the derivatives of the absorption spectrum data for qualitative and quantification chemical analysis. The derivative spectrometry technology was first introduced in 1950s by A. Giese et al [16] to measure the low intensity bands overlapped by bands of higher intensity in the absorption spectra. Our design has leveraged the tunable AWG structures for the spectrum derivation. An embedded heater structures were added on top of these AWG structures for the refractive index (n) modulation by the thermo-optic (TO) effect:

$$\frac{dn}{dT} = C_T \tag{7}$$

where $C_T$ is the TO coefficient and is determined to be $+1.84 \times 10^{-4}/K$ and $10^{-5}/K$ for the silicon and silicon dioxide, respectively. Hence, the central wavelength shift ($\Delta \lambda_c$) arising from the change of a temperature ($\Delta T$) due to the TO effect can be calculated by [17]:

$$\Delta \lambda_c = \frac{\lambda_c}{n_{si}} \left( \frac{\partial n_{si}}{\partial n_{si}} \Delta n_{si} + \frac{\partial n_{si}}{\partial n_{so2}} \Delta n_{so2} \right) \tag{8}$$

where $n_{si}$ and $n_{so2}$ denote the refractive index of silicon and silicon dioxide, respectively. When the center wavelength $\lambda_c$ is shifted, the transmission spectra at State 1 and State 2 are recorded. Then, the first-order derivative spectrum of the input signal can be directly obtained as $I' = (I_{state1} - I_{state2})/dl$.

2.4 Fabrication

The fabrication process of the on-chip spectrometer is shown in Figure 6. The device was fabricated on a silicon on insulator (SOI) wafer with 220 nm silicon and 2 μm buried oxide. The embedded heater structure for the on-chip spectrometer has also been added. The unit size of the on-chip spectrometer is 3mm×3mm.

Figure 6. The passive silicon photonic fabrication process

3. Results

The performance of the device was characterized by injecting 1mW (0dBm) optical signal with a $\lambda$ ranging from 1440 to 1640 nm into the spectrometer using a tunable laser, the measurement results for $\lambda_c$ of 1500 nm were shown in Figure 7. As shown in the Figure 7, the free spectral range and channel spacing of the AWG with a $\lambda_c$ of 1500 nm were determined to be 65 nm and 8 nm, respectively, which were less than the designed values of 100 nm and 12.5 nm, respectively. According to equation (5) and (6), these reductions indicated that the effective group index $n_g$ of the fabricated device is smaller than that of the computer simulation. Besides, the loss of the spectrometer was determined to be ~16 dB which was higher
than the 14.5 dB from the computer simulation. These performance degradations were generally due to fabrication tolerance and imperfection for device fabrication process.

Figure 7. The measurement result of the on-chip spectrometer: the spectra at the 8 output channels of the AWG structure with $\lambda_c = 1500$ nm

By applying a voltage ranging from 0 to 20 V across the embedded heater structure, a change of $\lambda_c$ from 0 to 0.8 nm was observed and this was sufficient for spectrum derivation, shown as Figure 8.

Figure 8. The measurement result of the thermo-optic effect for $\lambda_c$ modulation.

4. Discussion

Compared with dispersive spectrometers, Fourier transform spectrometers benefits the advantage of signal-to-noise ratio. Generally, on-chip Fourier transform spectrometers mainly rely on thermo-optic or electro-optic modulation to change the optical path length in a waveguide [18]. These effects can produce minute refractive index modifications, however, resulting in large device size and constraining the practically attainable spectral resolution to tens of cm$^{-1}$ in wave number. The digital Fourier transform spectrometer can overcome this limitation [19]. As shown in Figure 9(a), the spectrometer is a reconfigurable MZI. Each arm consists of $j/2$ cascaded sets of optical switches connected by waveguides of varying lengths, where $j$ is an even integer ($j=2(K+1)$, $k \in [1, K]$). When light propagates through the reference paths (marked with black color) in both MZI arms, the MZI is balanced with zero optical path length difference between the two arms. Lengths of the waveguide paths in red differ from the reference paths by a power of two times $\Delta L$. Each permutation of the switches thus corresponds to a unique optical path length difference between the arms, covering 0 to $(2j-1) \cdot n_g \cdot \Delta L$ with a step size of $n_g \cdot \Delta L$, where $n_g$ represents the waveguide group index. The number of spectral channels, defined by the distinctive optical states the device furnishes, is:

$$N = 2^j$$

and the spectral resolution is given following the Rayleigh criterion [19, 20]:

$$\delta \lambda = \frac{\lambda^2}{(2^j - 1) \cdot n_g \cdot \Delta L \cdot 2^j} \approx \frac{1}{2^j} \cdot \frac{\lambda^2}{n_g \cdot \Delta L}$$

where $\lambda$ denotes the center wavelength. A dFT spectrometer has been designed with $N=8$, and the layout is shown as Figure 9(b). Unlike the dispersive spectrometer, both the spectral channel count and resolution of the dFT spectrometer scale exponentially with the number of cascaded switch stages. This unique exponential scaling behavior allows high-resolution spectroscopy with a radically simplified device architecture. Then, compared to thermo-optic or electro-optic-based index modulation, direct modification of the waveguide path offers much larger optical length path tuning, enabling superior spectral resolution within a compact device. The device also benefits from the multiplex advantage to ensure significantly enhanced signal-to-noise ratio over the dispersive devices. Moreover, the spectrometer only requires a single-element photodetector rather than a linear array, which further reduces system complexity and cost. Thus, the dFT structure is a good alternative to replace AWGs, which is also the focus of the future research.

Figure 9. (a) Block diagram illustrating the generic structure of a dFT spectrometer. (b) The mask layout of the dFT structure with $j/2=8$
5. Conclusions

A multi-stage on-chip spectrometer has been proposed and demonstrated. The spectrometer was designed by a 2-stage waveguide structure with a cascaded MZI and AWGs, which can realize a spectral range of 400 nm and a spectrum resolution of 2 nm for performing spectrum derivation. From the characterization results, we found that the device performances, including free spectral range, channel spacing and optical loss, were slightly poorer than the simulation results. This implies that the fabrication tolerance and imperfection are larger than the disclosed design rule from the foundry. A dFT spectrometer has been also discussed to be used as the second stage of the on-chip spectrometer, which can help improve the resolution greatly. Compared with other existing on-chip spectrometer, the cascaded waveguide spectrometer can achieve both a broadband spectral range and a relatively high resolution, which makes it potential in sensing application.

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