FDFB: Full Domain Functional Bootstrapping Towards Practical Fully Homomorphic Encryption

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Abstract

Computation on ciphertexts of all known fully homomorphic encryption (FHE) schemes induces some noise, which, if too large, will destroy the plaintext. Therefore, the bootstrapping technique that re-encrypts a ciphertext and reduces the noise level remains the only known way of building FHE schemes for arbitrary unbounded computations. The bootstrapping step is also the major efficiency bottleneck in current FHE schemes. A promising direction towards improving concrete efficiency is to exploit the bootstrapping process to perform useful computation while reducing the noise at the same time. We show a bootstrapping algorithm, which embeds a lookup table and evaluates arbitrary functions of the plaintext while reducing the noise. Depending on the choice of parameters, the resulting homomorphic encryption scheme may be either an exact FHE or homomorphic encryption for approximate arithmetic. Since we can evaluate arbitrary functions over the plaintext space, we can use the natural homomorphism of Regev encryption to compute affine functions without bootstrapping almost for free. Consequently, our algorithms are particularly suitable for arithmetic circuits over a finite field with many additions and scalar multiplication gates. We achieve significant speedups when compared to binary circuit-based FHE. For example, we achieve 280-1200x speedups when computing an affine function of size 784 followed by any univariate function when compared to FHE schemes that compute binary circuits. With our bootstrapping algorithm, we can efficiently convert between arithmetic and boolean plaintexts and extend the plaintext space using the Chinese remainder theorem. Furthermore, we can run the computation in an exact and approximate mode where we trade-off the size of the plaintext space with approximation error. We provide a tight error analysis and show several parameter sets for our bootstrapping. Finally, we implement our algorithm and provide extensive tests. We demonstrate our algorithms by evaluating different neural networks in several parameter and accuracy settings.

1 Introduction

A fully homomorphic encryption scheme provides a way to perform arbitrary computation on encrypted data. The bootstrapping technique, first introduced by Gentry [Gen09], remains thus far the only technique to construct secure fully homomorphic encryption schemes. The reason is that in current homomorphic schemes evaluating encrypted data induces noise, which will eventually “destroy” the plaintext if too high. In practice bootstrapping also remains one of the major efficiency bottlenecks.

The most efficient bootstrapping algorithms to date are the FHEW-style bootstrapping developed by Ducas and Micciancio [DM15], and the TFHE-style bootstrapping by
Chillotti et al. [CGGI16, CGGI20]. At a high level, the bootstrapping procedure in these types of bootstrapping algorithms does two things. First, on input, a Regev ciphertext [Reg09] the bootstrapping algorithm outputs a ciphertext whose error is independent of the error of the bootstrapped ciphertext. Second, it computes a negacyclic function $F$ on the input plaintext. That is $F$ must satisfy $F(x + N) = -F(x) \mod 2 \cdot N$, where $x \in \mathbb{Z}_2 \cdot N$. This functionality, together with the linear homomorphism of Regev encryption [Reg09], is enough for FHEW and TFHE to compute arbitrary binary gates. For example, given $\text{Enc}(a \cdot 2N/3)$ and $\text{Enc}(b \cdot 2N/3)$, where $a, b \in \{0, 1\}$, we compute the NAND gate by exploiting the negacyclicity. We set $F$ to output $-1$ for $x \in [0, N)$, and from the negacyclicity property we have $F(x) = 1$ for $x \in [N, 2 \cdot N)$. To compute the NAND gate we first compute $\text{Enc}(x) = \text{Enc}(a \cdot 2N/3) + \text{Enc}(b \cdot 2N/3)$ and then $\text{Enc}(c) = \text{Enc}(F(x))$.

Note that only for $a = b = 1$, we have $x = 4N/3 > N$ and $c = 1$. For all other valuations of $a$ and $b$ we have $x < N$ and $c = -1$. Now, we can exploit that $c \in \{-1, 1\}$ to choose one of two arbitrary values $y, z \in \mathbb{N}$ by computing $y^{1+\ell} - z^{(c-1)/2}$. When computing the NAND gate we choose $y = 0$ and $z = 2N/3$. Similarly, we can realize other boolean gates.

The crucial observation is that the bootstrapping algorithm relies on the negacyclicity property of the function $F$ to choose an outcome. In particular, the outcome is a binary choice between two values.

Unfortunately, the requirement on the function $F$ stays in the way to efficiently compute useful functions on non-binary plaintexts with only a single bootstrapping operation. For instance, we cannot compute $\frac{1}{1+x}$, $\tanh(x)$, $\max(0, x)$, univariate polynomials or other functions that are not negacyclic. Hence, to evaluate such functions on encrypted data, we need to represent them as circuits, encrypt every input digit, and perform a relatively expensive bootstrapping per gate of the circuit. By solving the negacyclicity problem, we can hope for significant efficiency improvements by computing all functions over a larger plaintext space with a single bootstrapping operation. Additionally, we could leverage the natural and extremely efficient linear homomorphism of Regev encryption [Reg09], to efficiently evaluate circuits with a large number of addition and scalar multiplication gates like neural networks.

1.1 Contribution

Our main contribution is the design, detailed error analysis, implementation [Sch], and extensive tests of a bootstrapping algorithm that solves the negacyclicity problem. In particular, our bootstrapping algorithm can evaluate all functions over $\mathbb{Z}_t$ for some integer $t$, as it internally embeds a lookup table. We refer to our bootstrapping algorithm as “full domain bootstrapping” as opposed to a version of TFHE [CGGI16, CGGI20, CIM19], sometimes called PBS [CJP21, CLOT21] from “programmable bootstrapping scheme”, that can evaluate all functions only over half of the domain $[0, \lfloor t/2 \rfloor)$. We stress that the difference goes far beyond the size of the plaintext space. As we will explain in more detail in Section 1.2, TFHE for non-negacyclic functions cannot exploit the natural linear homomorphism of Regev encryption without any assumptions on the plaintext.

Our Full Domain Functional Bootstrapping (FDFB) does not require any assumptions on the plaintext. The ability to compute affine functions “almost for free” gives our FDFB a tremendous advantage in evaluation time for arithmetic circuits with a large number of addition and scalar multiplication gates. We show several parameters sets targeting different bit-precisions and security levels. In particular, we show parameters that allow our FDFB to bootstrap and compute any function $f : \mathbb{Z}_t \rightarrow \mathbb{Z}_t$ with very low error probability, where $t$ is a 7 or 8-bit prime number, but we note that we can bootstrap larger plaintexts when choosing a larger modulus $Q$ and degree $N$ of the ring $\mathcal{R}_Q$. We show that we can take the message modulus higher for approximate arithmetic—for instance, 10 or 11 bits. We also show how to leverage the Chinese remainder theorem to extend the plaintext space to $\mathbb{Z}_t$ for $t$ being a large (e.g., 32-bit or higher) composite integer.
In short, we use the fact that the ring $\mathbb{Z}_t$ for $t = \prod_{i=1}^{n} t_i$, where the $t_i$’s are pairwise co-prime is isomorphic with the product ring $\mathbb{Z}_{t_1} \times \cdots \times \mathbb{Z}_{t_n}$. To the best of our knowledge, our paper is the first to utilize the CRT trick in an FHEW/TFHE-style bootstrapping algorithm to extend the plaintext space. Furthermore, the technique was recently included as an important feature in a recent release of a major FHE library [CJL+20]. Finally, we note that our techniques can be directly applied to many other problems like efficient conversion between finite field and binary plaintexts, plaintext equality checks, polynomial evaluation, which efficiency is invariant of the degree of the polynomial, etc.

**Performance.** We implemented our bootstrapping algorithm using the Palisade library [PAL21]. We test the performance of our bootstrapping algorithm using the Palisade library [PAL21]. We test the performance of our FDFB on two use cases. We show the performance for arithmetic circuits modulo $t$. Furthermore, we exemplify the performance of our FDFB by evaluating neural networks. Roughly speaking, a neural network is a circuit where gates, called neurons, are parameterized by weights $w_0, w_1, \ldots, w_n \in \mathbb{N}$, take as input wires $x_1, \ldots, x_n \in \mathbb{N}$ and output $f(w_0 + \sum_{i=1}^{n} x_i \cdot w_i)$. The function $f$ is typically a non-linear function called the activation function. When homomorphically evaluating an encrypted query to the neural network, we compute the linear combination corresponding to each gate, called neurons, parameterized by weights. Furthermore, the technique was recently included as an important feature in a recent release of a major FHE library [CJL+20]. Finally, we note that our techniques can be directly applied to many other problems like efficient conversion between finite field and binary plaintexts, plaintext equality checks, polynomial evaluation, which efficiency is invariant of the degree of the polynomial, etc.

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We compare our FHE scheme with the state-of-the-art lookup table methods [CGGI16, CGGI20, CGGI17]. To give the best quality of comparison, we rewrite the lookup table algorithms from [CGGI17] in PALISADE [PAL21]. In particular, the arithmetic in $\mathcal{R}_Q$ and other algorithms that our algorithm has in common with [CGGI17] share the same code. We note that the timings in our works and previous works may differ. One reason, among other, is that our implementation is based on the number-theoretic transform as opposed to the fast Fourier transform as in [CGGI16, CGGI20, CGGI17]. Furthermore, we choose new parameters for [CGGI16, CGGI20, CGGI17] that target the same security levels as our bootstrapping algorithm. We report that evaluating neurons with 784 weights is over 280 to 1200 times faster than the lookup table methods, depending on the parameter setting.

1.2 Deeper Dive into the Problem and our Solution

We first give a high-level overview of FHEW and TFHE bootstrapping to showcase the problem for using it to compute arbitrary functions. In this section, we keep the exposition rather informal and omit numerous crucial details to highlight the essential ideas underlying our constructions.

**Regev Encryption.** First, let us recall the learning with errors based encryption scheme due to Regev [Reg09]. In the symmetric key setting the encryption algorithm chooses a vector $a \in \mathbb{Z}_q^n$ and a secret key $s \in \mathbb{Z}_q^n$, and computes an encryption of a message $m \in \mathbb{Z}_t$ as $[b, a^\top] \in \mathbb{Z}_q^{n+1} \times 1$, where $b = a^\top \cdot s + \hat{m} + e \mod q$, where $\hat{m} = \frac{1}{2} \cdot m$ and $e < \frac{2}{t}$ is a “small” error. We assume that $t|q$ for simplicity, but other settings are possible as well. To decrypt, we compute $\left| \frac{1}{q} \cdot ([b, a^\top] \cdot [1, -s^\top]) \right| = \left| \frac{1}{q} \cdot (\frac{1}{2} \cdot m + e) \right| = m$.

The ring version of the encryption scheme is constructed over the cyclotomic ring $\mathcal{R}_Q$ defined by $\mathcal{R} = \mathbb{Z}[X]/(X^N + 1)$ and $\mathcal{R}_Q = \mathcal{R}/Q\mathcal{R}$. Similarly as for the integer version we choose $a \in \mathcal{R}_Q$ and a secret key $s \in \mathcal{R}_Q$, and encrypt a message $m \in \mathcal{R}_t$ as $[b, a]$ with $b = a \cdot s + \frac{1}{2} \cdot m + e$, where $e \in \mathcal{R}_Q$ is a “small” error.

**FHEW/TFHE Bootstrapping.** Now let us proceed to the ideas underlying FHEW
bootstrapping, the plaintext is shifted to lie in $[0, N]$. The concept is to setup a homomorphic accumulator \text{acc} to be an RLWE encryption which initially encrypts the message $\text{rotP} \cdot X^e \in \mathcal{R}_q$, where $\text{rotP} = \frac{Q}{N} \cdot (1 - \sum_{i=2}^{N} X^{i-1}) \in \mathcal{R}_q$. Then we multiply \text{acc} with encryptions of $X^{-a[i]} s[i] \in \mathcal{R}_q$. So that after $n$ iterations the message of the accumulator is set to
\[
\text{rotP} \cdot X^{b - \sum_{i=1}^{n} a[i] s[i]} = \text{rotP} \cdot X^{k + m + e} \\
= \text{rotP} \cdot X^{m + e} \mod 2N \in \mathcal{R}_q.
\]
Note that we set the coefficients of \text{rotP} such that the constant coefficient of the resulting polynomial is $\frac{Q}{N}$ if $m + e \in [0,N)$, and $-\frac{Q}{N}$ if $m + e \in [N,2N)$. We can extract an LWE encryption of the constant term from the rotated accumulator and obtain an LWE ciphertext of the sign of the message without the error term $e$. Clearly, if the most significant bit of $m$ is set ($m$ is a negative number) and $e \leq q/2t$, then $m + e = q/t \cdot m + e \in [N, 2N)$. Otherwise, $m + e \in [0,N)$.

We can extend the method to compute other functions simply by setting the coefficient of \text{rotP} to have the desired value after multiplying it by $X^{m+e}$ in $\mathcal{R}_q$. To be precise we want to compute $F : \mathbb{Z} / (N^\ell) \mapsto \mathbb{Z}$, and for our reasoning we use $F : \mathbb{Z} / (N^\ell) \mapsto \mathbb{Z}$ such that $F(x) = \frac{Q}{N} \cdot f([\frac{x}{N^\ell}])$. Since we work over the multiplicative group $G$ of order $2N$, but \text{rotP} can only have $N$ coefficients, we can only compute functions $F$ such that $F(x + N \mod 2N) = -F(x)$ mod $Q$. In other words, multiplying any \text{rotP} by $X^N$ rotates \text{rotP} by a full cycle negating all its coefficients. In particular, it flips the sign of the constant coefficient. Note that this behavior also affects the function $f$ that we want to compute.

Lu et al. [jLHH+21] claim (see Fig. 2, Theorem 1 in [jLHH+21]) to compute any function with FHEW [DM15]. Unfortunately, they mistakenly assume the roots of unity in $\mathbb{Z}/(N^\ell)$ for the group $[1, X, \ldots, X^N]$ of order $N$, instead of $G$ of order $2N$. This issue invalidates Theorem 1 in [jLHH+21] and the correctness of their main contribution, as FHEW is in fact only capable to compute negacyclic functions.

**Problems when Computing Arbitrary Functions.** To compute arbitrary functions $f$ a straightforward solution is to assume that the phase $\bar{m} + e$ is always in $[0,N)$, which is the case when $m \in [0,t/2)$. In particular, $m$ must have its sign fixed. The immediate disadvantage is that we cannot safely compute affine functions on LWE ciphertexts without invoking the bootstrapping algorithm. As an example let us consider $\bar{m} = \frac{Q}{N}(m_1 - m_2)$ and let us ignore for simplicity the error terms. Clearly when $m_2 > m_1$ we have $m_1 - m_2 \mod t \in [t/2, t)$ and $\bar{m} \in [q/2, q)$. In this case, the output of the bootstrapping will be $-F(\bar{m} - q/2)$ assuming we set the rotation polynomial to correctly compute $F$ on the interval $[0, q/2)$. To summarize, to use the natural functional bootstrapping correctly, we need to use it as Carpow et al. [CIM19] to compute a lookup table on an array of binary plaintexts that are composed to an integer within the interval $[0,t/2)$. Another solution is to encode the messages in a fixed interval as done by Chillotti et al. [CJP21], and assume the outcome of the affine function does not go outside the interval. Before bootstrapping, the plaintext is shifted to lie in $[0,t/2)$. The interval encoding technique requires to sacrifice at least 2 bits of the plaintext space. We note that previous work including [CJP21] does not give a specification for the encoding method. We learned the method from a private conversation.

**Our Solution: Full Domain Functional Bootstrapping.** Our main contribution is the design of a bootstrapping algorithm that can compute all functions on an encrypted plaintext instead of only negacyclic functions. In particular, we do not need to assume...
that the plaintext lies in any particular interval. Moreover, we can handle arithmetic in $Z_t$, where $t$ is a prime or composite integer. Our first observation is that, as shown in Bourse et al. [BMMP18], we can compute the sign of the message. In particular, given that the message $m$ is encoded as $\tilde{m} = \frac{2N}{t} \cdot m$, where $t$ is the plaintext modulus, we have that if $\tilde{m} \in [0, N)$, then $\text{sgn}(m) = 1$, and $\tilde{m} \in [N, 2 \cdot N)$, then $\text{sgn}(m) = -1$. Note that this function does not return exactly the sign function since it returns $1$ for $m = 0$, but it is enough for our purpose. The idea is to make two bootstrapping operations: the first computes the sign; the second computes the function $F(x)$. Then we multiply the sign and the result of the function. Note that if $x \in [N, 2 \cdot N)$, then $-F(x) \cdot \text{sgn}(x) = F(x)$.

There are two problems with this solution. First of all, we cannot simply multiply two LWE ciphertexts in the leveled mode. While there are methods to do so [Bra12, BV11, BGV12, FV12], in these methods, the noise growth is quadratic and dependent on the size of the plaintexts. Moreover, one of those plaintexts may be large having a significant impact on the parameters. The second problem is that this way, the function $F$ must satisfy $F(x) = F(x + N)$. Thus our primary goal is not satisfied.

Let us first describe how to deal with the second problem. In short, instead of evaluating the sign, we will compute a bit $b$ that indicates the interval in which that plaintext lies. This bit will help us choose the correct output of the evaluated function. That is, we split the function into $F_0$ that computes $F$ correctly for plaintexts in $[0, N)$, and $F_1$ that computes correctly for the remaining plaintexts. To deal with the first problem, we resign of multiplying two ciphertexts at all. Note that a hypothetical multiplication algorithm could now bootstrap the bit $b$ using a bootstrapping that returns a GSW tuple, and in a leveled mode we could then compute $\text{GSW}(1 - b) \cdot \text{LWE}(F_0(x)) + \text{GSW}(b) \cdot \text{LWE}(F_1(x))$. From the properties of the GSW cryptosystem [GSW13], the resulting ciphertexts’ noise is at most an additive function of the noises of both ciphertexts. Furthermore, there are implementations for bootstrapping that return GSW ciphertexts [DM15, CGGI17]. We, however, slightly depart from the idea and give a more efficient solution with better error management. Instead of multiplying two ciphertexts, we leverage the fact that the functions $F_0$ and $F_1$ are publicly known, which means that the rotation polynomials for these functions are known as well. Now, based on LWE ciphertexts of a certain form, we will build a new accumulator that will encode one of the given rotation polynomials. Notably, our accumulator builder uses a version of a homomorphic Mux gate. Finally, we will bootstrap using the given accumulator and we obtain $F(x) = F_t(x)$ as desired. We note that this method is roughly twice as fast as creating a GSW ciphertext and multiplying it with LWE ciphertexts.

Now we can compute any function $F : Z_{2N} \rightarrow Z_Q$, and what follows $f : Z_t \rightarrow Z_t$, where $t \neq p$. It remains to use a different message scaling in the rotation polynomials. Furthermore, we can compute affine functions over $Z_t$ without bootstrapping which costs several microseconds per operation (see Table 6).

1.3 Related Work

Since Gentry’s introduction of the bootstrapping technique [Gen09], the design of fully homomorphic encryption schemes has received extensive study, e.g., [BV11, Bra12, FV12, BGV12, AP13, GSW13, HS15, CH18, HS21]. Brakerski and Vaikunathan [BV14] achieved a breakthrough and gave a bootstrapping method that, exploiting the GSW cryptosystem [GSW13] by Gentry, Sahai, and Waters, incurs only a polynomial error growth. These techniques require representing the decryption circuit as a branching program. Alperin-Sheriff and Peikert [AP14] showed a bootstrapping algorithm, where they represent the decryption circuit as an arithmetic circuit and do not rely on Barrington’s theorem [Bar89]. Their method exploits the GSW cryptosystem [GSW13] to perform matrix operations on LWE ciphertexts. Hiromasa, Abe, and Okamoto [HAO15] improved
the method by designing a version of GSW that natively encrypts matrices. Recently, Genise et al. [GGH+19] showed an encryption scheme that further improves the efficiency of matrix operations. Unfortunately, Lee and Wallet [LW20] showed that the scheme is broken for practical parameters.

Building on the ideas of Alperin-Sheriff and Peikert [AP14], Ducas and Miccancio [DM15] designed a practical bootstrapping algorithm called FHEW. FHEW exploits the ring structure of the RLWE ciphertexts and RGSW noise management for updating a so-called homomorphic accumulator. Chillotti et al. [CGGI16, CGGI20] gave a variant, called TFHE, with numerous optimizations to the FHEW bootstrapping algorithm.

The FHEW and TFHE bootstrapping algorithms constitute state-of-the-art methods to perform practical bootstrapping. Further improvements mostly relied on incorporating packing techniques [CGGI17, MS18], and improved lookup table evaluation [CGGI17, CIM19]. An interesting direction is to exploit the construction of FHEW/TFHE and embed a lookup table directly into the bootstrapping algorithm. To this end, Bonnoron, Ducas, and Fillinger [BDF18] showed a bootstrapping algorithm capable of computing over larger plaintexts, which, however, returns a single bit of output per bootstrapping operation and uses RLWE instantiated over cyclic rings instead of negacyclic. Carpov, Izabache, and Mollimard [CIM19] show that we can compute multiple functions on the same input plaintext at the cost of a single TFHE bootstrapping. Notably, their method enjoys amortized time only for functions with a small domain like binary. Guimarães, Borin, and Aranha [GBA21] show several applications of [CIM19]. As mentioned earlier, Lu et al. [JLHH+21] use FHEW to compute negacyclic functions on CKKS [CKKS17] ciphertexts.

Relation to BFV/BGV [BV11, Bra12, FV12, BGV12] type schemes. In this setting, we consider FHE with a very low probability of having erroneous outcome of the homomorphic computation. We automatically satisfy CPA security [LM20] in contrast to homomorphic encryption for approximate arithmetic. In short, Li and Miccancio [LM20] showed key recovery attacks against some cryptosystems that have a high probability of returning an erroneous plaintext when such plaintext is revealed to a passive adversary. Current bootstrapping algorithms [HS15, CH18, HS21] for BFV/BGV type systems like HeLib [HE1, HS14, HS15, Alb17] or [SEA22] reduce the error of the ciphertext and raise the modulus, without performing any additional computation. To compute in BFV/BGV, we represent the program as an arithmetic circuit. This may be sometimes problematic. For example, CryptoNets [DGBL+16], which use the SEAL library to homomorphically evaluate a neural network, cannot easily compute popular activation functions. Therefore, CryptoNets uses $x^2$ as an activation function. Furthermore, we need to discretize the trained network by multiplying the weights and biases by a parameter $\delta$ and rounding. Since there is no homomorphic division algorithm, homomorphic computation on discretised data will increase the parameter $\delta$. For example, when computing $\sum_{i=1}^m x_i \cdot w_i'$, where $x_i = \delta \cdot x_i$ and $w_i' = \delta \cdot w_i$ and the $x_i$’s and $w_i$’s are the original floating point inputs and weights, we end up with $\delta^2 \cdot \sum_{i=1}^m x_i \cdot w_i$. Note that we ignored the rounding for simplicity. Unfortunately, we cannot rescale the result since there is no natural division algorithm in BFV/BGV. Consequently, CryptoNets has to choose enormous parameters that grow exponentially with the depth of the neural network. Our functional bootstrapping algorithm resolves these issues, as we can compute any activation function that is discretized over $\mathbb{Z}_\delta$. Along the way, we can compute the rescaling, i.e., division by $\delta$ and rounding, together with the activation function within a single bootstrapping operation. This allows us to compute neural networks of unbounded depth without increasing the parameters. On the other hand, we preserve the extremely fast linear operations from BFV/BGV (see Table 5).

Relation to CKKS/HEAAN [CKKS17] type schemes. As hinted in the abstract, we can run our bootstrapping algorithm in an approximate mode. In particular, the prob-
ability that the error distorts the least significant bits of the message to be bootstrapped grows along with the message space. For example, we choose the plaintext space to be 11-bit, instead of 7-bit as in the “exact” computation setting. However, when using such parameters, we cannot claim that the homomorphic encryption is a “exact” FHE anymore. Consequently, as with all approximate schemes, we cannot formally claim CPA-security [LM20], but we can apply the same countermeasures [LMSS22] against the Li-Micciancio attack. Nevertheless, this setting allows to use a larger message modulus with the same efficiency and is especially useful when approximate computation is sufficient (e.g., neural networks). Note that without bootstrapping algorithms like ours, current approximate homomorphic encryptions need to approximate a function via a polynomial, discretize its coefficients and evaluate a circuit as deep as the degree of that polynomial. For the approximate homomorphic computation setting, Lu et al. [jLHH+21] proposed to use FHEW [DM15] to compute negacyclic functions only. When using our algorithm, we can compute all functions over the plaintext space without posing any restriction on the plaintexts.

Applications to Neural Network Inference. Bourse et al. [BMMP18] applied TFHE [CGGI16, CGGI20] to compute the negacyclic $\text{sgn}(x)$ function as activation for a neural network. Izabachéne, Sirdey, and Zuber [ISZ19] evaluate another neural network (a Hopfield network) using the same method as Bourse et al. [BMMP18]. Chillotti et al. [CJP21] use TFHE to evaluate deep convolution neural networks and report excellent timings for image classification based on the MNIST dataset. Finally, we note that Bourse et al. [BMMP18] left as an open problem to compute general functions with a functional bootstrapping. It is worth noting that oblivious neural network evaluation became a critical use case for fully homomorphic encryption and got much attention and publicity [DGBL+16, CdWM+17, LJAVA17, JKLs18, JVCA18, KSK+18, KSK+20, BGGJ18, AB-SdV19, CDKS19, RSC+19, BGPG20]. These works focus on applying existing homomorphic encryption libraries like SEAL, HeLib, and HEAAN, which we discussed earlier.

Concurrent and Followup Work. Concurrently and independently, Chillotti et al. [CLOT21] shows a concept for a full domain functional bootstrapping algorithm that at a very high level is similar to ours. We stress that the methods to achieve FDFB are different. Chillotti et al. [CLOT21] propose to use the BGV cryptosystem instead of GSW as we suggested for a hypothetical solution, to multiply and choose one of two bootstrapped values. In contrast to our work, the authors do not give parameters or an implementation. Nevertheless, we note that due to the use of BGV/BFV style multiplication, the error after bootstrapping grows quadratically and is dependent on the size of the message. In contrast, our algorithm’s error is independent of the message. The works from [YXS+21, LMP21] improved and build upon [Klu22] the FDFB technique. Nevertheless, we note that our paper describes additional techniques, like the CRT trick as well as implementation, comparison and analytical details that may be of independent interest.

2 Preliminaries

In this section, we recall the algorithms from the literature that we use for our bootstrapping algorithm. Here we give only the interfaces to the algorithm and state their correctness lemmas. However, for reference, we give the specifications and the correctness analysis of all the algorithms in a unified notation in Appendix B and Appendix C respectively.

Notation. We denote as $\mathbb{B}$ the set \{0, 1\}, $\mathbb{R}$ as the set of reals, $\mathbb{Z}$ the set of integers, and $\mathbb{N}$ as the set of natural numbers. Furthermore we denote as $\mathcal{R}_{\mathbb{N}, \infty}$ the ring of polynomials $\mathbb{Z}[X]/(X^N + 1)$ and as $\mathcal{R}_{\mathbb{N}, q} = \mathcal{R}_{\mathbb{N}, \infty}/q\mathcal{R}_{\mathbb{N}, \infty}$ the ring of polynomials with coefficients in $\mathbb{Z}_q$. We denote vectors with a bold lowercase letter, e.g., $\mathbf{v}$, and matrices with uppercase letters $V$. We denote a $n$ dimensional column vector as $[f(., i)]_{i=1}^n$, where $f(., i)$ defines
the $i$-th coordinate. For brevity, we will also denote as $[n]$ the vector $[i]_i=1^n$, and more generally $[n,m]_{i=n}^m$ the vector $[n,\ldots,m]^\top$. Finally, let $a = \sum_{i=1}^N a_i \cdot X^{i-1}$, be a polynomial with coefficients over $\mathbb{R}$, then we denote $\text{Coefs}(a) = [a_i]_{i=1}^N$ the vector of coefficients of the polynomial $a$. For a random variable $a \in \mathbb{Z}$ we denote as $\text{Var}(a)$ the variance of $a$, as $\text{stddev}(a)$ its standard deviation and as $E(a)$ its expectation. For $a \in \mathbb{R}_{N,\infty}$, we define $\text{Var}(a) = \text{Var}(\text{Coefs}(a))_{i=1}^N$, $\text{stddev}(a) = \text{stddev}(\text{Coefs}(a))_{i=1}^N$ and $E(a) = E(\text{Coefs}(a))_{i=1}^N$. We denote $\|a\|_p = (\sum_{i=1}^n |a_i|^p)^{1/p}$ the $p$-norm of a vector $a \in \mathbb{R}^n$, where $\|\cdot\|$ denotes the absolute value. For polynomials we compute the $\infty$-norm by taking its coefficient vector. By $\text{Ham}(a)$ we denote the hamming weight of vector $a$, i.e., the number of of non-zero coordinates of $a$. We also define a special symbol $\Delta_{q,t} = \lceil \frac{t}{q} \rceil$ and a rounding function for an element $\Delta_{q,t} \cdot a \in \mathbb{Z}_q$, as $[a]_q^\top = \lceil \frac{t}{q} \cdot \Delta_{q,t} \cdot a \rceil$. For ring elements, we take the rounding of the coefficient vector.

Throughout the paper we denote as $q \in \mathbb{N}$ and $Q \in \mathbb{N}$ two moduli. The parameter $n \in \mathbb{N}$ always denotes the dimension of a LWE sample, that we define below. For rings, we always use $N$ to denote the degree of $\mathcal{R}_{N,q}$ or $\mathcal{R}_{N,Q}$. We define $\ell = \lceil \log_q N \rceil$ for some decomposition basis $L \in \mathbb{N}$. We denote bounds on variances of random variables by $B \in \mathbb{N}$. Often we mark different decomposition bases $L_{\text{name}}$ and the corresponding $\ell_{\text{name}}$, or bounds $B_{\text{name}}$ with some subscript $\text{name}$. Finally, in order not to repeat ourselves and not to overwhelm the reader, we limit ourselves to define certain variables in the definitions of the algorithms, and we refrain to repeat them in the correctness lemmas.

**Learning With Errors.** We recall the learning with errors assumption by Regev [Reg05].

Our description is a generalized version due to Brakerski, Gentry, and Vaikuntanathan [BGV12].

**Definition 1 (Generalized Learning With Errors)** Let $\chi_B = \mathcal{X}(n)$ be a distribution over $\mathcal{R}_{N,q}$ such that $\|\text{Var}(\epsilon)\|_{\infty} \leq B$ and $E(\epsilon) = 0$ for all $\epsilon \in \chi_B$. For $a \leftarrow \chi_{N,q}$, $\epsilon \leftarrow \chi_B$ and $s \in \mathcal{R}_{N,q}^{n\times 1}$, we define a Generalized Learning With Errors (GLWE) sample of a message $m \in \mathcal{R}_{N,q}$ with respect to $s$, as

$$\text{GLWE}_{B,n,N,q}(s,m) = \begin{bmatrix} b = a^\top \cdot s + \epsilon \\ a^\top \\ 0 \end{bmatrix} \in \mathcal{R}_{N,q}^{(n+1)\times 1}.$$ 

The GLWE$_{B,n,N,q}$ problem is to distinguish the following two distributions: In the first distribution, one samples $[b,a]^\top$ uniformly from $\mathcal{R}_{N,q}^{(n+1)\times 1}$. In the second distribution one first draws $s \leftarrow \chi_B$ and then samples $\text{GLWE}_{B,n,N,q}(s,m) = [b,a]^\top \in \mathcal{R}_{N,q}^{(n+1)\times 1}$. The GLWE$_{B,n,N,q}$ assumption is that the GLWE$_{B,n,N,q}$ problem is hard.

In the definition, we choose the secret key uniformly from $\mathcal{R}_{N,q}$. We note that often secret keys may be chosen from different distributions. When describing our scheme, we will emphasize this when necessary.

We denote an Learning With Errors (LWE) sample as $\text{LWE}_{B,n,q}(s,m) = \text{GLWE}_{B,n,1,q}$, which is a special case of a GLWE sample with $N=1$. Similarly we denote an Ring-Learning with Errors (RLWE) sample as $\text{RLWE}_{B,n,q}(s,m) = \text{GLWE}_{B,1,q}$ which is the special case of an GLWE sample with $n=1$. Sometimes we use the notation $c \in \text{GLWE}_{B,n,N,q}(s,m)$ (resp. LWE and RLWE) to indicate that a vector $c$ is a GLWE (resp. LWE and RLWE) sample of the corresponding parameters and inputs. Sometimes we leave the inputs unspecified and substitute them with “.” when it is not necessary to refer to them within the scope of a function.

**Definition 2 (Phase and Error for GLWE samples)** We define the phase of a sample $c = \text{GLWE}_{B,n,N,q}(s,m)$, as $\text{Phase}(c) = [1,-s^\top] \cdot c$. We define the error of a GLWE sample $\text{Error}(c) = \text{Phase}(c) - m$. 

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Lemma 1 (Linear Homomorphism of GLWE samples) Let \( c = \text{GLWE}_{B, n, q}(s, m_c) \) and \( d = \text{GLWE}_{B_4, n, q}(s, m_4) \). If \( c_{\text{out}} \leftarrow c + d \), then \( c_{\text{out}} \in \text{GLWE}_{B, n, q}(s, m) \), where \( m = m_c + m_4 \), and \( B \leq B_c + B_4 \). Furthermore, let \( d \in \mathcal{R}_{N, L} \) where \( L \in \mathbb{N} \). If \( c_{\text{out}} \leftarrow c \cdot d \), then \( c_{\text{out}} \in \text{GLWE}_{B, n, q}(s, m_c \cdot d) \), where

- \( B \leq B_0^2 \cdot B_c \), when \( d \) is a monomial with \( \|d\|_\infty \leq B_0 \), and
- \( B \leq N \cdot B_0^2 \cdot B_c \), when \( d \) is a polynomial with \( \|d\|_\infty \leq B_0 \).

Gentry-Sahai-Waters Encryption. We recall the cryptosystem from Gentry, Sahai and Waters [GSW13].

Definition 3 (Gadget Matrix) We call the column vector \( \mathbf{g}_{\ell, L} = [L_i]_{i=1}^{\ell} \in \mathbb{R}^{\ell \times 1} \) a powers-of-L vector. For some \( k \in \mathbb{N} \) we define the gadget matrix as \( G_{\ell, L, k} = I_k \otimes \mathbf{g}_{\ell, L} \in \mathbb{R}^{k \ell \times k} \), where \( \otimes \) denotes the Kronecker product.

Definition 4 (Decomposition) We define the \( G_{\ell, q}^{-1} \) function to take as input an element \( a \in \mathbb{R}_{N, q} \) and return a row vector \( a = [a_1, \ldots, a_\ell] \in \mathbb{R}_L^{\ell \times 1} \) such that \( a = a \cdot \mathbf{g}_{\ell, L} = \sum_{i=1}^\ell a_i \cdot L_i^{-1} \). Furthermore, we generalize the function to matrices where \( G_{\ell, q}^{-1} \) is applied to every entry of the input matrix. Specifically, on input a matrix \( M \in \mathbb{R}_{N, q}^{m \times k} \), \( G_{\ell, q}^{-1} \) outputs a matrix \( D \in \mathbb{R}_L^{m \times k \ell} \).

Definition 5 (Generalized-GSW Sample) Let \( s \in \mathbb{R}_{N, q}^n \) and \( m \in \mathbb{R}_{N, q}^n \). We define Generalized-GSW (GSW) samples as \( \text{GSW}_{B, n, q, L}(s, m) = A + m \cdot G_{\ell, L, (n+1)} \in \mathbb{R}_{N, q}^{(n+1)\ell \times (n+1)} \), where \( A[i, *] = \text{GLWE}_{B, n, q}(s, 0)\top \) for all \( i \in [(n+1)\ell] \). In other words, all rows of \( A \) consist of (transposed) GLWE samples of zero.

Similarly, as with LWE and RLWE, we denote an GSW sample as \( \text{GSW}_{B, n, q, L}(s, m) = \text{GSW}_{B, n, 1, q, L}(s, m) \), which is a special case of a GGSW sample with \( N = 1 \). Similarly we will denote an Ring-GSW (RGSW) sample as \( \text{RGSW}_{B, n, q, L}(s, m) = \text{GSW}_{B, 1, n, q, L}(s, m) \), which is the special case of an GGSW sample with \( n = 1 \). Will use the notation \( C \in \text{GSW}_{B, n, q, L}(s, m) \) (resp. GSW and RGSW) to indicate that a matrix \( C \) is a GGSW (resp. GSW and RGSW) sample with the corresponding parameters and inputs.

Definition 6 (Phase and Error for GGSW samples) We define the phase of a sample \( C = \text{GSW}_{B, n, q, L}(s, m) = A + m \cdot G_{\ell, L, (n+1)} \) as \( \text{Phase}(C) = C \cdot [1, -s]\top \). We define the error of a GLWE sample as \( \text{Error}(C) = \text{Phase}(C) - m \cdot G_{\ell, L, (n+1)} \cdot [1, -s]\top \in \mathbb{R}_{N, q}^{(n+1)\ell \times 1} \).

Lemma 2 (Linear Homomorphism of GGSW samples) Let \( C = \text{GSW}_{B, n, q, L}(s, m_c) \) and \( D = \text{GSW}_{B, n, q, L}(s, m_d) \). If \( C_{\text{out}} \leftarrow C + D \), then \( C_{\text{out}} \in \text{GSW}_{B, n, q, L}(s, m_c + m_d) \), where \( m = m_c + m_d \), and \( B \leq B_c + B_d \). Furthermore, let \( d \in \mathcal{R}_{L, b} \), where \( L_b \in \mathbb{N} \). If \( C_{\text{out}} \leftarrow C \cdot d \), then \( C_{\text{out}} \in \text{GSW}_{B, n, q, L}(s, m_c \cdot d) \) where

- \( B \leq B_0^2 \cdot B_c \), when \( d \) is a monomial with \( \|d\|_\infty \leq B_0 \), and
- \( B \leq N \cdot B_0^2 \cdot B_c \), when \( d \) is a polynomial with \( \|d\|_\infty \leq B_0 \).

The external product multiplies an RGSW sample with an RLWE sample resulting in an RLWE sample of the product of their messages.

Definition 7 (External Product) The external product \( \text{extProd} \) given as input \( C \in \text{GSW}_{B, n, q, L}(s, m_c) \) and \( d \in \text{GLWE}_{B, n, q, L}(s, m_d) \), outputs \( \text{extProd}(C, d) = G_{L, q}^{-1}(d) \cdot C \).
Lemma 3 (Correctness of the External Product) Let \( C = \text{GGSW}_{B_C, n, Q, L}(s, m_C) \), where \( m_C \) consists of a single coefficient, and \( d = \text{GLWE}_{B_d, n, Q}(s, m_d) \). If \( c \leftarrow \text{extProd}(C, d) \), then \( c^\top \in \text{GLWE}_{B, n, Q}(s, m) \), where \( m = m_C \cdot m_d \) and

- \( B \leq N \cdot (n + 1) \cdot \ell \cdot L^2 \cdot B_C + N \cdot B_{mc}^2 \cdot B_d \) in general, and
- \( B \leq N \cdot (n + 1) \cdot \ell \cdot L^2 \cdot B_C + B_{mc}^2 \cdot B_d \) when \( m_C \) is a monomial.

with \( B_{mc} \leq ||m_C||_\infty \).

**Mux Gate.** Informally, the Mux function takes as input a control GGSW sample and two GLWE samples. The gate outputs one of the input GLWE samples depending on the bit encrypted in the GGSW sample.

**Definition 8 (Mux Gate) The Mux function is defined as follows.**

- \( \text{Mux}(C, g, h) \): The algorithms takes as input \( C \in \text{GGSW}_{B_C, n, Q, L}(s, m_C) \), where \( m_C \in \mathbb{B} \), \( g \in \text{GLWE}_{B_g, n, Q}(s, m_g) \) and \( h \in \text{GLWE}_{B_h, n, Q}(s, m_h) \).
  
  If \( c_{out} \leftarrow \text{Mux}(C, g, h) \), then \( c_{out} \in \text{GLWE}_{B, n, Q}(s, m_{out}) \), where \( m_{out} = m_g \) for \( m_C = 0 \) and \( m_{out} = m_g \) for \( m_C = 1 \), and \( B \leq \frac{1}{3} \cdot (n + 1) \cdot N \cdot \ell \cdot L^2 \cdot B_C + \max(B_g, B_h) \).

**Modulus Switching.** The modulus switching technique, developed by Brakerski and Vaikuntanathan [BV11], allows an evaluator to change the modulus of a given ciphertext without the knowledge of the secret key.

**Definition 9 (Modulus Switching for LWE Samples) We define modulus switching from \( \mathbb{Z}_Q \) to \( \mathbb{Z}_q \) by the following algorithm.**

- \( \text{ModSwitch}(c, Q, q) \): On input a LWE sample \( c = [b, a]^\top \in \text{LWE}_{B, n, Q}(s, .) \), and moduli \( Q \) and \( q \), the function outputs \( c_{out} \in \text{LWE}_{B, n, q}(s, .) \).

**Lemma 4 (Correctness of the Mux gate) Let \( C \in \text{GGSW}_{B_C, n, Q, L}(s, m_C) \), where \( m_C \in \mathbb{B} \). Let \( g \in \text{GLWE}_{B_g, n, Q}(s, m_g) \) and \( h \in \text{GLWE}_{B_h, n, Q}(s, m_h) \).
  
  If \( c_{out} \leftarrow \text{Mux}(C, g, h) \), then \( c_{out} \in \text{GLWE}_{B, n, Q}(s, m_{out}) \), where \( m_{out} = m_g \) for \( m_C = 0 \) and \( m_{out} = m_g \) for \( m_C = 1 \), and \( B \leq \frac{1}{3} \cdot (n + 1) \cdot N \cdot \ell \cdot L^2 \cdot B_C + \max(B_g, B_h) \).

**Modulus Switching.**

**Definition 10 (Sample Extraction) Sample extraction consists of algorithms KeyExt and SampleExt.**

- \( \text{KeyExt}(s) \): Takes as input a key \( s \in \mathcal{R}_{N, q} \), and outputs \( \text{Coefs}(s) \).
- \( \text{SampleExt}(ct, k) \): Takes as input \( ct = [b, a]^\top \in \text{RLWE}_{B, N, Q}(s, .) \) and an index \( k \in [N] \). The function outputs \( c \in \text{LWE}_{B, N, q}(s, .) \).

**Lemma 5 (Correctness of Modulus Switching) Let \( c = \text{LWE}_{B, n, Q}(s, m \cdot (\frac{Q}{q})) \) where \( s \in \mathbb{Z}_q^n \) and \( Q = 0 \) mod \( t \). If \( c_{out} \leftarrow \text{ModSwitch}(c, Q, q) \), then \( c_{out} \in \text{LWE}_{B, n, q}(s, m \cdot (\frac{Q}{q})) \), where \( B \leq \frac{q^2}{Q^2} \cdot B_C + \text{Ham}(s) \cdot ||\text{Var}(s)||_\infty \).

**Sample Extraction.** Informally, sample extraction allows extracting from an RLWE sample an LWE sample of a single coefficient without increasing the error rate. Here we give a generalized version of the extraction algorithm, which can extract an LWE sample of any coefficient of the RLWE sample.

**Definition 11 (Sample Extraction)**

**Lemma 6 (Correctness of Sample Extraction) Let \( ct \in \text{RLWE}_{B, n, Q}(s, m) \), where \( s, m \in \mathcal{R}_{N, q} \). Denote \( m = \sum_{i=1}^N m_i \cdot X^{i-1} \), where \( m_i \in \mathbb{Z}_q \). If \( s \leftarrow \text{KeyExt}(s) \) and \( c \leftarrow \text{SampleExt}(ct, k) \), for some \( k \in [N] \), then \( c \in \text{LWE}_{B, N, q}(s, m_k) \), where \( B \leq B_{ct} \).**
**Key Switching.** Key-switching is an important technique to build scalable homomorphic encryption schemes. In short, having a key switching key, the evaluator can map a given LWE sample to an LWE sample of a different key.

**Definition 11 (Key Switching LWE to GLWE)** We recall key switching key generation, and LWE to GLWE key switching procedures as follows.

- **KeySwitchSetup**(\(B_{kK}, n, n', N, q, s, s', L_{kK}\)): Takes as input bound \(B_{kK} \in \mathbb{N}\), dimensions \(n, n' \in \mathbb{N}\), a degree \(N \in \mathbb{N}\) and a modulus \(q \in \mathbb{N}\), vectors \(s \in \mathbb{Z}_q^n\), \(s' \in \mathbb{R}^{n'}_q\), and a basis \(L_{kK} \in \mathbb{N}\). The algorithm outputs \(k_{sK} \in \text{GLWE}_{B_{kK}, n', N, q}(s', .)\).

- **KeySwitch**(\(c, k_{sK}\)): Takes as input \(c = [b, \mathbf{a}^T] \in \text{LWE}_{B_{sK}, n, q}(s, .)\) and a key switching key \(k_{sK} \in \text{GLWE}_{B_{sK}, n', N, q}(s', .)\). The algorithm outputs \(c_{out} \in \text{GLWE}_{B_{sK}, n', N, q}(s', .)\).

**Lemma 7 (Correctness of Key Switching)** If \(k_{sK} \leftarrow \text{KeySwitchSetup}(B_{kK}, n, n', N, q, s, s', L_{kK})\) and \(c_{out} \leftarrow \text{KeySwitch}(c, k_{sK})\) then \(c_{out} \in \text{GLWE}_{B_{sK}, n', N, q}(s', m)\) where \(B \leq B_{c} + n \cdot \ell_{kK} \cdot L_{kK}^2 \cdot B_{kK}\)

and \(\ell_{kK} = \lfloor \log_{B_{kK}} q \rfloor\).

**TFHE Blind Rotation and Bootstrapping.** Below we recall the blind rotation introduced by Chlötti et al. [CGGI16]. For both blind rotation and bootstrapping we use a decomposition vector \(u \in \mathbb{Z}^u\) for which the following holds. For all \(y \in S\) with \(S \subseteq \mathbb{Z}_2^n\) there exists \(s \in \mathbb{B}^\mathbb{N}\) such that \(y = \sum_{i=1}^u x[i] \cdot u[i] \mod 2 \cdot N\). For example for \(S = \{0,1\}\) we have \(u = \{1\}\), and for \(S = \{-1,0,1\}\) we have \(u = \{-1,1\}\).

**Definition 12 (TFHE Blind Rotation)** Blind rotation consists of a key generation algorithm \(\text{BRKeyGen}\), and blind rotation algorithm \(\text{BlindRotate}\).

- **BRKeyGen**(\(n, s, u, B_{kK}, N, Q, L_{\text{RGSW}}, s\)): Takes as input a dimension \(n\), a secret key \(s \in \mathbb{Z}_t^n\) and a vector \(u \in \mathbb{Z}^u\), a bound \(B_{kK}\), a degree \(N\) and a modulus \(Q\) defining the ring \(\mathbb{R}_{N,Q}\), a basis \(L_{\text{RGSW}} \in \mathbb{N}\), and a secret key \(s \in \mathbb{R}_{N,Q}\). The algorithm outputs a blind rotation key \(br_{K}\).

- **BlindRotate**(\(br_{K}, acc, ct, u\)): This algorithm takes as input a blind rotation key \(br_{K} = \text{RGSW}_{B_{kK}, N, Q, L_{\text{RGSW}}}(s, .)\), an accumulator \(acc \in \text{RLWE}_{B_{kK}, N, Q}(s, .)\), a ciphertext \(ct \in \text{LWE}_{B_{kK}, n, 2N}(s, .)\), where \(s \in \mathbb{Z}_t^n\) with \(t \leq 2 \cdot N\), and a vector \(u \in \mathbb{Z}^u\). The algorithm outputs \(acc_{out}\).

**Lemma 8 (Correctness of TFHE Blind-Rotation)** Let \(ct \in \text{LWE}_{B_{kK}, n, 2N}(s, .)\) be a LWE sample and denote \(ct = [b, \mathbf{a}^T] \in \mathbb{Z}_N^{(n+1)\times 1}\). Let \(acc \in \text{RLWE}_{B_{kK}, N, Q}(s, m_{acc})\). If \(br_{K} \leftarrow \text{BRKeyGen}(n, s, u, B_{kK}, N, Q, L_{RGSW}, s)\) and \(acc_{out} \leftarrow \text{BlindRotate}(br_{K}, acc, ct, u)\), then \(acc_{out} \in \text{RLWE}_{B_{kK}, N, Q}(s, m_{out})\), where \(m_{out} = m_{acc} \cdot X^b \cdot a \mod 2 \cdot N \in \mathbb{R}_{N,Q}\) and \(B_{out} \leq B_{acc} + 2 \cdot n \cdot u \cdot N \cdot L_{\text{RGSW}}^2 \cdot B_{kK}\).

**Definition 13 (Bootstrapping)** Bootstrapping is as follows.

- **Bootstrap**(\(br_{K}, u, ct, rotP, k_{sK}\)): This algorithm takes as input a blind rotation key \(br_{K} = \text{RGSW}_{B_{kK}, N, Q, L_{kK}}(s, .)\), a vector \(u \in \mathbb{Z}^u\), a LWE sample \(ct = \text{LWE}_{B_{kK}, n, q}(s, .) = [b, \mathbf{a}^T] \in \mathbb{Z}_q^{(n+1)\times 1}\), a polynomial \(rotP \in \mathbb{R}_{N,Q}\), and a LWE to LWE key switching key \(k_{sK}\). The algorithm outputs \(ct_{out}\).
| Name      | Description                                                                                                                                 |
|-----------|--------------------------------------------------------------------------------------------------------------------------------------------|
| extProd   | Given a linear transformation $B = \mathbf{BR}(s, mC)$ and a ciphertext $\mathbf{ct} = \mathbf{GLWE}(s, m)$, extProd computes the product $\mathbf{ct}' = \mathbf{GLWE}(s, m)$ with $m = mC \cdot m$. |
| Mux       | Given a linear transformation $B = \mathbf{BR}(s, mC)$, $g = \mathbf{GLWE}(s, m_0)$ and $h = \mathbf{GLWE}(s, m_h)$ with $mC \in \mathbb{B}$, Mux outputs $c = \mathbf{GLWE}(s, m)$ with $m = m_0$ if $mC = 1$ and $m = m_h$ otherwise. |
| ModSwitch | Given a LWE sample $c$ encrypted under source modulus $q$ and a target modulus $q'$, ModSwitch returns a LWE sample $c'$ encrypted under modulus $q'$ encoding the same message as $c$, w.r.t. to a message space $t$. |
| SampleExt | Given a RLWE sample $\mathbf{ct}$ and index $k \in \mathbb{N}$, SampleExt outputs a LWE sample $\mathbf{ct}'$ encoding the $k$-th coefficient of the polynomial encrypted in $\mathbf{ct}$. |
| KeySwitchSetup | On input a set of parameters, and two LWE secret keys $\mathbf{s}, \mathbf{s}'$, KeySwitchSetup generates a key-switching key $\mathbf{ksK}$. |
| KeySwitch  | On input a key-switching key $\mathbf{ksK}$ for the LWE keys $\mathbf{s}, \mathbf{s}'$ and a LWE sample $\mathbf{ct}$ under key $\mathbf{s}$, KeySwitch outputs a LWE sample $\mathbf{ct}'$ under key $\mathbf{s}'$ encoding the same message as $\mathbf{ct}$, w.r.t. to a message space $t$. |
| BRKeyGen   | On input a set of parameters, LWE key $\mathbf{s}$ and RLWE key $\mathbf{r}$, BRKeyGen generates a blind rotation key. |
| BlindRotate | Given a blind rotation key, a LWE sample $\mathbf{ct}$ under modulus $2N$ and an accumulator $\mathbf{acc} = \mathbf{RLWE}(s, m_{\mathbf{acc}})$, BlindRotate returns a sample $\mathbf{acc}_{\mathbf{out}} = \mathbf{RLWE}(s, m_{\mathbf{out}})$ with $m_{\mathbf{out}} = m_{\mathbf{acc}} \cdot X^{|\text{Phase}(\mathbf{ct})|}$. |
| Bootstrap  | Given a blind rotation key, a key switching key, a sample $\mathbf{ct} = \mathbf{LWE}(s, \Delta_{q,t} \cdot m)$ and a polynomial $\mathbf{rotP}$ correctly encoding a function $F : \mathbb{Z}_t \mapsto \mathbb{Z}_t$, Bootstrap returns $\mathbf{ct}_{\mathbf{out}} = \mathbf{LWE}(s, m_{\mathbf{out}})$ such that $m_{\mathbf{out}} = \Delta_{q,t} \cdot F(m)$ if $m < \frac{1}{2}$ and $m_{\mathbf{out}} = -\Delta_{q,t} \cdot F(m)$ otherwise. |

Table 1: List of procedures. The table contains an informal summary of the procedures from Section 2.

Theorem 1 (Correctness of TFHE Bootstrapping) Let $\mathbf{ct} \in \mathbf{LWE}_{B_{\mathbf{r}, n, q}(s, \Delta_{q,t} \cdot m)}$. Let $\mathbf{brK} \leftarrow \mathbf{BRKeyGen}(n, \mathbf{s}, \mathbf{u}, \mathbf{BRK}, N, Q, R_{\mathsf{GSW}}, \mathbf{s}, \mathbf{s}_f) \leftarrow \mathbf{KeySwitchSetup}(\mathbf{BrK}, N, n, q, \mathbf{s}_f, \mathbf{s}, \mathbf{LakK})$. Let $\mathbf{ct}_N \leftarrow \mathbf{ModSwitch}((\mathbf{ct}, q, 2 \cdot N) \in \mathbf{LWE}_{B_{\mathbf{r}, n, 2N}(s, \Delta_{2N,t} \cdot m)}$. Let $\hat{m} \leftarrow \mathbf{Phase}(\mathbf{ct}_N)$, $m = \lfloor \hat{m} \rceil^2 N$ and $\mathbf{rotP} \in \mathbb{R}_{N,Q}$ be such that if $\hat{m} \in [0, N)$, then $\mathbf{Coefs}(X^\hat{m} \cdot \mathbf{rotP})[1] = \Delta_{Q,t} \cdot F(m)$, where $F : \mathbb{Z}_t \mapsto \mathbb{Z}_t$. If $\mathbf{ct}_{\mathbf{out}} \leftarrow \mathbf{Bootstrap}(\mathbf{brK}, \mathbf{u}, \mathbf{ct}, \mathbf{rotP}, \mathbf{ksK})$ and $m \leq \frac{1}{2} - 1$, then $\mathbf{ct}_{\mathbf{out}} \in \mathbf{LWE}_{B_{\mathbf{r}, n, q}(s, m_{\mathbf{out}})}$, where $m_{\mathbf{out}} = \Delta_{Q,t} \cdot F(m)$ and

$$B_{\mathbf{out}} \leq \left( \frac{q^2}{Q^2} \right) \cdot (B_{\mathbf{BR}} + B_{\mathbf{KS}}) + \mathbf{Ham}(s) \cdot ||\mathbf{Var}(s)||_{\infty},$$

where $B_{\mathbf{BR}} \leq 2 \cdot n \cdot u \cdot N \cdot \mathbf{r}_{\mathsf{GSW}} \cdot L_{\mathsf{GSW}}^2 \cdot \mathbf{r}_{\mathsf{BRK}}$ and $B_{\mathbf{KS}} \leq N \cdot \mathbf{L}_{\mathsf{GSW}} \cdot L_{\mathsf{BRK}}^2 \cdot \mathbf{L}_{\mathsf{BRK}}$. If $Q = q$, then $B_{\mathbf{out}} \leq B_{\mathbf{BR}} + B_{\mathbf{KS}}$.

Finally we have,

$$B_{\mathbf{N}} = \left( \frac{2 \cdot N}{q} \right)^2 \cdot B_{\mathbf{ct}} + \mathbf{Ham}(s) \cdot ||\mathbf{Var}(s)||_{\infty}$$

if $N \neq 2 \cdot q$, and $B_{\mathbf{N}} = B_{\mathbf{ct}}$ otherwise, where $B_{\mathbf{ct}} = \max(B_{\mathbf{fresh}}, B_{\mathbf{out}})$ and $B_{\mathbf{fresh}}$ is the bound in the error variance of a fresh ciphertext.
3 Our Functional Bootstrapping Technique

In this section, we show our functional bootstrapping algorithm. First, however, we show two sub-procedures that help us construct the bootstrapping procedure.

3.1 Public Mux Gate and Building a Homomorphic Accumulator

We describe a public version of the homomorphic Mux gate which aims to choose one of two given polynomial plaintexts based on an encrypted bit. Furthermore, we show how to use the public Mux gate to build an accumulator for the functional bootstrap. Roughly speaking, the accumulator builder gets an encrypted bit and based on that bit chooses one of two different rotation polynomials. The difference between the accumulator builder and the public mux is that our accumulator builder needs to switch the encrypted bit from one of two different rotation polynomials. The algorithms are depicted in Figure 1 and Figure 2.

Below we state the correctness theorems for both algorithms, but we devise the proofs to Appendix A.

Lemma 9 (Correctness of Public Mux) Let \( \{c_i\}_{i=1}^t \) where \( c_i \in GLWE_{B_{c,n},N,q}(s, m \cdot \Delta_{q,t} \cdot L^{-1}) \) and where \( m \in \{0,1\} \) and \( \ell = \lceil \log q \rceil \), Let \( p_0, p_1 \in R_{N,q} \). If \( c_{\text{out}} \leftarrow \text{PubMux}(\{c_i\}_{i=1}^t, p_0, p_1) \), then \( c_{\text{out}} \in GLWE_{B_{c,n},N,q}(s, \Delta_{q,t} \cdot p_m) \), where \( B_{\text{out}} \leq N \cdot \ell \cdot L^2 \cdot B_c \). If \( p_0, p_1 \) are monomials then \( N \) disappears from the above inequality.

Lemma 10 (Correctness of the Accumulator Builder) Let \( \{acc_i\}_{i=1}^t \) be a vector of \( LWE \) samples \( acc_i \in LWE_{B_{\text{acc}},n,q}(s, \Delta_{q,t} \cdot m \cdot L^{-1}) \), where \( m \in \{0,1\} \), \( n, q \in N \), \( B_{\text{acc}} \leq q \), \( s \in \mathbb{Z}_{q}^{n \times 1} \), and \( \ell = \lceil \log q \rceil \). Let \( ksK \leftarrow \text{KeySwitchSetup}(B_{kSK}, n, 1, N, q, s, s, L_{kSK}) \), where \( N \in \ldots \)
SetupPolynomial(BootsMap, N)

**Input:** A lookup table BootsMap : Z_{t} \rightarrow Z_Q that maps elements in Z_{t} to elements in Z_Q, and the degree that defines the ring \mathcal{R}_{N,Q}.

**Output:** Rotation polynomials \text{rotP}_0, \text{rotP}_1 \in \mathcal{R}_{N,Q}.

1: For \( m \in [0,t-1] \) and for \( e \in \left[ -\left\lfloor \frac{N}{t} \right\rfloor, \left\lceil \frac{N}{t} \right\rceil \) do
2: Set \( y \leftarrow \left\lfloor \frac{2 \cdot N}{t} \right\rfloor \cdot m + e \pmod{2N} \).
3: If \( y = 0 \),
4: set \( p_0[1] \leftarrow \text{BootsMap}[m + 1] \).
5: If \( y \in [1,N-1] \),
6: set \( p_0[N - y + 1] \leftarrow -\text{BootsMap}[m + 1] \in Z_Q \).
7: Set \( y' \leftarrow y - N \).
8: If \( y = N \),
9: set \( p_1[1] \leftarrow -\text{BootsMap}[m + 1] \).
10: If \( y \in [N+1,2 \cdot N-1] \),
11: set \( p_1[N - y' + 1] \leftarrow \text{BootsMap}[m + 1] \in Z_Q \).
12: Set \( \text{rotP}_0 = \sum_{i=1}^{N} p_0[i] \cdot X^{i-1} \).
13: Set \( \text{rotP}_1 = \sum_{i=1}^{N} p_1[i] \cdot X^{i-1} \).
14: Return \((\text{rotP}_0, \text{rotP}_1)\).

Figure 3: Algorithm to set up the rotation polynomial based on a look up table

\( N, s \in \mathcal{R}_{N,Q} \) and \( \text{L}_{\text{ksK}} \leq q \). Finally let \( p_0, p_1 \in \mathcal{R}_{N,Q} \). If \( c_{\text{out}} \leftarrow \text{BuildAcc}([\text{acc}_i \cdot t]_t, p_0, p_1, \text{ksK}) \), then \( c_{\text{out}} \in \text{RLWE}_{\mathcal{R}_{N,Q}}(s, \Delta_{Q,t} \cdot p_m) \), where

\[
B_{\text{out}} \leq N \cdot t \cdot \text{L}^2 \cdot (B_{\text{acc}} + n \cdot \text{L}_{\text{ksK}}^2 \cdot B_{\text{ksK}}) .
\]

If \( p_0, p_1 \) are monomials, then the degree \( N \) disappears from the above inequality.

### 3.2 Construction of the Rotation Polynomial

In Figure 3 we describe how to build the polynomials for our bootstrapping to compute arbitrary functions, and by Theorem 2, we show the correctness of our methodology. We defer the proof to Appendix A. We want to evaluate BootsMap : Z_{t} \rightarrow Z_Q. Informally, we partition the rotation polynomials into chunks where each chunk represents a value in the domain of BootsMap. The chunk size is determined by the error bound of the bootstrapped LWE sample. Note that BootsMap already assumes that the domain is scaled to fit the ciphertext modulus. That is, if we want to compute \( f : Z_{t} \rightarrow Z_{t'} \) for some \( t' \in \mathbb{N} \), then we set the lookup table BootsMap[x] = \Delta_{Q,t'} \cdot f(x) \) for all \( x \in Z_{t} \).

**Theorem 2** Let \( f : Z_{t} \rightarrow Z_Q \) be a function with a lookup table BootsMap such that for all \( x \in Z_{t} \), we have that BootsMap[x + 1] = f(x). Let

\[
(\text{rotP}_0, \text{rotP}_1) \leftarrow \text{SetupPolynomial}(\text{BootsMap}, N)
\]

for \( N \in \mathbb{N} \). Let \( y = \left\lfloor \frac{2 \cdot N}{t} \right\rfloor \cdot m + e \), for \( m \in Z_{t} \) and \( e \in \left[ -\left\lfloor \frac{N}{t} \right\rfloor, \left\lceil \frac{N}{t} \right\rceil \) \). Then
Let $\text{Theorem 3 (Correctness of the Bootstrapping)}$

Below we state the correctness theorem and devise the proof to Appendix A.

Our bootstrapping algorithm is depicted in Figure 4. There are three phases. First, we
compute an extended ciphertext whose plaintext indicates whether the given ciphertexts
phase is in $[0, N)$ or in $[N, 2 \cdot N)$. Based on this ciphertext, we build the accumulator. The
final step is to blind rotate the rotation polynomial and switch the ciphertext from RLWE
to LWE. Remember that the vector $u \in \mathbb{Z}^u$ is such that for all $y \in S$ with $S \subseteq \mathbb{Z}_{2 \cdot N}$ there exists $x \in \mathbb{B}^u$ such that $y = \sum_{i=1}^{u} x[i] \cdot u[i] \mod 2 \cdot N$. Furthermore, we denote as $L_{\text{RLWE}}$, $L_{\text{sk}}$ and $L_{\text{pk}}$ the decomposition basis for RGSW samples, and two different key switch keys. Below we state the correctness theorem and devise the proof to Appendix A.

**Theorem 3 (Correctness of the Bootstrapping)** Let $s \in \mathbb{Z}_q^n$ for some $q, n \in \mathbb{N}$, $s \in \mathbb{R}_{N,q}$, and $s_F \leftarrow \text{KeyExt}(s)$. Furthermore, let us define the following.

- $\text{brK} \leftarrow \text{BRKeyGen}(n, s, u, B_{\text{brK}}, N, Q, L_{\text{RLWE}}, s)$.
- $\text{pK} \leftarrow \text{KeySwitchSetup}(B_{\text{pk}}, N, 1, N, Q, s_F, s, L_{\text{pk}})$.

**Figure 4: Full domain bootstrapping algorithm.**

- $\text{Coefs}(\text{rotP}_0 \cdot X^u)[1] = f(m)$ for $y \in [0, N - 1]$, and
- $\text{Coefs}(\text{rotP}_1 \cdot X^u)[1] = f(m)$ for $y \in [N, 2 \cdot N - 1]$.}

### 3.3 The Functional Bootstrapping Algorithm

Our bootstrapping algorithm is depicted in Figure 4. There are three phases. First, we
compute an extended ciphertext whose plaintext indicates whether the given ciphertexts
phase is in $[0, N)$ or in $[N, 2 \cdot N)$. Based on this ciphertext, we build the accumulator. The
final step is to blind rotate the rotation polynomial and switch the ciphertext from RLWE
to LWE. Remember that the vector $u \in \mathbb{Z}^u$ is such that for all $y \in S$ with $S \subseteq \mathbb{Z}_{2 \cdot N}$ there exists $x \in \mathbb{B}^u$ such that $y = \sum_{i=1}^{u} x[i] \cdot u[i] \mod 2 \cdot N$. Furthermore, we denote as $L_{\text{RLWE}}$, $L_{\text{sk}}$ and $L_{\text{pk}}$ the decomposition basis for RGSW samples, and two different key switch keys. Below we state the correctness theorem and devise the proof to Appendix A.

**Theorem 3 (Correctness of the Bootstrapping)** Let $s \in \mathbb{Z}_q^n$ for some $q, n \in \mathbb{N}$, $s \in \mathbb{R}_{N,q}$, and $s_F \leftarrow \text{KeyExt}(s)$. Furthermore, let us define the following.

- $\text{brK} \leftarrow \text{BRKeyGen}(n, s, u, B_{\text{brK}}, N, Q, L_{\text{RLWE}}, s)$.
- $\text{pK} \leftarrow \text{KeySwitchSetup}(B_{\text{pk}}, N, 1, N, Q, s_F, s, L_{\text{pk}})$. 

![Figure 4: Full domain bootstrapping algorithm.](image_url)
We can correctly compute affine functions without the need to bootstrap immediately.

Finally, let $\hat{m} \leftarrow \text{Phase}(ct_N)$, $[\hat{m}]^2_N = m$ and rotP$_0$, rotP$_1 \in R_{N,Q}$ be such that

- if $\hat{m} \in [0,N)$, then Coefs($X^{\hat{m}} \cdot \text{rotP}_0$)\[1\] = $\Delta_{Q,t'} \cdot f(m)$, and
- if $\hat{m} \in [N,2 \cdot N)$, then Coefs($X^{\hat{m}} \cdot \text{rotP}_1$)\[1\] = $\Delta_{Q,t'} \cdot f(m)$, where $f : \mathbb{Z}_t \mapsto \mathbb{Z}_{t'}$.

If $ct_{\text{out}} \leftarrow \text{Bootstrap}(brK,u,ct,\text{rotP}_0,\text{rotP}_1,L_{\text{boot}},pK,ksK)$ and given that $B_N \leq N$, then $ct_{\text{out}} \in \text{LWE}_\mathbb{B}_{\text{out}} (s, \Delta_{q,t'} \cdot f(m))$, where

$$B_N \leq \left(\frac{2 \cdot N}{q}\right)^2 \cdot B_{\text{ct}} + \text{Ham}(s) \cdot ||\text{Var}(s)||_{\infty},$$

for $N \neq 2 \cdot q$, and $B_N = B_{\text{ct}}$ otherwise, and $B_{\text{ct}} \leq \max(B_{\text{fresh}},B_{\text{out}})$. We use the bound $B_{\text{fresh}}$ if $ct$ is a freshly encrypted ciphertext. Finally the bound $B_{\text{out}}$ of the bootstrapped ciphertext $ct_{\text{out}}$ that is as follows

$$B_{\text{out}} \leq \frac{q^2}{Q^2} \cdot (B_F + B_{\text{BR}} + B_{KS}) + \text{Ham}(s_F) \cdot ||\text{Var}(s_F)||_{\infty},$$

for $Q \neq q$, and $B_{\text{out}} \leq B_F + B_{\text{BR}} + B_{KS}$ for $Q = q$, where

- $B_F \leq N \cdot \ell_{\text{boot}} \cdot L^2_{\text{boot}} \cdot (B_{\text{BR}} + B_{F})$
- $B_{\text{BR}} \leq 2 \cdot n \cdot u \cdot N \cdot \ell_{\text{RGSW}} \cdot L^2_{\text{RGSW}} \cdot B_{\text{rkK}}$
- $B_{KS} \leq N \cdot \ell_{\text{skK}} \cdot L^2_{\text{skK}} \cdot B_{\text{skK}}$, and
- $B_P \leq N \cdot \ell_{\text{pk}} \cdot L^2_{\text{pk}} \cdot B_{\text{pk}}$

### 3.4 Application of Functional Bootstrapping

Below we discuss a few simple tricks on how to use our bootstrapping algorithm efficiently. We stress that the bootstrapping can compute any function over $\mathbb{Z}_t$ for an arbitrary modulus $t \in \mathbb{N}$. Furthermore, we can keep computing very efficient affine functions on bootstrapped Regev ciphertexts before the next bootstrapping operation.

**Computing in $\mathbb{Z}_t$.** Note that with the full domain functional bootstrapping algorithm we can correctly compute affine functions without the need to bootstrap immediately. Specifically, we use the linear homomorphism of LWE samples (see Lemma 1) to compute on the ciphertext $c_Q$ at Step 13 of Bootstrap at Figure 4. In Section 4 we evaluate correctness for this particular setting as in this case computing the linear homomorphism will not increase the error that stems from the key switching key.

To compute the product $x \cdot y$, we compute $\left(\frac{(x+y)}{2}\right)^2 - \left(\frac{(x-y)}{2}\right)^2$. Therefore evaluating the product requires only two functional bootstrapping invocations, we compute the square, and the addition/subtraction induces only a small error when done outside the bootstrapping procedure. Similarly, we can compute $\max(x,y)$ by $\max(x-y,0) + y$, which costs only one functional bootstrapping.

To summarize, since we can compute all functions over $\mathbb{Z}_t$ with a single bootstrapping operation and efficiently compute affine functions on plaintexts in $\mathbb{Z}_t$, we can compute arithmetic circuits complemented with gates beyond only addition and multiplication. For example, we can compute the multiplicative inverse modulo $t$ needed for Gaussian elimination and solve systems of linear equations homomorphically. Note that FHE schemes
like BGV/BFV [BV11, Bra12, FV12, BGV12] can only compute addition and multiplication. Hence to compute a multiplicative inverse with BGV/BFV, we need to represent the computation as a boolean circuit. With FDFB, we can compute the inverse with just one bootstrapping operation.

**Multiple Univariate Functions With the same Input.** For efficiency it is important to note, that we might amortize the cost of computing multiple functions on an encrypted ciphertext. After the first bootstrapping operation, we may reuse the \( \text{acc}_{c,i} \) values to compute the next function on the same input. This observation is particularly important for the decomposition of the encrypted integers. Namely, we can binary decompose a field element and then compose binary vector back to integers.

**Extending the Arithmetic.** Finally, we can leverage the Chinese Reminder theorem and the fact that the ring \( \mathbb{Z}_t \) for \( t = \prod_{i=1}^{m} t_i \) where the \( t_i \) are pairwise co-prime is isomorphic to the product ring \( \mathbb{Z}_{t_1} \times \cdots \times \mathbb{Z}_{t_m} \) as a powerful tool to extend the size of the arithmetic. This way we can handle modular arithmetic for a large composite modulus, while having cheap leveled linear operations, and one modular multiplication per factor. Note that we can compute arbitrary polynomials in \( \mathbb{Z}_t \) in the CRT representation. To set the rotation polynomials for each component of the ciphertext vector we do as follows. For each number \( x \in \mathbb{Z}_t \), we compute its CRT representation \( x_1, \ldots, x_m \). Then we compute the polynomial \( y = P(x) \) and \( y \)'s CRT representation \( [y_i]_{m_i} \). We set the rotation polynomials for the \( i \)th bootstrapping algorithm to map \( x_i \) to \( y_i \), as described in Section 3.2. Furthermore, additions and scalar multiplications can still be performed without bootstrapping.

3.4.1 Computing Negacyclic Functions.

To compute negacyclic functions, we use the TFHE bootstrapping algorithm given by Definition 13. Note that FDFB contains all the necessary public key material to use the standard TFHE bootstrapping algorithm, which is faster and sufficient for certain applications. Therefore we may view FDFB as an extension of TFHE rather than a substitute. While the algorithm is from [CGGI16], we show by Theorem 1 that, assuming the plaintext is in \([0, t/2)\), the outcome of the bootstrapping is determined by the message and the rotation polynomial. In contrast to full domain bootstrapping, in this method, we must be careful when performing leveled operations. In particular, when the message exceeds the interval \([0, t/2)\), then the outcome of the bootstrapping may be incorrect.

4 Parameters, Performance, and Tests

In the next paragraph, we describe our parameter sets and discuss the strategy for how we chose the parameters. Furthermore, we give the security and correctness estimates for all proposed parameter sets. Then, we discuss the details of our implementation and give the results of our microbenchmarks. Finally, we describe our example applications and give the results of the performance tests for those applications.

**Parameter Sets.** Table 2 shows our different parameter sets, Table 4 gives correctness for each parameter set and the size of the message space, Table 5 contains an overview of the ciphertext size, and time to run a single bootstrapping operation, and Table 3 gives the security estimations that we obtained when running the LWE-estimators [APS15, CSY21].

We denote the parameter sets \( \text{FDFB:s:t} \) and \( \text{TFHE:s:t} \) where \( s \) is the security level, and \( t \) is the number of bits of the plaintext space for which the probability of having an incorrect outcome is very low. For example, \( \text{FDFB:100:7} \) offers 100 bits of security and is targeted at 7-bit plaintexts. We stress that the targeted plaintext space does not exclude larger plaintext moduli. In particular, according to Table 4, all parameter sets can handle larger plaintexts, but the probability that the least significant bit of the input
message changes obviously grows with the plaintext space size, shifting the scheme from FHE towards homomorphic encryption for approximate arithmetic.

First, we note that if the ratio $Q/q$ is high, then the error after modulus switching from $Q$ to $q$ is dominated by the Hamming weight of the LWE secret key $s$. To minimize the error, we choose $q = 2 \cdot N$ to avoid another modulus switching. To be able to compute number theoretic transforms we choose $Q$ prime and $Q = 1 \mod 2 \cdot N$. An important observation is that the error does not depend on the RLWE secret key $s$. Hence, we choose $s$ from the uniform distribution over $\mathcal{R}_{N,Q}$. For all RLWE samples, we set the standard deviation of the error as 3.2. We choose the LWE secret key $s$ from $\{0, 1\}^n$ such that the Hamming weight of the vector is 64. Recall that LWE with binary keys is asymptotically as secure as the standard LWE [Mic18], and is also used in many implementations of TFHE. The Hamming weight is the same as used in the HElib library [HS14, HS15, Alb17].

To compensate for the loss of security due to a small Hamming weight, we choose the standard deviation of LWE samples error much higher that the in case of RLWE. For binary components of $s$ we have $||u|| = 1$ which is as in TFHE [CGGI16, CCGI20]. We choose the decomposition bases $L_{\text{boot}}, L_{\text{RGSW}},$ and $L_{\text{pk}}$ to minimize the amount of computation. Due to the higher noise variance of the LWE samples in the key switching key, we choose the decomposition basis $L_{\text{pk}}$ relatively low, but we note that in terms of efficiency, key switching does not contribute much.

To evaluate security, we use the LWE estimator [APS15] commit fb7deba and the Sparse LWE estimator [CSY21] commit 6ab7a6e. The LWE Estimator estimates the security level of a given parameter set by calculating the complexity of the meet in the middle exhaustive search algorithm, Coded-BKW [JGS15], dual-lattice attack and small-

| Set         | FDFB20.7 | FDFB100.7 | FDFB80.8 | FDFB100.8 | TFHE100.7 | TFHE80.2 | TFHE100.2 |
|-------------|----------|-----------|----------|-----------|-----------|----------|-----------|
| $n$         | 700      | 1100      | 700      | 1100      | 1500      | 424      | 525       |
| $N$         | 213      | 213       | 213      | 213       | 213       | 214      | 213       |
| $q$         | 24       | 24        | 24       | 24        | 24        | 24       | 24        |
| $Q$         | 4        | 4         | 4        | 4         | 4         | 4        | 4         |
| Lboot       | 211      | 211       | 213      | 214       | 214       | 214      | 211       |
| Lnow        | 24       | 23        | 23       | 23        | 23        | 23       | 23        |
| Lpk         | 213      | 213       | 213      | 213       | 213       | 213      | 213       |
| stddev      | 258,3.2  | 258,3.2   | 258,3.2  | 258,3.2   | 258,3.2   | 258,3.2  | 258,3.2   |
| $s$         | [0,1],64 | [0,1],64  | [0,1],64 | [0,1],64  | [0,1],64  | [0,1],64 | [0,1],64  |

Table 3: **Parameter Sets.** The modulus $Q_1 = 2^{62} - 65535$, $Q_2 = 2^{63} - 278527$, $Q_3 = 2^{48} - 163839$, and $Q_4 = 2^{12} - 139263$. For all parameters sets we set the error variance of fresh (R)LWE samples to 10.24 which corresponds to standard deviation 3.2. The key $s$ is always binary; thus $n = 1$.

| Set         | uSVP   | dec | dual | LWE   | uSVP   | dec | dual | Hyb. Primal |
|-------------|--------|-----|------|-------|--------|-----|------|-------------|
| FDFB20.7    | 282.4  | 306.1 | 294.7 | 87.8   | 309.5  | 82.7 | 81.6         |
| FDFB100.7   | 282.4  | 306.1 | 294.7 | 138.2  | 644.3  | 129.0 | 100.0        |
| FDFB80.8    | 548.5  | 596.2 | 570.1 | 87.8   | 309.5  | 82.7 | 81.6         |
| FDFB100.8   | 548.5  | 596.2 | 570.1 | 138.2  | 644.3  | 129.0 | 100.0        |
| TFHE100.7   | 236.9  | 255.5 | 247.4 | 139.6  | 322.7  | 128.9 | 100.8        |
| TFHE80.2    | 116.9  | 128.3 | 125.3 | 81.3   | 207.3  | 86.2 | 88.9         |
| TFHE100.2   | 116.9  | 128.3 | 125.3 | 100.0  | 264.3  | 107.1 | 111.0        |

Table 3: **Security estimations for the parameter sets.** This table contains the the security estimates obtained by running the LWE estimator [APS15] for RLWE and LWE samples. The numbers represent base two logarithms of estimated required ring operations to lunch a sucessfull attack.
Table 4: Correctness. The table contains upper bounds on the probability to have an error in the outcome of the bootstrapping algorithms. For our full domain bootstrapping, we give two values. The first is the upper bound on the error probability for ciphertexts after bootstrapping. The second value is the upper bound for a ciphertext after bootstrapping and computing an affine function of size 784. That is, a function of the form $w_0 + \sum_{i=1}^{784} x_i \cdot w_i \in \mathbb{Z}$, where $x_i$ are the input plaintexts, and the $w_i$’s are the weights. We emphasize that the probabilities are upper-bounds, and the empirical correctness errors are much lower.

| Set / $\log_2(t)$ | 6 | 7 | 8 | 9 | 10 | 11 | Key |
|-------------------|---|---|---|---|----|----|-----|
| FDFB:80:7         | 0.0, 0.0 | $2^{-31}, 2^{-24}$ | $2^{-9}, 0.07$ | 0.12, 0.61 | 0.44, 0.89 | 0.69, 0.97 | 95% |
| FDFB:100:7        | 0.0, 0.0 | $2^{-31}, 2^{-18}$ | $2^{-9}, 0.13$ | 0.12, 0.67 | 0.44, 0.91 | 0.69, 0.97 | 95% |
| FDFB:80:8         | 0.0, 0.0 | 0.0, 0.0 | $2^{-26}, 2^{-17}$ | $2^{-8}, 0.12$ | 0.16, 0.66 | 0.48, 0.90 | 67% |
| FDFB:100:8        | 0.0, 0.0 | 0.0, 0.0 | $2^{-26}, 2^{-13}$ | $2^{-8}, 0.23$ | 0.16, 0.75 | 0.48, 0.99 | 67% |
| TFHE:100:7        | 0.0 | $2^{-24}$ | $2^{-7}$ | 0.18 | 0.50 | 0.73 | 99% |
| TFHE:80:2         | 0.57 | 0.77 | 0.89 | 0.94 | 0.97 | 0.98 | 14% |
| TFHE:100:2        | 0.58 | 0.78 | 0.89 | 0.94 | 0.97 | 0.98 | 17% |

Implementation. We ran our experiments on a virtual machine with 60 cores, featuring 240GB of RAM and using Intel Xeon Cascade lake processors, and the measurements are averaged over five runs. We implemented our bootstrapping technique by modifying the latest version of the PALISADE [PAL21] library, using the Intel HEXL [BKS+21] library, as it is already featured in the implementation of [DM14] and [CGGI16]. Note that the timings in PALISADE for TFHE are different than reported in [CGGI16]. This is mostly due to an optimization in TFHE that chooses the modulus $Q$ as $Q = 2^{32}$ or $Q = 2^{64}$ and uses the natural modulo reduction of unsigned int and long C++ types. Then instead of computing the number theoretic transform, TFHE computes the fast Fourier transform. We note that the same optimization is possible for our algorithms.

Evaluation. Table 6 shows the efficiency of modular addition, scalar multiplication and multiplication targeting different plaintext spaces, respectively, for our parameter sets. We compare our approach to operations on binary ciphertexts. To this end, we use the vertical look-up tables from [CGGI17], as they turned out to be faster than a circuit-based implementation. Note that, while vertical LUTs are significantly faster than the horizontal tables, which were also introduced in [CGGI17], they can compute at most one function at the same time. Therefore multiple tables need to be maintained for each output bit, requiring additional memory. We can observe that for modular multiplication, our approach is less efficient than the LUT based one. However, since we can use the linear homomorphism of LWE samples, our techniques are especially well suited to addition and scalar multiplication.
Table 5: **Complexity for the parameter sets.** We computed the size of the keys and ciphertexts by taking the byte size of every field element and counting the number of field elements.

| Set     | Time [ms] | PK [MB] | CT [KB] |
|---------|-----------|---------|---------|
| FDFB:80:7 | 9278      | 2351.95 | 1.40    |
| FDFB:100:7| 13781     | 4624.31 | 2.20    |
| FDFB:80:8 | 33117     | 7388    | 1.40    |
| FDFB:100:8| 45266     | 14318   | 3.00    |
| TFHE:80:2 | 306       | 31.72   | 0.72    |
| TFHE:100:2| 321       | 25.62   | 0.58    |

Table 6: **Basic Performance Tests.** The table contains timings for modular Addition (Add), Scalar Multiplication (S. Mul) and Multiplication (Mul). All timings in seconds. We consider the timings for operations from $\mathbb{Z}_{2^k} \times \mathbb{Z}_{2^k}$ to $\mathbb{Z}_{2^k}$ for $k \in \{7, 8\}$.

| Param.    | Add          | S.Mul        | Mul          |
|-----------|--------------|--------------|--------------|
| FDFB:80:7 | $1 \cdot 10^{-6}$ | $4 \cdot 10^{-6}$ | 18           |
| FDFB:100:7| $1 \cdot 10^{-6}$ | $7 \cdot 10^{-6}$ | 26           |
| TFHE:80:2 | 15.6         | 15.6         | 15.6         |
| TFHE:100:2| 19.2         | 19.2         | 19.2         |
| FDFB:80:8 | $1 \cdot 10^{-6}$ | $3 \cdot 10^{-6}$ | 66           |
| FDFB:100:8| $1 \cdot 10^{-6}$ | $4 \cdot 10^{-6}$ | 89           |
| TFHE:80:2 | 18           | 18           | 18           |
| TFHE:100:2| 21           | 21           | 21           |

To evaluate a neural network on encrypted inputs, we discretized two neural networks that recognize handwritten digits from the MNIST dataset. Note that the discretization of a floating-point neural network to integer models with modular arithmetic is a delicate process. Many operations such as normalization cannot be applied in the same manner, and therefore the discretization process often results in a drop of accuracy. Do discretize our neural networks we use a parameter $\delta$ called the discretization factor. The smaller the discretization factor, the smaller the accuracy. Increasing the discretization factor requires handling larger plaintexts. Hence we have a tradeoff between the accuracy of the discretized NN and the plaintext space of the FHE scheme. The configurations and accuracies are given at Table 9. Note that for our experiments, the peak is around an eight and 9-bit modulus and falls for smaller or larger modulus. For a smaller modulus, it is quite obvious that the precision is simply too small. We can explain the decrease in accuracy for larger plaintext moduli by the error from homomorphic evaluation. In particular, according to Table 4, bootstrapping, for example, an 11-bit plaintext, will be incorrect with a rather high probability. Hence the drop in accuracy.

In what follows, we give the notation used to specify the neural networks. We always denote the weights with $w$’s and biases with $b$’s. By **Dense**: $d$ we denote a dense layer that on input a vector $[x_i]_{i=1}^m$ outputs a vector $[f(b_j + \sum_{i=1}^m x_i \cdot w_{j,i})]_{j=1}^d$.

Convolution layers are denoted as **Conv2D**: $(h; (d, d); f)$, where $h$ is the number of filters, $(d, d)$ specify the shape of the filters and $f$ is an activation function. A Conv2D layer takes as input a tensor $[x_{g,i,j}]_{g=i,j=1}^{c,m,l}$ and outputs

$$\left[ f(b_{k}^{(g)}_{i,j}) + \sum_{g=1, i=1, j=1}^{c,d,d} x_{g,i+i',j+j'} \cdot w_{g,i,j}^{(k)} \right]_{i'=0, j'=0}^{m-d, l-d} h_{k=1}^{h}.$$
### Table 7: Performance for evaluating neural networks

The table contains the times to evaluate the MNIST-1-f and MNIST-2-f neural networks. The time to evaluate the neural networks is given in hours. Due to extremely high execution times, we only present estimates for TFHE, which we base on Table 6.

| Param / \( \log_2(t) \) | Evaluation Time MNIST-1-f | Evaluation Time MNIST-2-f |
|---------------------------|---------------------------|---------------------------|
| FDFB:80:7                 | 6.08                      | 0.38                      |
| FDFB:100:7                | 0.35                      | 1.65                      |
| FDFB:80:8                 | 0.17                      | 1.84                      |
| FDFB:100:8                | 0.76                      | 1.84                      |
| TFHE:80:2                 | 99.24                     | 122.18                    |
| TFHE:100:2                | 135.88                    | 165.9                     |
| TFHE:80:2                 | 177.28                    | 215.44                    |
| TFHE:100:2                | 225.84                    | 274.44                    |
| TFHE:80:2                 | 289.82                    | 351.42                    |
| TFHE:100:2                | 392.14                    | 460.94                    |

Average pooling \( \text{AvgPool2D}\{d,d\} \) on input a tensor \( x_{g,i,j}^{c,m,l} \), where \( g=1 \), \( i=1 \), \( j=1 \) outputs

\[
\left[ \frac{\sum_{i=1,j=1}^{d,d} x_{0,i+j'}+i'+j'}{d^2} \right]_{j'=0,j'=0}^{m-d,l-d} = \frac{1}{d^2} \sum_{i,j=1}^{d,d} x_{0,i+j'}+i'+j', g=1
\]

Finally, \( \text{Softmax}\{d\} \) takes as an input a vector \( x_{i}^{m} \), computes \( z = [b_j + \sum_{i=1}^{m} x_i \cdot w_j]_{j=1}^{d} \) and outputs

\[
\text{Softmax}(x, i) = \frac{e^{x_i}}{\sum_{j=1}^{d} e^{x_j}}, i=1,
\]

We compared FDFB with a functionally equivalent implementation based on vertical LUTs [CGGH17]. In particular, for activation functions, we run \( k \), \( k \)-to-1 bit LUTs for \( k \in \{6,7,8,9,10,11\} \) and to compute addition and scalar multiplication for the affine function, we must use \( k \), \( 2k \)-to-1 bit LUTs. We note that we also tested the method of Carpov et al. [CIM19] to compute the activation function on set TFHE:100:7. However, the timing to evaluate the affine function in the LUT-based method is so overwhelming that we did not notice any significant difference. Furthermore, we note that for the last layer, we chose a bigger plaintext modulus and compute the activation function in plaintext, that is we decrypt the result of the affine function before computing the softmax activation. We include the evaluation accuracies for plaintext spaces that exceed the target space. Such settings are extremely useful for computations in which a small amount of error is acceptable, such as activation functions in neural network inference. We can see that our approach significantly outperforms LUT-based techniques. The most important reason for this is that we can compute affine functions within neurons almost instantly due to the linear homomorphism of LWE samples. Binary ciphertext requires the evaluation of expensive LUT at each step. In the case of the LUT-based approach, we note that the computation will be exact. Therefore, the accuracy will be identical to the discretized neural network with applied modulo reduction. Table 7 shows the time required to evaluate the whole neural network. We test our network on 20 samples for each plaintext space and achieve the best speedups for sets FDFB:80:7 and FDFB:100:7 that outperform the LUT-based technique by a factor of over 1200 and 282. Even in the worst case with the MNIST-2-f network, for which the affine functions are significantly smaller, we obtain speedups 44.4 and 29.6 for FDFB:80:8 against TFHE:80:2, and respectively FDFB:100:8 against TFHE:100:2 for 8-bit plaintexts. By computing the activation functions of the network over 60 cores, we manage to lower the evaluation significantly. To estimate the speedup one would gain by parallelizing the LUT-based technique, we averaged the speedup gained by the FDFB and applied it to the LUT-based timings. We did not parallelize our bootstrapping technique itself. However, we note that there is a
Table 8: Accuracy for homomorphic evaluation of the neural networks. For accuracy we show values for $f = \text{ReLU}$, but stress that we can use arbitrary univariate functions without impacting execution time. We discretized the models using a discretization factor $\delta = 6$ for MNIST-1-$f$ and $\delta = 4$ for MNIST-2-$f$.

| Param. / $\log_2(t)$ | 6 | 7 | 8 | 9 | 10 | 11 |
|-----------------------|---|---|---|---|----|----|
| FDFB:80:7             | 0.90 | 0.90 | 0.95 | 0.95 | 0.90 | 0.70 |
| FDFB:100:7            | 0.90 | 0.90 | 0.95 | 0.90 | 0.90 | 0.80 |
| FDFB:80:8             | 0.85 | 0.90 | 0.95 | 0.90 | 0.95 | 0.90 |
| FDFB:100:8            | 0.80 | 0.95 | 0.95 | 0.95 | 0.95 | 0.90 |
| TFHE:80:2             | 0.82 | 0.94 | 0.95 | 0.95 | 0.95 | 0.95 |
| TFHE:100:2            | 0.82 | 0.94 | 0.95 | 0.95 | 0.95 | 0.95 |

| FDFB:80:7 | 0.15 | 0.25 | 0.85 | 0.90 | 0.90 | 0.90 |
| FDFB:100:7| 0.15 | 0.30 | 0.85 | 0.95 | 0.90 | 0.85 |
| FDFB:80:8 | 0.15 | 0.30 | 0.85 | 0.90 | 0.95 | 0.90 |
| FDFB:100:8| 0.15 | 0.30 | 0.90 | 0.90 | 0.95 | 0.95 |
| TFHE:80:2 | 0.14 | 0.22 | 0.57 | 0.93 | 0.93 | 0.93 |
| TFHE:100:2| 0.14 | 0.22 | 0.57 | 0.93 | 0.93 | 0.93 |

Table 9: Neural Networks Specification. The table contains specifications for both models that we train and evaluate and the baseline accuracy of the non-discretised model using floating point numbers. The “input” row gives the dimension of the inputs. The last row reports on the accuracy of the neural network. Other rows specify the given layer of the neural network.

| Input | MNIST-1-$f$ | MNIST-2-$f$ |
|-------|-------------|-------------|
| 1     | Dense:784:$f$ | Conv2D:4:(6,6):$f$ |
| 2     | Dense:510:$f$ | AvgPool2D:(2,2) |
| 3     | Softmax:10   | Conv2D:16:(6,6):$f$ |
| 4     |              | AvgPool2D:(2,2) |
| 5     |              | AvgPool2D:16:(3,3):$f$ |
| 6     |              | Dense:64:$f$ |
| 7     |              | Softmax:10   |

Baseline accuracy for $\text{ReLU}(x) = \max(0, x)$ 0.96 0.98
lot of room to do so.

Finally, let us recall that [BMMP18] tests neural network inference using TFHE and achieves very good timings and accuracies to classify the MNIST dataset. They report evaluating a network with 30 neurons in 0.49 seconds and achieving 93.71% accuracy, and with 100 neurons in 1.65 seconds and achieving 96.35% accuracy. For classification, they trained two neural networks, one with 30 neurons and one with 100, where the activation function was implemented by the sign function. Both networks had one hidden layer. Importantly note that the sign function is already negacyclic, which allows us to use the TFHE bootstrapping to compute the activation functions. Note that, especially the reported accuracies are very impressive since, as observed in [BMMP18], the chosen parameters had quite low correctness, and the authors observed many incorrect bootstrapping outcomes, yet the network is robust enough to achieve accuracy at a level that our (larger) network achieves in plaintext. In [CJP21] the authors claim to evaluate neural networks with 20, 50, and 100 layers that classify MNIST in time around one minute, depending on the network or parameter set used. In each case, the authors report accuracies that are usually over 90%. In this case, the neural networks had ReLU activation functions. We refer to [CJP21] for the reported timings and accuracies. While we did not replicate the result or establish all the relevant parameters of their TFHE instantiation, we note that FDFB, in principle, has the capability to achieve similar results. As described in Section 3.4, FDFB has all the required key materials necessary to run the TFHE bootstrapping algorithm used in both works [BMMP18, CJP21].

5 Conclusion

We believe that our evaluations showed that it is practically feasible to evaluate arithmetic circuits over encrypted data using fully homomorphic encryption. We showed that parallelization might play a pivotal role in further reducing the timing. Therefore, an open question is how much time can be improved when exploiting graphics processing units or larger clusters. Finally, we believe that our method may either be a complementary algorithm to CKKS methods or a competitive alternative that gives the evaluation’s exact results instead of approximate.

Acknowledgements. This work has been partially funded/supported by the German Ministry for Education and Research through funding for the project CISPA-Stanford Center for Cybersecurity (Funding number: 16KIS0927).

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A Correctness of the Bootstrapping

We give the proofs of the lemmas and theorems from our paper. For readability sake, we recall the lemmas and theorems.

**Lemma 9 (Correctness of Public Mux)** Let $[c_i]_{i=1}^\ell$, where $c_i \in \text{GLWE}_{B_{c}, n, N, q}(s, m \cdot \Delta_{q, t} \cdot L^{-1})$ and where $n, N, q \in \mathbb{N}$, $m \in \{0, 1\}$, $\ell = \lceil \log L \rceil$. Let $p_0, p_1 \in \mathbb{R}_{N, q}$. If $c_{\text{out}} \leftarrow \text{PubMux}([c_i]_{i=1}^\ell, p_0, p_1)$, then $c_{\text{out}} \leftarrow \text{GLWE}_{B_{\text{out}}, n, N, q}(s, \Delta_{q, t} \cdot p_m)$, where $B_{\text{out}} \leq N \cdot \ell \cdot L^2 \cdot B_c$. If $p_0, p_1$ are monomials then $N$ disappears from the above inequality.

**Proof 1** From the correctness of linear homomorphism of GLWE samples, we have

$$c_{\text{out}} = \begin{bmatrix} \Delta_{q, t} p_0 \\ 0 \end{bmatrix} + \sum_{i=1}^\ell p[i] \cdot \text{GLWE}_{B_{c}, n, N, q}(s, m \Delta_{q, t} \cdot L^{-1})$$

$$= \begin{bmatrix} \Delta_{q, t} p_0 \\ 0 \end{bmatrix} + \text{GLWE}_{B_{\text{out}}, n, N, q}(s, \Delta_{q, t} m (p_1 - p_0))$$

$$= \text{GLWE}_{B_{\text{out}}, n, N, q}(s, \Delta_{q, t} \cdot p_m),$$

where $B_{\text{out}} \leq N \cdot \ell \cdot L^2 \cdot B_c$. If $p_0, p_1$ are monomials then $N$ disappears from the above inequality.
Lemma 10 (Correctness of the Accumulator Builder) Let \([\text{acc}]_{i=1}^t\) be a vector of LWE samples \(\text{acc}_i \in \text{LWE}_{B_{\text{acc}}, n, q}(s_0, \Delta_q, t \cdot m \cdot L^{-1})\), where \(m \in \{0, 1\}\), \(n, q \in \mathbb{N}\), \(B_{\text{acc}} \leq q\), \(s \in \mathbb{Z}_q^{n 	imes 1}\), and \(\ell = \lceil \log_L q \rceil\). Let \(kSK \leftarrow \text{KeySwitchSetup}(B_{kSK}, n, 1, N, q, s, s, L_{kSK})\), where \(N \in \mathbb{N}\), \(s \in \mathbb{R}_{N, q}\) and \(L_{kSK} \leq q\). Finally let \(p_0, p_1 \in \mathbb{R}_{N, q}\). If \(c_{\text{out}} \leftarrow \text{BuildAcc}(\text{acc}, p_0, p_1, kSK)\), then \(c_{\text{out}} \in \text{RLWE}_{B_{\text{out}}, N, q}(s_0, \Delta_q, t \cdot p_m)\), where

\[
B_{\text{out}} \leq N \cdot \ell \cdot L^2 \cdot (B_{\text{acc}} + n \cdot L_{kSK} \cdot L_{kSK}^2 \cdot B_{kSK})
\]

If \(p_0, p_1\) are monomials, then the degree \(N\) disappears from the above inequality.

Proof 2 From correctness of KeySwitch we have that \(\text{acc}_{R, i} \in \text{RLWE}_{B_{R}, n, q}(s_0, \Delta_q, t \cdot m \cdot L^{-1})\), where \(B_R \leq B_{\text{acc}} + n \cdot L_{kSK} \cdot L_{kSK}^2 \cdot B_{kSK}\). From correctness of PubMux we have that \(\text{c}_{\text{out}} \in \text{RLWE}_{B_{\text{out}}, N, q}(s_0, \Delta_q, t \cdot p_m)\), where \(B_{\text{out}} \leq N \cdot \ell \cdot L^2 \cdot B_R\). Also from correctness of PubMux we have that if \(p_0, p_1\) are monomials, then the degree \(N\) disappears from the above inequalities.

Theorem 2 Let \(f : \mathbb{Z}_s \rightarrow \mathbb{Z}_t\) be a function with a lookup table \(\text{BootsMap}\) such that for all \(x \in \mathbb{Z}_s\), we have that \(\text{BootsMap}[x + 1] = \Delta_q, f(x)\). Let

\[
(\text{rot}P_0, \text{rot}P_1) \leftarrow \text{SetupPolynomial}(\text{BootsMap}, N)
\]

for \(N \in \mathbb{N}\). Let \(y = \lfloor \frac{2N}{t} \rfloor \cdot m + e\), for \(m \in \mathbb{Z}_s\) and \(e \in \left[-\frac{2N}{t}, \frac{2N}{t}\right]\). Then

- Coefs(\text{rot}P_0 \cdot X^y) \equiv f(m) \mod (N - 1, N - 1), and
- Coefs(\text{rot}P_1 \cdot X^y) \equiv f(m) \mod (N, 2N - 1).

Proof 3 (Correctness of the Polynomial Construction) Let us first consider the case \(y = \lfloor \frac{2N}{t} \rfloor \cdot m + e \in [0, N - 1]\). In this case we have

\[
\text{rot}P_0 \cdot X^y = - \sum_{i=1}^{y} p_0[i - y] \cdot X^{i-1} + \sum_{i=y+1}^{N} p_0[i - y] \cdot X^{i-1} \in \mathbb{R}_{N, Q}
\]

Hence, for \(y = 0\), we have Coefs(\text{rot}P_0 \cdot X^y) \equiv f(m) \mod (N + 1, N + 1), and Coefs(\text{rot}P_0 \cdot X^y) \equiv -f(m) \mod (N + 1, N + 1).

The case \(y \in [N, 2N - 1]\) is analogous. Let us first denote \(y' = N + y\). In particular, we have

\[
\text{rot}P_1 \cdot X^y = \text{rot}P_1 \cdot X^{y'} = -\text{rot}P_1 \cdot X^{y'}
\]

\[
= -(- \sum_{i=1}^{y'} p_1[i - y'] \cdot X^{i-1} + \sum_{i=y'+1}^{N} p_1[i - y'] \cdot X^{i-1}) \in \mathbb{R}_{N, Q}
\]

Hence, for \(y = N\), we have \(y' = 0\) and Coefs(\text{rot}P_1 \cdot X^y) \equiv f(m) \mod (N + 1, N + 1). For \(y' \in (0, N - 1)\), and Coefs(\text{rot}P_1 \cdot X^y) \equiv f(m) \mod (N + 1, N + 1). For \(y' \in (N - 1, 2N - 1)\), and Coefs(\text{rot}P_1 \cdot X^y) \equiv f(m) \mod (N + 1, N + 1).

Theorem 3 (Correctness of the Bootstrapping) Let \(s \in \mathbb{Z}_q^m\) for some \(q, n \in \mathbb{N}\), \(s \in \mathbb{R}_{N, q}\), and \(s_F \leftarrow \text{KeyExt}(s)\). Furthermore, let us define the following.

- \(b_{K} \leftarrow \text{BRKeyGen}(n, s, u, B_{BR}, N, Q, L_{\text{RGSW}}, s)\).
- \(p_{K} \leftarrow \text{KeySwitchSetup}(B_{pk}, N, 1, N, Q, s_F, s, L_{pk})\).
- \(k_{SK} \leftarrow \text{KeySwitchSetup}(B_{kSK}, N, 1, Q, s_F, s, L_{kSK})\).
Let us denote $ct \in \text{LWE}_{ct,n,q}(s, \Delta_{q,t} \cdot m)$.

$ct_N \leftarrow \text{ModSwitch}(ct, q, 2 \cdot N)$, and let us denote $ct_N \in \text{LWE}_{ct,n,2 \cdot N}(s, \Delta_{2 \cdot N,t} \cdot m)$.

Finally, let $\tilde{m} \leftarrow \text{Phase}(ct_N)$, $[\tilde{m}]_i^{2 \cdot N} = m$ and $\text{rotP}_0, \text{rotP}_1 \in \mathcal{R}_{N,Q}$ be such that

- if $\tilde{m} \in \{0, N\}$, then $\text{Coefs}(X^{\tilde{m}} \cdot \text{rotP}_0)[1] = \Delta_{Q,t} \cdot f(m)$, and
- if $\tilde{m} \in \{N, 2 \cdot N\}$, then $\text{Coefs}(X^{\tilde{m}} \cdot \text{rotP}_1)[1] = \Delta_{Q,t} \cdot f(m)$, where $f : \mathbb{Z} \mapsto \mathbb{Z}$.

If $ct_{\text{out}} \leftarrow \text{Bootstrap}(\text{brK}, u, ct, \text{rotP}_0, \text{rotP}_1, L_{\text{boot}}, pk, ksK)$ and given that $B_N \leq \frac{N}{\tau}$, then $ct_{\text{out}} \in \text{LWE}_{ct,n,q}(s, \Delta_{q,t} \cdot f(m))$, where

$$B_N \leq \left(\frac{2}{q} \cdot \frac{N}{q}\right)^2 \cdot B_{ct} + \text{Ham}(s) \cdot ||\text{Var}(s)||_{\infty},$$

for $N \neq 2 \cdot q$, and $B_N = B_{ct}$ otherwise, and $B_{ct} \leq \max(B_{\text{fresh}}, B_{\text{out}})$. We use the bound $B_{\text{fresh}}$ if $ct$ is a freshly encrypted ciphertext. Finally the bound $B_{\text{out}}$ of the bootstrapped ciphertext $ct_{\text{out}}$ that is as follows

$$B_{\text{out}} \leq \frac{q^2}{Q^2} \cdot (B_F + B_{BR} + B_{KS}) + \text{Ham}(s_F) \cdot ||\text{Var}(s_F)||_{\infty},$$

for $Q \neq q$, and $B_{\text{out}} \leq B_F + B_{BR} + B_{KS}$ for $Q = q$, where

- $B_F \leq N \cdot \ell_{\text{boot}} \cdot L_{\text{boot}}^2 \cdot (B_{BR} + B_F)$
- $B_{BR} \leq 2 \cdot n \cdot u \cdot N \cdot \ell_{\text{RGSW}} \cdot L_{\text{RGSW}}^2 \cdot B_{\text{RK}}$
- $B_{KS} \leq N \cdot \ell_{\text{sk}} \cdot L_{\text{sk}}^2 \cdot B_{\text{skK}}$, and
- $B_P \leq N \cdot \ell_{\text{pk}} \cdot L_{\text{pk}}^2 \cdot B_{\text{pk}}$.

**Proof 4** Let us denote

$$\text{Phase}(\text{ModSwitch}(ct, q, 2N)) = \tilde{m} = \Delta_{2 \cdot N,t} \cdot m + e \quad \text{mod} \ 2N.$$

From correctness of modulus switching we have $||\text{Var}(e)||_{\infty} \leq B_N \leq \left(\frac{2N}{q}\right)^2 \cdot B_{ct} + \text{Ham}(s) \cdot ||\text{Var}(s)||_{\infty}$. The first $\ell_{\text{boot}}$ blind rotations compute powers of $L_{\text{boot}}$ of the sign function. Specifically from the correctness of blind rotation we have $\text{acc}_{e,i} \in \text{LWE}_{\text{boot},n,Q}(s, L_{\text{boot}}^{-\Delta_{Q,t}}, \Delta_{Q,t} \cdot \text{sgnP} \cdot X^{\tilde{m}})$. Hence, if $\tilde{m}$ mod $2N \in \{0, N\}$, then the constant coefficient is equal to $L_{\text{boot}}^{-\Delta_{Q,t}} \cdot \frac{\Delta_{Q,t}}{2} \in \mathbb{Z}_Q$. If $\tilde{m}$ mod $2N \in \{N, 2 \cdot N\}$ then the constant coefficient is equal to $-L_{\text{boot}}^{-\Delta_{Q,t}} \cdot \frac{\Delta_{Q,t}}{2} \in \mathbb{Z}_Q$. Equivalently, the constant coefficient is $\text{sgn}(m) \cdot L_{\text{boot}}^{-\Delta_{Q,t}} \cdot \frac{\Delta_{Q,t}}{2} \in \mathbb{Z}_Q$.

Note that $\text{sgn}(m)$ mod $2 \cdot N = \text{sgn}(m)$ mod $t$. From correctness of sample extraction and additive homomorphism of LWE samples we have $\text{acc}_{e,i} \in \text{LWE}_{\text{boot},n,Q}(s_{\text{sgn}}, m_{\text{BR}i})$, where $B_{\text{BR}}$ is the error stemming from the blind rotation, and $m_{\text{BR}i} = \text{sgn}(m) \cdot L_{\text{boot}}^{-\Delta_{Q,t}} + L_{\text{boot}}^{-\Delta_{Q,t}} \cdot \frac{\Delta_{Q,t}}{2}$. Note that if $\text{sgn}(m) = 1$, then $m_{\text{BR}i} = L_{\text{boot}}^{-\Delta_{Q,t}}$, and if $\text{sgn}(m) = -1$, then $m_{\text{BR}i} = 0$.

The next step is to create a new accumulator holding the polynomial $\text{rotP}_{\text{inter}} \in \mathcal{R}_{N,Q}$, where $\text{inter} = 0$ if $m + e \in \{0, N\}$ and $\text{inter} = 1$ if $m + e \in \{N, 2N\}$. From correctness of the accumulator builder we have $\text{acc} \in \text{RLWE}_{\text{boot},n,Q}(s, \text{rotP}_{\text{inter}})$, where

$$B_F \leq N \cdot \ell_{\text{boot}} \cdot L_{\text{boot}}^2 \cdot (B_{\text{BR}} + B_P),$$

with $B_P$ being the bound on the error induced by the LWE to RLWE key switching algorithm.
Definition 14 (External Product) The external product $\text{extProd}$ given as input $C \in \text{GGSW}_{B_a,n,N,q,L}(s,m_C)$ and $d \in \text{GLWE}_{B_a,n,N,q}(s,m_d)$, outputs the following

$$\text{extProd}(C,d) = G_{L_q}^{-1}(d^\top) \cdot C.$$
Figure 7: Sample and key extraction. The KeyExt(s) function on input key s ∈ RN,q outputs its coefficient vector. That is it outputs Coefs(s).

KeySwitchSetup(Bksk, n, n’, N, q, s, s’, Lskk)

Input: A bound Bksk ∈ N, dimensions n, n’ ∈ N, a degree N ∈ N and a modulus q ∈ N that defines RN,q, vectors s ∈ Znq, s’ ∈ RN,q and a basis Lskk ∈ N.

Output: A key switching key ksK ∈ GLWEBSK,n’,N,q(s’, )n×Lskk.

1: Set ℓskk = ⌈logLskk q⌉.
2: For i ∈ [n], j ∈ [ℓskk]
3: ksK[i, j] ← GLWEBSK,n’,N,q(s’, s[i] · Lskk−1).
4: Output ksK ∈ GLWEBSK,n’,N,q(s’, )n×Lskk.

Figure 8: Key switching setup

To conveniently describe our SampleExt, we first need to introduce a step function which we call NSgn : R → {−1, 1} and which is defined as

\[
NSgn(x) = \begin{cases} 
1 & \text{if } x > 0 \\
-1 & \text{otherwise}
\end{cases}
\]

Note that the function is equal in all points to the sign function except for x = 0, which its image is −1.

C Correctness Analysis for Preliminaries

Proof 5 (of lemma 1 - Linear Homomorphism of GLWE samples) Denote c = [b,c]T, where b = bT · s + mc + ce and d = [dT,d]T, where b = dT · s + md + ce. Then we have cout = [b,c]T = c + d = [b,c + b0+ (ac + ad)]T, and b = a1T · s + m + c, where m = mc + md and e = ce + cd. Thus, we have that Error(cout) = ce + cd, and ||Var(Error(cout))||∞ ≤ B0 + Bd.

Let ϕ ∈ RN,max and cout = ϕ · d = [bϕ,cϕ]T, where aϕ = ϕ · d and bϕ = a1T · s + m + c · d.

If ϕ is a monomial with ||ϕ||∞ ≤ B0, then we have ||Var(Error(cout))||∞ ≤ ||B0 · Var(ce)||∞ ≤ B02 · Bc.

If ϕ ∈ RN,max is a polynomial with ||ϕ||∞ ≤ B0 then ||Var(Error(d · c))||∞ ≤ N · B02 · Bc.

Proof 6 (of lemma 2 - Linear Homomorphism of GSW samples) The proof follows from the fact that rows of GSW samples are GLWE samples.
KeySwitch($c$, $ksK$)

**Input:** Takes as input $c = [b, a^\top]^\top \in \text{LWE}_{B, n, q}(s, .)$ and a key switching key $ksK \in \text{GLWE}_{B_{\text{ksK}}, n', N, q}(s', .)^{n \times \ell_{\text{ksK}}}$.

**Output:** A ciphertext $c_{\text{out}} \in \text{GLWE}_{B, n', N, q}(s', m)$. 

1: Denote $\ell_{\text{ksK}} = \lceil \log_{L_{\text{ksK}}} q \rceil$.
2: Compute $A \leftarrow G_{L_{\text{ksK}}, q}(a) \in \mathbb{Z}^{n \times \ell_{\text{ksK}}}$. 
3: Output $c_{\text{out}} \leftarrow [b, 0]^\top - \sum_{i=1}^{n} \sum_{j=1}^{\ell_{\text{ksK}}} A[i, j] \cdot ksK[i, j]$. 

Figure 9: Key switching algorithm and its setup.

BRKeyGen($n, s, u, B_{\text{brK}}, N, Q, L_{\text{RGSW}}, s$)

**Input:** A dimension $n \in \mathbb{N}$, a secret key $s \in \mathbb{Z}^{n \times 1}$, a vector $u \in \mathbb{Z}^n$, a bound $B_{\text{brK}}$, a degree $N \in \mathbb{N}$, a modulus $Q$ defining the ring $\mathcal{R}_{N, Q}$, a basis $L_{\text{RGSW}} \in \mathbb{N}$, and a secret key $s \in \mathbb{R}_{N, Q}$.

**Output:** A blind rotation key $brK \in \text{RGSW}_{B_{\text{brK}}, N, Q, L_{\text{RGSW}}}(s, .)^{n \times u}$.

1: Set a matrix $Y \in \mathbb{B}^{n \times u}$ as follows:
2: For $i \in [n]$ do
3: Set the $i$-th row of $Y$, to satisfy $s[i] = \sum_{j=1}^{u} Y[i, j] \cdot u[j]$. 
4: For $i \in [n], j \in [u]$ do
5: Set $brK[i, j] = \text{RGSW}_{B_{\text{brK}}, N, Q, L_{\text{RGSW}}}(s, Y[i, j])$.
6: Output $brK$.

Figure 10: Blind rotation setup

BlindRotate($brK, acc, ct, u$)

**Input:** Blind rotation key $brK = \text{RGSW}_{B_{\text{brK}}, N, Q, L_{\text{RGSW}}}(s, .)^{n \times u}$, an accumulator $acc \in \text{RLWE}_{B_{\text{acc}}, N, Q}(s, .)$, a ciphertext $ct \in \text{LWE}_{B_{\text{ct}}, n, 2N}(s, .)$, where $s \in \mathbb{Z}^n$ with $t \leq q$, and a vector $u \in \mathbb{Z}^n$.

**Output:** A ciphertext $acc_{\text{out}} \in \text{RLWE}_{B_{\text{acc}}, n, Q}(s, m_{\text{acc}})$.

1: Let $c = [b, a^\top]^\top \in \mathbb{Z}_{2N}^{(n+1) \times 1}$.
2: For $i \in [n]$ do
3: For $j \in [u]$ do
4: $acc \leftarrow \text{Mux}(brK[i, j], acc \cdot X^{-a[i]} \cdot u[j], acc)$.
5: Output $acc$.

Figure 11: Blind Rotation.
Proof 8 (of lemma 4 - Correctness of the Mux Gate) Let us first denote $d = g - h = \text{GLWE}_{B_g}(s, m_g) - \text{GLWE}_{B_h}(s, m_h) = \text{GLWE}_{B_d}(s, m_d)$, where $m_d = m_g - m_h$. Then from correctness of the external product and linear homomorphism of GLWE samples we have

$$c_{\text{out}} = \text{extProd}(C, d) + h = \text{extProd}(G\text{GSW}_{B_g^\perp,n,N,Q,I}(s, m_C), + \text{GLWE}_{B_d}(s, m_d)) + \text{GLWE}_{B_h}(s, m_h) = \text{GLWE}_{B}(s, m_{\text{out}}),$$

where $m_{\text{out}} = m_C \cdot (m_g - m_h) + m_h$. Hence if $m_{\text{out}} = 0$, we have $m_{\text{out}} = m_h$, and if $m_{\text{out}} = 1$, we have $m_{\text{out}} = m_g$.

For the error analysis let us denote explicitly $\text{Error}(g) = e_g$ and $\text{Error}(h) = e_h$. Thus we also have $\text{Error}(d) = e_d = e_g - e_h$. We also denote $\text{Error}(C[i]) = e_i$ for $i \in [(n+1) \cdot \ell]$, and $\ell = \lceil \log_q N \rceil$, and $G_{\text{rotP}}^{-1}(d) = r$. Then, from the correctness analysis of the external product and linear homomorphism of GLWE samples, we have

$$r = e_d \cdot C,$$

and since $m_{\text{out}} = 1$, we have $m_{\text{out}} = m_g$.

Figure 12: TFHE Bootstrapping.
distribution over their support. Let us denote $\text{Ham}$ where $r$ is given that the expectation of $e$ is $\|r\|_1$. In the above we assume that $t$ is in $\mathbb{Z}_Q$. Finally, we can place an upper on the variance $B \leq (n+1) \cdot \ell \cdot L^2 \cdot B_{\text{C}} + \max(B_{\text{D}}, B_{\text{h}})$.

Proof 9 (of lemma 5 - Correctness of Modulus Switching) Denote $c = (b, a)$, where $b = a^T \cdot s + \Delta_{Q,t} \cdot m + e \in \mathbb{Z}_Q$ and $|\text{Var}(e)|_\infty \leq B_c$. From the assumption that $Q = 0 \mod t$ we have $\Delta_{Q,t} = \frac{Q}{t}$. Then we have that the following:

\[
\text{Phase}\left(\frac{q}{Q} \cdot c\right) = \left|\frac{q}{Q} \cdot b\right| - \left|\frac{q}{Q} \cdot a^T \cdot s\right|
\]

\[
= \frac{q}{Q} \cdot b + r - \frac{q}{Q} \cdot a^T \cdot s + r^T \cdot s
\]

\[
= \frac{q}{Q} \cdot \Delta_{Q,t} \cdot m + \frac{q}{Q} \cdot e + r^T \cdot s
\]

\[
= \frac{q}{Q} \cdot \Delta_{Q,t} \cdot m + \frac{q}{Q} \cdot e + r^T \cdot s
\]

where $r \in \mathbb{R}$ and $s \in \mathbb{R}^n$ are in $[-\frac{1}{2}, \frac{1}{2}]$. Furthermore, $r$ and $s$ are close to uniform distribution over their support. Let us denote $\text{Ham}(s) = h$. Therefore, we have

\[
\left|\text{Var}\left(\text{Error}\left(\frac{q}{Q} \cdot c\right)\right)\right|_\infty = \left|\text{Var}\left(\frac{q}{Q} \cdot e + r + r^T \cdot s\right)\right|_\infty
\]

\[
= \left|\text{Var}\left(\frac{q}{Q} \cdot e\right)\right|_\infty + \left|\text{Var}(r^T \cdot s)\right|_\infty
\]

\[
\leq \frac{q^2}{Q^2} \cdot B_c + \sum_{i=1}^{\text{Ham}(s)} \left|\text{Var}(r[i] \cdot s[i])\right|_\infty
\]

\[
\leq \frac{q^2}{Q^2} \cdot B_c + \text{Ham}(s) \cdot \left|\text{Var}(s)\right|_\infty
\]

In the above we assume that $|\text{Var}(s[i])|_\infty = |\text{Var}(s)|_\infty$ for all $i \in [n]$.

The expectation of the output noise satisfies

\[
\left|\frac{q}{Q} \cdot E(\text{Error}(c)) + E\left(\frac{q}{Q} \cdot e + r + r^T \cdot s\right)\right|
\]

\[
= \left|\frac{q}{Q} \cdot E(\text{Error}(c))\right| + \left|E(r + r^T \cdot s)\right|
\]

\[
= \left|\frac{q}{Q} \cdot E(\text{Error}(c))\right| + \left|E(r) + \sum_{i=1}^{n} E(r \cdot s)\right|
\]

\[
\leq \left|\frac{q}{Q} \cdot E(\text{Error}(c))\right| + 1/2 + 1/2 \cdot \text{Ham}(s) \cdot |E(s)|
\]

given that the expectation of $e$ is 0.
Proof 10 (of lemma 6 - Correctness of Sample Extraction) Denote $s = \text{Coefs}(s) \in \mathbb{Z}_q^N$ and $b' = \text{Coefs}(b)[k] \in \mathbb{Z}_q$ and $a = \text{Coefs}(a) \in \mathbb{Z}_q^N$. Denote $b = a \cdot s + m + e \in \mathbb{R}_{N,q}$. $m = \sum_{i=1}^{N} m_i \cdot X_i^{i-1}$ and $e = \sum_{i=1}^{N} e_i \cdot X_i^{i-1}$ then it is easy to see, that $b' = \text{Coefs}(a \cdot s)[k] + \text{Coefs}(m)[k] + \text{Coefs}(e) = \text{Coefs}(a \cdot s)[k] + m_k + e_k$. Furthermore, denote $a = \sum_{i=1}^{N} a_i \cdot X_i^{i-1}$ and $s = \sum_{i=1}^{N} s_i \cdot X_i^{i-1}$. Denote $s \cdot a = (\sum_{i=1}^{N} a_i \cdot X_i^{i-1}) \cdot (\sum_{i=1}^{N} s_i \cdot X_i^{i-1})$. By expanding the product we have that the $k$-th coefficient of $s \cdot a$ is given by $\sum_{i=1}^{N} s_i \cdot \text{NSgn}(k-i+1) \cdot a_{(k-i \mod N)+1}$. Note that when working over the ring $\mathbb{R}_{N,q}$ defined by the cyclotomic polynomial $\Phi_N = X^N + 1$, we have that for $(k-i \mod N) + 1 \leq 0$, the sign of $a_{(k-i \mod N)+1}$ is reversed, thus we multiply the coefficient with $\text{NSgn}(k-i+1)$. Now if we set $a'[i] = \text{NSgn}(k-i+1) \cdot a_{(k-i \mod N)+1}$, then the extracted sample is a valid LWE sample of $m_k$ with respect to key $s$. Finally, since the error in $e_k$ is a single coefficient of $e$, we have that $B_{c_t} = B$.

Proof 11 (of lemma 7 - Correctness of Key Switching) Let us first note that for all $i \in [n]$ we have

$$[b_i, a_i^T]^T = \sum_{j=1}^{\ell_{sk}} A[i,j] \cdot ksk[i,j]$$

$$= \sum_{j=1}^{\ell_{sk}} A[i,j] \cdot \text{GLWE}_{B_{sk},n',N,q}(s', s[i] \cdot L_{sk}^L)$$

$$= \text{GLWE}_{B_{sk},n',N,q}(s', a[i] \cdot s[i]),$$

where $B_1 \leq \ell_{sk} \cdot L_{sk}^L \cdot B_{sk}$. Then note that

$$[b', a'^T]^T = \sum_{i=1}^{n} \text{GLWE}_{B_2,n',N,q}(s', a[i] \cdot s[i])$$

$$= \text{GLWE}_{B_2,n',N,q}(s', \sum_{i=1}^{n} a[i] \cdot s[i])$$

$$= \text{GLWE}_{B_2,n',N,q}(s', a^T \cdot s),$$

where $B_2 \leq n \cdot B_1 \leq n \cdot \ell_{sk} \cdot L_{sk}^L \cdot B_{sk}$. Let us denote $b = a^T \cdot s + m + e$ and $b' = a'^T \cdot s + a^T \cdot s + e'$, then

$$c_{out} = [b, 0]^T - [b', a'^T]^T$$

$$= [b - b', -a'^T]^T$$

$$= [-a'^T \cdot s' + m + e - e'].$$

Hence, $c_{out}$ is a valid GLWE sample of $m$ with respect to key $s'$ and

$$\|\text{Var} (\text{Error}(e))\|_\infty \leq B$$

$$\leq ||e - e'||_2 \leq B_c + B_2$$

$$\leq B_c + n \cdot \ell_{sk} \cdot L_{sk}^L \cdot B_{sk}.$$

Proof 12 (of lemma 8 - Correctness of TFHE-Style Blind-Rotation) Note that the blind rotation key $\text{brK}$ consists of samples of bits: $\text{RGSW}_{\text{brK},N,Q}(s, y[i,j])$, where $Y[i,j] \in B$ for $i \in [n]$ and $j \in [u]$. Denote the current accumulator as $\text{acccurr} \in \text{RLWE}_{\text{brK},N,Q}(s, m_{\text{curr}})$ for some $m_{\text{curr}} \in \mathbb{R}_{N,q}$. If we increment $\text{accnext} \leftarrow \text{Mux}(\text{brK}[i,j], \text{acccurr} \cdot X - a[i] \cdot u[i], \text{acccurr})$ for some $i \in [n]$ and $j \in [u]$, then from correctness of the $\text{Mux}$ gate we
have that $acc_{\text{next}} \in RLWE_{B_{\text{next}}, N, Q}(s, m_{\text{next}})$, where $m_{\text{next}} = m_{\text{curr}} \cdot X^{-a[i]} u[j] K[i, j] \mod 2N \in \mathcal{R}_{N, Q}$. Overall, we can represent the last message $m_{\text{out}}$ as

$$m_{\text{out}} = m_{\text{acc}} \cdot \prod_{i=1}^{n} \prod_{j=1}^{u} X^{-a[i]} u[j] K[i, j]$$

$$= m_{\text{acc}} \cdot \prod_{i=1}^{n} X^{-a[i]} s[i]$$

$$= m_{\text{acc}} \cdot X^{-a^T s} \mod 2N.$$

From the analysis of the Mux gate we have that $B_{\text{next}} \leq B_{\text{curr}} + 2 \cdot N \cdot \ell_{\text{RGSW}} \cdot L_{\text{RGSW}}^2 \cdot B_{\text{brK}}$.

Finally since we perform $n \cdot u$ iterations, we have that

$$B_{\text{out}} \leq B_{\text{acc}} + 2 \cdot n \cdot u \cdot N \cdot \ell_{\text{RGSW}} \cdot L_{\text{RGSW}}^2 \cdot B_{\text{brK}}.$$

**Proof 13 (of theorem 1 - Correctness of TFHE Bootstrapping)** Let us denote

$$\text{Phase}(\text{ModSwitch}(ct, q, 2N)) = m' = \Delta_{2N, t} \cdot m + e \mod 2N.$$

From correctness of modulus switching we have $||\text{Var}(e)||_{\infty} \leq B_N \leq \left(\frac{2N}{q}\right)^2 \cdot B_{ct} + \text{Ham}(s) \cdot ||\text{Var}(s)||_{\infty}$. From the correctness of blind rotation we have $acc_{BR, F} \in RLWE_{B_{\text{BR}}, N, Q}(s, \text{rotP} \cdot X^{m'} \mod 2N)$, where $B_{\text{BR}}$ is the bound induced by the blind rotation algorithm. From the assumption on $\text{rotP}$, given that $m' \leq N$ and the correctness of sample extraction we have $c_Q \in LWE_{B_{c, Q}, N, Q}(s_F, \Delta_{Q, t} \cdot F(m))$, where $B_{c, Q} \leq B_{\text{BR}}$.

From correctness of key switching we have that $c_{Q, ksK} \in LWE_{B_{c, Q}, q}(s, \Delta_{Q, t} \cdot F(m))$, where $B_{c, Q, ksK} \leq B_{c, Q} + B_{KS}$, where $B_{KS}$ is the bound induced by the key switching procedure. From correctness of modulus switching we have that $ct_{\text{out}} \in LWE_{B_{ct}, q}(s, \Delta_{q, t} \cdot F(m))$, where

$$B_{\text{out}} \leq \left(\frac{q^2}{Q^2}\right) \cdot B_{c, Q, ksK} + \text{Ham}(s_F) \cdot ||\text{Var}(s_F)||_{\infty}.$$

To summarize we have:

$$B_{\text{out}} \leq \left(\frac{q^2}{Q^2}\right) \cdot (B_{\text{BR}} + B_{KS}) + \text{Ham}(s_F) \cdot ||\text{Var}(s_F)||_{\infty},$$

where

$$B_{\text{BR}} \leq 2 \cdot n \cdot u \cdot N \cdot \ell_{\text{RGSW}} \cdot L_{\text{RGSW}}^2 \cdot B_{\text{brK}}, \text{ and}$$

$$B_{KS} \leq N \cdot \ell_{ksK} \cdot L_{ksK}^2 \cdot B_{ksK}.$$