Solving Systems of Linear Equations with a Superconducting Quantum Processor

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Linear system lies at the heart of many areas of science and engineering. To solve a system of linear equations with \( N \) variables, the best known classical algorithm requires a time of \( O(N) \). Harrow, Hassidim, and Lloyd (HHL) \cite{Harrow2009_1} showed that in principle quantum computers can solve linear systems exponentially faster by calculating the expectation value of an operator associated with the solution, which may lead to many practical applications of quantum computation other than those previously known \cite{Abrams1999_1, Childs1999_1, Childs2009_1}. For an \( s \)-sparse system matrix of size \( N \times N \) and condition number \( \kappa \), the HHL algorithm can reach a desired computational accuracy \( \epsilon \) within a running time of \( O(\log(N) s^2 \kappa^2 / \epsilon) \) under certain circumstances \cite{Childs2010_1}, comparing to the best known classical algorithm of \( O(\text{N} s \kappa^3 / \epsilon) \). Such an exponential speedup promises widespread applications that address large-scale systems. Indeed, several applications based on the HHL algorithm, such as data processing \cite{Paetznick2013_2}, numerical calculation \cite{Gilyen2017_1} and artificial intelligence \cite{Dunjko2017_1, Lim2017_1}, have been proposed in recent years.

Compiled version of the HHL algorithm was previously only demonstrated with parametric down-converted single photons \cite{O’Brien2008_2, Chou2010_1} and liquid nuclear magnetic resonance \cite{Masance2012_1}, both of which are considered not easily scalable to large number of qubits. For example, the optical demonstration was limited by the probabilistic photon generation and two-photon gate operation. For a deterministic, and more scalable implementation, here we turn to a solid-state system, i.e., a superconducting quantum circuit in this experiment, which has attracted significant attentions due to a number of merits, including the much-improved coherence \cite{Fedorov2009_1, Higginbotham2016_1, Zhu2017_1}, the excellent scalability \cite{Bienfait2015_1, Izaac2016_1, Wang2017_1}, and the remarkable high-fidelity and fast control \cite{Pekola2016_1, Franco2017_1, Romito2016_1}. In addition, compiled versions of various quantum algorithms such as the Deutsch-Jozsa algorithm \cite{Deutsch1992_1, Jozsa1992_1}, the Grover’s algorithm \cite{Grover1997_1}, and the Shor’s algorithm \cite{Shor1994_1} have been successfully tested on this solid-state platform on a small scale.

In this Letter, we demonstrate a nontrivial instance of a quantum linear solver, based on the HHL algorithm for a \( 2 \times 2 \) system, with a superconducting circuit consisting of four Xmon qubits which are variant of the transmon qubits \cite{Koch2007_1}. We test the solver with 18 distinct quantum-state inputs that uniformly distribute on the Bloch sphere, from which the non-trace-preserving quantum process tomography (QPT) can be reliably determined \cite{Chuang1997_1}. For various quantum inputs, our quantum solver can return the desired solutions with reasonably high precision, yielding an averaged QPT fidelity of \( 0.837 \pm 0.006 \). As such our experiment represents the first demonstration of the quantum algorithm for solving systems of linear equations on a solid-state platform.

The device was fabricated on the sapphire substrate in three steps following the procedure outlined previously \cite{Huang2017_1}: (1) deposit the aluminum film on the degassed substrate; (2) define circuit wirings using wet-etch; (3) double-angle evaporate the Al/AlO\(_x\)/Al Josephson junctions. Figure 1 shows the optical micrograph of the device, with the four Xmon qubits labeled from \( Q_1 \) to \( Q_4 \). Each qubit has its own frequency-control Z line, for rotations of the qubit state around Z axis on the Bloch sphere. \( Q_1 \) and \( Q_2 \) (\( Q_3 \) and \( Q_4 \)) share the microwave XY line on the left (right) that is closer to \( Q_1 \) (\( Q_4 \)), for single-qubit rotations around X and Y axes. The microwave pulses transmitted through each XY line have two-frequency components, and the drive strength to \( Q_1 \) (\( Q_2 \)) is more than that to \( Q_2 \) (\( Q_3 \)) by a factor of 11.7 (6.7) as experimentally calibrated.
Each qubit dispersively couples to its own readout resonator, and all readout resonators couple to a common transmission line, enabling simultaneous single-shot quantum nondemolition measurement on multiple qubits using frequency-domain multiplexing. The signal-to-noise ratio is further improved by a quantum limited parametric amplifier, similar to that described previously [27].

The circuit Hamiltonian under the rotating wave approximation is

\[ H = - \sum_{j=1}^{4} \omega_j(t) \sigma_j^Z/2 + \sum_{j=1}^{3} g_{j,j+1} (\sigma_j^+ \sigma_{j+1}^- + \sigma_j^- \sigma_{j+1}^+), \]

where \( \omega_j(t) \) is the resonant frequency of the \( j \)-th qubit that can be tuned over time, \( \sigma_j^Z \) is the Pauli operator, \( \sigma_j^\pm \) are the raising and lowering operators, and \( g_{j,j+1} \) is the nearest-neighbor coupling strength. The coupling strengths of \( g_{j,j+1}/2\pi \), for \( j = 1 \) to 3, are measured to be around 13.0, 9.8, and 14.1 MHz, respectively. These Xmon qubits typically have a maximum frequency around 5.1 GHz and an anharmonicity around 250 MHz. In this experiment, qubit idle frequencies \( \omega_j(t = 0)/2\pi \), for \( j = 1 \) to 4, are arranged in a zigzag pattern at 5.073, 4.074, 4.948, and 4.547 GHz, respectively, which ensures that the nearest-neighbor and next nearest-neighbor couplings along the qubit chain are effectively turned off when idling. At the above-listed frequencies, the qubit lifetimes \( T_1 \) s are measured to be around 15.9, 7.4, 7.8, and 14.1 \( \mu\)s, and the Gaussian dephasing times \( T_2^* \) s are around 8.7, 2.3, 5.2, and 3.4 \( \mu\)s, respectively (see [28] for more information on the device).

The HHL algorithm aims to solve a system of linear equations \( A\vec{x} = \vec{b} \) for \( \vec{x} \) given the \( N \times N \) Hermitian matrix \( A \) and the input vector \( \vec{b} \). The process involves three subsets of qubits: a single ancilla qubit, a register of \( k \) qubits used to store the eigenvalues of \( A \) to a binary precision of \( k \) bits, and a memory of \( O(\log(N)) \) qubits used to load \( \vec{b} \) and also store the output \( \vec{x} \). For simplicity we assume that \( \vec{b} \) is a unit vector, whose entries \( \{b_i\} \) can be encoded in the memory formatted to a quantum state \( |\vec{b}\rangle = \sum_i b_i |i\rangle \), where \( |i\rangle \) denotes the computational basis of the \( O(\log(N)) \) qubits. Next is the core of the HHL algorithm responsible for the exponential speedup: with carefully designed quantum logic gates including mapping the Hermitian matrix \( A \) to the system Hamiltonian, the quantum state \( |\vec{x}\rangle \) representing the desired solution \( \vec{x} \) can be synthesized in the memory conditional upon the state of the ancilla qubit. Afterward one can either map the quantum state \( |\vec{x}\rangle \) to recover all entries of the vector \( \vec{x} \), or, more efficiently, perform the quantum measurement corresponding to an operator \( M \), that one is interested in, to extract its expectation value \( \langle x|M|x\rangle \).

With the system initialized in the state \( |0\rangle_a |0\rangle_r |\vec{b}\rangle_m \), where the subscripts \( a, r, \) and \( m \) index, respectively, the subsets of qubits in the ancilla, the register, and the memory (here and below we keep the subscripts in wavefunctions only when the states of two or three subsets are quoted simultaneously), a general description of the HHL core is as follows: (1) with quantum phase estimation [1, 30] using the controlled unitary transformations in the form \( e^{-iAt} \) for a variable time \( t \), decompose \( |\vec{b}\rangle \) in the eigenbasis of \( A \), i.e., \( |\vec{b}\rangle = \sum_j \beta_j |u_j\rangle \), and map the corresponding eigenvalues \( \lambda_j \) into the register in a binary form to transform the system to \( \sum_j \beta_j |0\rangle_a |\lambda_j\rangle_r |u_j\rangle_m \); (2) perform controlled rotation \( R(\lambda^{-1}) \) on the ancilla according to \( \lambda_j \) stored in the register, which transforms the system to

\[ \sum_j \beta_j \left( \sqrt{1 - \frac{C^2}{\lambda_j^2}} |0\rangle_a + \frac{C}{\lambda_j^2} |1\rangle_a \right) |\lambda_j\rangle_r |u_j\rangle_m, \]

where \( C (\leq 1) \) is a constant that can be selected as any real number to make the controlled rotation physical [1]; (3) reverse the procedure in (1) to disentangle and clear the register, and the system evolves to

\[ \sum_j \beta_j \left( \sqrt{1 - \frac{C^2}{\lambda_j^2}} |0\rangle_a + \frac{C}{\lambda_j^2} |1\rangle_a \right) |0\rangle_r |u_j\rangle_m. \]

A postselection of the \( |1\rangle \)-state outcome of the ancilla will yield the desired output in the memory \( |x\rangle \sim \sum_j C(\beta_j/\lambda_j) |u_j\rangle \), with a success probability of \( \sum_j (C\beta_j/\lambda_j)^2 \).
FIG. 2: (a) Compiled quantum circuits for solving $2 \times 2$ linear equations with four qubits. There are three subroutines and more than 15 gates as indicated. (b) The Bloch-sphere illustration of the controlled rotation subroutine for $C = 1$, where the two rotation angles of $\pi$ and $\pi/3$ are achieved for eigenvalues $\lambda_1 = 1$ ($|01\rangle$) and $\lambda_2 = 2$ ($|10\rangle$), respectively. (c) The $Q_3$-$Q_4$ CZ gate. Top: qubit energy level arrangement showing that $|0\rangle \leftrightarrow |1\rangle$ transition of the target $Q_4$ is on resonance with the $|1\rangle \leftrightarrow |2\rangle$ transition of the control $Q_3$. Only when $Q_3$ is in $|1\rangle$, the state in $Q_4$ will make a full cycle and gain an additional phase of $\pi$. Middle: the quantum circuits for calibrating the CZ gate sandwiched in between two $\pi/2$ rotations, where the second $\pi/2$ rotation axis has an angle $\theta$ from $X$ axis in $XY$ plane of the Bloch sphere. Bottom: the calibrated Ramsey interference curves of $Q_4$ when $Q_3$ is in $|0\rangle$ (blue) and $|1\rangle$ (red), which differ by a phase of $\pi$. No single-qubit phase gates were used during this measurement to cancel the dynamical phase due to the change of qubit frequency.

As argued elsewhere [5], conversions between the classical vectors and their quantum counterparts, i.e., $\tilde{x} \leftrightarrow |\tilde{x}\rangle$ and $\tilde{y} \leftrightarrow |\tilde{y}\rangle$, take extra time and may eventually kill the exponential speedup gained during execution of the HHL core. Nevertheless, the HHL algorithm provides a general template and represents a real advance in the theory of quantum algorithms. Our immediate goal in this experiment is to implement a purely quantum version of the HHL algorithm, i.e., we aim to test the above-mentioned HHL core with quantum inputs and outputs.

In our demonstration, the four-qubit solver is set to run a nontrivial instance, where the system matrix $A$ is chosen as $A = \begin{pmatrix} 1 & 0.5 \\ 0.5 & 1.5 \end{pmatrix}$. Eigenvectors of $A$ formatted to quantum are $|u_1\rangle = (|0\rangle - |1\rangle) / \sqrt{2}$ and $|u_2\rangle = (|0\rangle + |1\rangle) / \sqrt{2}$ with eigenvalues of $\lambda_1 = 1$ and $\lambda_2 = 2$, respectively. Accordingly, our four qubits are divided into three subsets: the ancilla ($Q_1$), the register ($Q_2$,$Q_3$), and the memory ($Q_4$). The binary representations of “$Q_2$”,” “01” and “10”, record eigenvalues $\lambda_1$ and $\lambda_2$, respectively. The input $|b\rangle$ is prepared in $Q_1$ by single-qubit rotations.

The pre-chosen $2 \times 2$ Hermitian matrix $A$ allows us to optimize the circuit that consists of three subroutines as shown in Fig. 2(a), where all two-qubit gates fit to our system Hamiltonian (Eq. 1). **Subroutine 1**: the phase estimation subroutine is pre-compiled with a controlled-phase (CZ) gate, two √ISWAP gates, and two single-qubit gates acting on the memory ($Q_1$) and the register ($Q_2$,$Q_3$), which can be described as follows: first prepare $|b\rangle = \beta_1 |u_1\rangle + \beta_2 |u_2\rangle$ in $Q_1$, following which the Hadamard gate transforms $Q_1$ to $|b\rangle = \beta_1 |1\rangle + \beta_2 |0\rangle$; meanwhile a $\pi$ rotation on $Q_3$ yields $|01\rangle$ in the $Q_2$-$Q_3$ register; next the sandwiched √ISWAP-CZ-√ISWAP gate combo fulfills a controlled-iSWAP gate, which swaps the states between $Q_2$ and $Q_3$ up to a phase factor of $-i$ only if $Q_1$ is in $|0\rangle$. At the end of this subroutine, the state of $|Q_2Q_3\rangle_1|Q_1\rangle_m$ goes to $\beta_1 |01\rangle_1 |1\rangle_m - i\beta_2 |10\rangle_1 |0\rangle_m$, which correlates the binary representations of the eigenvalues in the register (ignoring the phase factor as we omit an insignificant single-qubit phase gate here) with the eigenstates in the memory, since $|1\rangle$ and $|0\rangle$ correspond to $|u_1\rangle$ and $|u_2\rangle$, respectively, up to a Hadamard gate. **Subroutine 2**: the controlled rotation $R(\lambda^{-1})$ subroutine acts on the ancilla ($Q_1$) depending on the state of the register $(Q_2Q_3)$, whose effect can be best visualized using the Bloch sphere representation as illustrated Fig. 2(b). Here we choose $C = 1$, and according to Eq. 2 the rotation angles around $Y$ axis for $Q_4$ should be $\pi$ for $\lambda_1 = 1$ and $\pi/3$ for $\lambda_2 = 2$. **Subroutine 3**: this one is exactly the reverse of Subroutine 1, in which the register $(Q_2Q_3)$ is disentangled and cleared, and $|1\rangle$ and $|0\rangle$ are transformed back to the eigenstates $|u_1\rangle$ and $|u_2\rangle$, respectively, with the final Hadamard gate on $Q_1$.

The compiled HHL circuits in Fig. 2(a) consist of more than 15 one- and two-qubit gates, excluding phase compensation and tomography gates that are not shown. Execution of these gates with reasonably high precisions are therefore important, though phase errors may not be critical at certain steps, e.g., the process of mapping eigenvalues to the register. Our single-qubit rotations have been calibrated by randomized benchmarking to be of reasonably high fidelity as limited
by qubit coherence [31]. $Q_3$’s $\pi$ gate is 300 ns in length, and qubit coherence would limit the gate fidelity to be just under 0.98. For the $Q_2$-$Q_3$ $\sqrt{iSWAP}$ and iSWAP gates, we tune the qubits on-resonance for periods of $\pi/(4g_{2,3})$ and $\pi/(2g_{2,3})$, respectively, similar to those demonstrated using the qubit-resonator architecture [32]. However, here due to the crosstalk issue, which likely results from insufficient crossover wirings to tie the grounds together [33], our $\sqrt{iSWAP}$ and iSWAP gates show slightly lower performances, with fidelity values estimated to be slightly above 0.98 and 0.97, respectively. For the CZ gate we include the qubit’s second excited state [2] and implement similarly as that demonstrated using the qubit-resonator-qubit architecture [34]. The fidelity of our CZ gate is estimated to be about 0.95 using the calibrated Ramsey interference data shown in Fig. 2(c). We note that the CZ gate can also be implemented, with high fidelity and without exchange of excitations, by optimizing one qubit’s frequency trajectory to mediate the two-qubit $|11\rangle$ state close to the avoided-level crossing with the $|02\rangle$ state [19], which would be hard to be implemented in our device due to the crosstalk issue.

We prepare 18 different input states $|b_j\rangle$, for $j = 1$ to 18, on the Bloch sphere as shown in Fig. 3(a). With the output state stored in $Q_1$, we can measure the expectation value of a certain operator that we are interested in (see, e.g., the case of $|b_1\rangle = |0\rangle$ in Fig. 3(b)). We can also characterize the input and output states of the above instance by quantum state tomography (QST): qubit polarization along $Z$ axis of the Bloch sphere can be directly measured; for polarizations along $X$ and $Y$ axes the qubit is rotated around $Y$ and $X$ axes, respectively, before measurement. The corresponding output $|x_j\rangle$ in $Q_1$ and the state of $Q_4$ are simultaneously measured [29], which allows us to reconstruct $|x_j\rangle$ using only the data corresponding to the $|1\rangle$-state outcome of $Q_4$. As shown in Fig. 3(c), fidelity values of the 18 output states by QST are reasonably high, ranging from $0.840 \pm 0.006$ to $0.923 \pm 0.008$.

With all the input and output states being measured, we are able to characterize the solver more exactly by QPT, where a non-trace-preserving process matrix in the Pauli basis is used to linearly map $|b_j\rangle$ to $|x_j\rangle$. The experimental $\chi_{\text{exp}}$ matrix, inferred reliably only with all 18 input states being used, and the ideal matrix $\chi_{\text{id}}$ are shown together in Fig. 4 for comparison, which yields a process fidelity [35] of $F = \text{Tr}(\chi_{\text{id}}\chi_{\text{exp}})/[\text{Tr}(\chi_{\text{id}})\text{Tr}(\chi_{\text{exp}})] = 0.837 \pm 0.006$. Here the error bar is estimated using simulated random normal distributed noise associated with the QST data [36]. $\chi_{\text{exp}}$ has a trace of 0.497, which indicates the averaged success probability of our solver for the 18 input states, while ideally the trace is 0.625 for $C = 1$ [29].

In summary, we have demonstrated a superconducting quantum linear solver for a $2 \times 2$ system with output state fidelities ranging from $0.840 \pm 0.006$ to $0.923 \pm 0.008$ and quantum process fidelity of $0.837 \pm 0.006$. The achieved fidelities are limited by decoherence and by errors in our implementation of the two-qubit gates, the latter of which is related to the insufficient grounding that can be fixed by adding an extra lithography layer in the device fabrication [17, 19]. With future improvements on superconducting qubit coherence and circuit complexity, the superconducting quantum cir-
cuits could be used to implement more intricate quantum algorithms on a larger scale and ultimately reach quantum computational speedup.

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