EFFICIENCY EVALUATION TECHNIQUE OF THE SEMICONDUCTOR DC-DC CONVERTER APPLICATION IN THE POWER-SUPPLY SYSTEM

Purpose. To specify efficiency evaluation of the DC-DC power semiconductor converter application in the power-supply system.

Methodology. We have chosen application version of converter and then used the statistical modeling of DC-DC converter for its efficiency evaluation at varied input voltage according to proposed technique. We have compared the simulated result with the data presented in reference literature. Results. We have proposed the efficiency evaluation technique of converter application. Proposed technique includes detection of external factors influenced on the converter efficiency; development of efficiency evaluation models; selecting application version of converter; determination of the correlation coefficients between the values of external factors; definition of the converter efficiency; obtaining of the converter efficiency distribution. Originality. For the first time, we have developed the evaluation technique of converter efficiency corrected for application version of converter. Practical value. Presented in the study results could be useful for specialists in semiconductor equipment, electrical facilities and systems. References 10, figures 8.

Key words: semiconductor converter, efficiency of converter application, statistical modeling, evaluation of the effectiveness, converter efficiency.

Introduction. To meet the power requirements of different types and parameters, and to effectively manage its various distribution converting devices are needed. An important requirement which is presented to the converters is to provide high performance.

Semiconductor converters have a high efficiency, so are widely used in industrial equipment and electric transport [1].

To quantify the efficiency we use the ratio of output power (or energy) to the power consumed by the converter from the mains, at a certain input voltage and load, often nominal. For converters the value of efficiency contained in the documents and is one of the important indicators. Reduced efficiency in converters due to losses in the converter electronic components – power semiconductor devices (PSD), transformers, inductance, capacitance. Basic types of losses in converters are switching losses arising at the switching of power switches, conduction losses and [2]. Known studies [3-5] are aimed at finding the maximum efficiency of the converter which is achieved by selecting the topology optimization of the parameters of passive components, the use of PSD with a low resistance in the conducting state and switching losses, choice of algorithm switching and PSD switching frequency. The quantity also depends on the efficiency of external factors such as input voltage, load resistance, and others. There are a number of applications of semiconductor converters which are characterized by a change in a wide range of input voltage and load current. It is for wind energy converters, power supply from the battery, electric motor and other. Researchers [6] revealed dependence $E = f(V_{\text{in}}, I_o)$ which are the extremum in the middle ranges of values $V_{\text{in}}$ and $I_o$. For example, the input voltage of the converter power supply system of own needs (SSON) of electric rolling stock (ERS) varies widely (on the susceptor of the electric rolling stock with a nominal voltage of 3000 V – from 2200 to 3850 V [7]). In this case, most of the time converter of the SSON works at non-optimal conditions, its efficiency is reduced [8].

Problem definition. In itself, the value of the efficiency does not provide complete information about the amount of energy loss in the converter in particular its application in the power supply system, as the use mode converter may differ from that in which the efficiency

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was measured. In real terms the converter operates in a range of efficiency. Therefore, to the average evaluating the effectiveness of the use of the converter in the power supply system needs to take into account the nature of the change in time of the input voltage and load. Depending on the application, the converter, these changes can be both deterministic and stochastic.

The goal of the work is to update evaluation the effectiveness of the use of the DC voltage inverter in the power supply system.

For the efficiency density distribution and integral distribution function of the efficiency the applied method of statistical Monte-Carlo simulation is used.

Results of investigations.

The proposed technique includes the following stages:

1. Identification of a number of external factors $A_{ext}$ on which the efficiency of the converter depends. As a rule, they are voltage $V_{in}$ and current $I_{o}$.

2. Development of a model for converter efficiency evaluation as a function of external factors $Efficiency = f(A_{ext})$. As is known, the converter efficiency is determined in the losses of its circuit elements. These losses depend on the ratings and the quality of its circuit elements, and the switching converter topologies key algorithms. At the stage of evaluation of the efficiency of the model are identified depending on the components of loss on external factors values.

3. To select the option of using the converter, which is described by some time interval $[t_1, t_2]$, duration $\Delta t_i$ in time changes the characteristics of the external factor values $A_{ext} = f(t)$, $t \in [t_1, t_2]$ including a range of possible values $[A_{ext, min}, A_{ext, max}]$ and the duration of action to the converter of external factor with a value $A_{ext}$. At the stochastic nature of the change in time value of external factors $A_{ext} \in [0, 1]$ to determine the probability density function of values $A_{ext}(t)$ on the converter model input.

4. To determine the correlation coefficients between the values of external factors on the inputs of the converter model. These factors are taken into account when setting the values of the dependent from each other external factors. For example, intelligent power supply control system can limit the load at lower voltage $V_{in}$ to a critical value.

5. To calculate the converter efficiency by modeling his work with the given values of external factors for each subinterval $\Delta t_i$ owned interval $\Delta t$. For stochastically changing external factors to conduct a series of experiments with the converter model and the values of the input factors, which are randomly selected, taking into account the probability of occurrence of a given value.

6. The resulting simulation efficiencies to divide into ranges of values and to determine the relative frequency of efficiency values belonging to each interval. On the basis of these frequencies to determine the density distribution and the cumulative distribution function of the converter efficiency for the selected option of using it.

We illustrate the proposed methodology on the example of evaluating the effectiveness of the use of a synchronous buck-boost DC converter at random variation of the input voltage. Functional diagram of the considered converter is shown in Fig. 1 [9].

To evaluate the effectiveness of the converter we perform actions according to the proposed method.

1. As input factor we selected $V_{in}$ which, as noted above, significantly influences the efficiency of the converter.

2. To obtain the dependence of the efficiency of the converter voltage varying randomly we simulated a synchronous buck-boost DC converter. As the basis of the model a Simulink model of a synchronous DC buck-boost converter with a subsystem that performs the output power, the loss of power and efficiency of the converter calculation is taken [9].

Fig. 2 shows a model of a synchronous buck-boost DC converter and Fig. 3 demonstrates subsystems that perform calculation of output power, power losses and converter efficiency. The model takes into account the main factors affecting the efficiency of the converter: Key resistance in the open state; direct drain-source voltage; duty cycle; operating frequency; fall time and rise time when the key switch; static and dynamic losses in the inductor and the capacitor equivalent series resistance.

The presented in Fig. 3 model for calculation of efficiency determines the power losses in the converter as the sum of the power loss in the switch to the keys (SS Switch Loss and LS Switch Loss), the power loss in the keys in the conductive state (SS Conduction Loss and LS Conduction Loss), losses in «internal» key diode (LS Body Diode Loss), the losses due to «dead time” (Dead-Time Loss).
Fig. 2. A model of the synchronous buck-boost DC converter

Fig. 3. A model of the subsystem calculating output power, power losses and efficiency
Parameters values of model’s elements not specified in [9] are presented in the m-file listing (Fig. 4).

Fig. 4. Listing of the m-file setting parameters of the converter’s circuit elements

3. We simulate the conditions specific to the converter SSON ERS. The voltage at the input of the converter ($V_{in}$) depends on the voltage of the traction substation, the ERS position relative to the points of the supply voltage to the contact wire, the presence of other loads at the site of the power station, weather conditions and other factors, wearing casual character. In accordance with the provisions of the central limit A.M. Lyapunov theorem if the random variable $x$ is the sum of a very large number of mutually independent random variables, each of which impact on the entire amount is negligible, then $x$ is distributed according to a law close to normal [10]. Therefore, we believe that the input voltage $V_{in}$ converter has a normal distribution.

The density of the random variable $V_{in}$ is given by [7]:

$$f(V_{in}, \mu, \sigma) = \frac{1}{\sigma \sqrt{2\pi}} e^{-\frac{(V_{in} - \mu)^2}{2\sigma^2}},$$

(1)

where $\mu$ is the average value (nominal input voltage of the converter); $\sigma$ is the standard deviation.

For a DC converter SSON ERS [7, 10] $\mu = 3000$ V and $\sigma = 2.1$. Modeling the circuit (Fig. 2) we assume $\mu = 13.8$ V and $\sigma = 2.1$.

Random values of $\mu$ are generated by functions NORM.INV and RAND of the Microsoft Excel Microsoft Office 2010.

Density plot of a probability distribution of the input voltage is shown in Fig. 5. The generated sample consists of 100 values. With this number of values the maximum relative deviation of the generated density distribution of the density distribution determined by the formula (1) does not exceed 12 % (Fig. 6), which is acceptable in engineering calculations.

Fig. 5. Probability density generated and determined by formula (1) distribution density of the input voltage value $V_{in}$

Fig. 6. Relative deviation of the generated distribution

4. As seen in the example of the effect of only one value (input voltage) of the converter efficiency, the correlations between the values of influencing factors are absent.

5. For generated random values $V_{in}$ we simulated currents in the branches and the voltages in the circuit nodes, as well as the modeling of output power losses and converter efficiency. Simulation results with comparable input data agree to within 1 % of the data obtained in [10].

6. As a result of a series of experiments we obtained 100 values of the converter efficiency which are in the range of 0.9187 to 0.9584. Further, this range is divided into intervals (in this example – 0.005) and identified the relative and absolute frequency of the efficiency values within each interval. Based on these data, the density distribution plotted converter efficiency (Fig. 7) and the cumulative distribution function converter efficiency (Fig. 8).

The following distribution corresponds to Fig. 6:
The efficiency of the converter $E$ is given by:

$$f(E;\mu,\sigma) = \frac{1}{\sigma\sqrt{2\pi}} e^{-\frac{(E-\mu)^2}{2\sigma^2}},$$

where $E$ is the converter efficiency; $\mu$ is the average value; $\sigma$ is the standard deviation.

In the correspondence with Fig. 7, $\mu = 0.935$ and $\sigma = 0.016$.

In accordance with Fig. 8, the probability that the data in the efficiency of the converter at given conditions of use will be not less than 0.935, it is 0.65. Thus, account of risks of reducing the efficiency based on the proposed integrated assessment helps to clarify evaluate the effectiveness of the use of the converter in a particular application.

**Conclusions.**

1. A technique for evaluating the effectiveness of using of a semiconductor DC converter in the power supply system is proposed.
2. It is shown that the calculated by the proposed method the efficiency of the converter utilization is below the average efficiency values defined by the baseline method.
3. It is intended to use obtained results during converter design at the company «NII Preobrazovatel» (Zaporozhye, Ukraine).

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