SparseCCL: Connected Components Labeling and Analysis for sparse images

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Abstract—Connected components labeling and analysis for dense images have been extensively studied on a wide range of architectures. Some applications, like particles detectors in High Energy Physics, need to analyse many small and sparse images at high throughput. Because they process all pixels of the image, classic algorithms for dense images are inefficient on sparse data. We address this inefficiency by introducing a new algorithm specifically designed for sparse images. We show that we can further improve this sparse algorithm by specializing it for the data input format, avoiding a decoding step and processing multiple pixels at once. A benchmark on Intel and AMD CPUs shows that the algorithm is from $\times 1.6$ to $\times 2.5$ faster on sparse images.

I. INTRODUCTION

In computer vision, Connected Component labeling (CCL) is a common and wide spread algorithm. Its purpose is to assign a unique label to each group of connected pixels. These groups of pixels, called Connected Components (CC), are then used for higher level tasks, like tracking, motion detection or optical character recognition. First instances of this algorithm were proposed by pioneers like Rosenfeld [20] or Haralick [8]. In High Energy Physics (HEP), CCL is used for tracking particles by labeling hits on the detectors’ sensors to extract the real impact positions.

A CCL algorithm by itself, only provides the association of pixels, this is why it is followed by an analysis algorithm. The purpose of the analysis is to compute features of each CC, like the bounding box or the first statistical moments in order to compute the center of gravity. If naive algorithm perform the labeling first and then the analysis, the optimized algorithms do the analysis during the labeling. These algorithms are called Connected Component Analysis (CCA).

Most of CCL algorithms used to be sequential ones developed on single-core processors [9] [4]. Recently, new parallel algorithms were developed for multi-core processors [16] [7], SIMD processors [22] [14] [10], GPUs [18] [11] and FPGAs [13].

These algorithms are very efficient for natural images but not for very low density images (very few pixels set to one) like those generated in HEP experiments.

The case considered here is similar to matrix algebra. When a matrix has very few non-zero value, the classical dense structure and the classical dense algorithms turn out to be inefficient. The dense structure is replaced by various flavours of lists that hold the non-zero value and specialized or dedicated algorithms are designed to process these data efficiently. In the case of tracking hits on detectors’ sensors, the same phenomenon happens: even if CCA algorithms are very fast, they are inefficient to cluster and label matrices of hits with a density around 0.5%.

The Section II presents some classic connected components labeling algorithms. Section III introduces a new algorithm for connected components labeling of sparse images and its specialization for the pattern recognition of CERN’s LHCb experiment. Finally, in Section IV, we evaluate this new algorithm and compare it to state-of-the-art.

II. CLASSIC ALGORITHMS FOR DENSE IMAGES

In this section, we present three classes of connected components labeling algorithms.

A. One component at a time

In this first class of algorithm, we process one connected component at a time. The image is scanned one time and, for every foreground pixel encountered, a traversal of the connected component is done to label all the pixels. This algorithm and its variants are often called flood fill or sometimes seed fill. The traversal can be done using a stack in depth-first order or a queue, in breadth-first order. Implementations of algorithms of this class are found in [21] and [1]. This algorithm can be optimized by only adding, on top of the stack, the branching pixels – i.e. the pixels that have more than one non-visited neighbour – and directly processing the others. Doing so, we avoid a store and a load for these pixels. If the image is sparse and if we have a list of pixel coordinates, we can directly start at known pixel positions avoiding the read of many background pixels. However, this does not prevent the test of every pixel on the contour of the connected component and it adds the cost of removing pixels from the list. An implementation of this algorithm specialized for the LHCb experiment is described in [3].

B. Iterative algorithms

The second class was introduced by Haralick [8]. Each pixel is initialized with a unique temporary label, then this label is propagated to the pixel’s neighbors using local minimum
or maximum propagation. The propagation step is repeated until the image of labels reaches stabilization, i.e. there is no more change within the image. This algorithm was particularly fitted for implementations on parallel architectures, due to its high regularity, before the apparition of fast scatter and gather operations needed for union-find based algorithms. The number of iterations, and thus the processing time, of iterative algorithms depends on the longest path in the image. For an \( n \times n \) spiral, the number of iterations is equal to \( \frac{4n^2}{\pi^2} \).

C. Direct two-pass algorithms

The two-pass CCL algorithms are split into three steps and perform two image scans (like the pioneer algorithm of Rosenfeld [20]). The first scan (or first labeling) assigns a temporary label to each CC and some label equivalences are built if needed, using an union-find equivalence table \( T \) [17]. The second step solves the equivalence table by computing the transitive closure of the graphs associated to the label equivalences. The third step performs a second scan (or second labeling) that replaces temporary labels of each CC by its final label, by doing a simple lookup: \( I(i, j) \leftarrow T[I(i, j)] \).

Figure 1 defines some notations and gives an example of a classic Rosenfeld algorithm execution. Let \( p_x \) the current pixel and \( e_x \) its label. Let \( p_a, p_b, p_c, p_d \) the neighbor pixels, and \( a, b, c, d \) the associated labels. \( T \) is the equivalence table, \( e \) a label and \( r \) its root. The first scan of Rosenfeld is described in algorithm 1, the transitive closure in Algorithm 2, while the classical union-find algorithms are provided in Algorithms 3 & 4. In the example (Fig. 1), we can see that the rightmost CC requires three labels (1, 2 and 4). When the mask is in the position seen in figure 1 (in bold type), the equivalence between 2 and 4 is detected and stored in the equivalence table \( T \). At the end of first scan, the equivalence table is complete and applied to the image.

III. CONTEXT AND SPARSECCL

In this section we present SparseCCL, a parameterizable connected components labeling and analysis algorithm for sparse images. Then, we present a specialization of the algorithm in the context of the LHCb experiment, where the very few hits of high-energy particles are scattered across the detector’s sensors.

A. General parameterizable ordered SparseCCL

In this first version of the algorithm, we assume that the image is represented by a list of active pixels ordered by their coordinates. This kind of representation allows the algorithm to take advantage of the sparse nature of the data. This is due to the size of the list scaling directly with the number of pixels to label and not the total number of pixels. Another case where this representation is useful is when the coordinate ranges are too large or if the number of dimensions makes the storage not practical. Figure 2 gives an example of such an image and its list representation.

Algorithm 1: Rosenfeld algorithm – first labeling (step 1)

Input: \( a, b, c, d \), four labels, \( p_x \), the current pixel in \((i, j)\)

1. if \( p_x \neq 0 \) then
2. \( a \leftarrow E[i-1][j-1], \ b \leftarrow E[i-1][j] \)
3. \( c \leftarrow E[i+1][j+1], \ d \leftarrow E[i][j-1] \)
4. if \((a = b = c = d = 0)\) then
5. \( ne \leftarrow ne + 1, \ e_x \leftarrow ne \)
6. else
7. \( r_a \leftarrow \text{find}(T, a), \ r_b \leftarrow \text{find}(T, b) \)
8. \( r_c \leftarrow \text{find}(T, c), \ r_d \leftarrow \text{find}(T, d) \)
9. \( e_x \leftarrow \min(r_a, r_b, r_c, r_d) \)
10. if \((r_a \neq 0 \text{ and } r_a \neq e_x)\) then \( \text{union}(T, e_x, r_a) \)
11. if \((r_b \neq 0 \text{ and } r_b \neq e_x)\) then \( \text{union}(T, e_x, r_b) \)
12. if \((r_c \neq 0 \text{ and } r_c \neq e_x)\) then \( \text{union}(T, e_x, r_c) \)
13. if \((r_d \neq 0 \text{ and } r_d \neq e_x)\) then \( \text{union}(T, e_x, r_d) \)
14. else
15. \( e_x \leftarrow 0 \)

Algorithm 2: Sequential solve of equivalences (step 2)

for \( e \in [1 : n] \) do
2. \( T[e] \leftarrow T[T[e]] \)

Algorithm 3: \( \text{find}(T, e) \)

Input: \( e \) a label, \( T \) an equivalence table
Result: \( r \), the root of \( e \)
1. \( r \leftarrow e \)
2. while \( T[r] \neq r \) do
3. \( r \leftarrow T[r] \)
4. return \( r \)

Algorithm 4: \( \text{union}(T, e_1, e_2) \)

Input: \( e_1, e_2 \) two labels, \( T \) an equivalence table
Result: \( e \), the least common ancestor of the \( e \)'s
1. if \( e_1 < e_2 \) then
2. \( e \leftarrow e_1, \ T[e_2] \leftarrow e \)
3. else
4. \( e \leftarrow e_2, \ T[e_1] \leftarrow e \)
5. return \( e \)

![Algorithm 1](image1.png)

![Algorithm 2](image2.png)

![Algorithm 3](image3.png)

![Algorithm 4](image4.png)
The algorithm parameterization is done through the functions `is_adjacent` / `is_far_enough` for the labeling and `init_features` / `accumulate_features` for the analysis. The algorithm is generic enough to be adapted to n-dimensions and every type of connectivity and pixel format. Algorithm 5 gives the complete algorithm.

**Algorithm 5: SparseCCL**

```plaintext
// First scan: pixel association
start_i ← 0
for i ← 0 to n - 1 do
  T[i] ← i
  ai ← i
  for j ← start_i to i - 1 do
    if is_adjacent(pixel[i], pixel[j]) then
      ai ← union(T, ai, find(T, j))
    else if is_farEnough(pixel[i], pixel[j]) then
      start_j ← start_j + 1

// Second scan: transitive closure and analysis
labels ← 0
for i ← 0 to n - 1 do
  if T[i] = 1 then
    labels ← labels + 1
    l ← labels
    init_features(l, pixel[i])
  else
    l ← T[T[i]]
    accumulate_features(l, pixel[i])
  T[i] ← l
```

SparseCCL is designed to minimize the memory footprint in order to have the best data locality and to fit in the L1 cache. The only internal memory it needs is an integer table of size n to store the equivalences. The input table containing the pixels and the output tables containing the connected components features are allocated outside of the algorithm. The algorithm is divided into two parts: the first scan where pixels are associated using an equivalence table and the second scan, where we resolve equivalences, by performing a transitive closure of the graph embedded in the equivalence table. We can also do an on-the-fly analysis of connected components in the second scan.

The first scan iterates over the pixels in the list and adds them one by one to the equivalence table. The equivalence table is an index table implementing a forest of equivalence trees. Each cell of the table corresponds to one pixel, the content of the cell is the index of the parent pixel. A pixel is a root if its entry in the equivalence table is its own index. For each pixel, the algorithm checks on previously added pixels for adjacency and merge their two equivalence trees if they are adjacent. The merging is done by calling the `union` and `find` functions described in algorithms 4 and 3. Because the list is ordered, we can keep track of a start index to avoid testing pixels that are too far from each other. With this optimization, the first scan complexity becomes $O(kn)$ instead of $O(n(n - 1)/2)$, with $k$ a constant much smaller than $n$, as we test only the few last pixels. The `is_adjacent` and `is_far_enough` parameterization for 2-dimensions 8-connectivity labeling is described in Algorithms 6 and 7. Here, the adjacency is tested by comparing the L1 distance between two pixel to a radius of 1, the pixels are far enough if the signed distance is bigger than the radius.

**Algorithm 6: is_adjacent(p1, p2)**

```plaintext
return |p1.x - p2.x| ≤ 1 and |p1.y - p2.y| ≤ 1
```

**Algorithm 7: is_far_enough(p1, p2)**

```plaintext
return p1.y - p2.y > 1
```

The second scan iterates over each temporary label in the equivalence table. If the label is a root, it creates a new connected component label by incrementing the `labels` counter and initialises the features for this connected component. If the label has a parent, it takes the parent’s label and accumulates the features. Because the temporary label of a parent is always smaller than the one of the child, we know that the parent is already processed. Before continuing to the next label, we update the equivalence table with the new label. Algorithms 8 and 9 give an example of the features needed to compute the connected components center of gravity $(G_x, G_y) = (S_x/S, S_y/S)$. $(S_x, S_y)$ are the sums of $x$ and $y$ coordinates and $S$ the number of pixels.

**Algorithm 8: init_features(label, pixel)**

```plaintext
sum_x[label] ← pixel.x
sum_y[label] ← pixel.y
sum[label] ← 1
```

**B. Acceleration structure for unordered pixels**

In some scenarios, we might not receive an ordered list of pixels as input and sorting them would already take too much
time. We also cannot afford accessing a full pixel image buffer because of data locality and the time it will take to reset such buffer between two images labeling. We compromise by doing dimension reduction: we use a table for each row and add the pixels to its corresponding row when we encounter it in the first scan. Now, when checking for adjacency, we only have to check the previous, the current and the next row of the table. The pixels within each row are not sorted so they have to be all checked. Each row has a size property ($N_{row}$) that keeps track of how many pixels were added to the row. When resetting the tables, we only have to set the size of used rows to zero. Table I shows an example of such structure.

| Row | $N_{row}$ | Pixels (index, column) |
|-----|----------|------------------------|
| 0   | 1        | (5, 1)                 |
| 1..3| 0        |                        |
| 4   | 2        | (9, 8), (7, 10)        |
| 5   | 2        | (4, 9), (1, 10)        |
| 6..11| 0        |                        |
| 12  | 1        | (8, 12)                |
| 13  | 2        | (3, 3), (6, 11)        |
| 14  | 1        | (2, 4)                 |
| 15  | 0        |                        |

TABLE I
STRUCTURE REPRESENTING THE RECEIVED PIXELS FROM FIGURE 2

C. Case study: specialization for LHCb VELO Upgrade

LHCb, one of the four major experiments at the Large Hadron Collider (LHC) [2], is a general purpose spectrometer optimized for the study of particles containing b and anti-b quarks (B mesons). The experiment’s detector is specifically designed to filter out these particles and the products of their decay. Each LHCb sub-detector is specialized in measuring a different characteristic of the particles produced by colliding protons. Collectively, the detector’s components gather information about the identity, trajectory, momentum and energy of each generated particle to single them out. After the upcoming high luminosity upgrade, the LHC will collide protons 30 million times per second at LHCb. These collision “events” must be analysed in real time by a system called the High Level Trigger 1 (HLT1), in order to find and keep the small fraction of these events which contain B mesons.

The VELO (VErtex LOcator) sub-detector, shown in Figure 3, is a high precision pixel detector surrounding the beamline where the collision occurs. It is divided into 52 L-shaped modules. Each module is itself composed by 4 sensors of 3 chips each. The chips have 256×256 pixels, so the sensors have 256 rows and 768 columns. Each pixel is a square with a length of 55 microns. The sensor pixels are packed into Super-Pixels (SP) of size 2×4 pixels, so the sensors have 64 SP rows and 384 SP columns [12] [19].

The modules are positioned along the z axis. More information about the geometry can be found in the [19].

![CERN LHCb VELO geometry](image)

Fig. 3. CERN LHCb VELO geometry

![CERN LHCb VELO Super-pixel format](image)

Fig. 4. CERN LHCb VELO Super-pixel format

To take advantage of the data format, we specialized the SparseCCL algorithm to label SP instead of pixels. This allows to further reduce the amount of memory needed and to skip a decoding step. We first start by preparing the data: we remove the SP that are known to be isolated and resolve them using lookup tables. For the remaining SP, we test if there is more than one CC inside and split them if necessary. Figure 5 shows the two possible configurations for a SP: one CC or two CCs. Because there can be at most 2 clusters per SP, the maximum number of clusters in the image is $2 \times$ the number of SP. Once the SP list is prepared, we run the algorithm using a combination of bitwise operations and a lookup table to test the adjacency. Another lookup table is used for a fast computation of the first statistical moment and the number of pixels within a SP.

In 8-connectivity CCL there are eight directions of adjacency, but by using symmetries we can reduce their number to four. Figure 6 shows the configurations of SP and the pixels we have to test. Configurations $a$, $b$ and $c$ are quickly tested using only bitwise operations. While configuration $d$ could be tested the same way, it was found faster to use a 256-entry lookup table using the dark pixels bit pattern as the address. Configuration $e$ shows the pixels required to take the decision to split the SP in two.

Algorithm 9: accumulate_features(label, pixel)

1. `sum_x[label] ← sum_x[label] + pixel.x`
2. `sum_y[label] ← sum_y[label] + pixel.y`
3. `sum_n[label] ← sum_n[label] + 1`
looking only at active pixels is 10 times faster at a density of 768 pixels. While these algorithms are well optimized, a simple flood fill algorithm performs poorly. We observe, that for a granularity of 1, the ordered SparseCCL working on pixels has the best behavior at low density. The Super-Pixels variant is slower at low densities because each SP is more likely to contain only 1 pixel. It presents no advantage over the pixel versions. The row table variant starts higher than the ordered one because of the cost of table reset. The flood fill algorithm is significantly slower than other version at low density, but scales better with the number of pixels and eventually becomes faster for densities > 1.8%. The benchmarks were conducted on an Intel Xeon Gold 6162 @2.6GHz (in Figures 7 & 8) and on an AMD EPYC 7301 @2.2GHz, the cpp (time normalized by frequency in cycle per pixel) were similar on both architectures.

In our test configuration, the number of pixels ranges from 0 to 6988. We observe that for a granularity of 1, the ordered SparseCCL working on pixels has the best behavior at low density. The Super-Pixels variant is slower at low densities because each SP is more likely to contain only 1 pixel. It presents no advantage over the pixel versions. The row table variant starts higher than the ordered one because of the cost of table reset. The flood fill algorithm is significantly slower than other version at low density, but scales better with the number of pixels and eventually becomes faster for densities > 1.8%. The benchmarks were conducted on an Intel Xeon Gold 6162 @2.6GHz (in Figures 7 & 8) and on an AMD EPYC 7301 @2.2GHz, the cpp (time normalized by frequency in cycle per pixel) were similar on both architectures.

Figure 7 shows the measured time in cpp for the four algorithms, for 364 × 768 pixels images of varying densities from 0% to 2.5% and a granularity g=1. The number of pixel n is given by the formula $n = d^w \times h$, where $d$ is the density, $w$ the number of columns and $h$ the number of rows. In our test configuration, the number of pixels ranges from 0 to 6988. We observe that for a granularity of 1, the ordered SparseCCL working on pixels has the best behavior at low density. The Super-Pixels variant is slower at low densities because each SP is more likely to contain only 1 pixel. It presents no advantage over the pixel versions. The row table variant starts higher than the ordered one because of the cost of table reset. The flood fill algorithm is significantly slower than other version at low density, but scales better with the number of pixels and eventually becomes faster for densities > 1.8%. The benchmarks were conducted on an Intel Xeon Gold 6162 @2.6GHz (in Figures 7 & 8) and on an AMD EPYC 7301 @2.2GHz, the cpp (time normalized by frequency in cycle per pixel) were similar on both architectures.

Figure 8 shows the same algorithms, but with a granularity g=2. The flood fill algorithm benefits from the data locality induced by the increased granularity. On the contrary, the pixel based SparseCCL variants are slowed down by it, as the number of tests they have to perform is slightly higher. Thanks to the Super-Pixel encoding, the last variant of SparseCCL specialized for the LHCb experiment speeds up.

Following the method described in [10], we wrote an AVX-512 version of SparseCCL. The SIMD version of the algorithm uses AVX-512’s scatter and gather instructions to implement the union-find operations. For low densities, the cost of control flow is greater than the gain due to SIMD parallelism, making this SIMD version slower than the scalar one.
This article introduced a new parameterizable connected component labeling and analysis algorithm for sparse images of densities lower than 0.5%, and a specialization of this algorithm for a practical application. We studied the impact of different versions on images of varying densities and granularities and showed that, at low densities our algorithm performed better than state-of-the-art dense CCL algorithms and flood fill algorithm optimized for sparse images.

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REFERENCES

[1] A. A. AbuBaker, R. Qahwaji, S. S. Ipson, and M. S. Saleh. One scan connected component labeling technique. 2007 IEEE International Conference on Signal Processing and Communications, pages 1283–1286, 2007.

[2] A. A. Alves, Jr. et al. The LHCb Detector at the LHC. JINST, 3:S08005, 2008.

[3] T. Bird, T. Britton, O. Callot, V. Coco, P. Collins, T. Evans, T. Head, K. Hennessy, W. Hulsbergen, D. Hynds, P. Jalocha, M. John, T. Ketel, M. Kucharczyk, D. Martinez-Santos, W. Qian, K. Rinnert, H. Schindler, T. Skwarnicki, H. Snoek, P. Tsopeas, and D. Vieira. VP Simulation and Track Reconstruction. Technical Report LHCb-PUB-2013-018. CERN-LHCb-PUB-2013-018, CERN, Geneva, Oct 2013.

[4] F. Boletti, M. Cancilla, L. Baraldi, and C. Grana. Toward reliable experiments on the performance of connected components labeling algorithms. Journal of Real-Time Image Processing (JRTIP), pages 1–16, 2018.

[5] L. Cabaret and L. Lacassagne. What is the world’s fastest connected component labeling algorithm? In IEEE International Workshop on Signal Processing Systems (SiPS), pages 97–102, 2014.

[6] L. Cabaret, L. Lacassagne, and D. Etienoble. Parallel Light Speed Labeling for connected component analysis on multi-core processors. Journal of Real Time Image Processing, pages 1–24, 2016.

[7] S. Gupta, D. Palsetia, M. A. Patwary, A. Agrawal, and A. Choudhary. A new parallel algorithm for two-pass connected component labeling. In Parallel & Distributed Processing Symposium Workshops (IPDPSW), pages 1355–1362. IEEE, 2014.

[8] R. Haralick. Some neighborhood operations. In Real-Time Parallel Computing Image Analysis, pages 11–35. Plenum Press, 1981.

[9] L. He, X. Ren, Q. Gao, X. Zhao, B. Yao, and Y. Chao. The connected-component labeling problem: a review of state-of-the-art algorithms. Pattern Recognition, 70:25–43, 2017.

[10] A. Hennequin, I. Masliah, and L. Lacassagne. Designing efficient simd algorithms for direct connected component labeling. In Proceedings of the 5th Workshop on Programming Models for SIMD/Vector Processing, WPMVP’19, pages 4:1–4:18, New York, NY, USA, 2019. ACM.

[11] A. Hennequin, P. Q. Meunier, L. Lacassagne, and L. Cabaret. A new direct connected component labeling and analysis algorithm for GPUs. In IEEE International Conference on Architectures and Architectures for Signal and Image Processing (DASP), pages 1–6, 2018.

[12] K. Henessy and LHCb VELO Upgrade Collaboration. LHCb VELO upgrade. Nuclear Instruments and Methods in Physics Research A, 845:97–100, Feb 2017.

[13] M. Klaiber, D. Bailey, and S. Simon. A single cycle parallel multi-slice connected components analysis hardware architecture. Journal of Real-Time Image Processing, 2016.

[14] L. Lacassagne, L. Cabaret, F. Hebache, and A. Petreto. A new SIMD iterative connected component labeling algorithm. In ACM Workshop on Programming Models for SIMD/Vector Processing (PvP), pages 1–8, 2016.

[15] LHCb Collaboration. LHCb VELO Upgrade Technical Design Report. Technical Report CERN-LHCC-2013-021. LHCb-TDR-013, CERN, Nov 2013.

[16] M. Niknam, P. Thulasiraman, and S. Camorlinga. A parallel algorithm for connected component labeling of gray-scale images on homogeneous multicore architectures. Journal of Physics - High Performance Computing Symposium (HPCS), 2010.

[17] M. Patwary, J. Blair, and F. Manne. Experiments on union-find algorithms for the disjoint-set data structure. In L. - Springer, editor, International symposium on experimental algorithms (SEA), pages 411–423, 2010.

[18] D. P. Playne and K. Hawick. A new algorithm for parallel connected-component labeling on GPUs. IEEE Transactions on Parallel and Distributed Systems, 2018.

[19] T. Poikela, M. D. Gaspari, J. Plosila, T. Westerlund, R. Ballabriga, J. Buytaert, M. Campbell, X. Llopart, K. Wyllie, V. Gromov, M. van Beuzekom, and V. Zikvick. VeloPix: the pixel ASIC for the LHCb upgrade. Journal of Instrumentation, 10(01):C01057–C01057, Jan 2015.

[20] A. Rosenfeld and J. Platz. Sequential operator in digital pictures processing. Journal of ACM, 13:4:471–494, 1966.

[21] L. Vincent and P. Soille. Watersheds in digital spaces: An efficient algorithm based on immersion simulations. IEEE Trans. Pattern Anal. Mach. Intell., 13(6):583–598, June 1991.

[22] F. Wende and T. Steinke. Swendsen-wang multi-cluster algorithm for the 2d/3d Ising Model on Xeon Phi and GPU. In ACM, editor, International Conference on High Performance Computing (SuperComputing), pages 1–12, 2013.