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Model Predictive Direct Power Control of Doubly Fed Induction Generator with Dead-Time Compensation

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Abstract: The paper presents the control of a doubly fed induction generator connected with a three-level neutral point clamped inverter with compensation of dead-time effects. Model Predictive Direct Power Control is synthesized using a dynamical model of the doubly fed induction generator and three-level neutral point clamped inverter. The principle of the proposed control scheme is to use the dynamical model to compute predictions of the future values of the stator flux, rotor current and DC-link capacitor voltages for all possible configurations of voltage vectors. However, the dead-time to avoid the short circuit in the inverter also causes the modeling errors. Thus, by taking into account the dead-time in the model, it is possible compensate the dead-time effect of the switching devices. The active and reactive powers can be estimated based on the stator flux and the rotor current. The cost function considers the error between the active, reactive powers and their references, balance the DC-link capacitor voltage and reduce the switching frequency and common-mode voltage. The optimal switching state that minimizes the cost function is selected and applied to the inverter. The simulation results were carried out with Matlab under different conditions of wind speed and verifying the effectiveness of the proposed method.

Keywords: Power Plants and Power Systems, Control of renewable energy resources, Application of power electronics, Intelligent control of power systems and Optimal operation and control of power systems.

1. INTRODUCTION

Nowadays, to the best of the author’s knowledge, the global wind energy capacity has increased rapidly and became one of the fastest developing renewable technologies. Most of the major wind turbine manufactures are developing larger wind turbines in the 1.5-6 MW range. For such an application, doubly fed induction generator (DFIG) represents an attractive solution due to its advantages: for instance, it allows the power converter to deal with approximately 30% of the generator power, reduces converter cost and power lost (Fig. 1) (Abad et al. (2011)). By using this configuration, it is possible to allow both bidirectional active and reactive power flow from the rotor side to grid through the rotor side converter (RSC) and grid side converter (GSC). Furthermore, from the technological point of view, the three-level neutral point-clamped (3L-NPC) inverter structure represents a good solution for high power due to its advantages: reduction of the total harmonic distortion (THD) and increasing the capacity of the inverter due to a decreased voltage applied to each component.

Several approaches have been proposed to control DFIG for wind energy generation. Most of the existing control methods use the classical vector control based on the orientation of the flux stator (stator flux oriented) or stator voltage (stator voltage oriented). This method allows controlling independently the electromagnetic torque or active power and reactive power by means the components of the rotor current (Abad et al. (2011)). However, one drawback of this method is that its performance depends on accurate machine parameters such as stator and rotor
resistance and mutual inductance. Next, another drawback of using PI controller is the necessity of tuning of the gains in the whole operating range of wind speed. In addition, a complex modulation technique along with the DC-link capacitor voltages balancing is required to apply these techniques to 3L-NPC inverter.

Recently, direct torque control (DTC) or direct power control (DPC) (Xu and Cartwright (2006)) have been proposed to improve the controller performance. These methods used the hysteresis control and the inverter switching states, selected from a look-up table (LUT) based on the errors between the reference and estimated values, and rotor or stator flux position. Therefore, these methods do not require the current control loops and space vector modulation. Nevertheless, the drawback of LUT is that it has large active and reactive power ripple and switching frequency variation. In addition, a high-sampling frequency is used for DTC/DPC to guarantee acceptable steady-state and dynamic performances. To overcome this problem several techniques have been developed such as using DPC with space vector modulation (SVM) (Kazemi et al. (2010)), predictive control strategy (Sayritupac et al. (2015)) and model predictive power control (MPDP). Moreover, the dead-time effect will distort the output voltage, neutral-point voltage and current. Thus, a dead-time compensation is necessary to be added into the control scheme (Irmura et al. (2011)) based on stator fluxed orientation. The stator capacitor voltages balancing is required to apply these techniques to 3L-NPC inverter.

The present paper proposes the model predictive direct power control (MPDPC) to control the active and reactive power for DFIG connected to a 3L-NPC inverter while maintaining the balance between the DC link capacitor voltage, reducing the switching frequency and the common-mode voltage. These objectives are accomplished through the cost function in a predictive control strategy. In this paper, we focus on the modelling of errors caused by dead-time induced by the physical switching mechanism. With this approach, the model to predict the inverter output voltage and neutral-point voltage takes into account the dead-time take of the converter to compensate its effects. No current loops are considered and the inverter switches are directly obtained from the cost function minimization. This control allows improving the quality of the power regulation and minimizing the switching losses. In order to reduce the computational effort, a control horizon of two is used for the prediction, where only combination of inputs having a difference of one switch in the inverter is considered.

The remaining paper is organized as follow: Section 2 presents the mathematical model of direct power control with dead-time compensation for a DFIG connected to a 3L-NPC. Next, section 3 details the proposed control method. In section 4, simulation results are represented and analyzed and, finally, section 5 draws the conclusion.

2. MODEL OF DFIG CONNECTED 3L-NPC INVERTER

The doubly fed induction generator (DFIG) can be modelled by the equivalent circuit in dq coordinate (see Abad et al. (2011)) based on stator fluxed orientation. The stator and rotor voltage vectors can be described from Fig. 2 as follows:

$$\dot{u}_{s}^{dq} = R_{s}s^{dq} + \frac{d\psi_{s}^{dq}}{dt} + j\omega_{s}\psi_{s}^{dq}$$  \hspace{1cm} (1)

$$\dot{u}_{r}^{dq} = R_{r}r^{dq} + \frac{d\psi_{r}^{dq}}{dt} + j\omega_{r}\psi_{r}^{dq}.$$  \hspace{1cm} (2)

where $u_{s}^{dq}, i_{s}^{dq}, \psi_{s}^{dq}$ are the stator voltage, current and flux vector in dq reference frame while $u_{r}^{dq}, i_{r}^{dq}, \psi_{r}^{dq}$ are the rotor counterparts with respect to the stator. $R_{s}, R_{r}$ are the stator resistance and the rotor resistance referred to the stator, $\omega_{s}, \omega_{r}$ are the synchronous speed of stator flux and angular rotor speed (rad/s), $\omega_{r} = \omega_{s} - \omega_{m} = s\omega_{s}$ is the rotor angular frequency, with the coefficient $s$ denoting the slip.

![Fig. 2. Equivalent circuit of the DFIG in dq reference frame](image)

The relationship between fluxes and currents is:

$$\psi_{s}^{dq} = L_{s}s^{dq} + L_{m}r^{dq},$$  \hspace{1cm} (3)

$$\psi_{r}^{dq} = L_{r}r^{dq} + L_{m}s^{dq},$$  \hspace{1cm} (4)

with $L_{s}$ and $L_{r}$ are the stator and rotor inductances and $L_{m}$ the magnetizing inductance.

By substituting the stator current $i_{s}^{dq}$ from equation (3) into equation (1), the stator flux dynamics can be expressed as follows:

$$\frac{d\psi_{s}^{dq}}{dt} = \frac{1}{T_{s}} \left(- (1 + j\omega_{s}T_{s})\psi_{s}^{dq} + L_{m}r^{dq} + T_{s}u_{s}^{dq} \right),$$  \hspace{1cm} (5)

where $T_{s} = L_{s}/R_{s}$ is the time constant of the stator.

By substituting equation (5) and the rotor flux $\psi_{r}^{dq}$ from equations (3) and (4) into equation (2), the dynamics of the rotor currents are represented as follows:

$$\frac{di_{r}^{dq}}{dt} = \frac{1}{\sigma L_{r}} \left( \psi_{r}^{dq} \left( \frac{L_{m}}{L_{r}T_{s}} + j\omega_{r} \frac{L_{m}}{L_{s}} \right) + u_{r}^{dq} \right)$$  \hspace{1cm} (6)

$$- \frac{1}{\sigma L_{r}} \left( \psi_{r}^{dq} \left( R_{r} + j\omega_{r}\sigma L_{r} \right) + \frac{L_{m}}{L_{s}}u_{s}^{dq} \right),$$

where $R_{r} = R_{r} + L_{m}/L_{r}T_{s}, \sigma = 1 - L_{m}^2/L_{s}L_{r}$ is the leakage coefficient.

Based on equations (5) and (6), the dynamical model of DFIG can be expressed in matrix form as below:

$$\dot{x} = A(\omega_{s}, \omega_{m})x + Bu,$$

where $x = \begin{bmatrix} \psi_{ds} \\ \psi_{qs} \\ i_{dr} \\ i_{qr} \end{bmatrix}$, $u = \begin{bmatrix} u_{ds} \\ u_{qs} \\ u_{dr} \\ u_{qr} \end{bmatrix}$,
The inverter output voltage \( U \) in rotor reference (DFIG), is also the output voltage of 3L-NPC inverter shown in Fig. 4. The real output voltage is reduced with the mean value of one phase period \( T_s \) (Irnura et al. (2012)). The real output voltage of the 3L-NPC inverter, considering a dead-time in the modeling is necessary to prevent the simultaneous conduction of two switching devices in each leg of the inverter. The dead-time causes output current waveform distortion and increases the THD, especially with high switching frequency (Kuznetsov et al. (2015); Irnura et al. (2012)). The voltage vector during the dead-time is determined according to the state between previous switching state and present switching state and the sign of the phase current. For example, in switching state \([0]\), switches \( S_{11} \) and \( S_{12} \) are turned “on”. The clamping diode \( D_{Z11} \) is turned “on” by the positive load current \( i_{ro} > 0 \). For ideal case without dead-time, the switch \( S_{12} \) is turned “off” and \( S_{12} \) is turned “on” simultaneously, so that the output voltage changes from zero to \( U_{dc}/2 \) at the same time. If the dead-time is considered, the real gate signals and output voltage approximation are shown in Fig. 5(a). During the dead-time interval, \( S_{12} \) is being turned “off” and the path of \( i_{ro} \) remains unchanged. Thus, the output actual voltage is clamped is zero. After \( S_{12} \) is turned “on” (switching state \([P]\)), the load current is commutated from \( D_{Z11} \) to \( S_{12} \) and the output voltage switches to \( U_{dc}/2 \) (Fig. 6(a)). Hence, the real output voltage is reduced with the mean value \( U_{m} \) and shown with the rectangular pattern in Fig. 5(a). In brief, the influence of dead-time on output voltage can be illustrated in Fig. 5. Thus, in order to compensate the dead-time, the real voltage applied during a sampling period \( T_s \) can be represented by a voltage \( U_{dc} \) during the dead-time \( T_d \) and the predicted voltage vector with the rest time \( T_s - T_d \) (Irnura et al. (2012)). The real output voltage of one phase \( u_{x \text{rel}} \) can be expressed as follows:

(i) \( (S_x(k-1) = 0) \) & \( (S_x(k) = 1) \),
(ii) \( (S_x(k-1) = -1) \) & \( (S_x(k) = 0) \),
Fig. 5. The effect of dead-time on inverter output voltage

(iii) \( (S_x(k - 1) = 1) \) & \( (S_x(k) = 0) \),
(iv) \( (S_x(k - 1) = 0) \) & \( (S_x(k) = -1) \),

\[
u_{xx,real}(k) = \begin{cases} -U_{dt} + u_{xx}(k) & \text{if satisfy (sign}(i_{xa}) > 0) \text{ & } ((i) \lor (ii)) \text{,} \\ U_{dt} + u_{xx}(k) & \text{if satisfy (sign}(i_{xa}) < 0) \text{ & } ((iii) \lor (iv)) \text{,} \\ u_{xx}(k) & \text{otherwise,} \end{cases} \tag{17}\]

where \( u_{xx} \), \( i_{xa} \) are the inverter output voltage and rotor current of one phase \( x = a, b, c \), \( U_{dt} = \frac{t_d}{T_{sp}} \frac{U_{dc}}{2} \).

Based on the configuration in Fig. 4, the dynamic of neutral-point voltage \( (Z) \) is obtained based on the rotor currents and the switching states of 3L-NPC inverter (Ngo et al. (2016)):

\[
du_z(t) = -\frac{1}{2C}i_{dc1} \tag{18}\]

Furthermore, the dead-time can also affect the neutral-point voltage because of the different neutral-point current as shown in Fig. 6 (Sprenger et al. (2013)). Summarizing, the neutral-point current during the dead-time only exists when there are transitions between switching states \([O]\) and \([P]\) with positive load current or between switching states \([N]\) and \([O]\) with negative load current or remain the switching state \([O]\). Thus, the neutral-point current of one phase during the dead-time can be described:

(v) \( \text{sign}(i_{xa}) > 0 \) & \( ((i) \lor (ii)) \text{,} \)
(vi) \( \text{sign}(i_{xa}) < 0 \) & \( ((iii) \lor (iv)) \text{,} \)
(vii) \( (S_x(k - 1) = 0) \) & \( (S_x(k) = 0) \),

\[
i_{dc1x} = \begin{cases} \frac{t_d}{T_{sp}}i_{xa} & \text{if satisfy } ((v) \lor (vi) \lor (vii)) \text{,} \\ 0 & \text{otherwise,} \end{cases} \tag{19}\]

where \( i_{dc1x} \) is the neutral-point current during the dead-time of one phase \( x = a, b, c \).
Similarly, the real neutral-point current during a sampling period is expressed by the neutral-point current during the dead-time $T_d$ and the predicted current with the rest time $T_{sp} - T_d$:

$$i_{dc1\text{-real}} = i_{dc1a\cdot dt} + i_{dc1b\cdot dt} + i_{dc1c\cdot dt} + \frac{T_{sp} - T_d}{T_{sp}} i_{dc1}. \quad (20)$$

By using the equation (18), the neutral-point voltage for compensate the dead-time can be rewritten:

$$\frac{du_{dc1\text{-real}}}{dt} = -\frac{1}{2C} i_{dc1\text{-real}}. \quad (21)$$

In an inverter driven system, the common-mode voltage ($u_{cm}$) is defined as the voltage between the rotor neutral (N) and the neutral-point voltage ($V_d$). Thus, the common-mode voltage (CMV) can be expressed as follows (Rodriguez and Cortes (2012)):

$$u_{cm} = u_{AZ} + u_{BZ} + u_{CZ} \quad (22)$$

Therefore, a compensation for the dead-time can be established for MPDPC. By using modified voltage vector in equation (17), (21) and (22), we can predict the power taking into account the dead-time voltage at switching state.

3. MODEL PREDICTIVE DIRECT POWER CONTROL APPLIED TO DFIG CONNECTED 3L-NPC

The aim of the predictive direct power control scheme is to minimize the error between the predicted active and reactive powers and their reference values, to maintain voltage balance of the capacitor and to reduce the switching frequency and common-mode voltage. In order to achieve these objectives, the cost function for the DFIG connected 3L-NPC inverter with two-step prediction can be expressed as follows (Rodriguez and Cortes (2012)):

$$g = |P_s^*(k + 2) - P_{sp}^s(k + 2)| + |Q_s^*(k + 2) - Q_{sp}^s(k + 2)| + \lambda_{dc}|u_{dc}^2(k + 2)| + \lambda_{cm}|u_{cm}| + \lambda_n n_c. \quad (23)$$

where $\lambda_{dc}$, $\lambda_n$ and $\lambda_{cm}$ are the weighting factors of the capacitor voltage balancing, the reduction of commutation and CMV. $n_c$ penalizes the number of switching changes when the switching state $S(k)$ is applied compared with previous state $S(k - 1)$. It can be expressed as:

$$n_c = |S_a(k) - S_a(k - 1)| + |S_b(k) - S_b(k - 1)| + |S_c(k) - S_c(k - 1)|. \quad (24)$$

In the real-time implementation of the control scheme, the calculation time of the control law will induce some sample time delay in the actuation (Sun and Wang (2016); Errouissi et al. (2016)). Several studies to address the delay compensation, but this particular aspect is not the focus of this paper. A simple solution to compensate the computational delay is to compute at time instant $k$ the cost function corresponding to time instant $k + 1$ using an estimation of the state at time $k + 1$ and then the optimal switching state is applied at time $k + 1$. In this paper, we used the method proposed by Ngo et al. (2016) to reduce the computational effort and allow a straightforward implementation.

The equation (7) can be discretized considering $T_{sp}$ as a sampling period and $k$ as the sampling time by using zero-order hold (ZOH) with no delay:

$$x(k + 1) = A_d x(k) + B_d u(k) \quad (25)$$

where

$$A_d = e^{A T_{sp}}, B_d = \int_0^{T_{sp}} e^{A (T_{sp} - \tau)} B d\tau \approx B T_{sp}. \quad (26)$$

Since the active and reactive powers references are DC quantities, to reduce the computational time and the oscillations in the reference powers when a sudden change occurs, the extrapolation can be simplified as follows:

$$P_s^*(k + 2) = P_s^*(k) ; Q_s^*(k + 2) = Q_s^*(k). \quad (27)$$

Finally, the objective of proposed predictive control can be obtained by evaluating some appropriate cost function for all considered input combinations (see algorithm 1).

**Algorithm 1** Algorithm of model predictive direct power control for DFIG connected 3L-NPC inverter

1. Measure $i_s(k)$, $i_r(k)$, $u_{dc}(k)$, $u_s(k)$, $\omega_m$ and $U_{dc}$; Read the reference values $P_{sp}^s(k)$ and $Q_{sp}^s(k)$
2. Estimation of stator flux $\psi_{ds}(k)$; Extrapolation of references $P_{sp}^s(k + 2)$, $Q_{sp}^s(k + 2)$ and $u_{dc}^k(k + 1)$
3. %comment: $x_{opt}$, $g_{opt}$ are the optimal values of the switching states and cost function$\%$
4. Initialize optimal values: $x_{opt}$, $g_{opt}$
5. Predict stator flux: $\psi_{ds}^k(k + 1)$

**for** $i = 1$ to $27$ **do**
6. Compute predictions with dead-time compensation: $i_{ds}^p(k + 1)$, $i_{qr}^p(k + 1)$ and $u_{dc}^k(k + 1)$
7. Estimate the values: $n_e$ and $n_{cm}$
8. Predict corresponding switching transitions$\%$
9. **for** $j = 1$ to $m$ **do**
10. Predict: $i_{ds}^p(k + 2)$, $i_{qr}^p(k + 2)$, $\psi_{ds}^p(k + 2)$ and $u_{dc}^k(k + 2)$
11. Estimation of power: $P_{sp}^s(k + 2)$ and $Q_{sp}^s(k + 2)$
12. Compute the cost function $g$
13. **if** $g < g_{opt}$ **then**
14. $g_{opt} = g; x_{opt} = i$
15. **end if**
16. **end for**
17. **end for**

Store the present value of $x_{opt}$ and apply $S_a, S_b, S_c$ considering the dead-time.

4. SIMULATION RESULTS

In order to validate the effectiveness of the model predictive direct power control (MPDPC) strategy with dead-time compensation for DFIG under different modes of speed operation, the whole control scheme has been simulated using MATLAB/Simulink and the SimPowerSystems toolbox with the parameters as indicated in Table A.1.

With the aim to evaluate the steady state performance, the mean absolute percentage error (MAPE) is used. This quantity can be expressed as follows:

$$MAPE = \frac{1}{n} \sum_{i=1}^{n} \left| \frac{y_i^* - y_i}{y_i^*} \right|. \quad (28)$$

where $y_i^*$ is the reference vector and $y_i$ is the measurement vector.

On the other hand, the average switching frequency per semiconductor ($f_{sw}$) is calculated according to the
Following expression proposed in (Rodriguez and Cortes (2012)): 

\[
\overline{f_{sw}} = \frac{\sum_{x=a,b,c} f_{sw1x} + f_{sw2x}}{6},
\] 

(29)

where \( f_{sw1x} \) and \( f_{sw2x} \) are the switching frequencies of each upper switch.

With the purpose of control the power factor (PF), the reactive power reference is given by:

\[
Q_{s,ref} = P_{s,ref} \sqrt{1 - PF^2}. 
\] 

(30)

In our comparison, the DFIG was assumed to be in speed control, i.e., the rotor speed is set externally, and has slow changes because of the large inertia of the wind turbine. In order to observe the dynamics response of the DFIG, various active and reactive power steps with rotor speed varying from 1200 to 1800 rpm (Fig. 7) were carried out. The active power reference is changed from -2 MW to -1 MW at 1.5 s and from -1 MW to -1.5 MW at 2 s (Fig. 8(a)). While the reactive power reference is changed from -1.24 MVar to 0.62 MVar at 1.5 s and from 0.62 MVar to 0 Var at 2 s, corresponding with the change of power factor from 1 to a leading (0.85) or lagging (-0.85). When the rotor speed of the generator is greater than synchronous speed, the slip is negative \( (s = -0.2) \), thus, the rotor power \( P_r \) will be transferred from the generator rotor to the grid through the rotor converters of the DFIG, whereas the RSC operates as a rectifier and the GSC as an inverter. The powers delivered to the grid \( P_g \) and \( Q_g \) which are the sum of the stator and rotor powers are illustrated in Figs. 8(a) and 8(b). On the contrary, the slip is positive \( (s = 0.2) \) with sub-synchronous speed. This positive slip means that the rotor power \( P_r \) will be positive and will be received from the grid through the converters in which the RSC operates as an inverter and the GSC as a rectifier (Fig. 8). In addition, Figs. 8(a) and 8(b) indicate that the active power is tracking their references with fast dynamics and without affecting the reactive power. The mean absolute percentage error of active and reactive power for uncompensated case are 1.68% and 2.25%, whereas, for the proposed method they are 1.65%, and 2.22%, respectively.

For further verification of the dead-time compensation method, the spectrum of the stator current is analyzed and compared for the uncompensated and compensated case in Fig. 9. For the uncompensated case in Fig. 9(a), the amplitude of the order harmonic is larger, which is caused by the dead-time effect. The THD for the uncompensated stator current is around 4.3%. But for the compensated current in Fig. 9(b), the order harmonic components have been largely reduced, especially the 21th, 22th and 23th order harmonic. Also the THD for the stator current is reduced from 4.3 % to 3.03 % which is under the 5% limit required by the IEEE 519 standards.

5. CONCLUSIONS

In this paper, a new direct power control with dead-time compensation method for DFIG system has been proposed to control the active and reactive powers directly. First, the mathematical model including the dead-time errors is required by the IEEE 519 standards. Then the cost function, which contains the power errors, the capacitor voltage balancing and the mathematical model including the dead-time errors is proposed. In order to verify the performance of control scheme, there is a
Weighting factor of the voltage balancing

Dead-time of IGBT

Each phase nominal stator current

2.9 [mΩ]

Rotor resistance

2.5 [mH]

Value

Rated stator three phase active power

16000 [W]

Line to line nominal stator voltage

1200 [V]

Forward voltage of IGBT and diode

0.8; 0.1 [V]

Line to line nominal rotor voltage

35000 [V]

DC link capacitor

690 [V]

Synchronous speed

1500 [rpm]

Switching frequency reduction factor

5 [Hz]

The number pairs of poles

1760 [A]

DC link voltage

2.6 [mΩ]

Stator resistance

2070 [V]

Mutual inductance

5037–5042.

Forward voltage of IGBT and diode

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Appendix A. PARAMETERS FOR DFIG, CONTROLLER AND SWITCHING DEVICES

| Parameter | Value | Description |
|-----------|-------|-------------|
| \( F_{rated} \) | 2 [MW] | Rated stator three phase active power |
| \( U_{s\text{-}rated} \) | 690 [V] | Line to line nominal stator voltage |
| \( U_{r\text{-}rated} \) | 2070 [V] | Line to line nominal rotor voltage |
| \( I_{s\text{-}rated} \) | 1760 [A] | Each phase nominal stator current |
| \( n_{s\text{-}rated} \) | 1500 [rpm] | Synchronous speed |
| \( p \) | 2 | The number pairs of poles |
| \( R_s \) | 2.0 [mΩ] | Stator resistance |
| \( R_r \) | 2.9 [mΩ] | Rotor resistance |
| \( L_{ss} \) | 87 [mH] | Stator leakage inductance |
| \( L_{sr} \) | 87 [mH] | Rotor leakage inductance |
| \( L_m \) | 2.5 [mH] | Mutual inductance |
| \( J \) | 0.314 [kg.m²] | Moment of inertia |
| \( f_s \) | 50 [Hz] | Frequency of the grid |
| \( f_{sp} \) | 10 [kHz] | Sampling frequency of MPDPC |
| \( U_{dc} \) | 1200 [V] | DC link voltage |
| \( C \) | 16000 [μF] | DC link capacitor |
| \( t_d \) | 5 [μs] | Dead-time of IGBT |
| \( V_{f}, V_{fd} \) | 0.8; 0.1 [V] | Forward voltage of IGBT and diode |
| \( \lambda_{dc} \) | 200 | Weighting factor of the voltage balancing |
| \( \lambda_{n} \) | 35000 | Switching frequency reduction factor |
| \( \lambda_{cm} \) | 200 | Weighting factor of the reduction of CMV |

Fig. 9. Stator current spectrum with and without compensation comparison between the case without compensation and the proposed method. The simulation results show that this method can successfully track the active and reactive powers, maintain their balanced capacitor voltages, reduce the switching frequency and common-mode voltage. Furthermore, by using the proposed method we can reduce the voltage error due to switching dead-time while maintaining an acceptable quality of current and power. The linear PI controllers for the power, current loops and the modulation block are further eliminated. In addition, by maintaining the device switching frequency below 1 kHz, the proposed method is an interesting alternative to control the DFIG connected 3L-NPC inverter for megawatt range wind power applications.

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