Abstract — Zirconia nanotubular layer with an outer tube diameter ≠ 25 nm was synthesized by potentiostatic anodization. The Zr/ZrO₂-NT/Au memristive structure is fabricated using stencil mask and magnetron sputtering techniques. Current-voltage characteristics are measured in full cycles of resistive switching with varying parameters of the applied harmonic voltage. An equivalent circuit with unidirectional electrical conductivity for the studied structure is proposed. Estimates of the electrical resistance of memristors in high- and intermediate resistivity states are performed. The high synaptic plasticity of memristors based on the Zr/ZrO₂-NT/Au structure is shown.

Keywords— memristor, zirconia nanotubes, anodization, resistive switching

I. INTRODUCTION

Developing of hybrid schemes that have analogous-digital architecture combined with crossbar structure is one of the promising directions for fabrication of neural chips, neurocomputing systems and artificial neural networks (ANNs) nowadays [1, 2]. These schemes have neurons based on common integral CMOS transistors. There, metallic conducting structures are axons and dendrites, wherein, two-terminal memristive elements of commutation matrix serve as synapses. Memristors are formed at crossing points of conductors which connect pre- and postsynaptic neurons.

Biological systems have synaptic weight of connection between neurons regulated by ionic flow. It is many known values of synaptic weights and possibility to change them that enables learning and functioning in biological systems [3, 4]. It can be noted that memristor is a two-terminal electron device with synaptic plasticity or ability to change synaptic weight, since its conductivity depends on total charge flowing [2].

As a rule, the connections between neurons in biological systems and, consequently, in the existing variety of ANN architectures are unidirectional in terms of excitation propagation [6]. Thus, fabrication and studying of memristive structures of metal-insulator(semiconductor)-metal (MIM structures) with unilateral electrical conductivity appear to be promising directions.

It is known that memristive behavior is caused by mobility of cation and anion vacancies, etc. in insulator layer. Its thickness and presence of defects determine electrical resistance in low- (LRS), high- (HRS) and intermediate resistivity states (IRS) [1, 3]. There is an effect of reversible resistance switching for several MIM structures based on transition metals (TiO₂, ZrO₂, HfO₂, etc.) obtained by anodizing or electrochemical oxidation [2, 7–10].

Therefore, the aim of this work was to synthesize, characterize and study static current-voltage (CV) characteristics of layered structure with unidirectional electrical conductivity based on anodic zirconia.

II. EXPERIMENTAL SECTION

A. Fabrication of Zr/ZrO₂-NT/Au structure

The Zr/ZrO₂-NT/Au memristor structure was fabricated by anodizing of Zr metal and magnetron sputtering of Au top layer on the surface of the oxide layer.

The Zr metal foil of 120 µm thick was used to synthesize the ZrO₂-NT layer by anodization. The Zr foil was pre-washed with acetone, etched in a solution of HF:HNO₃:H₂O₂ = 1:6:20 acids, intensively washed with distilled water and dried in air. Synthesis of the oxide layer was carried out in a two-electrode cell at a constant voltage of 20 V for 15 min, where Zr foil was an anode and a steel plate as a cathode. The electrolyte was an ethylene glycol solution containing 5 wt. % H₂O and 1 wt.% NH₄F. All chemical reagents were analytical grade purity.

Gold contacts were sputtered using a stencil mask of 100 holes with a diameter of 1 mm by magnetron unit Q150T ES Quorum Technologies on the surface of the obtained Zr/ZrO₂-NT sample. Deposition was carried out for 500 s, as a result of which the thickness of the Au contact was 50 nm. Thus, 100 samples of memristors of Zr/ZrO₂-NT/Au sandwich-structure were formed.

B. Characterization of Zr/ZrO₂-NT/Au structure

The morphology of the synthesized structures was study with a scanning electron microscope SIGMA VP Carl Zeiss.

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The current-voltage measurements in the static operation mode were carried out with the original installation [11]. The experimental setup includes a Cascade Microtech MPS150 micro-probing station and a modular device Source Measure Unit (SMU) PXI-4130 from National Instruments. The Zr foil was grounded, and the harmonic signal \( U(t) = U_0 + U_m \sin(2\pi f t) \) with frequency of \( f = 0.01 \) Hz was applied to the Au contacts. The amplitude of the applied voltage \( U_m \) and the bias voltage \( U_0 \) were varied to change \( U(t) \) in the range from \(-8 \) V to \( 9 \) V. Automation of the procedure for measuring and storing experimental data was carried out using VI “CVC” virtual instrument in the graphical design platform LabVIEW [11].

III. RESULT AND DISCUSSIONS

A. Characterization of \( \text{ZrO}_2\)-NT layer

Fig. 1 shows a typical SEM image of the surface of a synthesized zirconia layer (a) and its cross-section (b). As a result of electrochemical oxidation of zirconium foil a self-ordered array of zirconia nanotubes with an external diameter of \( \approx 25 \) nm and a length of \( \approx 3 \) \( \mu \)m was formed. In our earlier study was shown that as-grown layer of \( \text{ZrO}_2\)-NT contains 90% of tetragonal and 10% of monoclinic phases [12].

B. Current-voltage measurements of \( \text{Zr/ZrO}_2\)-NT/Au structure

Fig. 2 shows the time dependence of the current through the memristor and the applied harmonic voltage \( U(t) \) \( (U_0 = 2 \) V, \( U_m = 6 \) V) for 4 measurement periods. It is seen when \( U(t) < 0 \) V the reverse current through the memristor is \( I_r < 0.05 \) mA. The current jumps in the region of the maximum values of the applied voltage are observed for \( U(t) > 0 \) V. This behavior is typical for electronic devices with unidirectional conductivity, i.e. diodes. It is known that the work function of metal contacts is \( W_{\text{Au}} = 5.1 \) eV, \( W_{\text{Zr}} = 4.05 \) eV [13] and \( W < 4.6 \) eV for stoichiometric \( \text{ZrO}_2 \) [14]. Comparing the \( W \) values for the materials of the investigated MIM structure, we can conclude that a Schottky barrier is formed at the Au/\( \text{ZrO}_2\)-NT interface, and the Zr/\( \text{ZrO}_2\)-NT contact is ohmic.

From Fig. 2 it is also seen that the maximum value of \( I \) increased monotonically in the range of \( 73 - 150 \) mA for each subsequent period of the \( U \) change. This fact indicates about a 2-fold decrease of the electrical resistance of the studied structure under the given experimental conditions. The Fig. 3a shows the CV curves corresponding to the experimental data presented in Fig. 2. It is seen that for \( U > 0 \) V a typical CV loops similar to MIM structures with bipolar resistive switching are registered [1, 2]. Otherwise, for \( U < 0 \) V a typical branch of CV curve representative of a diode under reverse voltage is recorded. In the inset to Fig. 3a, the connection scheme for investigated MIM structure to the measuring equipment is shown.

Based on the above mentioned the inset on Fig. 3b shows the equivalent circuit for the studied Zr/\( \text{ZrO}_2\)-NT/Au sandwich-structure. The electrical circuit consists of a series connection of a \( \eta \)D Schottky diode and a \( \eta \) memristor.
certain experimental conditions. Therefore, $R$ evaluation in HRS and IRS were conducted under $U = 5$ V. Values of $R_{\text{HRS}} = 13.0 \pm 0.5$ kOhm and $R_{\text{IRS}}$ from $\approx 5.75$ to $\approx 0.35$ kOhm were calculated according to CV characteristic of studied structure. It was substantiated that there are significant range of synaptic weights variation and high synaptic plasticity in studied samples due to $R_{\text{HRS}}/R_{\text{IRS}}$ resistance ratio varying from $\approx 2$ to $\approx 37$.

IV. CONCLUSIONS

$\text{ZrO}_2$-NT nanotubular layer was synthesized by zirconium foil anodizing in 5 wt. $\%$ $\text{H}_2\text{O}$ and 1 wt. $\%$ $\text{NH}_4\text{F}$ ethylene glycol solution under constant 20 V voltage for 15 min. Scanning electron microscopy analysis showed that oxide layer had $\approx 3$ $\mu$m thickness and consisted of nanotubes with $\approx 25$ nm diameter.

$\text{Zr/ZrO}_2$-NT/Au memristive structures with 1 mm diameter gold contacts were fabricated by magnetron sputtering technique using the stencil mask. CV characteristics of full resistance switching cycles under varied applied harmonic voltage parameters were studied. Equivalent electrical circuit with unidirectional conductivity was proposed for MIM structure consisting of Schottky diode and memristor series connection according to experimental dependencies analysis. Values of $R_{\text{HRS}} = 13.0 \pm 0.5$ kOhm and $R_{\text{IRS}} = 5.75 – 0.35$ kOhm electrical resistivity were calculated for memristors in HRS and IRS. It was found that investigated structures had high synaptic plasticity according to $R_{\text{HRS}}/R_{\text{IRS}} = 2 \div 37$ ratio range. The study showed prospects for use of $\text{Zr/ZrO}_2$-NT/Au based memristor with unidirectional electrical conductivity for solid state modeling of synapses in artificial neural systems.

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Fig. 4 shows dependencies of electrical resistance $R$ versus $U(t)$ voltage applied to $\text{Zr/ZrO}_2$-NT/Au structure for straight branches of CV characteristics measured for memristors in different intermediate state. It is important to note that considerable $R$ values fluctuations are presented for $U < 5$ V. The fact can be attributed to current registration accuracy for

So, it can be stated that electrical conductivity of oxide layer is changed under harmonic $U(t)$ voltage applied to studied structure due to movement of intrinsic defects relative to their original location. Pointed out defects positions correspond to several IRSs in memristors (see Fig. 3a) and responsible for their synaptic weight. These researches showed that maximum backward voltage of studied MIM structure was $U_{\text{BB}} > 8$ V. Thus, a series of measurements for memristors under $U_{\text{mm}} = 8$ V were carried out in order to transfer them to nominal HRS (IRS with the highest resistance for certain experimental conditions) with subsequent switching to IRS. Fig. 3b shows eleven full resistivity switching cycles for the conditions. It can be derived from CV curves that backward voltage decrease to $U = -8$ V causes reverse current rise to $I_S \leq 0.4$ mA. A reproduction of CV characteristic branches in HRS and monotone increase of straight current through structure in IRSs for every consecutive measurement cycle are registered for $U > 0$ V.

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