Optimal Integration of Distributed Generation in Distribution System: A Case Study of Sallaghari Feeder from Thimi Switching Station, Bhaktapur, Nepal

Basant Pancha1,2, Rajendra Shrestha1,3, Ajay Kumar Jha1,4
1Department of Mechanical Engineering, Pulchowk Campus, IOE, TU, Lalitpur, Nepal
Corresponding author: 2basanta.pancha@gmail.com, 3rsfluid@hotmail.com, 4akjha@ioe.edu.np

Received: Nov 2, 2018 Revised: Jan 3, 2019 Accepted: Jan 5, 2019

Abstract: The modern power distribution network is constantly being faced with an ever-growing load demand resulting into increased burden and reduced voltage, which leads to find alternative source of energy to meet it. In Nepal, the electricity supply is based on hydropower primarily, which are situated very far from load centers and hence the generated power are to be transmitted through transmission and distributed system. Among the systems, radial distribution system is popular because of low cost and simple design, but it has power quality issues like low voltage profile and higher loss. In response to the problem of increased load demand, efforts have been made to decentralize this infrastructure through the use of distributed generators. However, the improper sizing and placement of DG unit may lead to higher power loss and power instability. The optimization problem of DG unit placement and its capacity determination were performed in this research. The study has been carried out for Thimi-Sallaghari Feeder and this feeder has low voltage profile and higher power loss. The IEEE 33 bus test system was examined as a test case to demonstrate the effectiveness of the proposed approach. The study has been carried out in MATLAB using “Backward and Forward Sweep Method” for load flow analysis and Genetic Algorithm for optimization. The number of DG unit of different size integrated was varied from one to ten. The result of this study showed that the voltage at minimum voltage node, maximum active and reactive loss reduction of Thimi-Sallaghari feeder have been improved by 3.69% (from 0.942 p.u. to 0.976 p.u), 75.88 % and 75.88 % respectively with placement of DG units at three bus locations of total 658.2019 kW and 395.873 kVAR capacity. Likewise, the voltage at minimum voltage node, maximum active and reactive loss of IEEE- 33 bus system have been improved by 6.88 %, 90.11% and 89.9% respectively with placement of DG units of total 2215.488 kW and 1176.059 kVAR at 6 different locations of the network.

Keywords: Distributed generation, optimal DG allocation, genetic algorithm
1. Introduction
Load demand is increasing day by day and it is economically, technically and environmentally not recommended to meet the increasing load demand through constructing new hydropower and expanding transmission and distribution system. Therefore, small distributed generating plants fueled by renewable energy sources can be integrated at the load centers to meet the load demand. The losses in the distribution system are significantly high as compared to that in transmission system due to low distribution voltage levels. Strategies like network reconfiguration, shunt compensator, optimal selection of conductors and above all DG penetration can be done to reduce the power losses and to improve the voltage profile of the system. Distribution Generation can be defined as “an electric power generation source connected directly to the distribution network or on the customer side of the meter [1]. The integration of DG units facilitates the voltage profile improvement and power loss reduction along with restructure planning for the utility thereby increasing reliability of the distributed system [3].

Optimal DG unit size and planning for DG unit location were used to improve the voltage profile and efficiency of the system. M. Gomez-Gonzalez introduced a hybrid approach which employed discrete particle swarm optimization (PSO) and optimal power flow [4]. Genetic Algorithm (GA) mimics the process of evolution and the most promising individuals have greater chances of transmitting their genes to offspring [5]. The DG unit placement in distribution system impacts the power flow and voltage conditions at consumers of the utility equipment [6].

2. Methodology
The proposed methodology for optimization of DG size and its placement formulation has an objective function of minimizing active and reactive power loss using Genetic Algorithm under given constraints. The codes used in MATLAB scripting language were initially verified for IEEE standard 33 bus system and then were applied for 11 kV Thimi-Sallaghari Feeder of Bhaktapur distribution system network consisting 11 buses. The load flow analysis was done using sweep algorithm to find power losses and bus voltages of the system. Minimum tolerance of objective function value was considered as the convergence criteria. The optimization was done for one to ten DG unit placements and financial analysis was also performed for each cases.

Fig. 1: Flowchart of load flow and optimization using GA algorithm
2.1 Data Collection

The secondary data includes line data (resistance and reactance) and load data (active load and reactive load) of Sallaghari Feeder [7].

2.2 Load flow analysis using Sweep algorithm

Branch current was calculated from end node to source node in backward sweep and in forward sweep, the node voltage was calculated from source node to end node.

2.3 Objective Function Formulation

This study has performed assessment with objective function in order to find the optimum placement and sizing of DG under subjected constraints into an IEEE 33 buses test network and Sallaghari Feeder.

2.3.1 Minimization of Active and Reactive Power Loss:

If $P_L$ and $Q_L$ denotes active and reactive power loss which should be minimum and determined by equation (1) and (2) respectively:

$$\text{Min } f_1 = P_L = \sum_{i=1}^{m} I_i^2 R_i$$

$$\text{Min } f_2 = Q_L = \sum_{i=1}^{m} I_i^2 X_i$$

Where $I$, $R$, $X$, $m$ are branch current, resistance, reactance and branch number respectively.

2.3.2 Minimization of Size of DG:

If $Q_C$ and $P_g$ are total reactive and active power of DG respectively which should be minimized and is represented by equation (3):

$$\text{Min } f_3 = Q_C + P_g = \sum_{i=1}^{n} Q_C_i + P_g_i$$

where $n$ is number of node.

$0.85 \leq \text{power factor } \leq 1$;

$0 \leq P_g \leq 60\%$ of $P_{total}$; where, $P_{total}$ is the total active power of the bus loads.

2.3.3 Operation Constraints:

The voltage magnitude at each bus ($V$) after the placement of DG must be within a permissible range and is expressed as:

$0.95 \text{ p.u.} \leq V \leq 1.05 \text{ p.u.}$

3. Results and Discussion

3.1 Optimal Allocation of DG unit in IEEE 33 Bus Test System

The single line diagram of test system IEEE 33 bus system is shown in Fig. 2 [2].
2.1 Data Collection
The secondary data includes line data (resistance and reactance) and load data (active load and reactive load) of Sallaghari Feeder [7].

2.2 Load flow analysis using Sweep algorithm
Branch current was calculated from end node to source node in backward sweep and in forward sweep, the node voltage was calculated from source node to end node.

2.3 Objective Function Formulation
This study has performed assessment with objective function in order to find the optimum placement and sizing of DG under subjected constraints into an IEEE 33 buses test network and Sallaghari Feeder.

2.3.1 Minimization of Active and Reactive Power Loss:
If PL and QL denotes active and reactive power loss which should be minimum and determined by equation (1) and (2) respectively:

\[
\text{Min } f_1 = PL = I_i' R_{mi} i \leq 3 \tag{1}
\]

\[
\text{Min } f_2 = QL = I_i' X_{mi} i \leq 3 \tag{2}
\]

Where I, R, X, m are branch current, resistance, reactance and branch number respectively.

2.3.2 Minimization of Size of DG:
If QC and Pg are total reactive and active power of DG respectively which should be minimized and is represented by equation (3):

\[
\text{Min } f_3 = QC + Pg = Q_{Ci} n_i \leq 3 + P_{gi} \tag{3}
\]

where n is number of node.

0.85 ≤ power factor ≤ 1; 0 ≤ Pg ≤ 60% of Ptotal; where, Ptotal is the total active power of the bus loads.

2.3.3 Operation Constraints:
The voltage magnitude at each bus (V) after the placement of DG must be within a permissible range and is expressed as:

0.95 p.u. ≤ V ≤ 1.05 p.u.

3. Results and Discussion
3.1 Optimal Allocation of DG unit in IEEE 33 Bus Test System
The single line diagram of test system IEEE 33 bus system is shown in Fig. 2 [2].

The results of optimization as shown in Fig. 3 have shown that the maximum active and reactive loss saving was minimized to 19.9616 kW (90.117% saving) and 13.5954 kVAR (89.9025% saving) respectively from six DG integration at bus 8 (301.525 kW and 158.482 kVAR), bus 13 (461.237 kW and 253.393 kVAR), bus 14 (89.169 kW and 41.941 kVAR), bus 25 (415.896 kW and 166.741 kVAR), bus 30 (787.493 kW and 476.767 kVAR) and bus 33 (160.168 kW and 78.734 kVAR) of total size 2215.48 kW and 1176.059 kVAR. Similarly, the minimum p.u bus voltage was improved to 0.9809 p.u (7.39% improvement).

The optimization results for single DG placement have shown that the total active and reactive power loss has been reduced to 64.172 kW (68.2147 % saving) and 49.5613 kVAR (63.19 % saving) respectively with the integration of a DG of size 2228.99 kW and 1381.41 kVAR at bus 26. Similarly, the minimum bus p.u voltage was improved to 0.9593 p.u from 0.9133 p.u (5.03% improvement).
3.2 Optimal Allocation of DG unit in Thimi-Sallaghari Feeder

The distribution feeder of Thimi-Sallaghari network and load and line data are shown in Fig. 7 and Table 1 respectively [7].

| Bus Num. | Bus Name          | Active Power (kW) | Reactive Power (kVAR) | From | To   | Branch Number | R (ohm) | X (ohm) |
|----------|-------------------|-------------------|-----------------------|------|------|----------------|---------|--------|
| 1        | Substation        | 0                 | 0                     | 1    | 2    | 1              | 1.695   | 1.575  |
| 2        | NEA               | 80                | 60                    | 2    | 3    | 2              | 0.968   | 0.8995 |
| 3        | Plastic Factory   | 96                | 128                   | 3    | 4    | 3              | 0.9705  | 0.9018 |
| 4        | Near Plastic Factory | 128             | 96                    | 4    | 5    | 4              | 1.017   | 0.945  |
| 5        | Yeti Carpet       | 120               | 160                   | 2    | 6    | 5              | 0.678   | 0.63   |
| 6        | Siya Ram Textile  | 90                | 120                   | 6    | 7    | 6              | 1.017   | 0.945  |
| 7        | Annapurna Paper   | 189               | 252                   | 7    | 8    | 7              | 0.5755  | 0.5348 |
| 8        | Duwakot 2         | 80                | 60                    | 8    | 9    | 8              | 0.5713  | 0.5309 |
| 9        | KNK Hostel        | 80                | 60                    | 9    | 10   | 9              | 0.6056  | 0.5627 |
| 10       | KMC               | 160               | 120                   | 10   | 11   | 10             | 0.6441  | 0.5985 |
| 11       | Duwakot Phasintar | 80                | 60                    |      |      |                |         |        |

Fig. 5: Active Power loss profile of IEEE 33 bus system

Fig. 6: Reactive power loss profile of IEEE 33 bus system
Fig. 8: Power loss saving profile of 11 bus Sallaghari Feeder bus system for up to 10 DG unit

The optimization results as shown in Fig. 8 have shown that the maximum active and reactive loss was minimized to 13.567 kW (75.88% saving) and 12.6065 kVAR (75.889% saving) respectively from three DG integration at bus 5 (195.036 kW and 116.963kVAR), bus 8 (175.620kW and 102.793kVAR) and bus 10 (287.545 kW and 176.117 kVAR) of total size 658.2019 kW and 395.87 KVAR. Similarly, the minimum p.u bus voltage was improved to 0.9768 p.u (3.694% improvement).

Fig. 9: Voltage profile of Sallaghari 11 bus system

The optimization results for single DG placement have shown that the total active and reactive power loss was reduced to 16.0841 kW (71.4152 % saving) and 14.9456 kVAR (71.4149 % saving) respectively with the integration of a DG of size 661.7926 kW and 410.1405 kVAR at bus 9. Similarly, the minimum bus p.u voltage was improved to 0.9722 p.u from 0.942 p.u (3.206% improvement).
3.3 Financial Analysis

For financial analysis of DG integration in Sallaghari Feeder, the cost of electricity was taken as Rs.11 per unit. The market interest rate was taken 10% and 1 U.S Dollar was taken equivalent to 100 Nepali Rupees. The life of capacitor and solar panel was taken 12 and 20 years respectively. The load factor was taken 0.6 and cost of capacitor and solar panel was taken $10 per kVAR and $280 per kW respectively.

| S.N. | No. of DG | Equivalent Worth | Annual saving (Rs.) | Net Present Value(NPV) (Rs.) | Simple Payback Period (Years) | B/C ratio | IRR(%) |
|------|----------|------------------|---------------------|-----------------------------|-------------------------------|-----------|--------|
| 1    | 1        |                  | 1259974.239         | 720180.2872                 | 8.152                         | 1.04      | 11     |
| 2    | 2        |                  | 1402644.275         | 1942497.572                 | 7.677                         | 1.10      | 11     |
| 3    | 3        |                  | 1412345.795         | 2078120.204                 | 7.625                         | 1.11      | 12     |
| 4    | 4        |                  | 1397770.503         | 1922491.612                 | 7.686                         | 1.10      | 11     |
| 5    | 5        |                  | 1395101.288         | 1883010.302                 | 7.701                         | 1.10      | 11     |
| 6    | 6        |                  | 1362853.77          | 1649743.948                 | 7.787                         | 1.09      | 11     |
| 7    | 7        |                  | 1399009.288         | 1931850.722                 | 7.682                         | 1.10      | 11     |
| 8    | 8        |                  | 1367417.551         | 1687568.279                 | 7.773                         | 1.09      | 11     |
| 9    | 9        |                  | 1360204.563         | 1626657.387                 | 7.797                         | 1.09      | 11     |
| 10   | 10       |                  | 1388374.949         | 1845234.42                  | 7.714                         | 1.10      | 11     |

The financial analysis was done for one to ten DG placements and from the results of Equivalent worth analysis, simple payback period, B/C ratio and IRR analysis, the best optimization was found to be three DG unit placement in Sallaghari Feeder.
4. Conclusion

In this paper, Genetic algorithm for optimal DG allocation has efficiently minimized the total active and reactive power loss satisfying the voltage limit constraints in IEEE 33 bus test system and 11 bus Sallaghari Feeder distribution system. The study was carried out in MATLAB using “Backward and Forward Sweep Method” for load flow analysis and Genetic Algorithm for optimization. The number of DG units of different size integrated was varied from one to ten. The result of this study showed that the voltage at minimum voltage node, maximum active and reactive loss reduction of Thimi-Sallaghari feeder were improved by 3.69% (from 0.942 p.u. to 0.976 p.u), 75.88 % and 75.88 % respectively with placement of DG units at three bus locations of total 658.2019 kW and 395.873 kVAR capacity. Likewise, the voltage at minimum voltage node, maximum active and reactive loss of IEEE 33 bus system were improved by 6.88 %, 90.11% and 89.9% respectively with placement of DG units of total 2215.488 kW and 1176.059 kVAR at 6 different locations of the network.

In addition to this, the financial analysis was performed to find the best optimal solution among ten DG unit placements in Sallaghari Feeder and from the analysis, it was found that the net annual saving, B/C ratio and IRR was found to be Rs.1412345.795, 1.11 and 12% respectively for three DG unit placement which were highest among ten cases. So, from the results, the best optimization was found to be three DG unit placement in Sallaghari Feeder.

References

[1] Al-Abri R (2012), Voltage Stability Analysis with High Distributed Generation Penetration.
[2] Arif W and Naeem A (2016), Analysis and Optimization of IEEE 33 Bus Radial Using Optimization Algorithm, Journal of Emerging Trends in Applied Engineering, 1(2): 19-21.
[3] Chiradeja P and Ramakumar R (2004), An Approach to Quantify the Technical Benefits of Distributed Generation, IEEE Trans. Energy Convers., 19(4): 764-773.
[4] Gonzalez MG, López A and Jurado F (2012), Optimization of Distributed Generation Systems Using a New Discrete PSO and OPF. Electr. Power Syst. Res., 84(1): 174–180.
[5] Keane A (2007), Integration of Distributed Generation. University College Dublin.
[6] Masters CL (2002), Voltage Rise: The Big Issue When Connecting Embedded Generation to Long 11 kV Overhead Lines, POWER Eng. J., 16(1): 5-12.
[7] Regmi TR (2017), Optimal Allocation of Capacitor Bank for Loss Minimization and Voltage Improvement Using Analytical Method, Department of Electrical and Electronics Engineering, Kathmandu University, Nepal.