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Abstract

Piezoresistive sensor is an essential component of wearable electronics that can detect resistance changes when pressure is applied. An effective approach to enhance its performance is to make micro-structured device. In this paper, porous carbon nanotubes (CNTs) are formed by blue laser (BL) exposure on CNTs layer, which increases its thickness ~4 times compared to the as-deposited layer. Then, the pressure sensor is fabricated by spin coating of styrene-ethylene-butylene-styrene (SEBS) elastomer on the porous CNTs layer. A 1.32 µm thick pressure sensor exhibits a high sensitivity of 6.54 x 10^6 kPa⁻¹, a wide sensing range of 278 Pa ~ 40 kPa, and fast response/recovery times of 900/760 µs, respectively. The stability of the pressure sensor is demonstrated by repeated loading and unloading of 20 kPa for 3600 cycles. The stretchable pressure sensor was also demonstrated using lateral CNT electrodes on SEBS surface, exhibiting stable pressure performance up to 20% stretching. Finally, a 32 x 32 active-matrix pressure sensor array is demonstrated consisting of amorphous InGaZnO_4 thin-film transistor (TFT) backplane for pressure mapping and real-time monitoring. The sensor array demonstrates dynamic area touch by pen writing with ~1 cm/s speed.

Introduction

The pressure sensor is of increasing interest because of its various applications such as object detection, display touch, electronic skin (e-skin), etc.¹⁻³ Mobile and healthcare devices with touch sensors are being used in human activities.⁴⁻⁵ The demand for the development of pressure sensors, converting the external stimulus into electrical signals, is continuously increasing.⁶⁻⁸ In general, the pressure sensors can be classified as piezoresistive,⁹⁻¹⁶ capacitive,¹⁷⁻²⁶ and piezoelectric types.²⁷⁻³⁵

The piezoresistive type pressure sensors operate on a simple principle that detects the change in device resistance when pressure is applied.¹⁴⁻¹⁶ Usually, conductive fillers such as carbon nanotubes
(CNTs), metal nanoparticles, and nanowires have been used to fabricate piezoresistive type pressure sensors. However, polymer composite film-based piezoresistive pressure sensors have the disadvantages of low sensitivity. Therefore, the pressure sensors with porous sensing films, microstructure, and pyramid structure were developed to achieve improved sensitivity and response time. The microstructure efficiently modulates the contact resistance, improves the response speed, recovery speed, and stability of the pressure sensor. A pressure sensor with a sensitivity of 8655.6 kPa⁻¹ was fabricated through a pyramid microstructure by Huang et al. and another pressure sensor with a sensitivity of 83.9 kPa⁻¹ was fabricated from the porous structure by Zhao et al. In general, the microstructure can be made by the complicated process, including molding and transfer processes.

In this study, porous CNT sensing layer was prepared by blue laser annealing (BLA) of conventional CNTs layer. Then, this porous CNT layer was mixed with styrene-ethylene-butylene-styrene (SEBS) elastomer to fabricate a stretchable pressure sensor. The fabricated pressure sensor exhibits a wide sensing range of ~ 30 kPa with a high sensitivity of 6.54 × 10⁶ kPa⁻¹. It shows the fast-rising time of 900 μs and falling time of 760 μs, with excellent mechanical durability against repeated loading and unloading test under high pressure of 20 kPa for 3600 cycles. Finally, a-IGZO TFT-based 32 x 32 active-matrix pressure sensor array is demonstrated for pressure mapping and real-time monitoring with dynamic area touch capability by pen writing speed of ~1 cm/s.

**Results and Discussion**

The schematic diagram of spray-coating of the CNTs and blue laser (BL) exposure on the CNTs layer to obtain porous layer are shown in Fig. 1a and 1b, respectively. The BL beam has a length of 520 µm and 20 µm width, as shown in Fig. 1c. The cross-sectional scanning electron microscopic (SEM) views of the CNTs before and after BL (laser energy density of 5.06 J cm⁻²) exposure are shown in Fig. 1d and 1e, respectively. The thickness of the as-sprayed CNTs layer is 330 nm, which is uniform over the substrate and increases to 1.32 µm upon BLA, which is almost 4 times by BL exposure. It is clearly observed that the surface morphology of CNTs layer changes significantly after BL exposure, where the top and bottom regions are very different, as shown in Fig. 1f and 1g, respectively. The morphology change of CNTs layer as a variation of incident BL energy density can be seen in Supplementary Fig. S1. With increasing the laser energy density up to 5.06 J cm⁻², the top region of the CNTs layer changes its morphology to very porous structure due to the absorption of the BL beam. The carbon-carbon bond in CNTs structure breaks under laser exposure, and the broken C atoms aggregate with the other C atoms. Note that the
sensitivity of the piezoresistive sensor depends on the microstructure of the porous CNTs.\textsuperscript{44,58,59} The sheet resistance of BLA CNTs decreases with increasing BL exposure energy, as shown in Fig. 1h, from 955 ohm sq\textsuperscript{-1} to less than 490 ohm sq\textsuperscript{-1} when the BL energy density increases to 5.06 J cm\textsuperscript{-2}. The reduction in sheet resistance of the CNTs layer is mainly due to the evaporation of surfactants by the heat absorbed from BL energy and the sequential increase of electrical conductivity.\textsuperscript{60}

\textbf{Fig. 1 Fabrication of porous CNTs by BL exposure, its morphology, and sheet resistance.} Schematic views of a spray-coating of CNTs and b BLA on the CNTs layer. c BLA equipment with the BL beam of 520 \(\mu\)m x 20 \(\mu\)m. d, e Cross-sectional and f, g top SEM views of the CNTs layer before and after BLA (5.06 J cm\textsuperscript{-2}) respectively. h The sheet resistance of the porous CNTs as a function of BL energy density.

The porous CNTs film fabricated by BL exposure was coated with the highly elastic and stretchable SEBS layer to realize our pressure sensor. We considered SEBS as elastomeric matrix, of which the thickness could be controlled by changing the concentration diluted into toluene to achieve a highly sensitive, stretchable sensor. The detailed fabrication process of the porous CNT/SEBS pressure sensor is illustrated in Supplementary Fig. S2. The porous CNT/SEBS pressure sensor is simply fabricated by spin-coating SEBS of 60 mg ml\textsuperscript{-1} on the porous CNTs layer of 2.5 x 2.5 cm\textsuperscript{2}.
Fig. 2a shows the cross-sectional SEM image of porous CNT/SEBS composite, indicating that CNTs are filled with SEBS. The optical and SEM images of the porous CNT/SEBS layer are shown in Supplementary Fig. S3, indicating the CNTs with fully covered by SEBS elastomer. The CNT/SEBS film was detached from carrier glass as shown in Fig. 2b and then transferred onto the lateral CNT electrodes on SEBS substrate. The fabrication process of stretchable pressure sensor was completed by the SEBS passivation, as shown in Fig. 2c. The detailed fabrication process of the stretchable pressure sensor can be seen in Supplementary Fig. S4. The schematic diagram for the operation of the pressure sensor is shown in Fig. 2d and 2e, with low and high pressures, respectively. Note that the top region of the CNT/SEBS film is more porous than the bottom CNT/SEBS layer due to more BL absorption at top region. Generally, the sensitivity of pressure sensor depends on the pressure range. At low-pressure region between 278 Pa to 537 Pa, the current starts to flow between stretchable electrodes through porous CNTs/SEBS sensor through near bottom CNT region, as shown using yellow line direction in Fig. 2d. High sensitivity at low pressures can be achieved by causing a large resistance change at the porous CNTs. However, at high pressure, the currents are contributed from both top and bottom regions of the CNT/SEBS film and then gradually being saturated, leading to reduced sensitivity at high pressure region, as shown in Fig. 2e. The sensitivity of the proposed pressure sensor, therefore, depends on the pressure range.

Fig. 2 The fabrication process of the stretchable pressure sensor and working principle under external pressure. a Cross-sectional SEM image of fabricated porous CNT/SEBS pressure sensor. Inset shows a cross-sectional schematic illustration of the pressure sensor. b Schematic of the mechanical detachment of the SEBS coated porous CNTs layer and c transfer of the CNTs sensor on the CNTs lateral electrodes on SEBS film. Mechanism of the pressure sensor: The schematic views of the operation of
porous CNTs pressure sensor under d low external pressure (0 ~ 0.5 kPa), and e high external pressure (5 ~ 40 kPa). To characterize the piezoresistive performance, the piezoresistive response is monitored according to the applying pressure at a constant bias voltage, V = 0.1 V. The sensor performance using the porous CNT/SEBS layer under a variable pressure range at 278 Pa ~ 40 kPa is shown in Supplementary Fig. S5. The measurements were done by loading and unloading using a force gauge on the CNT/SEBS sensor on glass substrate. The sensor shows a very low initial current (less than ~10^{-11} A) at zero pressure because of the existence of the insulting SEBS materials covered on the porous CNTs.

Fig. 3a shows the current changes plotted as a function of applied pressure. The sensitivity (S) is calculated by the following equation:

\[
S = \frac{\delta(\Delta I/I_0)}{\delta P},
\]

where \(I_0\) is the initial current, \(\Delta I\) is the current change during the pressure loading, and \(P\) is applied pressure. The porous CNT/SEBS sensor shows a broad sensing range (278 Pa~40 kPa) with very high sensitivity of \(1.13 \times 10^5\) kPa\(^{-1}\) in low-pressure region 278~537 Pa, \(6.54 \times 10^6\) kPa\(^{-1}\) in medium pressure region 2~4 kPa, \(6.63 \times 10^5\) kPa\(^{-1}\) in high-pressure region 5.7~38.9 kPa, respectively. High sensitivity can be achieved due to the extremely low initial current and highly sensitive current under applied external pressure. The comparison of pressure sensing performance without and with BLA on CNTs can be seen at the pressures of 2.5, 5.0, and 10 kPa, respectively, in Supplementary Fig. S6a and S6b. The sensor performance is also affected by the thickness of SEBS, as shown in Supplementary Fig. S7. When the thickness of SEBS layer is 2 µm, the sensor current is very low (less than 0.3 µA) at the high pressure of 150 kPa, but there is no response when the thickness of over 3 µm.

We compare the sensor performances and structures reported in the literatures shown in Table 1 together with our proposed device. Note that the thickness of our sensor is 1.32 µm which is extremely thin compared to the others. Thinner sensors have many advantages, such as easy adoption in mobile electronic systems including displays.

**Table 1. Summary of the performances of the pressure sensors with high sensitivity reported in the literatures.**

| Structure                        | Pressure range (Pa) | Sensitivity (kPa\(^{-1}\)) | Response/recovery time (ms) | Structure thickness (µm) | Reference |
|----------------------------------|--------------------|-----------------------------|-----------------------------|-------------------------|-----------|
| Polypyrrole/PDMS micropyramid    | 0.075 – 1k         | \(1.9 \times 10^3\) 4.6 \(\times 10^2\) | 0.05 / 6.2 | >20 | 49 |
| Sandpaper-molded rGO/PDMS        | 10 – 400k          | \(2.5\) 1.1 \(\times 10^3\) | 150 / 40 | >500 | 44 |
| cellulose/ nanowire nanohybrid   | 100 – 150k         | \(> 5 \times 10^3\) | - / <1 | ~1 | 45 |
network

| Network                                 | Sensitivity (kPa) | Conductivity (S/m) | Sensitivity (g) | Conductivity (S/m) | Sensitivity (g) | Conductivity (S/m) | Sensitivity (g) | Conductivity (S/m) |
|-----------------------------------------|-------------------|--------------------|-----------------|--------------------|-----------------|--------------------|-----------------|--------------------|
| Au/PDMS micropyramid & PEDOT:PSS/Au/PI | 0.425 – 2k        | 9.2 x 10^2         | 0.44 / 0.08     | -                  | 50              |
| Au/PMDS pyramids                        | 0.25 – 56k        | 3.8 x 10^2         | 75 / 50         | 555                | 51              |
| PEDOT:PSS/PUD interlocked with TPU electrode | 0.025 – 100k   | ~3.8 x 10^5        | 0.016 / -       | ~5                 | 46              |
| Ag nanocrystal/PDMS janus-like pyramid  | 220 – 3k          | 1.9 x 10^4         | -               | >250               | 52              |
| Porous CNT/SEBS                         | 278 – 40k         | 1.1 x 10^3         | <0.9 / < 0.76   | ~1                 | This work        |

Fig. 3b shows the real-time sensor performance at the pressure of 2.5 ~ 40 kPa. The measurements were performed with and without loading a pressure on the sensor. The result indicates that our sensor exhibits excellent sensing performance below 6 kPa, however, sensitivity saturates for above 6 kPa. Fig. 3c shows the response and recovery times of the pressure sensor. A faster response and recovery times of 900 µs and 760 µs, respectively, explaining the excellent capability of the proposed pressure sensor. The mechanical durability of the sensors was tested by loading and unloading pressure of 20 kPa at V = 0.1 V for 4000 cycles. Fig. 3d shows stable current changes with a high current ratio even at high pressure of 20 kPa until 4000 cycles.
Fig. 3 Electrical characteristics of porous CNT/SEBS pressure sensor. a Relative current change as a function of pressure at V = 0.1 V. Sensitivities (S1, S2, S3) were extracted in low-, middle-, high-pressure regions. b Real-time measurements of current changes under different pressures. c The response and recovery times of the pressure sensor. d Durability test of the pressure sensor until 4000 cycles at 20 kPa.

Fig. 4a and 4b shows the photographs of stretchable pressure sensor at 20 % stretched and released states, respectively, using a stretchability measurement machine. Fig. 4c shows the current change of the pressure sensor with and without strain under dynamic pressure. Note that the stretchability of pressure sensor could be achieved by fabricating the pressure sensor on rigid and flexible polyimide (PI) island transferred onto SEBS substrate. Fig. 4d shows the relative current changes of the pressure sensor plotted as a function of strain. It demonstrates that the stretchable pressure sensor can be operated well at 20 % strain. The stretchable pressure sensor was mounted to the joint of a finger to mimic the motions of the human body as shown in Fig. 4e and 4f for the real-time current measurement as shown in Fig. 4g. Fig. 4h shows the current changes in the stretchable pressure sensor under different bending angles. The results show that the current increases and reaches to the maximum at 60°. A bending angle above 60° leads to a reduction of sensing current between CNT electrodes, which might be due to the reduction of sensor thickness of CNT/SEBS film.
Fig. 4 Performance of the stretchable porous CNTs pressure sensor. Photographs of a stretchable pressure sensor under a 0% and b 20% strain. c Current change of the stretchable pressure sensor by loading a copper block under 0% and 20% stretching, respectively. d Normalized currents as a function of external pressure measured at 0% and 20% strain, respectively. I₀ is the initial current before stretching, and I is the current measured after applying an external strain of 20% stretching. Photographs of stretchable pressure sensor attached on the finger at e flat and f bent state. g The current change of the stretchable pressure sensor on finger under flat state (red) and bent state (blue). h The current change of the stretchable pressure sensor under bending of the angle between 0 and 90°.

The porous CNT/SEBS film was integrated with amorphous InGaZnO₄ (a-IGZO) thin-film transistor (TFT) backplane for a high-resolution active-matrix sensor array. Fig. 5a,b shows the photograph and cross-sectional illustration of a 32 x 32 active-matrix pressure sensor array using the CNT/SEBS film on the backplane. The detailed process flow of a-IGZO TFT can be seen in the Method section. The CNT/SEBS film was flipped and transferred on the a-IGZO TFT backplane. The CNT/SEBS film, pressure sensor, makes contact through the two vias with the patterned electrodes metal layer in a pixel. Fig. 5c shows a schematic of a pixel circuit, which consists of three switching TFTs, one 1 pF capacitor, and one pressure sensor. The optical image of active-matrix (AM) sensor array can be seen in Fig. 5d, and
the one-pixel dimension is 626 x 625 μm². Fig. 5c shows a schematic of a pixel circuit, which consists of three switching TFTs, one 1 pF capacitor, and one pressure sensor. The optical image of active-matrix (AM) sensor array can be seen in Fig. 5d, and the one-pixel dimension is 626 x 625 μm², and the contact area between the metal electrode and sensor is 30 x 400 μm². Fig. 5e and f show transfer and output characteristics of a-IGZO TFT (W/L = 20/6 μm) in the pixel circuit. The threshold voltage (V_{th}), field-effect mobility (μ_{fe}), and subthreshold swing (SS) of the a-IGZO TFT are -0.3 V, 18.6 cm²/V·s, 0.53 V/dec, respectively. All the TFTs were used as switching units for charging, readout, and reset periods. Fig. 5g shows the timing diagram of the pixel circuit. The concept of readout circuit is to read the charges stored at the q node for the resistance change during the pressure being applied. The operation of the pixel, such as pre-charging, readout, and reset of the pixel circuit, is shown in Supplementary Fig. S8. In the pre-charging state, the gate signal from the n-1ᵗʰ stage is applied to T1, as shown in Supplementary Fig. S8a. Then, T1 is turned on, and the q node could be charged depending on the resistance value. The CNT/SEBS sensor can demonstrate a wide sensing range from 278 Pa to 40 kPa, corresponding current change from 2.5 nA to 20 μA as shown in Fig. 3a. Note that the off-state current of the sensor is less than 10 pA, as shown in Supplementary Fig. S5. When T2 and T3 are turned off, and capacitor is serially connected to the q node, charged voltage at q node (V_q) could be obtained from the following equation:

\[ V_q = V_{DD} \times (1 - e^{-t/(RC)}) \]

where V_q, V_{DD}, t, R, and C are charged voltage at q node, input voltage, gate pulse width, the resistance of pressure sensor, and capacitance of 1 pF, respectively. The V_q is decided by the n-1ᵗʰ scan signal time t and the resistance of pressure sensor R. We set all scan signal time to be 20 μs for detecting the low-pressure sensing region (278 Pa (39.7 M-ohm) to 537 Pa (0.61 M-ohm)). When the resistance of the pressure sensor is between 0.61 and 39.7 M-ohm, it is called the low-pressure region in this paper. The RC time, 3τ, reaching 95% of V_{DD}, would be 1.82 μs and 119 μs, respectively. This means that V_q could be determined by scan time, t. If the scan time t is 20 μs, V_q would be 0.395 x V_{DD} under 278 Pa (39.7 M-ohm) and fully charged under 537 Pa (0.61 M-ohm). The charges will go through the V_{OUT} node in Supplementary Fig. S8b. Then, the n+1ᵗʰ scan signal is applied to turn on T3, and the q node is discharged to V_{SS}, as shown in Supplementary Fig. S8c. The simulation results using Smart Spice are shown in Fig. 5h when the scan signal of 10 V is applied to gate for 20 μs. The charges in the q node depend on the resistance of pressure sensor.

The gate driver was integrated into the sensor array. The gate shift register (GSR) was designed and used to apply the gate pulse to the pixels, as shown in Supplementary Fig. S9. Three scan signals are connected to one pixel, as shown in Supplementary Fig. S9a. Two sequential scan lines are applied to a particular pixel. The nth scan line is divided into two parallel lines at the nth stage and connected to the particular
pixel and is also connected to the n-1th and n+1th pixels. The timing diagram and optical image of GSR are shown respectively in Supplementary Fig. S9b and S9c. The detailed operation of GSR appears in our previous work. The GSR output signals are working through the last stage, 32nd, as can be seen in Supplementary Fig. S9d. The pressure mapping was tested using the capital letters “A”, “D”, “R”, and “C”. The 32 x 32 sensor array shows the clear pressure response without crosstalk, as shown in Fig. 5i-l. The V_{DD}, V_{GS}, and t used for the sensor operation are 2.84 V, 10 V, and 20 μs, respectively. The letters are softly pressed under 300 Pa to detect the characters in the low-pressure sensing region. The output signals of the pressed region are 1.32 V, which is similar to the simulation value of 1.33 V.

We also demonstrated the dynamic pressure distribution for real-time monitoring. The experimental set for displaying dynamic sensing on the screen is shown in Supplementary Fig. S10. The circuit schematic for the integrated AM sensor array can be seen in Supplementary Fig. S10a. The photographs of the customized board and zig with full integration are shown in Supplementary Fig. S10b and S10c. Using the driving board, the pressure detection can be seen in the range as shown in Video VS1, demonstrating the sensing Alphabet (A, D, R, C) writing on the sensor array respectively. Table 2 shows the summary of AM pressure sensor arrays reported in the literatures. Previous studies on the AM pressure sensors use the sensors such as commercial products (Pressure-sensitive rubber, PSR), and conductive filler/polymer composites, and pressure-sensitive TFT. The key advantage of the sensor is ultra-thin (1 μm) so that can be applied to large-area electronic skin. The real-time writing on the AM sensor array can be seen on the PC screen.
Fig. 5 Pressure mapping on the integrated 32 x 32 a-IGZO active-matrix sensor array. a optical image of the active-matrix pressure sensor array with BLA CNT/SEBS film. b Schematic illustration of AM sensor array. c schematic and d back optical image of a pixel circuit. One pixel consists of three TFTs, one capacitor, and one pressure sensor. Red dashed box shows current flowing area contacted with the lateral electrode. e Transfer and f output characteristics a-IGZO TFT. g The timing diagram of a pixel circuit and h simulated results with pressure variation (0 ~ 537 Pa). Pressure mapping of fabricated 32 x 32 AM pressure sensor with letters i A, j D, k R, and l C, respectively. Inset shows letters placed on the sensor array.

Table 2. Summary of the reported active-matrix pressure sensor arrays in the literatures.

| Pressure sensor | TFT (W/L, µm) | Driving voltage \(V_{GS}/V_{DS}\) | Resolution | Pixel size | Output signal | Reference |
|-----------------|---------------|-----------------------------------|------------|-----------|--------------|-----------|
| PSR             | Ge/Si NW (250/3) | 5/0.5                             | 19 x 18    | 2.5 mm    | ~15 kPa      | 63        |
| PSR             | CNT (2000/20)  | -5/10                             | 16 x 16    | ~1 mm     | 600 µA at 39.2 kPa | 64 |
| PSR             | SWCNT (1250/100) | -10/-5                            | 20 x 20    | -         | ~1 µA at 7.2 kPa | 65 |
| PSR             | SWCNT (365/130) | -20/-20                           | 20 x 20    | 9.3 ppi   | ~0.5 µA at 17.8 kPa | 66 |
Conclusion

In summary, a highly sensitive pressure sensor is demonstrated using porous CNTs by the BL exposure on the CNTs layer. The CNTs were coated with SEBS elastomer, resulting in a very thin (1.32 µm) highly sensitive pressure sensor. The sensor is capable to operate for the wide pressure range from 278 Pa to 40 kPa. The pressure sensor shows the high sensitivity of $6.54 \times 10^6$ kPa$^{-1}$ at pressure range of 2 kPa ~ 3.89 kPa and high current ratio of > 400 at the lowest pressure of 278 Pa. The pressure sensor has the fast-rising time of 900 µs and falling-time of 760 µs and excellent durability with repeated loading and unloading tests under 20 kPa for 3600 cycles. It maintains its piezoresponse characteristic until 20% stretching when it was made with CNTs lateral electrodes on SEBS substrate. The porous CNT/SEBS film was integrated on a 32 x 32 oxide TFT array to display the letters and dynamic writing. Therefore, porous CNT/SEBS film on oxide TFT array can be applied to ultra-thin large area e-skin sensors.

Methods

Fabrication of porous CNT/SEBS film. The process flow of CNTs coating and BL exposure on the CNTs layer to make porous CNTs can be seen in Supplementary Fig. S2. A CNTs/graphene oxide (GO) layer was spray-coated on the carrier glass using a mixture of CNT/GO solution using the mixing ratio of CNT/GO = 1/8 for detaching the PI substrate from the carrier glass. The very thin CNT/GO layer was soft-baked at 130 °C for 15 min in air and then hard-baked at 290 °C for 2 h in a vacuum oven. The PI
layer was then spin-coated on glass and then soft-baked at ~ 140 °C for 30 min in a hot plate and then cured at 450 °C for 2 h in N\textsubscript{2} atmosphere. A buffer layer of SiNx/SiO\textsubscript{2} was deposited on the PI substrate at the substrate temperature of 420 °C.\textsuperscript{72,73} The surface of the SiO\textsubscript{2} layer was treated with UV/O\textsubscript{3} for 300 s for uniform coating of CNTs by spray on the carrier glass and annealed at 290 °C, for 2 h in a vacuum. The CNTs layer was exposed by a line beam of a BL (beam size: 520 µm x 20 µm, laser energy density 5.06 J cm\textsuperscript{-2}) as shown in Supplementary Fig. S1c. The 60 mg ml\textsuperscript{-1} SEBS solution diluted in toluene was spin-coated and cured on BLA CNT at 120 °C for 10 minutes.

**Fabrication of a-IGZO TFT backplane.** On the buffer layer, 30 nm a-IGZO was deposited by reactive sputtering using a polycrystalline IGZO target (InO\textsubscript{3}:Ga\textsubscript{2}O\textsubscript{3}:ZnO = 1:1:1 mol %). Then, a 100 nm-thick SiO\textsubscript{2} was deposited on the top of the a-IGZO by PECVD as a gate insulator (GI) without breaking vacuum. A 100 nm Mo was deposited by sputtering and patterned as the top gate electrode. GI layer was etched by a self-aligned process with the gate pattern. Then, a 300 nm-thick SiO\textsubscript{2} layer was deposited as the interlayer by PECVD followed by the formation of via holes, and a 200 nm-thick Mo layer was deposited and patterned for the S/D electrodes. Then, SiN\textsubscript{x} and SiO\textsubscript{2} double passivation layers were deposited through PECVD at 200 °C. Finally, the devices were annealed at 300 °C in a vacuum for 1 h.

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**Author contributions**
All authors contributed to this work equally.

**Competing interests**
The authors declare no competing financial interests.
Data availability

The authors confirm that all the data supporting the findings of this study are available within the article [and/or] its supplementary materials.
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