Electrochemical Cell Loss Minimization in Modular Multilevel Converters Based on Half-Bridge Modules

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Abstract: In the developing context of distributed generation and flexible smart grids, in order to realize electrochemical storage systems, Modular Multilevel Converters (MMCs) represent an interesting alternative to the more traditional Voltage Source Inverters (VSIs). This paper presents a novel analytical investigation of electrochemical cell power losses in MMCs and their dependence on the injected common mode voltage. Steady-state cell losses are calculated under Nearest Level Control (NLC) modulation for MMCs equipped with a large number of half-bridge modules, each directly connected to an elementary electrochemical cell. The total cell losses of both a Single Star MMC (SS-MMC) and a Double Star MMC (DS MMC) are derived and compared to the loss of a VSI working under the same conditions. An optimum common mode voltage injection law is developed, leading to the minimum cell losses possible. In the worst case, it achieves a 17.5% reduction in cell losses compared to conventional injection laws. The analysis is experimentally validated using a laboratory prototype set-up based on a two-arm SS-MMC with 12 modules per arm. The experimental results are within 2.5% of the analytical models for all cases considered.

Keywords: lithium batteries; loss minimization; Modular Multilevel Converters; optimization methods

1. Introduction

The rapid advances in energy storage technologies that have occurred in recent years, together with the urgent issue of environmental pollution, have driven innovative power electronic solutions that, apart from the main function of power conversion, provide additional functionality, such as minimizing system energy losses while assuring the maximum lifetime of the storage devices.

One set of promising converter topologies are based on the series connection of single half-bridge or full-bridge modules [1], each interfaced with a storage device, such as electrochemical cells or super-capacitors. The connection between the power module and the storage device can be either direct or occur via an additional DC/DC converter [2]. The series connection of a fixed number of modules represents an arm [3] of the converter. Based on the number of arms and their relative configuration, different versions of the Modular Multilevel Converter (MMC) can be realized [4,5].

The star connection of n arms can be adopted in order to realize the simplest n phase AC output converter [6], i.e., Single Star MMC (SS-MMC). On the other hand, a Double Star (DS) connection of 2n arms leads to the DS-MMC [7], where each pair of arms of the same phase are interfaced by two buffer inductances, providing both an n phase AC output and a DC interface. Both architectures have been widely proposed in several contexts, including traction applications [8–12] and transformerless grid connected storage systems [13–16].

The inherent modularity of MMCs helps minimize the system maintenance costs and generates fault tolerance features [14,17,18]. In the case of failure, the failed module can be bypassed without interrupting the operation of the system. The improved system reliability
allows scaling to high voltages by connecting a very high number of modules in series. Therefore, a direct (transformerless) connection to high-voltage grids is feasible [13] by means of switches characterized by relatively low-voltage ratings, leading to a significant cost reduction and competitive total system efficiency [15]. A large number of modules also results in smaller rates-of-change of voltage (since the output voltage of each module is only a small percentage of the peak output voltage), strongly reducing Electro-Magnetic Interference (EMI)-related issues.

In the context of electrochemical storage devices [19], in order to safeguard the cells and guarantee the highest number of charge/discharge cycles, it is necessary to keep all of the cells balanced in terms of their State of Charge (SoC). While traditional converters are normally equipped with an additional Battery Management System [20], MMC solutions inherently provide this balancing functionality at the module level [12,16,21,22]. As an additional advantage, these modular converter designs lead to a significant reduction of the converter switching losses, especially when a large number of modules (and subsequently, voltage levels) are available [11]. In these cases, high switching frequency Pulse Width Modulation (PWM) schemes can be replaced by Nearest Level Control (NLC) modulation [23]. NLC generates a nearly-sinusoidal AC output by combining the DC voltages of the converter modules while driving the devices with a switching frequency equal to the fundamental, thus producing negligible switching losses. However, since at least one device per module is always in the current path, for large-scale systems, this approach could result in a substantial increase of conduction losses. For this reason, the careful selection of low-voltage Metal Oxide Semiconductor Field Effect Transistors (MOSFETs) optimized for low on-state resistance is necessary.

In most studies to-date where efficiency is considered, MMC solutions are compared with conventional Voltage Source Inverters (VSIs), but only power electronic losses are taken into account, whereas losses occurring in electrochemical cells are ignored [11]. This approach can be misleading; indeed, according to [15], MMC cell losses can form a significant fraction of overall system losses and will disproportionately affect MMC-type designs because the cells are operated under a low-frequency pulsed current (as opposed to a constant DC current in a VSI). On the other hand, this suggests a considerable margin for improvement, since the potential reduction of the MMC cell losses would have a great impact on the total system efficiency.

These considerations underline the need for a more detailed study where the possibility to influence the MMC cell power losses should be investigated. In fact, although many authors recognize the common mode reference [5] as a degree of freedom for the MMCs, no analysis of its effect on the cell power losses has been conducted.

In the context of MMCs comprising a large number of half-bridge modules, each including an elementary electrochemical cell and driven by NLC modulation, this paper presents a novel analytical investigation where the calculation of the cell losses is generalized for any common mode voltage value, so that the dependence of the cell losses from the common mode component is analyzed. Moreover, an optimum common mode voltage injection law is developed, assuring the minimization of cell power losses. The proposed optimum strategy is compared with traditional approaches, showing a significant decrease in the cell losses: Considering that MMC cell losses can be a considerable fraction of total system losses, their minimization can significantly improve the system efficiency and reduce detrimental cell heating.

The total cell losses of an SS-MMC and DS-MMC are derived in all of the considered cases and compared with the loss associated with an equivalent VSI solution. Finally, all of the mathematical derivations are validated by means of an experimental set-up (see Figure 1) based on a two-arm SS-MMC with 12 modules per arm, each containing an elementary lithium ion cell. The results exhibit good agreement with the analytic model and demonstrate the effectiveness of the proposed strategy.
2. Description of the System

Figure 1 shows a single generic converter arm, composed of $N$ half-bridge modules; $v_a$ and $i_a$ represent the total arm voltage and the arm current, respectively. Each module is driven by an electrochemical cell at the voltage $V_{cell}$, which is the average cell voltage. The SS-MMC and DS-MMC feed symmetrical three-phase loads. Given the possibility of the DS-MMC exchanging power through a DC interface, an optional capacitor (gray dashed line) has also been added. The experimental topology is outlined.

When a module is activated, the corresponding upper switching device is turned ON and its output voltage equals $V_{cell}$, while the arm current $i_a$ flows in the cell. When a module is de-activated, its output voltage is zero and no current flows in the cell, i.e., the cell is bypassed.

When NLC modulation is implemented, the total number of active modules required to synthesize the instantaneous arm reference voltage $v_a^*$ is given by

$$N_{on} = \text{round}(v_a^*/V_{cell}).$$

In this case, State of Charge (SoC) balancing between the cells can be achieved by choosing which cells contribute to the output arm voltage $v_a$ based on their SoC, i.e., when the system is charging, the $N_{on}$ cells with the lowest SoC will be activated, whereas during discharge, the $N_{on}$ cells with the highest SoC will be placed in the current path.

A second balancing action should be implemented in order to equalize the mean SoCs between the arms through the injection of a proper circulating current value. Since the circulating current is normally a small percentage of the nominal current and is only present during cell balancing [11], its contribution to the losses is not significant and will be neglected in the present discussion.

SS-MMC

With reference to an SS-MMC, denoting the three arm reference voltages as $v_1^*$, $v_2^*$, $v_3^*$, it is possible to define the following common mode component:

$$v_0^* = (v_1^* + v_2^* + v_3^*)/3$$

Based on Equation (2), the reference voltages can be written as

$$\begin{align*}
  v_1^* &= v_{1,d}^* + v_0^* \\
  v_2^* &= v_{2,d}^* + v_0^* \\
  v_3^* &= v_{3,d}^* + v_0^*
\end{align*}$$

(3)
where \( v_{k,d}^* \) (with \( k = 1,2,3 \)) is, by definition, the differential component of \( v_k^* \). Since the load currents are not affected by \( v_0^* \), it represents an effective degree of freedom in the arm modulation.

As \( 0 \leq v_k^* \leq NV_{cell} \) (\( k = 1,2,3 \)), the quantity \( v_0^* \) must fulfill the following constrains:

\[
\begin{align*}
 v_0^* &\geq -\min\left( v_{1,d}^*, v_{2,d}^*, v_{3,d}^* \right) = v_{0,min}^* \\
 v_0^* &\leq N V_{cell} - \max\left( v_{1,d}^*, v_{2,d}^*, v_{3,d}^* \right) = v_{0,max}^* .
\end{align*}
\]

(4)

The common mode voltage can be decomposed into its DC and AC components:

\[
v_0^* = v_{0,DC}^* + v_{0,AC}^* .
\]

In a normal steady-state condition, the differential component \( v_{1,d}^*, v_{2,d}^*, v_{3,d}^* \) will be imposed as a three-phase sinusoidal symmetrical system:

\[
\begin{align*}
 v_{1,d}^* &= V_L \sin(\omega t - \varphi) \\
 v_{2,d}^* &= V_L \sin(\omega t - \varphi - 2\pi/3) , \\
 v_{3,d}^* &= V_L \sin(\omega t - \varphi - 4\pi/3)
\end{align*}
\]

(5)

where \( V_L \) is the load voltage amplitude and \( \omega \) is the angular frequency.

In the presence of a symmetrical load, given the high number of cells per arm, the three arm currents can be assumed to be sinusoidal and, in particular, since the circulating currents have been neglected, a three-phase sinusoidal symmetrical system can be considered:

\[
\begin{align*}
 i_1 &= I_L \sin(\omega t) \\
 i_2 &= I_L \sin(\omega t - 2\pi/3) , \\
 i_3 &= I_L \sin(\omega t - 4\pi/3)
\end{align*}
\]

(6)

where \( I_L \) is the load current amplitude and \( \varphi \) is the phase shift delay.

By defining \( \xi = 2 V_L / NV_{cell} \) as the arm modulation index, Equation (5) can be rewritten as

\[
v_{k,d}^* = NV_{cell} / 2 \sin(\omega t - \alpha_k),
\]

(7)

where \( \alpha_k = 2\pi(k-1)/3 \).

Equivalently, by introducing \( \xi_{DC} = 2V_{0,DC}/NV_{cell} \), the DC component of \( v_0^* \) can be written as

\[
v_{0,DC}^* = NV_{cell} / 2 .
\]

(8)

Based on Equations (7) and (8), Equation (3) can be written as

\[
v_k^* = NV_{cell} (\xi \sin(\omega t - \alpha_k) + \xi_{DC}) / 2 + v_{0,AC}^* .
\]

(9)

From Equations (1) and (9), the number of modules \( N_{on}(t) \) switched ON at instant \( t \) is

\[
N_{on}(t) = \text{round}\left( N(\xi \sin(\omega t - \alpha_k) + \xi_{DC}) / 2 + v_{0,AC}^* / V_{cell} \right) .
\]

(10)

If \( N \) is large enough, \( N_{on}(t) \) can be reasonably approximated with its continuous equivalent:

\[
N_{con}(t) = \frac{N(\xi \sin(\omega t - \alpha_k) + \xi_{DC})}{2} + v_{0,AC}^* / V_{cell} .
\]

(11)

DS-MMC

With reference to Figure 1, while, for the SS-MMC, the arm currents are equal to the load currents, in the DS configuration, each \( k \)-th phase load current \( i_{1,k} \) is given by the difference of a TOP arm current \( i_{k,T} \) and a BOTTOM arm current \( i_{k,B} \). In fact, the DS-
MMC can be seen as being composed of three star-connected TOP arms (with voltages \( v_{1T}, v_{2T}, v_{3T} \)) and three star-connected BOTTOM arms (with voltages \( v_{1B}, v_{2B}, v_{3B} \)):

\[
\begin{align*}
\text{TOP :} & \quad \begin{cases} 
v_{0T}^* = \left( v_{1T}^* + v_{2T}^* + v_{3T}^* \right) / 3 = v_{0T,DC}^* + v_{0T,AC}^* \\
 v_{kT}^* = v_{kT,d}^* + v_{0T}^*
\end{cases} \\
\text{BOT :} & \quad \begin{cases} 
v_{0B}^* = \left( v_{1B}^* + v_{2B}^* + v_{3B}^* \right) / 3 = v_{0B,DC}^* + v_{0B,AC}^* \\
 v_{kB}^* = v_{kB,d}^* + v_{0B}^*
\end{cases}
\end{align*}
\]

(12)

with symbols having similar meanings to those in the SS-MMC.

In a normal sinusoidal steady-state condition, and for each \( k \)-th phase, the differential component of the BOTTOM arm reference voltage lags the TOP one by an angle of 180°. Moreover, when the circulating currents are neglected, each BOTTOM arm current is centered in the range of the possible values \( [-v_{0B,AC}^*, v_{0B,AC}^*] \), while the AC component \( v_{0B,AC}^* \) can be equal to either \( v_{SVM}^* \) or \( v_{THI}^* \), with \( v_{SVM}^* \) representing a traditional Space Vector Modulation (SVM) AC common mode component and \( v_{THI}^* \) representing a third harmonic of a proper amplitude:

\[
\begin{align*}
v_{0,DC}^* &= NV_{cell}/2 \\
v_{0,AC}^* &= \begin{cases} 
0 & \text{for } \xi \leq 1 \\
 v_{SVM}^* \text{ or } v_{THI}^* & \text{for } \xi > 1
\end{cases} 
\end{align*}
\]

For the traditional SVM injection,

\[
v_{SVM} = \begin{cases} 
\frac{NV_{cell}\xi \sin(\omega t - \frac{\pi}{4})}{4} & \forall \omega t \in A_{p,h}, \\
\frac{NV_{cell}\xi \sin(\omega t + \frac{2\pi}{3} - \frac{\pi}{4})}{4} & \forall \omega t \in A_{n,h},
\end{cases}
\]

(15)

where \( \begin{cases} 
A_{p,h} \equiv (-\pi/6 + 2\pi/3h, \pi/6 + 2\pi/3h) \\
A_{n,h} \equiv (\pi/6 + 2\pi/3h, \pi/2 + 2\pi/3h)
\end{cases} \) and \( h \) is an integer number.

For the third harmonic injection,

\[
v_{THI} = \frac{NV_{cell}\xi \sin 3\omega t}{12}.
\]

(16)

Figure 2 shows the qualitative waveform of \( v_{k,d}^* + v_{0,AC}^* \) (normalized with respect to \( NV_{cell}/2 \)) in the presence of the conventional AC common mode injection methods. In particular, starting from the differential component \( v_{k,d}^* \) (Figure 2a), the \( v_{THI}^* \) or \( v_{SVM}^* \) common mode AC component (Figure 2b) can be added, producing the waveform of Figure 2c or Figure 2d, respectively.
Figure 2. Qualitative waveform of the generic arm reference voltage in the presence of the conventional AC common mode injection: (a) Differential component; (b) AC conventional common mode components; and (c,d) total arm reference voltage AC component.

For the sake of clarity, Figure 2 can refer to either an SS-MMC or the three TOP arms of a DS-MMC, while for the BOTTOM arms, the corresponding waveforms are shifted by 180°.

Thanks to the symmetry of the MMC arms (for both SS and DS configurations), the cell power loss calculation can be performed with reference to a single arm. In particular, if a cell is modeled as a DC voltage source with a series resistance \( R_{\text{cell}} \), the instantaneous power dissipated into the arm cells \( p_i(t) \) can be expressed as a function of \( N_{\text{c,on}} \):

\[
p_i(t) = N_{\text{c,on}}(t) R_{\text{cell}} i_a^2(t),
\]

where \( i_a \) is the instantaneous arm current. By substituting Equation (11) into Equation (17), the average power losses are given by

\[
P_j = \frac{1}{T} \int_0^T N_{\text{c,on}} R_{\text{cell}} i_a^2 dt = R_{\text{cell}} \left( \frac{N_{\xi,\text{DC}} I_a^2}{4} + \frac{1}{T} \int_0^T \frac{v_{0,\text{AC}}}{V_{\text{cell}}} i_a^2 dt \right).
\]

By replacing \( v_{0,\text{AC}} \) in Equation (18) with its Fourier-series \( \sum_{n=1}^{\infty} V_{\text{ac,}n} \sin(n \omega t - \phi_{\text{ac,}n}) \), \( P_j \) is derived:

\[
P_j = R_{\text{cell}} \left( \frac{N_{\xi,\text{DC}}}{4} - \frac{V_{\text{ac,2}}}{V_{\text{cell}}} \sin(2\varphi - \phi_{\text{ac,2}}) \right) I_a^2.
\]

According to conventional strategies, considering that \( \xi_{\text{DC}} = 1 \) and that \( v_{\text{SVM}} \) and \( v_{\text{THI}} \) do not contain the second harmonic, \( P_j \) becomes

\[
P_j = N R_{\text{cell}} I_a^2 / 4,
\]

where \( I_a \) is the arm current amplitude: \( I_a = I_L \) for the SS-MMC and \( I_a = I_L / 2 \) for the DS-MMC.

4. Cell Loss Comparison of MMC and VSI

From Equation (19), it is straightforward to compute the total cell power loss for either a three-phase SS-MMC or DS-MMC characterized by the same number \( N \) of cells per arm:

\[
\begin{align*}
P_j,\text{SS-MMC} &= \frac{3}{2} N R_{\text{cell,SS}} I_a^2, \\
P_j,\text{DS-MMC} &= \frac{3}{2} N R_{\text{cell,DS}} I_a^2.
\end{align*}
\]

In order to produce the same AC output voltage, the SS-MMC converter requires half as many modules compared to the DS-MMC, where, for each phase, two arms are parallel-connected with respect to the AC side. As a result, for the same total system storage capacity, an SS-MMC cell can be regarded as equivalent to two parallel-connected DS-MMC cells, i.e., \( R_{\text{cell,DS}} \simeq 2 R_{\text{cell,SS}} \). Therefore, \( P_j,\text{SS-MMC} \simeq P_j,\text{DS-MMC} \).
The mean value of the load active power $P_L$ is naturally independent of the converter configuration:

$$P_L = \frac{3}{2} V_L I_L \cos \varphi = \frac{3}{4} N V_{cell} I_L \cos \varphi. \quad (22)$$

The cell power losses in the MMC (henceforth indicated as $P_{J,MMC}$ for both the SS-MMC and DS-MMC) can be compared to the cell losses $P_{J,VSI}$ occurring in a traditional two-level Voltage Source Inverter (VSI). An equivalent VSI battery pack may be built using three parallel-connected stacks of $N$ cells, as shown in Figure 3. A DC-side $LC$ filter ensures that a smooth DC current $i_{dc}$ is drawn from the pack.

**Figure 3.** Voltage Source Inverter (VSI).

In this configuration, each stack effectively corresponds to the cells contained in the modules of one SS-MMC arm. If the VSI switching frequency $f_s$ is much greater than the fundamental output frequency $f$, the current $i_{dc}$ flowing in the battery pack can be assumed to be constant once the DC filter components have been properly chosen. If the converter losses are neglected, $i_{dc}$ only depends on $P_L$:

$$i_{dc} = \frac{P_L}{N V_{cell}} = \frac{3}{4} \xi I_L \cos \varphi. \quad (23)$$

From Equation (23), the cell losses are

$$P_{J,VSI} = \frac{1}{3} N R_{cell} i_{dc}^2 = \frac{3}{16} N R_{cell} (\xi I_L \cos \varphi)^2. \quad (24)$$

This may be directly compared with the first of Equation (21) with $R_{cell,SS} = R_{cell}$ (or with the second of Equation (21) with $R_{cell,DS} = 2 R_{cell}$).

As it can be noted, the VSI cell losses depend on both the modulation index and the load power factor (in fact, $P_{J,VSI} \propto \xi^2$). On the contrary, $P_{J,MMC}$ does not depend on $\xi$ or $\cos \varphi$, i.e., at constant $I_L$, the cell losses do not depend on the output power. Moreover, it is evident that $P_{J,MMC}$ is considerably higher than $P_{J,VSI}$; in the case of the unitary power factor and full modulation index, the cell losses in the MMC are four times those of the VSI (i.e., Equation (21) divided by Equation (24)).
5. Proposed Optimum Injection Strategy

With reference to a three-phase system, $v_{0,AC}^*$ is built on multiples of the third harmonic in order to keep the arm reference voltages symmetrical across the converter phases. In this context, as per Equation (19), $P_J$ does not directly depend on $v_{0,AC}^*$. Nevertheless, a proper $v_{0,AC}^*$ injection law can be formulated in order to decrease the minimum settable value of $\xi_{DC}$, to which, as stated by Equation (19), the arm cell losses are proportional. $\xi_{DC}$ can be regarded as the shift to be applied to the AC component of the arm reference voltage $(v_{k,d}^* + v_{0,AC}^*)$ in order to guarantee that $v_k^* \in [0, NV_{cell}]$. Therefore, the range of possible values for $\xi_{DC} \in [\xi_{DC,min}, \xi_{DC,max}]$ depends on both the modulation index $\xi$ and on $v_{0,AC}^*$. The cell loss minimization problem can thus be reduced to the formulation of an optimal injection law able to minimize $\xi_{DC,min}$ for each $\xi \in [0, 2/\sqrt{3}]$. The proposed common mode voltage injection law is based on the following AC component:

$$v_{0,AC,OPT} = -\frac{NV_{cell}}{2} \xi \left( \sin\left(\omega t - \frac{2\pi h}{3}\right) + \frac{3\sqrt{3}}{2\pi} \right) \forall \omega t \in A_h,$$

where $A_h \equiv (7\pi/6 + 2\pi/3h, 7\pi/6 + 2\pi/3(h + 1))$ and $h$ is an integer number.

Figure 4a shows the qualitative waveform of $v_{0,AC,OPT}$, while the total AC arm reference voltage $v_{k,d}^* + v_{0,AC,OPT}$ (normalized with respect to $NV_{cell}/2$) is shown in Figure 4b; as previously mentioned, for the BOTTOM arms of the DS-MMC, a $180^\circ$ shift must be considered.

![Figure 4. Qualitative waveform of the generic arm reference voltage in the presence of the proposed AC common mode injection: (a) AC proposed common mode components, and (b) total AC arm reference voltage.](image)

As can be noted from Figure 2a, when $v_{0,AC}^* = 0$, i.e., no injection is used, the minimum $\xi_{DC}$ value, necessary to shift the resultant arm reference voltage such that $v_k^* \geq 0$, is equal to the modulation index $\xi$ (i.e., $\xi_{DC,min} = \xi$). Instead, in both cases, when $v_{0,AC}^* = v_{SVM}$ or $v_{0,AC}^* = v_{THI}$, it is $\xi_{DC,min} = \sqrt{3}\xi/2$ (Figure 2c,d). On the other hand, the proposed optimum law gives $\xi_{DC,min} = 3\sqrt{3}\xi/2\pi$ (Figure 4b), which is the lowest of the three values.

The proposed strategy can be summarized by

$$\begin{align*}
{v}_{0,AC}^* &= v_{0,AC,OPT} \\
\xi_{DC} &= 3\sqrt{3}\xi/2\pi \\
\Rightarrow P_{J,opt} &= \frac{3\sqrt{3}}{8\pi} \xi R_{cell} I_a^2.
\end{align*}$$

In order to verify that this is actually the optimum voltage injection law, which guarantees the lowest $\xi_{DC,min}$ leading to the lowest cell losses of a three-phase MMC, it is convenient to consider the constrains Equation (4). In the possible range $v_{0,min}^* \leq v_0^* \leq v_{0,max}^*$, it is evident that the choice $v_0^* = v_{0,min}^*$ minimizes the quantity $\xi_{DC}$, thus guaranteeing the lowest cell losses. On the other hand, it is easy to verify that the DC
component of \( v_{0,min} \) can be obtained by Equation (8), with \( \xi_{DC} = \frac{3\sqrt{3}}{2} \xi / 2\pi \), while its AC component corresponds to the \( v_{0,AC,OPT} \) of Equation (15). It can be pointed out that the lower achievable \( \xi_{DC,\min} \) is linked to the asymmetrical waveform of \( v_{0,AC,OPT} \), which leads to a lower injected absolute minimum value (Figure 4b) with respect to the other methods.

Applying the proposed method, the total cell power losses for an SS-MMC and a DS-MMC characterized by the same number of cells per arm \( (N) \) are

\[
\begin{align*}
P_{J,\text{opt},SS-MMC} &= \frac{9}{8\pi} \xi N R_{cell,SS} I_L^2 \\
P_{J,\text{opt},DS-MMC} &= \frac{9}{16\pi} \xi N R_{cell,DS} I_L^2.
\end{align*}
\]

(27)

It should be noted that in the case of a DS-MMC whose DC side is not isolated (see Figure 1), the power loss minimization techniques cannot be employed since \( \xi_{DC} \) is constrained to the total DC-link voltage value.

By comparing Equation (27) with Equation (21), the cell energy saving \( (P_J - P_{J,\text{opt}}) / P_J \) guaranteed by the proposed method is linear with \( \xi \), and in particular, it is equal to 100% for \( \xi = 0 \) and around 5% for \( \xi = 2/\sqrt{3} \).

As an example, if a grid connected MMC is sized such that SoC = 0 corresponds to the maximum modulation index \( \xi = 2/\sqrt{3} \) (worst case for evaluating the benefit of the proposed technique), during a complete charge (or discharge) cycle, and considering the typical lithium cell voltage variation, the modulation index will vary from \( 2/\sqrt{3} \) to about 0.9, with an average \( \xi \) value equal to about 1. The corresponding average energy saving is equal to about 17.5%. Naturally, in an application characterized by a lower value of \( \xi \), the improvement is even more significant. It is important to note, however, that, even with a unitary power factor and full modulation index, an MMC operated with the optimal injection law will still cause approximately 3.3 times the cell loss of an equivalent VSI (i.e., Equation (26) divided by Equation (24)). In practice, the choice between an MMC and VSI design must involve a careful trade-off between the significantly higher cell loss incurred by the MMC and the inherent modularity, BMS flexibility, low EMI, and high-voltage advantages that an MMC has over a VSI.

6. Experimental Validation

To validate the power loss minimization method presented in section V, the experimental setup of Figure 5 was used.

![Figure 5. Experimental setup: (a) Photograph and (b) schematic.](image-url)

Each arm in Figure 5 comprises twelve modules connected in series. Each module includes a 20 Ah lithium titanate cell that is individually controlled by an H-bridge converter (a detailed description of the control architecture of the experimental hardware is
presented in [16]). In these experiments, each H-bridge is operated as a half-bridge, only allowing for a positive or zero voltage output.

Detailed characteristics of the setup and experiments are presented in Table 1. According to the cell loss minimization techniques explained in the previous section and, in particular, by changing the settings of the variables $\xi_{DC}$ and $v_{a,0,AC}$, four different tests were performed.

Table 1. Details of the performed tests and results.

| Test n° | Conventional method | 2 | 3 | Proposed method |
|---------|---------------------|---|---|------------------|
| $\xi_{DC}$ | 1 | 2/3 | 1/$\sqrt{3}$ | $3\sqrt{3}\xi/2\pi$ |
| $v_{a,0,AC}$ | 0 | 0 | $v_{THI}$ | $v_{0,AC,OPT}$ |
| $P_J$ (mW) | 14.56 | 9.75 | 8.47 | 7.95 |
| $P'_J$ (mW) | 14.67 | 9.83 | 8.43 | 8.12 |
| Error (%) | $-0.76$ | $-0.84$ | $0.48$ | $-2.13$ |

Test 1 refers to the conventional method: $v_{a,0,AC}$ = 0 and $\xi_{DC}$ = 1.
In Test 2, no AC voltage injection was used, but $\xi_{DC}$ was set to a lower value ($\xi_{DC}$ = 2/3 instead of $\xi_{DC}$ = 1).
In Test 3, a third harmonic AC voltage injection was added and $\xi_{DC}$ was further decreased to 1/$\sqrt{3}$.
Test 4 refers to the optimum proposed method with $v_{a,0,AC}$ = $v_{0,AC,OPT}$ and $\xi_{DC}$ = $3\sqrt{3}\xi/2\pi$.

For each test, the current and voltage of one arm were sampled through the oscilloscope at a frequency of 25 MHz (500 k samples per 50 Hz period). Each cell current was calculated as follows:

$$i_{cell,n}(k) = g_n(k) \cdot i_a(k), \quad (28)$$

where $i_{cell,n}(k)$ is the current of the $n$-th cell at the sampling time $k$, $i_a(k)$ is the arm current measured at the sample point $k$, and $g_n(k)$ is the switching signal:

$$g_n(k) = \begin{cases} 
0, & \text{if } v_{a1}(k) < nV_{cell} (n-cell bypassed) \\
1, & \text{if } v_{a1}(k) \geq nV_{cell} (n-cell online)
\end{cases}. \quad (29)$$

For each test, the total cell loss of the considered arm is calculated by

$$P'_J = \frac{R_{cell}}{N_s} \sum_{k=1}^{N_s} \sum_{n=1}^{12} g_n(k) \cdot i_{a,0}^2(k), \quad (30)$$

where $N_s$ is the total number of samples per cycle.
Table 1 reports these results, together with the theoretical calculation of $P_J$. All the tests were carried out in correspondence with the same modulation index value, such that the arm output power $P_{arm}$ was fixed. The error between the experimental and theoretical results, which is also shown, is less than 2.5% for all cases (within the experimental error, e.g., due to arm current measurements).

As can be noted from the Test 1 results (Figure 6a,e), the arm voltage is centered around 15 V ($N_{cell}/2$) and its waveform corresponds to a quantized sine wave. The theoretical calculated cell losses (see Table 1) are substantially equal to the experimental ones.

Figure 6. Experimental results: total voltage and current during: (a) Test 1, (b) Test 2, (c) Test 3, (d) Test 4; cells currents waveform during: (e) Test 1, (f) Test 2, (g) Test 3, (h) Test 4.

As per the theoretical considerations, the Test 2 results testify that the cell losses are dependent on $v^*_{0,AC}$. Indeed, in comparison with Test 1, the losses are lower, proportional to the decrease of $\xi_{DC}$. From Figure 6b, it can be noted that the arm voltage reaches the zero value. In fact, the $\xi_{DC}$ setting of Test 2 corresponds to the minimum $\xi_{DC}$ value, considering that no AC common mode voltage has been injected.

Test 3 clarifies that even if $P_J$ does not directly depend on $v^*_0$, an AC injection can affect the minimum settable value of $\xi_{DC}$. In this case, indeed, it was possible to set $\xi_{DC} = 1/\sqrt{3}$, without violating $0 \leq v^*_k \leq N_{cell}$. The third injected harmonic can be recognized from the altered waveform of the arm voltage (Figure 6c), which shows a wider constant value in correspondence with the maximum and minimum point.

Finally, the optimum AC common mode voltage injection of Test 4, which displays visible notches in the arm voltage (Figure 6d), guarantees the minimum settable value of $\xi_{DC}$, leading to the minimization of cell losses. The decrease of the cell losses is associated with a decrease of the total rms current value in the 12 cells, which can be easily noted when compared to Figure 6e,h. In comparison with the conventional method of Test 1, the proposed method assures a cell energy saving of about 45%, which is consistent with the fact that the experimental converter is operating with a mid-range $\xi$ value.

7. Additional Losses Numerical Example

In order to better evaluate the relative impact of the cell loss reduction introduced by the proposed method, other losses occurring in the system should be considered. These are principally switching and conduction losses occurring in the power electronic devices. For an MMC with a large number of modules driven by NLM, the switching losses are negligible, since the power devices switch at the fundamental output frequency. The VSI is
instead characterized by both conduction and switching losses. A rapid example can be
developed by completing the analysis of [11] (which compares the power electronics losses
of a traditional VSI to those of a DS-MMC) with the electrochemical cell losses evaluation of
the present work. While the VSI is equipped with FZ300R12KE3G IGBT, the MMC is built
upon AUIRFS8409-7P power MOSFET. Both solutions constitute a 80 kW–220 V–250 A
converter, fed by a 24 kWh battery system based on 11 Ah Kokam SLPB55205130H cells
with an internal resistance of approximately 1.6 mΩ each.

The procedure given in [11] only considers the losses occurring in the power electronic
devices; at half full-load, with $\xi = 0.7$ and $\cos \phi = 1$, the power electronics losses are as
shown in Figure 7a.

![Figure 7. (a) Power losses of an 80 kW–24 kWh converter realized by the MMC and VSI solution: (a) Power electronics losses and (b) all losses.](image)

Cell losses may be added to the analysis of [11], by applying Equations (21), (24) and (27),
resulting in the loss breakdown given in Figure 7b. As expected, the MMC cell losses are
higher than those of VSI and represent an important fraction of the total losses. In this example,
the proposed technique produces a 42% reduction in cell losses, which equates to an overall
19% loss reduction. As a consequence of applying the proposed technique, the total efficiency
is increased by 1.8%.

Naturally, the actual increase of the MMC overall efficiency achieved through the
proposed technique depends on the particular operating condition. It is important to
highlight that loss reduction in electrochemical cells is doubly beneficial as it leads to a
direct reduction in detrimental cell heating and increase in the service life.

8. Conclusions

In the present work, the calculation of the cell losses for Modular Multilevel Converters
(MMCs) equipped with half-bridge modules, each directly connected to an elementary...
electrochemical cell and driven by Nearest Level Control (NLC), has been generalized for any common mode voltage value. It was observed that the total cell losses of both a Single Star MMC and a Double Star MMC are significantly higher than those occurring in an equivalent VSI.

The novel analytical investigation allowed the development of an optimal common mode voltage injection law that minimizes the cell power losses under all balanced operating conditions. The proposed method delivers a significant reduction in cell losses, which is dependent on the converter working condition and, in particular, is about 17.5% in the worst case.

All of the analytical derivations have been validated by means of an experimental set-up based on a two-arm SS-MMC with 12 modules per arm, showing an error within 2.5% in all the considered cases and confirming the effectiveness of the proposed technique, with a cell loss reduction of about 45% in the performed tests.

In conclusion, in comparison with conventional VSI solutions, MMC solutions show intrinsic advantages (modularity, BMS functionality, fault tolerance, reduced EMI issue, etc.), but are characterized by higher cell losses, which represent an important fraction of the total losses. The proposed optimum common mode voltage injection law introduces a significant reduction of the converter losses, making MMCs more attractive with respect to conventional power converters.

Author Contributions: Conceptualization, G.B. and I.S.; methodology, D.R. and I.S.; software, E.C.; validation, D.R. and G.B.; formal analysis, D.R.; investigation, E.C. and I.S.; data curation, G.B. and E.C.; writing—original draft preparation, G.B., E.C. and I.S.; writing—review and editing, D.R.; supervision, I.S. All authors have read and agreed to the published version of the manuscript.

Funding: This research received no external funding.

Institutional Review Board Statement: Not applicable.

Informed Consent Statement: Not applicable.

Conflicts of Interest: The authors declare no conflict of interest.

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