An intrinsic mobility ceiling of Si bulk

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Abstract
We compute by Density Functional Theory-Non Equilibrium Green Functions Formalism (DFT-
NEGFF) the conductance of bulk Si along different crystallographic directions. We find a ceiling
value for the intrinsic mobility of bulk silicon of $8.4 \cdot 10^6 \text{cm}^2/\text{Vs}$. We suggest that this result is
related to the lowest effective mass of the $<001>$ direction.

Silicon is the most widely used material in current electronic devices. Due to downscaling, much work
has been done on nanostructures such as nanowires. Their structural and electrical properties have
been studied experimentally and theoretically [1] and devices have been made with them [2]. Since
these new structures are to be used in electronic devices, an understanding of their electronic transport
behaviour is essential.

Carrier mobility is an important transport property of a semiconductor. It reflects the capacity of the
charge to move in the material and to respond to field changes [3].

Due to the challenges of current ab initio computations, transport properties have been studied by this
method only in very low dimensional systems [4, 5, 6]. The most common way to examine transport
and mobility in bulk silicon has been to solve the Boltzmann equation using MonteCarlo methodology
with electron-phonon interaction as a scattering mechanism [7, 8, 9, 10, 11]. To our knowledge, no
theoretical approach based on fully atomistic ab initio methods has been used to study bulk behavior.

Materials in devices are normally used in an anisotropic way: they are grown along a certain axis
and one direction of transport becomes predominant, for example, silicon nanowires (SiNWs). The
transport properties are expected to be sensitive to the crystal orientation, due to the anisotropy of
band structure of bulk silicon. Thus, a detailed understanding of the electrical properties as a result
of the different growth conditions is required in order to assess the impact of the crystal structure
on transport properties and to determine which orientation is best suited to the engineering of high
performance thick SiNW-based devices.

In this Letter, we present a theoretical study of the electron transport properties of bulk silicon as an
approximation to the transport in thick nanodevices, where the bulk properties are still predominant
over confinement effects [12]. For the sake of completeness, the most significant growth directions
of SiNWs, namely cubic diamond phase oriented along $<001>$, $<110>$ and $<111>$ axis, have been
considered. We have evaluated the intrinsic electron mobility due to elastic scattering of the electrons
inside the crystal. We compare the values with current experimental results from the literature and
with the corresponding results for small SiNWs, finding a ceiling of the value of bulk silicon mobility
depending on the crystallographic direction which is lower than the reported value for SiNWs
[13, 14, 15]. The authors suggest that surface states may be responsible for the measured conduction.

Here the electronic transport is studied inside an open-boundary bulk system described by first-
principles field within Non Equilibrium Green Functions Formalism (NEGFF) [16, 17]. We performed

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Density Functional Theory (DFT) calculations with a numerical atomic orbital basis set [18]. We used a double-$\zeta$ polarized basis set, with the generalized gradient approximation [19] for the exchange-correlation functional in the PBEsol parametrization [20]. Real space mesh cut-off and k-grid values were set to converge total energy values within 0.1 meV and maximum absolute forces over atoms are smaller than 0.004 eV/Å in all cases.

The electronic transport properties were studied with Non Equilibrium Green Functions Formalism, within the Keldysh formalism [21], based on DFT as implemented in TranSIESTA code [22]. It computes the electronic transport properties due to elastic scatter in a ballistic regime of a nanoscale system confined in a central scattering region (C) in contact with two semi-infinite left (L) and right (R) electrode regions at different electrochemical potentials. The bias voltage $V_b$ between them corresponds to the electrochemical potential difference between the left ($\mu_L$) and the right ($\mu_R$) electrodes, $eV_b = \mu_L - \mu_R$. In our case, both electrodes and the contact region were made of Si in the same crystallographic orientation.

The density matrix from the incoming scattering states of the left ($l$) and right ($r$) electrode is

$$D(\vec{x}, \vec{y}) = \sum_l \psi_l(\vec{x}) \psi^*_l(\vec{y}) n_F(\epsilon_l - \mu_L) + \sum_r \psi_r(\vec{x}) \psi^*_r(\vec{y}) n_F(\epsilon_r - \mu_R),$$

where the scattering states starting in the left electrode are

$$\psi_l(\vec{x}) = \psi^0_l(\vec{x}) + \int d\vec{y} G(\vec{x}\vec{y}) V_L(\vec{y}) \psi^0_l(\vec{y}).$$

These are generated from the unperturbed incoming states of the uncoupled, semi-infinite electrode $\psi^0_l$, using the retarded Green’s function $G$ of the coupled system [22] and the interaction between the left electrode and the contact region $V_L$.

The current $I$ through the contact region was calculated using the Landauer-Buttiker formula [23],

$$I(V) = G_0 \int_{-\infty}^{\infty} d\epsilon [n_F(\epsilon - \mu_L) - n_F(\epsilon - \mu_R)] T(\epsilon, V_b),$$

where $G_0 = 2e^2/h$ is the unit of quantum conductance, $n_F$ is the electrode distribution function and $T(\epsilon, V_b)$ is the transmission probability of electrons incident at an energy $\epsilon$ through the device under the potential bias $V_b$.

We have computed the I(V) characteristic (Fig. 1) in each direction and evaluated the intrinsic conductance.

We propose to calculate the corresponding electron mobility $\mu$ of the electrons along the different directions of bulk Si. Assuming an intrinsic concentration of carriers $n$ corresponding to a temperature of 300K, the corresponding mobility $\mu = \sigma/en$ (where $\sigma$ is the associated conductivity computed as the conductance across the corresponding unit cell in each direction) is shown in Fig. 2.

The mobility in bulk semiconductors as we go down from room temperature increases at first due to the suppression of phonon scattering [16]. But it does not increase any further once the phonon scattering is small enough so that impurity scattering becomes the dominant mechanism. With undoped samples we can obtain higher mobilities, because we have suppressed inelastic scattering centers (Fig. 2).

In agreement with these considerations our computed mobility is greater than experimental results and thus constitutes a ceiling value for them. TranSIESTA code only takes into account the transport due to elastic scatter inside the crystal, without considering the contribution of inelastic scatter due to phonons and impurities. Despite current advances [25], up to now there is no reliable way to compute
Figure 1: Comparison of the I(V) characteristic (I in this case is the current per unit of transversal area) of the three directions of cubic diamond $<001>$, $<110>$ and $<111>$.

Figure 2: Extract from ref. [3]. Experimental mobility of electrons in Si as a function of temperature with different impurity concentrations (point symbols). The dash-dot line indicates the pure lattice mobility obtained with Monte-Carlo simulation [9]. Our computed mobilities (continuous lines: red for $<001>$, blue for $<110>$ and green for $<111>$) are compared with experimental values for Si bulk (point symbols [3] and square symbol [24]) and experimental [14] (diamond symbol) and theoretical (with NEGFF) [15] (dot-dot line) values of thin SiNWs.
inelastic scattering in this framework. On the other hand, our calculated conductivities are lower than experimental ones in SiNWs because in bulk the surface states are not taken into account, and they seem to play an important role in transport properties in very thin SiNW [14, 15]. Thus, our result becomes a threshold to distinguish bulk conductivity from conductivity affected by nanoscale effects. Electrons in silicon occupy several valleys which have different orientations. The effective mass associated with these valleys gives the response of the electrons to an external field; the electrons with lower effective mass are faster, and the direction of these valleys is the preferred direction of the electron transport. The results are similar to the experiments [26] to increase the mobility due to an application of a uniaxial stress along a direction such that the valleys presenting the lower effective mass in the direction of the field have less energy than the other valleys. Electrons in equilibrium will have greater tendency to occupy these ”faster valleys”, and the overall mobility will increase.

We have calculated the effective mass (see Table 1), with respect to the free electron mass $m_0$, around the conduction band minimum (CBM) in each direction using the expression $\Delta E_c(k) = \hbar^2 k^2 / 2m^*$, where $m^*$ is the electron effective mass around the CBM. We fit a quadratic of the form $y = mx^2$, then we solve $m = \hbar^2 / 2m^*$. For bulk Si in <001> direction, we calculate $m^*$ to be 0.884 $m_0$. This value corresponds to the longitudinal effective mass of electrons in bulk silicon and it is close to the reported experimental values [27]. In order to check the calculations we computed the electron transversal effective mass and the light hole effective mass, and the values were 0.194 $m_0$ and 0.163 $m_0$, respectively. They are close to the reported experimental values [28].

|                | <001>  | <110>  | <111>  |
|----------------|--------|--------|--------|
| electron effective mass | 0.884  $m_0$ | 1.198  $m_0$ | 1.575  $m_0$ |
| mobility ($cm^2/Vs$) | 8.4 · $10^6$ | 6.04 · $10^6$ | 3.99 · $10^6$ |

Table 1: Electron effective mass and mobility for bulk Si oriented in <001>, <110> and <111> direction.

In conclusion, we have performed a theoretical study of the mobility of bulk Si based on the description with NEGFF of the electronic transport. We have evaluated different crystallographic directions and found the <001> the best for electrons in bulk Si due to the absolute minimum of the lowest conduction bands in the band diagram and the lower corresponding effective mass. We have performed the calculations inside the framework of Landauer-Buttiker formalism and found an intrinsic conductance due to elastic scattering of electrons inside silicon bulk, and we can associate a mobility value to it. In this sense, our computed values are a ceiling (i.e. the maximum possible mobility that the material could achieve). With the contribution of inelastic scatter due to phonons and impurities, the mobility is reduced. Moreover, our result becomes a threshold between bulk conductivity and conductivity affected by nanoscale effects. While the Non Equilibrium Green Functions Formalism is currently used to study transport in very small nanostructures, we have used it in huge configurations (bulk), showing that it is a very powerful technique for evaluating properties of many-body systems.

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