Gator: Customizable Channel Pruning of Neural Networks with Gating

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Abstract. The rise of neural network (NN) applications has prompted an increased interest in compression, with a particular focus on channel pruning, which does not require any additional hardware. Most pruning methods employ either single-layer operations or global schemes to determine which channels to remove followed by fine-tuning of the network. In this paper we present Gator, a channel-pruning method which temporarily adds learned gating mechanisms for pruning of individual channels, and which is trained with an additional auxiliary loss, aimed at reducing the computational cost due to memory, (theoretical) speedup (in terms of FLOPs), and practical, hardware-specific speedup. Gator introduces a new formulation of dependencies between NN layers which, in contrast to most previous methods, enables pruning of non-sequential parts, such as layers on ResNet’s highway, and even removing entire ResNet blocks. Gator’s pruning for ResNet-50 trained on ImageNet produces state-of-the-art (SOTA) results, such as 50% FLOP reduction with only 0.4%-drop in top-5 accuracy. Also, Gator outperforms previous pruning models, in terms of GPU latency by running 1.4 times faster. Furthermore, Gator achieves improved top-5 accuracy results, compared to MobileNetV2 and SqueezeNet, for similar runtimes. The source code of this work is available at: https://github.com/EliPassov/gator

1 Introduction

The use of NNs, in particular, convolutional neural networks (CNNs), in a variety of Computer Vision applications has surged considerably in recent years. Most NNs consist of tens to hundreds of millions of weight parameters and require typically billions of floating point operations (FLOPs) for processing a single image. This limits their use on available hardware, especially on small devices. Consequently, many works in recent years have focused on reducing the computational cost of network architectures without severely impairing their performance, either by creating more efficient network architectures or by applying network compression. Pruning is a popular method among compression methods, where network weights and activations are removed. Pruning methods can be categorized as unstructured, i.e., referring to the removal of individual weights,
or *structured*, i.e., referring mostly to the removal of convolutional channels. Unstructured pruning is considered much less practical, as it requires special hardware for efficient application.

More recent techniques also utilize dynamic computation in conjunction with *gating* mechanisms to control the network operations that are invoked for each input instance; thus, the computational cost is determined dynamically for each individual input. Our method utilizes dynamic gating mechanisms only during training, to learn which channels should be removed, thereby producing a pruned network without any gating mechanisms.

The effectiveness of pruning methods is usually assessed by these parameters:

- Reduction of memory required for storing weights;
- Theoretical speedup, measured by the reduction of the number of FLOPs;
- Latency/runtime on a specific hardware.

Note that latency is not commonly used as an optimization objective, and that theoretical speedup is never translated into the exact same practical speedup. For unstructured methods, this is due to the fact that most hardware does not support sparse operations, leading to no optimization in practice; for structured pruning, the sole focus on the FLOPs metric ignores hardware-specific behavior.

This paper makes the following contributions:

- Presentation of *Gator*, an innovative pruning technique that utilizes channel hard-gating to remove channels during training while also adjusting the remaining weights;
- Introduction of a novel formulation for dependencies between NN layers allowing Gator to prune non-sequential parts of NN architectures (such as ResNet’s highway), which are not handled by most pruning methods;
- Presentation of a customizable cost function, which reflects various practical compression criteria, such as memory reduction, FLOPs reduction, and hardware-specific speedup;
- Obtaining new SOTA results for ResNet-50 pruning on ImageNet, in particular, 50% FLOPs reduction with 0.4% top-5 accuracy drop and runtime speedup (latency reduction) of 1.4 for the same accuracy as other methods;
- Presentation of additional results for higher pruning rates, outperforming MobileNet V2 and SqueezeNet for the same runtime.

2 Related Work

2.1 Network Compression

Most methods for NN compression can be categorized into three categories. First, tensor decomposition ([20], [15], [17]), where the convolutional operation is approximated by decomposing it to smaller operations.

Second, quantization methods ([33], [34], [2]), in particular, binary methods ([24], [3]), where the network weights are approximated with lower precision variables allowing for memory reduction.
And third, pruning, where parts of layer outputs or weights are removed. In early works [21] and more recent ones [8], pruning methods are characterized by weight elimination and a "fine tuning" training stage. A thorough review of pruning methods can be found in [1], which also highlights the distinction between structured and unstructured pruning. Since unstructured pruning cannot reduce computational costs on most hardware types, we will focus and compare Gator only to other structured pruning methods.

### 2.2 Structured Channel Pruning

Most channel pruning works differ in the criteria by which channels are pruned. In [22] the smallest sums of weights of channels from all layers are pruned, while non sequential layers are being considered. The pruning criterion in [23] is the first order Taylor expansion of the loss ($w \frac{\partial L}{\partial w}$). Weights closest to the geometric median are pruned in [9], in an iterative process for each layer. And in [32], individually trained squeeze and excitation [12] blocks are used to prune layers with the lower attention values.

Other methods, such as [11], remove the convolutional input channels with the least impact on the output. While [35] first train auxiliary classification losses on intermediate layers and define reconstruction losses on potential channels to be remove. The channels chosen to be removed are those which have the smallest impact on the sum total of all classification and reconstruction losses.

### 2.3 Gating

Dynamic gating modules determine, for each individual input, which parts are executed and which are skipped within a computational NN graph. In [30], squeeze and excitation blocks [12] are used with a gumbel softmax [16] gate on individual residual blocks, enabling skipping some blocks dynamically. In [4], the same gating mechanism is applied on individual channels.

Methods which do not rely on gumbel softmax, such as [7], use regression on the global pooling vector of the convolutional input to determine dynamically which channels are passed to the convolution. In [13], convolutions are divided into two parts, such that the output of the first gates the execution of the second.

A soft sigmoid gate is used in [6] to prune connections in fully-connected layers. Our method also uses a sigmoid; however, it gates entire channels, using a hard gating mechanism with a different learning process which leads to practical results on ImageNet.

### 2.4 Network Architecture Search

Network architecture search (NAS) first introduced in [36]. It focuses on finding an optimal network architecture within a wide search space using methods such as reinforcement learning. Many NAS works, such as [29], typically regard the number of layer channels as part of the search space while minimizing the FLOPs
metric. The search space of consists of layer pruning ratios minimizing either FLOPs or memory metrics for a given target accuracy. Other methods, such as [31], optimize the latency metric. Although many NAS methods also share the idea of optimizing the number FLOPs, memory footprint or latency, and even yield SOTA performance in many cases, they are extremely computationally intensive compared to pruning methods, and thus exhibit sub-optimal latency.

3 Approach

In this section we describe the components of Gator. We start with the gating mechanism in Subsection 3.1 and then explain in Subsection 3.2 how modeling the network architecture as a hypergraph encapsulates the channel dependency between different layers in the network architecture, thereby allowing for non-sequential pruning. Finally, in Subsection 3.3 we present the loss function for computational cost and demonstrate how to customize it for FLOPs, memory, and latency reduction.

3.1 Channel Gating

Gator utilizes a gating mechanism which determines for each individual channel whether it should be passed or gated for each individual sample within a batch. The gating mechanism (see Eqn. 1) uses a binary version of the Gumbel softmax distribution (introduced in [16]), referred to as the logistic sigmoid distribution.

\[
\sigma(\theta) = \frac{1}{1 + e^{-\frac{\theta + x}{\tau}}} \quad \text{where} \quad x \sim \text{Logistic}(0, 1)
\]

\[
g(\theta) = \begin{cases} 
1 & \sigma(\frac{\theta + x}{\tau}) \geq 0.5 \\
0 & \sigma(\frac{\theta + x}{\tau}) < 0.5 \approx \text{sign}(\theta + x) 
\end{cases}.
\]

The gating binary value \( g(\theta) \) is determined by adding the gating weight \( \theta \) to a random variable \( x \) sampled from a logistic distribution. The purpose of the sigmoid function is to enable a continuous gradient while the threshold test provides a discrete output equivalent to a sign function in the forward pass. The temperature parameter \( \tau \), which controls the gradient magnitude, is set to the default value of 1 and is, henceforth, ignored. The addition of the random
variable $x$ is according to the reparameterization trick in [18], which enables a computational flow of the gradients for the gating weights as part of the backpropagation process, while providing a discrete value in the forward pass.

A gating module can be placed before or after a layer to pass or block individual channels, thereby simulating the effect of pruning input or output channels. Fig. [1] illustrates gating on an output of a model.

### 3.2 Hypergraph of Neural Network Channels

Early pruning methods on sequential network architectures, such as VGG [27], focus naturally on individual convolutions, so they are applicable to all layers. However, most recent architectures consist of many non-sequential parts. Considering ResNet architectures, for example, note that inputs of some convolutions depend on outputs of multiple convolutions residing in different ResNet blocks, as a result of the “highway” path configuration. Thus, using a channel pruning scheme, in an attempt to remove a channel from the output of a single convolution may not remove the same channel from the input of a dependent convolution or the output of another convolution, as implied by the network’s non-sequential architecture. In other words, one needs to consider the impact of a channel removal on the input and output channels of all convolutions dependent on that channel. This aspect is illustrated for the ResNet-18 architecture in Fig. [2]. We shall refer to a collection of channel-dependent convolutions, as far as their input and output channels are concerned, as a channel dependency group.

We define an undirectional hypergraph $H = (V, E)$, where the set of vertices $V$ corresponds to the union of the input and output channel sets over all convolutions. Let $C^{\text{in}}$ and $C^{\text{out}}$ denote these input and output sets, respectively. Specifically, the vertices $c_{i}^{\text{in}} \in C^{\text{in}}$ and $c_{i}^{\text{out}} \in C^{\text{out}}$ represent the (numbers of the) input and output channels of convolution $i$, respectively. The set of edges $E$ is composed of subsets of $V$, where an edge $e_{j} \in E$ corresponds to channel dependency group $j$. (Each edge in $E$ is referred to as a dependency edge.) We represent the dependency group as $e_{j} = c_{j}^{\text{in}} \cup c_{j}^{\text{out}} \subseteq V$, where $c_{j}^{\text{in}} \subseteq C^{\text{in}}$ and $c_{j}^{\text{out}} \subseteq C^{\text{out}}$ denote, respectively, the input and output channel subsets associated with dependency group $j$. We can now define pruning as a removal of channels from each dependency edge.

For sequential networks, each dependency edge is composed of a pair of a convolutional layer’s output and the following convolutional layer’s input. However, we can now also map channel dependency for non-sequential networks, such as ResNet-50, as detailed in Table [1]. Specifically, 53 convolutions and a fully-connected layer are mapped to 37 dependency edges, 32 of which are trivial edges representing sequential connections inside bottleneck blocks, and to 5 additional edges, which include more than two convolutions (some of which are located along the “highway” path).
Fig. 2. Layer 2 blocks 1,2 and layer 3 block 1 from ResNet-18 architecture, demonstrating channel dependency between 6 different convolutions illustrated by red lines; Thicker lines indicate the highway path which is the main cause of this dependency.

3.3 Loss Function for Computational Cost

For each edge $e_j$, we have $c_j(t)$ non-gated channels at iteration $t$ from a total of $c_j$ channels, computed from the binary gating values defined in Eqn. (1):

$$c_j(t) = \sum_{i=1}^{c_j} g(\theta_j^i).$$  (2)

Let $\lambda_j(t)$ denote the per-channel cost factor for iteration $t$. The change in cost for different iterations stems from the fact that the number of non-gated input and output channels of a convolution changes at each iteration.
Table 1. Dependency mapping for ResNet-50; Each row represents one edge composed of output and input layers (vertices); encoding used, L1B1-(example of) layer and block, C0-first convolution, C1,C2,C3-bottleneck convolutions, D-downsampling convolution, and FC-fully connected; for brevity, trivial edges inside bottlenecks are listed once.

| OUTPUT LAYERS | INPUT LAYERS | # EDGES |
|---------------|--------------|---------|
| c0            | L1B1D, L1B1C1 | 1       |
| c1            | c2           | 16      |
| c2            | c3           | 16      |
| L1B1D, L1B1C3, L1B2c3, L1B3c3 | L1B2c1, L1B3c1, L2B1D, L2B1C1 | 1 |
| L2B1D, L2B1C3, L2B2c3, L2B3c3, L2B4c3 | L2B2c1, L2B3c1, L2B4c1, L3B1D, L3B1C1 | 1 |
| L3B1D, L3B1C3, L3B2c3, L3B3c3, L3B4c3, L3B5c3, L3B6c3 | L3B2c1, L3B3c1, L3B4c1, L3B5c1, L3B6c1, L4B1D, L4B1C1 | 1 |
| L4B1D, L4B1C3, L4B2c3, L4B3c3 | L4B2c1, L4B3c1, FC | 1 |

We now define the auxiliary computational loss

$$L_{\text{computation}}(t) = \frac{\sum_{j=1}^{m} c_j(t) \lambda_j(t)}{\sum_{j=1}^{m} c_j \lambda_j}$$

(3)

where $m$ is the number of dependency edges, $\lambda_j(t)$ is the normalized, per-channel cost factor, and $\lambda_j$ is the initial cost factor. The normalization sets the initial loss to $L_{\text{computation}}(0) = 1$, so that it is easier to calibrate the total loss

$$L_{\text{total}}(t) = L_{\text{original}} + \alpha L_{\text{computation}}(t)$$

(4)

where $L_{\text{original}}$ is the original loss and $\alpha$ is a global weight being calibrated.

The memory footprint cost factor is the number of weights in all layers of each individual edge, i.e.,

$$\lambda_j(t) = \sum_{q_i \in C_j^{\text{in}}} k^w q_i k^h q_i c_{\text{out}}(t) + \sum_{q_i \in C_j^{\text{out}}} k^w q_i k^h q_i c_{\text{in}}(t)$$

(5)

where $C_j^{\text{out}}$ and $C_j^{\text{in}}$ are the convolution sets corresponding to $C_j^{\text{out}}$ and $C_j^{\text{in}}$, respectively. For each convolution $q_i$, $k^w$ and $k^h$ denote the kernel width and kernel height, respectively. Also, $c_{\text{in}}(t)$ and $c_{\text{out}}(t)$ denote, respectively, the number of non-gated input and output channels at iteration $t$.

The theoretical speed cost factor is computed for a single input pixel, by multiplying the number of weights of each layer by the downsample factor $d_i$, i.e., the ratio between the number of pixels in the current layer’s output and the original input image. Typically $d_i = 4^{-s_i}$, where $s_i$ is the number of stride-2 operations (e.g., convolutions, maxpooling, etc.) encountered on a computational path from the input image to the current layer $i$. Thus the FLOPs cost factor is...
given by

\[ \lambda_j(t) = \sum_{q_i \in C^\text{in}_j} d_i k_{q_i}^w k_{q_i}^b q_i^\text{in}(t) + \sum_{q_i \in C^\text{out}_j} d_i k_{q_i}^w k_{q_i}^b q_i^\text{out}(t). \] (6)

For latency, the cost factor cannot be generally defined, as it is hardware-specific. Instead, we approximate, in this case, the cost factor \( \lambda_j(t) \), by measuring the latency difference between the original network and a network with 50\% of the channels in \( e_j \) pruned.

### 3.4 Learning Rate Gradient Update Adjustment

During training, the loss function described in Eqn. 3 produces a gradient update equivalent to the cost factor. The cost factor can be quite different in scale for different layer. For FLOPs cost, for instance, the cost factor is smaller for deeper layers (since they have more channels). As a result, pruning of deeper layers will be slower (take more iterations) compared to shallow layers. In turn, this could also lead to over pruning of shallow layers, causing extensive adjustment of the rest of the network weights to compensate, which eventually produces a suboptimal pruning result.

To combat both issues, we define a different time-dependent learning rate for the weights of each individual dependency edge

\[ \eta_j(t) = \frac{\gamma \eta(t)}{\lambda_j(t)}, \] (7)

where \( \eta_j(t) \) is the learning rate of dependency edge \( j \), \( \eta(t) \) is the global learning rate, and \( \gamma \) is a constant parameter.

Breaking down the gradient update for an individual gating weight \( i \) on dependency edge \( j \), \( \theta_{ij} \), with a global pruning weight \( \alpha \) we get

\[ \eta_j(t) \nabla_{\theta_{ij}} = \eta_j(t) \frac{\partial L}{\partial \theta_{ij}} = \eta_j(t) \left( \frac{\partial L}{\partial g(\theta_{ij})} \right) \frac{\partial g(\theta_{ij})}{\partial \theta_{ij}} = \eta_j(t) \frac{\partial g(\theta_{ij})}{\partial \theta_{ij}} \left( \alpha \frac{\partial L_{\text{computational}}}{\partial g(\theta_{ij})} + \frac{\partial L_{\text{original}}}{\partial g(\theta_{ij})} \right), \] (8)

where we substituted, respectively, Eqn. 4 in the second line, Eqn. 3 and Eqn. 7 in the third line, and Eqn. 2 in the last line. The role of parameter \( \gamma \) is to balance out the large values of the term \( \frac{1}{\lambda_j(t)} \), since \( \lambda_j(t) \) is generally small.

In Eqn. 8 we observe the two components of the gradient produced by the computational loss and the original loss. The first component attempts to gate
the channel, while the other retains as much relevant information as possible. The gradient adjustment from Eqn. 7 is applied on both parts, which is crucial for a balanced optimization. The last line shows how the gradient adjustment cancels out $\hat{\lambda}_j(t)$ for the pruning cost component, making the impact of the pruning loss function on the gradient update similar for all edges.

Another insight from Eqn. 8 is the self-pruning phenomenon we have occasionally observed, where the original loss component contributes to the removal of certain channels on its own (even with a zero pruning loss). This could occur if a channel has a negative impact on the original loss, which in turn, produces a larger update on the gating weight compared to the update on the channel weight; thus, the channel is pruned faster then it can adjust. To alleviate this issue, we calibrate $\gamma$ by performing training with no pruning loss, and setting it to balance reasonable conversion time with minimal self-pruning.

4 Experiments

4.1 Methodology and Setup

The lack of a comparison standard for pruning results is discussed in [1], listing different types of pruning, different benchmark types, and a plethora of different network architectures on which various pruning methods are benchmarked. Hence, comparing between pruning methods is only relevant for compatible criteria, i.e., same pruning type (e.g., structured pruning), optimized parameter (e.g., FLOPs reduction), and architecture. (From a practical consideration, we only compared with structured channel pruning methods.) Similarly to most other works, we performed pruning on the ImageNet ILSVRC2012 [25] and CIFAR-10 [19] classification challenges. However, we believe that results for CIFAR-10 are not very indicative, due to the small dataset, the large variation in accuracy [5], and the fact that the dataset does not adequately represent “real-world” data. We chose the ResNet architecture, which enabled demonstrating Gator’s pruning capability for a non-sequential architecture and comparing it to other works. Note that newer and smaller architectures would require a much greater effort in recreating proper training and pruned versions, not to mention that there are very few results to compare to.

In both experiments, we trained our model with the standard SGD optimizer, setting the momentum to 0.9 and the weight decay to 0.0001. We initialized the gating probability to 0.005, which was found empirically to have a low impact on the network’s initial accuracy, while still enabling the pruning in a relatively low number of epochs. Note that a lower initial gating probability would have required more steps to reach the pruning threshold probability set to 0.5. Crossing the threshold probability marks a channel as permanently pruned. We performed iterative pruning with an increasing global weight $\alpha$. Each pruning iteration consisted of training with gating, and of additional fine-tuning without gating. The next iteration loaded the weights produced by the gating phase.
4.2 Results for ResNet-50 on ImageNet

For pruning ResNet-50 trained on ImageNet, we used the pretrained weights. The batch size was 256, which was split between two GPUs. However, due to the lack of RAM, we used gradient accumulation and split each half batch into 4 parts, training equivalently on 8 GPUs with sub-batches of size 32 each. (This only affects the batch normalization.) We trained with gates for 30 epochs, using learning rates of 0.01 and 0.001 for the first 20 epochs and the latter 10 epochs, respectively. Table 2 contains fine-tuning results for lower pruning rates \((\alpha \leq 2)\). We trained, in this case, for 40 epochs with a learning rate step policy of 0.001, 0.0001, and 0.00001 for 20,10, and 10 epochs, respectively. For high pruning rates, we trained for 20 epochs (given the available resources under the time constraints), with a learning rate step policy of 0.001, 0.0001, and 0.00001 for 10,5, and 5 epochs, respectively (see Table 3). For image preprocessing, we applied mean subtraction and standard deviation normalization for each color channel. Data augmentation consisted of selecting a random \(224 \times 224\) crop from the original image and applying to it a random horizontal flip. Practical speedup was measured by timing an inference run in Pytorch on GTX 1080 Ti GPU with a batch of size 32 for \(224 \times 224\) images.

Table 2 contains comparative results of optimized Gator pruning on FLOPs and latency, with other methods (also illustrated in Fig. 3(a)). Note the very similar accuracy obtained by the two optimization methods and how each type is better suited for its designated reduction metric. (Although there is a single instance that outperforms Gator (optimized on FLOPs reduction) for lower pruning rates, note that the accuracy reported in this case \([35]\) exceeds the original ResNet-50 accuracy; this could be explained by the use of auxiliary classification losses, known to enhance the performance \([28]\). Thus, we believe this isolated instance is not a fair comparison.)

Regarding the latency speedup, Gator outperforms all other methods, achieving a speedup of 1.4 for some pruning rates.

Results for higher pruning rates are available in Table 3. We compared Gator’s latency optimization to that of much smaller architectures, such as SqueezeNet \([14]\)
Table 2. Comparison of pruned ResNet-50 on ImageNet for Gator optimized on FLOPs and GPU latency; number of Gator variant indicates pruning weight $\alpha$; results are ordered and divided to categories by FLOPs reduction (%)

| Pruning Algorithm | Top-1 | Top-5 | FLOPs | Memory % | Practical Speed-up |
|-------------------|-------|-------|-------|----------|-------------------|
| channel pruning   | 72.30 | 90.80 | 24.14 | 15.52    | 1.069             |
| Gator latency 0.25| 76.00 | 92.94 | 26.75 | 5.22     | 1.310             |
| Gator flops 0.25  | 75.89 | 92.93 | 31.48 | 8.88     | 1.200             |
| DCP 30            | 76.35 | 93.09 | 36.04 | 33.45    | 1.145             |
| Geometric 30%     | 75.59 | 92.63 | 41.89 | 37.64    | 1.162             |
| Gator latency 0.5 | 75.28 | 92.50 | 48.27 | 18.05    | 1.647             |
| Gator flops 0.5   | 75.19 | 92.61 | 49.91 | 18.96    | 1.442             |
| Geometric 40%     | 74.83 | 92.32 | 53.16 | 47.72    | 1.251             |
| DCP 50            | 74.88 | 92.19 | 55.77 | 51.63    | 1.477             |
| PCAS              | 72.68 | 91.09 | 56.70 | 51.20    | –                 |
| Gator latency 1   | 74.24 | 91.95 | 61.17 | 31.18    | 1.857             |
| Gator flops 1     | 74.14 | 91.99 | 62.64 | 31.40    | 1.612             |
| DCP 70            | 72.65 | 91.03 | 71.10 | 66.00    | 1.569             |
| Gator flops 2     | 72.36 | 90.97 | 76.57 | 55.15    | 2.106             |

1 Speedup multiplier for batch 32 on single GTX 1080 Ti GPU
2 Better accuracy then baseline ResNet-50.

Table 3. Comparison of small networks to high rate pruned ResNet-50 on ImageNet for Gator optimized on GPU latency; number of Gator variant indicates pruning weight $\alpha$; results are ordered and divided to categories by speedup

| Pruning Algorithm | Top-1 | Top-5 | FLOPs | Speed
|-------------------|-------|-------|-------|------|
| Gator latency 2   | 72.69 | 90.98 | 973M  | 2.273 |
| MobileNet V2      | 71.87 | 90.29 | 301M  | 2.399 |
| squeezenet 1.0    | 58.10 | 80.42 | 888M  | 2.843 |
| Gator latency 4   | 69.48 | 89.3  | 551M  | 2.959 |
| Gator latency 16  | 61.07 | 83.72 | 191M  | 4.777 |
| squeezenet 1.1    | 58.19 | 80.62 | 387M  | 5.070 |
| Gator latency 20  | 56.89 | 80.67 | 134M  | 5.404 |

and MobileNet V2 [20], a newer architecture containing speed-efficient elements, such as inverse bottlenecks and grouped convolutions. According to Fig. 3b), our method slightly outperforms both of these small architectures.

4.3 Results for ResNet-56 on CIFAR-10

We trained ResNet-56 on CIFAR-10 from scratch for 240 epochs with a batch size of 128. The learning rate was 0.1 for the first 120 epochs, and was changed
Table 4. Pruning results of ResNet-56 on CIFAR-10 using Gator optimization on FLOPs and memory vs. other methods; baseline network accuracy is listed for comparison as it differs across methods

| Pruning Algorithm | Baseline Acc. % | FLOPs % Reduction | Memory % Reduction |
|-------------------|-----------------|-------------------|-------------------|
| Gator flops 1     | 93.63           | 33.50             |                   |
| Gator flops 2     | 93.63           | 33.21             | 42.70             |
| Gator memory 0.25 | 93.63           | 57.98             | 75.08             |
| Gator memory 0.5  | 93.63           | 57.98             | 75.08             |
| DCP               | 93.63           | 54.62             | 44.39             |
| DCP Adapt         | 93.80           | 49.75             | 49.24             |
| Geometric 40      | 93.63           | 52.30             | –                 |
| CHANNEL PRUNING   | 92.80           | 50.00             | –                 |
| PCAS              | 93.04           | 54.80             | 53.70             |
| Gator flops 16    | 93.63           | 59.33             | 73.40             |
| Gator memory 2    | 93.63           | 90.99             | 90.87             |

To 0.01 and 0.001 at epochs 120 and 180, respectively. For pruning, we trained with gating for 90 epochs, starting with a learning rate of 0.01 and decreasing it to 0.001 at epoch 60. This was followed by fine-tuning for another 30 epochs at the same learning rate. Image preprocessing consisted of mean subtraction and standard deviation division for each channel. Data augmentation consisted of random cropping and random horizontal flipping. The results are available in Table 4, where we see a significant reduction of FLOPs with little accuracy loss for Gator which also and best performance on memory reduction compared to other models. In addition Gator was able to prune entire ResNet blocks, thereby reducing the number of layers.

5 Conclusions

In this paper we presented Gator, a novel structural channel-pruning method, which utilizes dynamic channel gating during training to obtain smaller effective networks. We showed how to prune non-sequential architectures by using a hyper-graph formulation to encapsulate the mapping of dependencies between input and output channels of different convolutions. We also demonstrated how Gator can accommodate a variety of auxiliary cost functions to optimize FLOPs, memory, and latency reduction due to pruning. Latency reduction, in particular, is important for allowing to produce very light networks designed for small devices. Finally, we showcased SOTA results of pruning ResNet-50, trained on ImageNet, with respect to FLOPs reduction and practical speedup on actual hardware.

References

1. Blalock, D., Ortiz, J.J.G., Frankle, J., Guttag, J.: What is the state of neural network pruning? arXiv preprint arXiv:2003.03033 (2020)
2. Chen, W., Wilson, J., Tyree, S., Weinberger, K., Chen, Y.: Compressing neural networks with the hashing trick. In: International Conference on Machine Learning. pp. 2285–2294 (2015)
3. Courbariaux, M., Hubara, I., Soudry, D., El-Yaniv, R., Bengio, Y.: Binarized neural networks: Training deep neural networks with weights and activations constrained to $+1$ or $-1$. arXiv preprint arXiv:1602.02830 (2016)
4. Ehteshami Bejnordi, B., Blankevoort, T., Welling, M.: Batch-shaping for learning conditional channel gated networks. arXiv pp. arXiv–1907 (2019)
5. Frankle, J., Carbin, M.: The lottery ticket hypothesis: Finding sparse, trainable neural networks. arXiv preprint arXiv:1803.03635 (2018)
6. Gain, A., Kaushik, P., Siegelmann, H.: Adaptive neural connections for sparsity learning. In: The IEEE Winter Conference on Applications of Computer Vision. pp. 3188–3193 (2020)
7. Gao, X., Zhao, Y., Dudziak, L., Mullins, R., Xu, C.Z.: Dynamic channel pruning: Feature boosting and suppression. arXiv preprint arXiv:1810.05331 (2018)
8. Han, S., Pool, J., Tran, J., Dally, W.: Learning both weights and connections for efficient neural network. Advances in Neural Information Processing Systems 28, 1135–1143 (2015)
9. He, Y., Liu, P., Wang, Z., Hu, Z., Yang, Y.: Filter pruning via geometric median for deep convolutional neural networks acceleration. In: Proceedings of the IEEE Conference on Computer Vision and Pattern Recognition. pp. 4340–4349 (2019)
10. He, Y., Lin, J., Liu, Z., Wang, H., Li, L.J., Han, S.: Amc: Automl for model compression and acceleration on mobile devices. In: Proceedings of the European Conference on Computer Vision. pp. 784–800 (2018)
11. He, Y., Zhang, X., Sun, J.: Channel pruning for accelerating very deep neural networks. In: Proceedings of the IEEE International Conference on Computer Vision. pp. 1389–1397 (2017)
12. Hu, J., Shen, L., Sun, G.: Squeeze-and-excitation networks. In: Proceedings of the IEEE Conference on Computer Vision and Pattern Recognition. pp. 7132–7141 (2018)
13. Hua, W., Zhou, Y., De Sa, C.M., Zhang, Z., Suh, G.E.: Channel gating neural networks. In: Advances in Neural Information Processing Systems. pp. 1886–1896 (2019)
14. Iandola, F.N., Han, S., Moskewicz, M.W., Ashraf, K., Dally, W.J., Keutzer, K.: Squeezenet: Alexnet-level accuracy with 50x fewer parameters and < 0.5 MB model size. arXiv preprint arXiv:1602.07360 (2016)
15. Jaderberg, M., Vedaldi, A., Zisserman, A.: Speeding up convolutional neural networks with low rank expansions. arXiv preprint arXiv:1405.3866 (2014)
16. Jang, E., Gu, S., Poole, B.: Categorical reparameterization with gumbel-softmax. arXiv preprint arXiv:1611.01144 (2016)
17. Kim, Y.D., Park, E., Yoo, S., Choi, T., Yang, L., Shin, D.: Compression of deep convolutional neural networks for fast and low power mobile applications. arXiv preprint arXiv:1511.06530 (2015)
18. Kingma, D.P., Welling, M.: Auto-encoding variational bayes. arXiv preprint arXiv:1312.6114 (2013)
19. Krizhevsky, A., Hinton, G.: Learning multiple layers of features from tiny images (2009)
20. Lebedev, V., Ganin, Y., Rakhuba, M., Oseledets, I., Lempitsky, V.: Speeding-up convolutional neural networks using fine-tuned CP-decomposition. arXiv preprint arXiv:1412.6553 (2014)
21. LeCun, Y., Denker, J., Solla, S.: Optimal brain damage. Advances in Neural Information Processing Systems 2, 598–605 (1989)
22. Li, H., Kadav, A., Durandovic, I., Samet, H., Graf, H.P.: Pruning filters for efficient convnets. arXiv preprint arXiv:1608.08710 (2016)
23. Molchanov, P., Tyree, S., Karras, T., Aila, T., Kautz, J.: Pruning convolutional neural networks for resource efficient inference. arXiv preprint arXiv:1611.06440 (2016)
24. Rastegari, M., Ordonez, V., Redmon, J., Farhadi, A.: Xnor-net: Imagenet classification using binary convolutional neural networks. In: European Conference on Computer Vision. pp. 525–542. Springer (2016)
25. Russakovsky, O., Deng, J., Su, H., Krause, J., Santha, S., Ma, S., Huang, Z., Karpathy, A., Khosla, A., Bernstein, M., et al.: Imagenet large scale visual recognition challenge. International Journal of Computer Vision 115(3), 211–252 (2015)
26. Sandler, M., Howard, A., Zhu, M., Zhmoginov, A., Chen, L.C.: Mobilenetv2: Inverted residuals and linear bottlenecks. In: Proceedings of the IEEE Conference on Computer Vision and Pattern Recognition. pp. 4510–4520 (2018)
27. Simonyan, K., Zisserman, A.: Very deep convolutional networks for large-scale image recognition. arXiv preprint arXiv:1409.1556 (2014)
28. Szegedy, C., Vanhoucke, V., Ioffe, S., Shlens, J., Wojna, Z.: Rethinking the inception architecture for computer vision. In: Proceedings of the IEEE Conference on Computer Vision and Pattern Recognition. pp. 2818–2826 (2016)
29. Tan, M., Le, Q.V.: Efficientnet: Rethinking model scaling for convolutional neural networks. arXiv preprint arXiv:1905.11946 (2019)
30. Veit, A., Belongie, S.: Convolutional networks with adaptive inference graphs. In: Proceedings of the European Conference on Computer Vision. pp. 3–18 (2018)
31. Wu, B., Dai, X., Zhang, P., Wang, Y., Sun, F., Wu, Y., Tian, Y., Vajda, P., Jia, Y., Keutzer, K.: Fbnet: Hardware-aware efficient convnet design via differentiable neural architecture search. In: Proceedings of the IEEE Conference on Computer Vision and Pattern Recognition. pp. 10734–10742 (2019)
32. Yamamoto, K., Maeno, K.: Pcas: Pruning channels with attention statistics for deep network compression. arXiv preprint arXiv:1806.05382 (2018)
33. Zhou, A., Yao, A., Guo, Y., Xu, L., Chen, Y.: Incremental network quantization: Towards lossless cnns with low-precision weights. arXiv preprint arXiv:1702.03044 (2017)
34. Zhu, C., Han, S., Mao, H., Dally, W.J.: Trained ternary quantization. arXiv preprint arXiv:1612.01064 (2016)
35. Zhuang, Z., Tan, M., Zhuang, B., Liu, J., Guo, Y., Wu, Q., Huang, J., Zhu, J.: Discrimination-aware channel pruning for deep neural networks. In: Advances in Neural Information Processing Systems. pp. 875–886 (2018)
36. Zoph, B., Le, Q.V.: Neural architecture search with reinforcement learning. arXiv preprint arXiv:1611.01578 (2016)