Designing a Winner–Loser Gap for WTA in Subthreshold. Resolution Performance Revisited

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Abstract

The paper pioneers a thorough mathematical approach for the Lazzaro variant of the W(inner) T(ake) A(ll) maximum rank and amplitude analog selector. Two exact levels of output which split the maximum and determine the resolution, are found for the first time. At the input, a list of currents \( (I_1, I_2, \ldots, I_N) \) from a large family \( \mathcal{L} \) with smallest relative distance \( \Delta_1 \) on a \([0, I_M]\) scale is applied. To distinguish the largest current \( I_w \) (the winner) from the second largest \( I_l \) (the loser), the paper proposes two decision levels, \( D \) and \( D' \), for the output voltage list \( (U_1, U_2, \ldots, U_N) \). The upper level \( D \) is surpassed only by the \( U_w \) winner and encodes the winning rank \( w \). All other ranks are placed under the lower level \( D' \). Two rigorously treated optimization problems with inequality constraints lead to the identification of two input lists that yield the levels \( D \) and \( D' \) as outputs. They are valid for processing any list in the \( \mathcal{L} \) family. The index \( (D - D')/U_M \)—"the output resolution"—expresses how large the gap between the first and the second component on the \([0, U_M]\) scale is. It exceeds "the input resolution," i.e., the similar index \( \Delta/I_M \) at the input and the two depend monotonically on each other. Widely commented numerical examples are presented.

Keywords  MOS circuit theory · Winner take all · Subthreshold · Constrained optimization · Rank extraction · Resolution

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1 Introduction

The MOS-based WTA (Winner Take All) circuit—[14]—emerged when the neural-inspired VLSI analog circuits were launched [2, 22]. By making full use of its parallelism and compactness, the initial Lazzaro circuit and its variants have been implemented in a large diversity of applications such as image, sound and odor processing, classification, biomedical implants, motion control, computer memory, and neuromorphic circuits [1–5].

In this work, we apply a Lazzaro circuit as a current-voltage maximum rank selector. The circuit has \(N\) identical cells, among which Fig. 1 shows two consecutive cells. The cells share a common voltage \(V\) where the bias (tail) current \(I_C\) is connected. Fed with tiny currents \(I_1, I_2, \ldots, I_N\) (for example, at the order of nanoamps), the circuit should signal the rank \(w \in \{1, N\}\) of the largest current \(I_w\). Dissociating “the winner” \(I_w\) from “the loser” \(I_l\)—which is the second-largest current—becomes difficult when \(I_w\) and \(I_l\) are small currents with very close amplitudes. To resolve this issue, \(WTA\) provides an output list of voltages \(U_1, U_2, \ldots, U_N\) identically ordered by size as the currents. The difference is that the winning rank \(w\) is unambiguously separated from the loser rank \(l\). An input difference \(I_w - I_l\), which is very small in comparison with the largest possible current \(I_M\), yields a large output difference \(U_w - U_l\) relative to the largest possible output \(U_M\).

Now, consider that at input are applied successively all the lists of class \(L\) of currents, with maximum value \(I_M\) and with mutual distance at least \(\Delta\). Each list of currents produces at the output a list of voltages that has a \(U_w\) winner and a \(U_l\) loser. Our main achievement is the determination of three values \(\overline{D}, \underline{D},\) and \(U_M\) against which \(U_w\) and \(U_l\) of any processed list have the following positioning: \(U_M \geq U_w \geq \overline{D} > \underline{D} \geq U_l \geq 0\). Out of all possible outputs (when current lists in \(L\) are at the input), \(\overline{D}\) is

![Fig. 1 Lazzaro WTA \(j\)-th and \((j + 1)\)-th cells. \(I_1, I_2, \ldots, I_N\)—input currents; \(U_1, U_2, \ldots, U_N\)—output voltages; \(V\)—cell common voltage](image-url)
Fig. 2 The WTA + N comparators. $I_w$ is the largest input, and $U_w$ is the only output that surpasses $\overline{D}$. $I_l$ is the second-largest input, and $U_l$ is the largest output less than $\overline{D}$. All other ranks are smaller than $I_l$ and $U_l$, respectively.

the smallest of winners. To find it, we construct a classic optimization problem with inequality constraints. Solving this problem leads to a semi-analytical solution in the form of a concrete list $\mathcal{C}$ of currents that applied to the input gives an output winner $\overline{D}$. The lower level $D$ is obtained by a similar reasoning from another list $\mathcal{C}$ at input. Together with $\hat{\mathcal{C}}$ which yields the highest winner $U_M$, we have three tools $C$, $\mathcal{C}$ and $\hat{\mathcal{C}}$ to control by parameters the level of any maximum and its separation interval $[D, \overline{D}]$ from the second rank. So, we built a rank filter with fixed parameters to process the lists in $\mathcal{L}$.

Once we have the decision levels clearly found, they can be used for WTA subsequent connections in an analog or digital way. For instance, $\overline{D}$ can operate as a comparison level for each output voltages—Fig. 2. Only one of these voltages surpasses $\overline{D}$, and this encodes the winning rank $w$. All other ranks—the losers—are situated below $\overline{D}$. On the other hand, it is pretty apparent that the decision levels can be exploited to define a measure of the winner separation. Thus, if $\omega = \Delta / I_M$ is the “input resolution” meaning a measure of the separation of $I_w$ from $I_l$ on the $[0, I_M]$ scale, then $\Omega = (\overline{D} - D) / U_M$ is the “output resolution” meaning a measure of the separation of $U_w$ from $U_l$ on the $[0, U_M]$ scale of the output, for any input list in $\mathcal{L}$.

The low value of $\omega$ combined with the high value of $\Omega$ indicates the high capacity of the circuit to detach the winning rank even for crowded input lists. The possibility of error is small. We rigorously prove that the $\Omega (\omega)$ function is monotonically increasing and that the WTA always “amplifies” the resolution. Detailed examples motivate and verify the theory.

Everywhere in our work, we consider all MOS in subthreshold. To ensure this regime, Theorem 1 limits the values of the maximum current $I_M$ and the bias current $I_C$. These constraints, together with those of Theorem 4, which ensure the ordering of decision levels, prove easy in the examples provided.
Many researchers have studied and modified the original Lazzaro circuit to improve its performance. Let us refer briefly to some of the papers where resolution (accuracy) is of particular concern in the context of a weak inversion regime.

Thus, one of the first findings was that local excitatory feedback improves the resolution [27]. The same effect has been reported when distributed “hysteresis” (using a resistive network) were implemented [7]. It avoids resetting after each processed list when the winning input shifts between adjacent pixels. Next, by adding local inhibitory feedback, flexible functioning was obtained [11]. The selective enlargement of the input range (“adaptive thresholding”) when changing the input list has also led to enhanced resolution [8]. A circuit with good performance for rank order operation has been reported in [29]. It selects the winner with cells in parallel, while the other ranks are treated sequentially. Time domain encoding was proposed in [23] for resolution improvement. Preamplification of the input signals has the same effect [10]. The extensive use of Lazzaro type WTA in visual attention, target tracking and centroid computation can be seen in [3, 7, 21]. Let us also mention a WTA application to rank-read circuitry from multilevel-cell computer memories [13]. The parallelism of WTA cells makes the circuit sensitive to mismatch and process variations. These result in errors of selection. Mismatch compensation techniques using floating gate programming [26], or \( N - P \) MOS pairs instead of the original \( N - MOS \), Sundararajan and Winstead [28], reduce the threshold and zero-current deviation influence. In [9], the Lazzaro WTA circuit classifies the brain generated spikes as part of a neuromorphic sensor. Specific problems of analog classifiers for low-resolution images or for remote wireless sensors can be found in [4]. In [25] five WTA configurations are compared in terms of resolution, speed, compactness and power dissipation. A combination of Matrix-Multiply and WTA is presented in [24]. It is concluded that any perceptron can be modeled in this way. An extended plea for biological neural circuits (where WTA is also a key element) can be found in [12]. Costea and Marinov [6] deals with the correctness of the subthreshold dynamic model of Lazzaro circuit. Very recent papers are Lohmiller et al. [16] and Akbari et al. [1]. Finally, for classification problems in OpAmp Hopfield type neural networks see [18–20].

It seems that our work opens a new topic in analog circuits. It is about accurate determining of WTA output which leads to a new and exact definition of resolution. Our approach is detailed and mathematically rigorous.

Our paper is organized as follows.

Section 2 defines the \( \mathcal{L} \)-class of currents. In Sect. 3, Theorem 1 contains sufficient constraints to ensure the subthreshold regime, and also two useful properties of the solution. Section 4 contains the main results. Section 4.2 addresses the upper decision level \( \overline{D} \), which is found in Theorem 2. Section 4.3 with Theorem 3 finds the lower decision level \( \underline{D} \). Section 5 proves that, with a certain inequality restriction on parameters, the decision levels are properly positioned \( \overline{D} > \underline{D} \)—Theorem 4. Example 1 shows a case when \( \overline{D} < \underline{D} \), while Example 2 confirms Theorem 4. In Section 6, the monotonic behaviors of \( \overline{D}, \underline{D}, U_M \) and \( \Omega \) as functions of \( \omega \), are proven in Theorem 5. These properties are checked in Example 3 by solving the model equations. Section 7 summarizes the results. Some general conclusion can be found in Section 8. Our proofs imply extensive analytical derivations. Most are relegated to seven Appendices, out of which Appendix A contains the notations used in all the others.
2 The Family $\mathcal{L} (N, I_M, \Delta)$ of Input Currents

The currents to be processed by the WTA machine are grouped in vectors with $N$ components and called “lists” here. If $1, 2, \ldots, N$ are the input terminals, then $I = (I_1, I_2, \ldots, I_N)$ is such a list, whose $N$ currents are simultaneously fed into the circuit. We suppose that the currents are nonnegative, limited by $I_M$ and mutually distinct. Their relative distance is at least $\Delta$, a positive “input separation”:

\begin{align}
0 \leq I_j & \leq I_M, \ j \in \overline{1,N} \\
| I_j - I_k | & \geq \Delta > 0, \ j, k \in \overline{1,N}, \ j \neq k
\end{align}

The above three numbers $N$, $I_M$, and $\Delta$ characterize the set of lists we process. We denote this by $\mathcal{L} (N, I_M, \Delta)$—or simply $\mathcal{L}$ if confusion is not possible—and refer to it as the $\mathcal{L}$-family of lists. Certainly, we must have

$$\Delta (N - 1) \leq I_M \tag{3}$$

Let us denote by $S$ the set of all possible permutations of natural numbers $1, 2, \ldots, N$ denoting the terminals.

For each list $I = (I_1, I_2, \ldots, I_N) \in \mathcal{L}$, there exists a unique index permutation $\sigma = (\sigma_1, \sigma_2, \ldots, \sigma_N) \in S$ such that

$$I_M \geq I_{\sigma_1} > I_{\sigma_2} > \cdots > I_{\sigma_N} \geq 0 \tag{4}$$

That is, $\sigma$ arranges the components of $I$ in decreasing order, or we say that the vector $I$ “has the $\sigma$-order.” We write $I^\sigma = (I_{\sigma_1}, I_{\sigma_2}, \ldots, I_{\sigma_N})$ for the $\sigma$-ordered vector with the currents in $I = (I_1, I_2, \ldots, I_N)$. If the vector $I$ has no superscript, it is considered by convention as the vector with components in terminal order $I = (I_1, I_2, \ldots, I_N)$. If we write “$I \in \mathcal{L}$ with order $\sigma \in S$,” this will be equivalent to $I^\sigma \in \mathcal{L}$. The largest current $I_{\sigma_1}$ is called “the winner.” When the permutation $\sigma$ is not important, the largest current will be generically denoted by $I_w$. Similarly, $I_{\sigma_2}$, the second largest current of the list will be called “the loser”. When $\sigma$ is not important, $I_{\sigma_2}$ will be denoted by $I_l$. We also refer to $I_{\sigma_3}, I_{\sigma_4}, \ldots, I_{\sigma_N}$ as “losers.” This language is used for the output lists of voltages as well. If $I^\sigma \in \mathcal{L}$, (2) becomes

$$I_{\sigma_j} - I_{\sigma_{j+1}} \geq \Delta > 0, \ j \in \overline{1,N-1} \tag{5}$$

From (4) and (5), we immediately derive

$$(N - j) \Delta \leq I_{\sigma_j} \leq I_M - (j - 1) \Delta \tag{6}$$

Thus, for each $j \in \overline{1,N}$, (6) reveals two special currents $C_{jm}$ and $C_{JM}$, where

$$C_{jm} = (N - j) \Delta, \ j \in \overline{1,N} \tag{7}$$
and

\[ C_{jM} = I_M - (j - 1) \Delta, \quad j \in \overline{1,N} \]  

(8)

They depend on \( N, I_M, \Delta \) and rank \( j \) only, and do not depend on the particular list \( I^\sigma \). We will call \( C_{jm} \) and \( C_{jM} \) “characteristic currents” of family \( L \), and they will serve an important role below. Thus, for each rank of the descending order, we have

\[ I_{\sigma j} \in [C_{jm}, C_{jM}] \]  

(9)

that is, each current in \( L \) belongs to a “characteristic interval”.

3 The Subthreshold MOS Model

If \( V_G, V_S, \) and \( V_D \) are the MOS terminal voltages, the subthreshold (weak inversion) domain is defined by

\[ V_{GS} \leq V_T \quad \text{and} \quad V_{DS} \geq 0 \]  

(10)

where \( V_T \) is “the threshold voltage.” A common steady-state model of this regime—\([2, 15, 30]\)—takes the gate current as zero and provides the drain-to-source current by

\[ I_{DS} = I_0 \left[ 1 - \exp \left( -\frac{U_j}{V_t} \right) \right] \exp \left( k \frac{V_G}{V_t} \right) \exp \left( -\frac{V_D}{V_t} \right) \]  

(11)

Here, \( I_0 \) is “the zero current,” \( k \) is “the slope factor,” \( k < 1 \) and \( V_t \) is the “thermal voltage.” From Fig. 1, we have \( I_j = I_{Tj} \) and \( I_C = \sum_{j=1}^N I_{Tj} \), where \( I_{Tj} \) and \( I_{Tj}^* \) are the \( I_{DS} \) currents for \( T_j \) and \( T_j^* \) transistors, respectively. Thus, we find

\[ I_j = I_0 \left[ 1 - \exp \left( -\frac{U_j}{V_t} \right) \right] \exp \left( k \frac{V}{V_t} \right), \quad j \in \overline{1,N} \]  

(12)

\[ I_C = I_0 \left[ \exp \left( -\frac{V}{V_t} \right) \right] - \exp \left( -\frac{V_{DD}}{V_t} \right) \sum_{j=1}^N \exp \left( k \frac{U_j}{V_t} \right) \]  

(13)

Here, \( V_{DD} \) is the supply voltage, \( I_C > 0 \) is the “bias” or “tail” current and \( V \) is the common voltage of cells.

The subthreshold conditions (10) for \( T_j \) and \( T_j^* \) can be written as

\[ 0 \leq V \leq \min \{V_{DD}, V_T\} \]  

(14)

\[ 0 \leq U_j \leq V_T + V, \quad j \in \overline{1,N} \]  

(15)
For further reference, let us group the parameters $I_0$, $V_T$, $k$, $V_t$ and $V_{DD}$ into a set $P = \{I_0, V_T, k, V_t, V_{DD}\}$. Let us take an input list $I \in \mathcal{L} (N, I_M, \Delta)$ with $\sigma \in S$ its order.

If we denote

$$V_0 (I_{\sigma 1}) = \frac{V_t}{k} \ln \left( \frac{I_{\sigma 1}}{I_0} \right)$$

(16)

, then for $V$ belonging to the following interval,

$$V \in (V_0 (I_{\sigma 1}), +\infty)$$

(17)

we can solve (12) for $U_j$ and get

$$U_j = V_t \ln \left[ 1 - \frac{I_j}{I_0} \exp \left( -k \frac{V}{V_t} \right) \right]^{-1}, \quad j \in 1, N$$

(18)

By insertion into (13), we obtain

$$I_C = G (V, I)$$

(19)

where

$$G (V, I) = I_0 \left[ \exp \left( -\frac{V}{V_t} \right) - \exp \left( -\frac{V_{DD}}{V_t} \right) \right]$$

$$\times \sum_{j=1}^N \left[ 1 - \frac{I_j}{I_0} \exp \left( -k \frac{V}{V_t} \right) \right]^{-k}$$

(20)

is a scalar function defined on $(V; I) \in (V_0 (I_{\sigma 1}), +\infty) \times \mathcal{L}$.

We see that (18) + (19) is an input–output description of our WTA circuit: for $I = (I_1, I_2, \ldots, I_N)$, and given $P$ and $I_C > 0$, (19) yields the scalar function $V (I) = V (I_1, I_2, \ldots, I_N)$, and then (18) relates each pair $(V (I_1, I_2, \ldots, I_N), I_j)$ with the $j$-th output $U_j (I) = U_j (V (I_1, I_2, \ldots, I_N), I_j)$. For simplicity, we write $U_j (I) = U_j (I_1, I_2, \ldots, I_N)$ and denote the solution of (18) + (19) by $(V; U)$.

For a given $\mathcal{L} (N, I_M, \Delta)$ class, let us denote by $\hat{C}$ the vector of maximum characteristic currents

$$\hat{C} = (C_{1M}, C_{2M}, \ldots, C_{NM})$$

(21)

and by $\hat{C}$, the vector with all components being the minimum characteristic current except for the first component, which is the maximum characteristic current

$$\hat{C} = (C_{1M}, C_{2m}, \ldots, C_{Nm})$$

(22)
Let us also denote by $U_M$ the first component of the solution of (18) + (19) when the input currents are those in $\hat{C}$

$$U_M = U_1(\hat{C})$$  \hspace{1cm} (23)

We are now prepared to prove the results of this section grouped in the following theorem.

**Theorem 1** We take the sets $\mathcal{P}$ and $\mathcal{L}(N, I_M, \Delta)$ and the bias $I_C$ under the following assumptions:

$$V_T < V_{DD}$$  \hspace{1cm} (24)

$$I_0 \leq I_M \leq I_0 \exp \frac{kV_T}{V_t}$$  \hspace{1cm} (25)

$$\frac{I_0}{N - 1} \leq \Delta \leq \frac{I_M}{N - 1}$$  \hspace{1cm} (26)

$$I_C \geq G(V_T, \hat{C})$$  \hspace{1cm} (27)

Then,

(a) For a certain $I^\sigma \in \mathcal{L}(N, I_M, \Delta)$, the circuit model (18) + (19) has a unique solution $(V; U) = (V, U_1, U_2, \ldots, U_N) \in \mathbb{R} \times \mathbb{R}^N$ that fulfills the subthreshold conditions (14) and (15) and also

$$V \in (V_0(I^\sigma_1), V_T] \subset (0, V_T]$$  \hspace{1cm} (28)

$$U_M \geq U_{\sigma_1} > U_{\sigma_2} > \cdots > U_{\sigma_N} \geq 0$$  \hspace{1cm} (29)

(b) For any $I \in \mathcal{L}(N, I_M, \Delta)$, we have

$$\frac{\partial U_j}{\partial I_p}(I) < 0, \text{ where } j, p \in \{1, N\}, \ j \neq p, \ j \neq \sigma N$$  \hspace{1cm} (30)

$$\frac{\partial U_j}{\partial I_j}(I) > 0, \text{ if } j \in \{1, N\}, \ j \neq \sigma N$$  \hspace{1cm} (31)

**Proof** From (9), (16), (24) and (25), we have

$$V_0((N - 1) \Delta) \leq V_0(I^\sigma_1) \leq V_0(I_M) \leq V_T < V_{DD}$$  \hspace{1cm} (32)

On the interval $(V_0(I^\sigma_1), V_{DD})$, $G$ monotonically decreases with $V$ from $+\infty$ to 0—Fig. 3. Thus, for any $I_C \geq 0$, (19) has a unique solution $V$. From (18), we obtain unique $U_j \geq 0, j \in \{1, N\}$, and the order of $U_j$ is the same as the order of $I_j$, which is in (29). To restrict the $V$-part of solution to the interval $(V_0(I^\sigma_1), V_T]$ we take $I_C \geq G(V_T, I^\sigma)$—Fig. 3. To make this inequality valid for all $I^\sigma \in \mathcal{L}(N, I_M, \Delta)$, we take into account that $I_{\sigma j} \leq C_{jM}$ from (9) and that $G$ increases with each $I_{\sigma j}$.  

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Thus, (27) is sufficient for $V \leq V_T$, proving (28). For the proof of (30) and (31), see Appendix B.

Due to (30) and (31), we infer that the largest component of $U$ when $\hat{C}$ is the input is exactly $U_M$—the maximum value of outputs when all of $I \in \mathcal{L}$ are processed. Thus, (29) is fully proven.

Going further, the left-hand side of (26) implies $V_0 \left( \frac{(N - 1) \Delta}{I_0} \right) \geq 0$, and from (32), $V_0 (I_{\sigma 1}) \geq 0$ such that $V \geq 0$. Thus, the left-hand side of subthreshold condition (14) is fulfilled. Still to be proven is the right-hand side of (15), $U_j \leq V_T + V$, $j \in \{1, N\}$. This means $U_{\sigma 1} \leq V_T + V$ and from (18), we derive the condition

$$\frac{I_{\sigma 1}}{I_0} \exp \left[ (1 - k) \frac{V}{V_t} \right] \leq \exp \left( \frac{V}{V_t} \right) - \exp \left( -\frac{V_T}{V_t} \right)$$

(33)

To avoid unnecessary (and long) details, let us observe that even for the small practical values of $V_T$, such as $V_T = 0.1 \text{ Volt}$, we have $\exp \left( -\frac{V_T}{V_t} \right) = 0.02136$ (when $V_t = 0.026V$). Therefore, with reasonable approximation $\exp \left( -\frac{V_T}{V_t} \right)$—is negligible against $\exp \left( \frac{V}{V_t} \right) > 1$. This reduces (33) to $V_0 (I_{\sigma 1}) \leq V$, already met. Thus, the subthreshold conditions (14) and (15) are fulfilled.

Remarks:

- For practical values of $I_0$—see [30]—the limitation $I_0 \leq \Delta (N - 1)$ in (26) is totally acceptable.
- Due to assumption $I^\sigma \in \mathcal{L}(N, I_M, \Delta)$, the inequality $\Delta (N - 1) \leq I_M$ in (26) is in fact redundant—see (3).
- From (30) and (31), we see that the $i$–th output voltage increases with the $i$–th input and decreases with other currents. These will be essential for finding the decision levels.
Fig. 4 The input list \((I_1, I_2, I_3)\) yields the output list \((U_1, U_2, U_3)\); the input list \((J_1, J_2, J_3)\) yields the output list \((W_1, W_2, W_3)\). The winning ranks are “3” in the first case and “2” in the second, since \(U_3\) and \(W_2\) surpass \(D\).

- (30) and (31) imply that an increase in the winner input \(I_{\sigma 1}\) leads to an increase in \(U_{\sigma 1}\) output and to a decrease in all other outputs. This is the Winner Take All effect. In particular, it shows that in (23) \(U_M\) is the largest voltage.

\[\square\]

4 Decision Levels

4.1 Generalities: Formulation of Mathematical Problem

Let us consider our \(WTA\) in the particular case \(N = 3\) fed with the infinite number of lists in \(L (3, I_M, \Delta)\). The first list \(I = (I_1, I_2, I_3)\) with the (decreasing) order \(\sigma = (3, 1, 2)\) arrives at the \(WTA\) input—see Fig. 4. The goal is to signal the “winning” rank \(\sigma 1 = 3\) of the largest current \(I_3\), even in the extreme case when “the loser”—which is the second largest current \(I_1\)—is at the minimum distance \(\Delta, I_3 - I_1 = \Delta\) and \(\Delta\) is so small that the two are not distinguishable on the \([0, I_M]\) scale.

The \(WTA\) circuit translates the reading of the winner rank to the output list of voltages \(U = (U_1, U_2, U_3)\), which has the same order \(\sigma = (3, 1, 2)\)—Theorem 1. However, the winner \(U_3\) is now split from the loser \(U_1\) by a gap \(D - \overline{D}\), which is sufficiently large on the \([0, U_M]\) scale.

In fact, we have to have \(U_3 \geq \overline{D} > D \geq U_1 > U_2\). \(\overline{D}\) is called “the upper decision level” and has the property that it is surpassed only by the winner. Thus, the outputs \((U_1, U_2, U_3)\) are compared with \(\overline{D}\)—see Fig. 4—and rank 3 will be the unique winner.

Furthermore, \(D\) is called “the lower decision level,” and all the “losers” \((U_1\) and \(U_2\) here) are under it. The distance \(\overline{D} - D\) is significant on the scale \([0, U_M]\), where \(U_M\) is the maximum voltage. Returning to Fig. 4, let us consider a second list \((J_1, J_2, J_3)\) from \(L (3, I_M, \Delta)\) applied at the input. Suppose that \(J_2 > J_3 > J_1\) and the winner rank
“2” has to be signaled. This is done by obtaining the output voltages \((W_1, W_2, W_3)\) arranged as \(W_2 \geq \overline{D} > D \geq W_3 \geq W_1\), where the only rank surpassing the upper decision level \(\overline{D}\) is “2”, the winner. The losers are below the lower decision level \(D\).

The processing should be similar for any list from \(\mathcal{L}(3, I_M, \Delta)\) when using the same decision level \(\overline{D}\) and \(D\) and the same circuit parameters.

We are now prepared to formulate our general problem of finding \(\overline{D}\) and \(D\).

Let us consider the WTA circuit in Fig. 1, with \(N\) identical MOS devices, the set of parameters \(\mathcal{P}\) and the bias \(I_C\) and the class \(\mathcal{L}(N, I_M, \Delta)\) of input lists. Let also the restrictions (24)–(27) be valid such that for any input list \(I = (I_1, I_2, \ldots, I_N)\) in \(\mathcal{L}(N, I_M, \Delta)\) with \(\sigma\) order

\[
I_M \geq I_{\sigma 1} > I_{\sigma 2} > \cdots > I_{\sigma N} \geq 0 \quad (34)
\]

the circuit has a subthreshold solution. Moreover, the output list of voltages \(U = (U_1, U_2, \ldots, U_N)\) repeats the \(\sigma\) order of input. Let us denote by \(U^\sigma(I^\sigma) = (U_{\sigma 1}(I^\sigma), U_{\sigma 2}(I^\sigma), \ldots, U_{\sigma N}(I^\sigma))\) the \(\sigma\)-ordered output. We are looking for two voltage values \(\overline{D}\)—“the upper decision level”—and \(D\)—“the lower decision level”—such that

\[
U_M \geq U_{\sigma 1}(I^\sigma) \geq \overline{D} > D > U_{\sigma 2}(I^\sigma) \\
> U_{\sigma 3}(I^\sigma) > \cdots > U_{\sigma N}(I^\sigma) \geq 0 \quad (35)
\]

Taking into account that (35) should work with unchanged \(\overline{D}\) and \(D\), regardless of whether \(I \in \mathcal{L}(N, I_M, \Delta), \sigma \in \mathcal{S}\), we see that \(\overline{D}\) has to be the smallest possible winner while \(D\) has to be the largest possible loser. Thus, we define

\[
\overline{D} = \min \{U_{\sigma 1}(I^\sigma) \text{ for all } I \in \mathcal{L}(N, I_M, \Delta), \sigma \in \mathcal{S}\} \quad (36)
\]

and

\[
D = \max \{U_{\sigma 2}(I^\sigma) \text{ for all } I \in \mathcal{L}(N, I_M, \Delta), \sigma \in \mathcal{S}\} \quad (37)
\]

In this section, we find concrete lists in \(\mathcal{L}\) that provide outputs \(\overline{D}\) and \(D\).

### 4.2 The Upper Decision Level

According to definition (36), we search for the minimum of function \(U_{\sigma 1}\) on the set \(\mathcal{L}(N, I_M, \Delta)\) of \(\Re^N\).

Our result is

**Theorem 2** Let \(\mathcal{P}, I_C\) and \(\mathcal{L}(N, I_M, \Delta)\) under hypotheses (24)–(27).

Let

\[
\overline{C} = (C_{1m}, C_{2m}, \ldots, C_{Nm})
\]

be the input list from \(\mathcal{L}(N, I_M, \Delta)\) consisting of all lower characteristic currents. We denote by

\[
U(\overline{C}) = (U_1(\overline{C}), U_2(\overline{C}), \ldots, U_N(\overline{C}))
\]

\(\overline{C}\) Birkhäuser
its corresponding output.

Then, the upper decision level is the highest voltage in (39), namely,

$$
\overline{D} = U_1 (\overline{C})
$$

(40)

**Proof** Our idea is to write the fact that a list belongs to the class \(\mathcal{L}(N, I_M, \Delta)\) as a set of inequalities. Thus, minimizing \(U_{\sigma_1}(I^\sigma)\) on \(\mathcal{L}(N, I_M, \Delta)\) becomes a classical optimization problem with inequality constraints.

Let us denote by \(I^\gamma = (I_{\gamma 1}, I_{\gamma 2}, \ldots, I_{\gamma N})\) the vector in \(\mathcal{L}(N, I_M, \Delta)\), \(\gamma \in S\) giving the minimum of all \(U_{\sigma_1}(I^\sigma)\). The fact that \(I^\gamma \in \mathcal{L}(N, I_M, \Delta)\) leads to a set of \(N+1\) restrictions—see (4) and (5):

$$
\begin{align*}
H_1 & = I_{\gamma 1} - I_M \leq 0 \\
H_2 & = I_{\gamma 2} - I_{\gamma 1} + \Delta \leq 0 \\
H_3 & = I_{\gamma 3} - I_{\gamma 2} + \Delta \leq 0 \\
& \vdots \\
H_N & = I_{\gamma N} - I_{\gamma (N-1)} + \Delta \leq 0 \\
H_{N+1} & = -I_{\gamma N} \leq 0
\end{align*}
$$

(41)

The Kuhn–Tucker necessary conditions for this problem, Luenberger [17], ensure the existence of the nonnegative numbers \(\eta_1, \eta_2, \ldots, \eta_{N+1}\) such that

$$
\nabla U_{\gamma 1} (I^\gamma) + \eta_1 \nabla H_1 (I^\gamma) + \eta_2 \nabla H_2 (I^\gamma) + \cdots + \eta_{N+1} \nabla H_{N+1} (I^\gamma) = 0
$$

(42)

and

$$
\eta_j H_j (I^\gamma) = 0, \quad j \in 1, N+1
$$

(43)

In (42), by \(\nabla\), we understand the \(N\)-dimensional vector of derivatives with respect to currents. Since \(\nabla H_1 (I^\gamma) = (1, 0, 0, \ldots, 0)\), \(\nabla H_2 (I^\gamma) = (-1, 1, 0, 0, \ldots, 0)\), \(\cdots\), \(\nabla H_{N+1} (I^\gamma) = (0, 0, \ldots, 0, -1)\), the \(N\) equalities in (42) are

$$
\begin{align*}
\frac{\partial U_{\gamma 1}}{\partial I_{\gamma 1}} (I^\gamma) + \eta_1 - \eta_2 & = 0 \\
\frac{\partial U_{\gamma 1}}{\partial I_{\gamma 2}} (I^\gamma) + \eta_2 - \eta_3 & = 0 \\
\frac{\partial U_{\gamma 1}}{\partial I_{\gamma 3}} (I^\gamma) + \eta_3 - \eta_4 & = 0 \\
& \vdots \\
\frac{\partial U_{\gamma 1}}{\partial I_{\gamma N}} (I^\gamma) + \eta_N - \eta_{N+1} & = 0
\end{align*}
$$

(44)

From (31), Theorem 1, we know that \(\frac{\partial U_{\gamma 1}}{\partial I_{\gamma 1}} (I^\gamma) > 0\), and adding the fact that \(\eta_1 \geq 0\), from the first equation in (44), we find \(\eta_2 > 0\). Then, (43) yields \(H_2 = 0\). In the third
equation in (44), with \( \frac{\partial U_{\gamma 1}}{\partial I_{\gamma 3}} (I^\gamma) < 0 \) (see Theorem 1) and \( \eta_4 \geq 0 \), we obtain \( \eta_3 > 0 \). Then, \( H_3 = 0 \). Similarly, we find \( H_4 = 0, H_5 = 0, \cdots, H_N = 0 \). Therefore, (41) gives

\[
I_{\gamma 1} - I_{\gamma 2} = \Delta, \; I_{\gamma 2} - I_{\gamma 3} = \Delta, \ldots, \; I_{\gamma (N-1)} - I_{\gamma N} = \Delta
\]  

(45)

On the other hand, we know that \( I^\gamma \in L(N, I_M, \Delta) \) implies that \( I_{\gamma p} \) belongs to a feature interval \( I_{\gamma p} \in [C_{pm}, C_{pM}] \) for all \( p \in \overline{1,N} \)—see (9). This is equivalent to the existence of \( \varepsilon_p \in [0, 1] \) such that \( I_{\gamma p} = C_{pm} + \varepsilon_p (C_{pM} - C_{pm}) = C_{pm} + \varepsilon_p B \), where \( B = I_M - (N-1) \Delta \) does not depend on \( p \). By using (45), we infer that \( \varepsilon_1 = \varepsilon_2 = \cdots = \varepsilon_N \), and we denote by \( \varepsilon \) this common value. Thus,

\[
I_{\gamma p} = C_{pm} + \varepsilon B
\]  

(46)

which shows that the \( \gamma \)-order is in fact the order of terminals \( I^\gamma = I, \; I_p = C_{pm} + \varepsilon B \), with \( \varepsilon \in [0, 1] \). Now, we can show—see Appendix C—that the function \( \varepsilon \mapsto U_1(I(\varepsilon)) = U_1(C_{1m} + \varepsilon B, C_{2m} + \varepsilon B, \ldots, C_{Nm} + \varepsilon B) \) increases with \( \varepsilon \). Thus, the minimum point of \( U_1(I) \) occurs when \( \varepsilon = 0 \), and this minimum point is \( I = (C_{1m}, C_{2m}, \ldots, C_{Nm}) \), which is exactly \( \overline{C} \).

\[\square\]

4.3 The Lower Decision Level

Here, we are looking for the lower decision level \( D \). Definition (37) states that we must find the second-highest output component when all inputs in \( L(N, I_M, \Delta) \) are considered.

**Theorem 3** Let \( P, I_C \) and \( L(N, I_M, \Delta) \) under hypotheses (24)–(27).

Consider

\[
\underline{C} = (C_{1M}, C_{2M}, C_{3m}, \ldots, C_{Nm})
\]  

(47)

a particular list of characteristic currents at the input: all of them are the minimum currents except the first two, which are the maximum currents.

When \( \underline{C} \) is at the input, let us denote the output by

\[
U(\underline{C}) = (U_1(\underline{C}), U_2(\underline{C}), \ldots, U_N(\underline{C}))
\]  

(48)

Then, the lower decision level is

\[
D = U_2(\underline{C})
\]  

(49)

which is the second-largest voltage in (48).
The previous section has proven that, regardless of which list \( L \) is used, \( U_\sigma \) \((L, I^\sigma, \Delta)\) is processed, the highest output \( U_\sigma \) \((L, I^\sigma, \Delta)\) always surpasses the upper decision levels.

### 5.1 A Motivation Example

#### 5.1.1 A Gap between \( \bar{D} \) and \( D \)

The previous section has proven that, regardless of which list \( I^\sigma \) in \( L \) is processed, the highest output \( U_\sigma \) \((L, I^\sigma, \Delta)\) always surpasses the upper decision level \( D = U_1 \) \( (\bar{C}) \). The second-highest output \( U_\sigma \) \((L, I^\sigma, \Delta)\) always falls beneath the lower decision level \( \bar{D} = U_2 \) \( (\bar{C}) \). The next example shows that, for certain parameters,
the upper decision level can be smaller than the lower decision level, \( D > \overline{D} \). The output voltages do not fulfill (35). Consequently, the circuit cannot separate the highest output.

**Example 1** We take \( N = 10, I_0 = 10^{-18} \text{Amp}, I_C = 2 \times 10^{-15} I_0, I_M = 10^{11} I_0, \Delta = \frac{0.5}{9} I_M, V_T = 1V, k = 0.7, V_t = 0.026V, \) and \( V_{DD} = 1.5V \). The list \( \overline{C} = (C_{1m}, C_{2m}, C_{3m}, \ldots, C_{Nm}) \) in Theorem 2 is \( \overline{C} = 10^{11} \times \frac{1}{9} \times I_0 \times (4.5, 4, 3.5, 3, \ldots, 0.5, 0) \).

By numerically solving the 11-equation system in (18)+(19), we find

\[
U(C) = (U_1(C), U_2(C), \ldots, U_{10}(C)) = (15.37, 13.14, \ldots, 0) \text{ in mV}
\]

From here, we find \( \overline{D} = U_1(C) = 15.37 \text{ mV} \).

Furthermore, the list \( C = (C_{1M}, C_{2M}, C_{3m}, \ldots, C_{Nm}) \) in Theorem 3 is

\[
C = 10^{11} \times \frac{1}{9} \times I_0 \times (9, 8.5, 3.5, 3, \ldots, 0)
\]

Solving (18)+(19) with these input currents, we obtain

\[
U(C) = (U_1(C), U_2(C), \ldots, U_{10}(C)) = (40.15, 35.31, \ldots) \text{ mV}
\]

From here, \( \underline{D} = U_2(C) = 35.31 \text{mV}, \) which is larger than the \( \overline{D} = 15.37 \text{ mV} \) obtained above. The wrong order means that the circuit is not a rank selector.

### 5.2 Sufficient Conditions for a Gap between \( \overline{D} \) and \( D \)

This paragraph finds additional restrictions such that the decision levels are in good order. We need the following notation:

\[
F(\Delta) = I_0 \left(1 + \frac{1}{k}\right) - \frac{1}{k} \left(\Delta - k(N-1) - \frac{1}{k} \left[\Delta^{-k} I_M + (N-1) I_M \sum_{j=1}^{N-1} j^{(-k)}\right]\right)
\]  

The result is in the next Theorem.

**Theorem 4** With all assumptions in (24)–(27) in addition to

\[
I_C > F(\Delta),
\]

we have

\[
\overline{D} > D
\]
For the Proof, see Appendix E.

The theorem stipulates that a sufficiently large bias current pushes the higher decision level \( \overline{D} \) above the lower decision level \( \underline{D} \). It creates a “gap” interval \( [\underline{D}, \overline{D}] \) where none of the output voltages are placed. The lower bound \( F(\Delta) \) of \( I_C \) provided by the result is higher when the processed lists have higher currents and/or smaller separations. Indeed, due to Theorems 2 and 3, the above result implies that any \( I^\sigma \in \mathcal{L}(N, I_M, \Delta) \) at the input will produce a list \( U^\sigma \) of output voltages such that

\[
U_M \geq U_{\sigma_1} \geq \overline{D} > \underline{D} \geq U_{\sigma_2} > U_{\sigma_3} > \cdots > U_{\sigma_N}
\]

Thus, the fact that \( U_{\sigma_1} \) is the only component above the upper decision level \( \overline{D} \) signals that the same rank input component \( I_{\sigma_1} \) is the highest one of the \( I^\sigma \) lists. The WTA circuit reaches its goal. In addition, “the gap interval” \( [\underline{D}, \overline{D}] \) measures the level of error in detecting \( I_{\sigma_1} \). A larger gap means a smaller error. Example 2 makes the above facts clearer.

**Example 2** The \( \mathcal{L} \) family has \( N = 5, I_M = 100 \) nA, \( \Delta = 2.5 \) nA. The parameters in \( \mathcal{P} \) are \( I_0 = 10^{-18} \) Amps, \( k = 0.7 \), \( V_T = 1 \) Volt, \( V_i = 26 \) mV, and \( V_{DD} = 1.5 \) Volt.

Since \( I_M = 10^{-7} \in [I_0, I_0 \times 10^{11.71}] \) (25) is fulfilled. Also, \( \Delta = 2.5 \times 10^{-9} \in [I_0/4, 10^{-7}/4] \), i.e., (26) is valid. We also find \( G \left( V_T, C \right) = I_0 \times 10^{-16} \) and \( F(\Delta) = I_0 \times 10^{-6.58} \). We choose \( I_C = I_0 \times 10^{-6} \) Amp, which fulfils both (27) and (52).

Next, we compute the decision levels. For \( \overline{D} \), we need the input \( C = (C_{1m}, C_{2m}, C_{3m}, C_{4m}, C_{5m}) = (10, 7.5, 5, 2.5, 0) \) in nA. After numerically solving (18) + (19), we find \( U \left( \overline{C} \right) = (709, 72, 24, 5, 0) \) in mV. According to Theorem 2, \( \overline{D} \) is the largest voltage in this list \( \overline{D} = 709 \) mV. Theorem 3 states that for \( \underline{D} \), we need the input \( C = (C_{1m} \cdot C_{2m}, C_{3m}, C_{4m}, C_{5m}) = (100, 97.5, 5, 2.5, 0) \). After solving (18)+19), we obtain \( U \left( \underline{C} \right) = (727, 95, 52, 28, 0) \), and the second largest voltage is \( \underline{D} = 95 \) mV; see Fig. 5.

Finally, we solve for \( \hat{C} = (C_{1m} \cdot C_{2m}, C_{3m}, C_{4m}, C_{5m}) = (100, 7.5, 5, 2.5, 0) \) and find the largest possible voltage \( U_M = 830 \) mV. At this stage, our circuit is able to process any list in \( \mathcal{L} (5, 100 \) nA, 2.5 nA). On an output scale of 830 mV, each winner will surpass \( \overline{D} = 709 \) mV, while the losers will be below \( \underline{D} = 95 \) mV. Once we have designed “the machine,” let us choose to process the input list \( I = (45, 10, 50, 5, 47.5) \) written in the terminal order and in nA. The list in decreasing order is \( I^\sigma = (I_3, I_5, I_1, I_2, I_4) \). Clearly, it belongs to the family \( \mathcal{L} (5, 100, 2.5) \), since \( I_{\sigma_1} = 50 < I_M = 100 \) and the minimum distance between currents is 2.5 nA, which is exactly \( \Delta \). By numerically solving equations (18) + (19), where \( \frac{I_C}{I_0} = 10^{-6} \) as above, we obtain \( U \left( \hat{I} \right) = (60, 6, 794, 3, 78) \) in mV. We see first that the voltages exhibit the same order of amplitudes as the currents \( \sigma = (\sigma_1, \sigma_2, \sigma_3, \sigma_4, \sigma_5) = (3, 5, 1, 2, 4) \), \( U^\sigma = (U_3, U_5, U_1, U_2, U_4) \). Figure 5 separately shows the input currents and the output voltages. Thus, a difficult “reading” of the largest current of 50 nA against loser 47.5 nA is transformed through facile discernment of 794 mV of the winner against 78 mV of the loser.
6 Input and Output Resolutions and their Connection

The family $\mathcal{L}(N, I_M, \Delta)$ contains lists of currents on the $[0, I_M]$ scale, whose cramming is measured by $\Delta$. The difference between the largest and the second largest current of any list is at least $\Delta$. The coefficient $\omega$ defined by

$$\omega = \frac{\Delta}{I_M}$$

which will be called “the input resolution.” When $\omega$ is very small, perceiving $I_w$ (the winner) and $I_l$ (the loser) as distinct from each other is difficult and prone to error. On the output side, the voltages are similarly arranged on the $[0, U_M]$ scale—see (54). However, the positions of the $w$ and $l$ ranks are now controlled by the decision levels $\overline{D}$ and $\overline{\underline{D}}$:

$$U_M \geq U_w \geq \overline{D} > D > U_l > 0$$

$\overline{D}$ and $\overline{\underline{D}}$ do not change when a new list arrives. Under constraints in (24)–(27) and (52), $\overline{D}$ and $D$ are fixed by Theorems 2 and 3. Each winner of each list surpasses $\overline{D}$. Each loser of each list in $\mathcal{L}(N, I_M, \Delta)$ falls under $D$. $\overline{D} - D$ will be called “output separation,” and its ratio to the maximum output voltage $U_M$ will be denoted by $\Omega$ and called “the output resolution”:

$$\Omega = \frac{\overline{D} - D}{U_M}$$
The similarity between $\omega$ at input and $\Omega$ at output is complete. Both of them indicate how much of the “reading scale” is taken up by the smallest possible size difference between the $w$ and $l$ ranks. The circuit is effective if the $\Omega/\omega$ ratio is larger than 1—in other words, “if it amplifies” the resolution of the input list. The large values for $\Omega/\omega$ mean that the winning rank is highly distinct. To understand the WTA input-output mechanism, we study the function $\Omega(\omega)$ when $IM$ and $IC$ are unchanged. For clarity, we will translate the results obtained so far in terms of $\omega$, where $\omega = \Delta IM$. Thus, the family $L(N, IM, \omega IM)$ provides the currents for the model (18)+(19), which also contains $IC$ and the set $P$ of parameters. The hypotheses (24) and (25) are the same.

With $\Delta = \omega IM$, (26) will be replaced by

$$\omega_0 \leq \omega \leq \frac{1}{N - 1}$$  \hspace{1cm} (58)

where $\omega_0 = I_0/IM$ ($N - 1$).

We denote $\hat{\hat{C}}(\omega) = (C_{1M}, C_{2M}, C_{3M}, \ldots, C_{NM})$, where $C_{jM} = IM - (j - 1) \omega IM$, as in (8). Additionally, below we use $\hat{\hat{\hat{C}}}(\omega) = (C_{1m}, C_{2m}, C_{3m}, \ldots, C_{Nm})$, $\hat{\hat{\hat{C}}}(\omega) = (C_{1M}, C_{2M}, C_{3M}, \ldots, C_{NM})$, where $C_{jm} = (N - j) \omega IM$ as in (7).

To gather together the assumptions (27) and (52), we observe first that $G(VT, \hat{\hat{\hat{C}}}(\omega))$ from (20) and $F(\omega IM)$ from (51) both decrease with $\omega$. This means that both (27) and (52) are fulfilled on $[\omega_0, 1/N - 1]$ if we take $\omega = \omega_0$. Since $\omega_0$ is usually a very small number, we are led to very large values for $IC$. Therefore, we choose a $\omega_{min} \in (\omega_0, 1/N - 1)$ and perform our study on the $[\omega_{min}, 1/N - 1]$ interval for $\omega$. Then, the following inequality

$$IC \geq I_{C0} = \max \left\{ G(VT, \hat{\hat{\hat{C}}}(\omega_{min})) : F(\omega_{min} IM) \right\}$$  \hspace{1cm} (59)

is sufficient to fulfill (27) and (52).

Let $\hat{C}(\omega)$ be a generic notation for the particular lists $\hat{\hat{\hat{C}}}(\omega)$, $\hat{\hat{\hat{C}}}(\omega)$, $\hat{\hat{C}}(\omega)$, $\hat{C}(\omega)$. If $C(\omega)$ is applied at input, the model (18)+(19) furnishes the solution $U(C(\omega)) = (U_1(C(\omega)), U_2(C(\omega)), \ldots, U_N(C(\omega)))$ with properties (28)–(31) and (53). We are interested in studying the particular functions:

$\bar{D}(\omega) = U_1(C(\omega))$, see Theorem 2
$D(\omega) = U_2(C(\omega))$, see Theorem 3
$U_M(\omega) = U_1(\hat{C}(\omega))$
$\Omega(\omega) = [\bar{D}(\omega) - D(\omega)]/U_M(\omega)$ positive by Theorem 4

The results are in the following Theorem:
Theorem 5  Under the restrictions (24), (25), (59) and for any \( \omega \in [\omega_{\text{min}}, 1/N - 1] \)
where \( \omega_{\text{min}} > \omega_0 \), we have

\[
\begin{align*}
\frac{dD}{d\omega} (\omega) & > 0 \quad (60) \\
\frac{dD}{d\omega} (\omega) & < 0 \quad (61) \\
\frac{dU_M}{d\omega} (\omega) & < 0 \quad (62) \\
\frac{d\Omega}{d\omega} (\omega) & > 0 \quad (63)
\end{align*}
\]

There always exists \( \omega_1 \in [\omega_{\text{min}}, 1/N - 1] \) such that on \( [\omega_1, 1/N - 1] \) we have

\[
\frac{\Omega (\omega)}{\omega} > 1 \quad (64)
\]

Proof (60)–(62) are proven in Appendix F, while (63) derives readily from them. (64) is proven in Appendix G. This ends the proof.

(60) and (61) show that with \( \omega \) increasing (i.e., by processing less crammed lists), the upper decision level becomes larger, while the lower decision level decreases. Thus, the winning rank detaches clearly from the other ranks. Moreover, (63) shows that the proportion of the 
\([0, U_M] \) scale filled by the \([D, \bar{D}] \) gap is larger for less crowded currents. Another encouraging fact is the certainty of existence of the interval \([\omega_1, 1/N - 1] \) where the circuit amplifies the resolution—see (64). Although at this point we cannot theoretically evaluate \( \omega_1 \), the next example will show that its value can be few orders of magnitude beneath \( 1/N - 1 \). Certainly, apart from \( \Omega \) being large enough, the winner identification needs a maximum voltage \( U_M \) as high as possible. Although the result in (62) seems to endanger the \( U_M \) value, the next example shows that the variation of \( U_M \) with \( \omega \) is very small. The example will also verify the dependencies described in Theorem 5.

Example 3 Let us take a \( WTA \) with \( N = 100 \) cells and characteristic parameters in Table 1. Consider 3 values of \( I_M \), 10 nA, 100 nA, and 1 \( \mu \)A. They satisfy (24), i.e., \( 10^{-18} \leq I_M \leq 10^{-2.95} \). The \( \omega_0 \) computed for \( I_M = 10 \) nA is \( 1/99 \times 10^{-10} \). We choose \( \omega_{\text{min}} = 10^{-5} \). Next, we observe that \( G \left(V_T, \hat{C} \left( \omega_{\text{min}} \right) \right) \) increases with \( I_M \). To choose an \( I_C \) valid for any of the three values of \( I_M \), we use \( I_M = 1 \) \( \mu \)A for the maximum of \( G \left(V_T, \hat{C} \left( \omega_{\text{min}} \right) \right) \) and find it to be \( 10^{-31.2} \). On the other hand, \( F \left( \omega_{\text{min}} I_M \right) \) decreases with \( I_M \) such that, for its maximum we use \( I_M = 10 \) nA and obtain \( 10^{-21.28} \) for the largest value. According to (59), we should have \( I_C \geq I_{C0} = 10^{-21.28} \). We choose \( I_C = 10^{-20} = I_0 \times 10^{-2} \). Inside the interval \([\omega_{\text{min}}, \omega_{\text{max}}] \) where \( \omega_{\text{max}} = \frac{1}{N-1} = \frac{1}{99} \), we choose 4 values of \( \omega \), namely, \( 10^{-5} \approx 10^{-3} \omega_{\text{max}}, 10^{-4} \approx 10^{-2} \omega_{\text{max}}, 10^{-3} \approx 10^{-1} \omega_{\text{max}} \) and \( 10^{-2} \approx \omega_{\text{max}} \). Next, for each of the 12 pairs \((\omega; I_M)\), we numerically solve the 101 equations in (18) + (19) three times: with currents in \( \hat{C} \) to obtain \( \bar{D} \), with
Table 1  Example 3: $N = 100$, $I_0 = 10^{-18}$, $k = 0.9$, $V_T = 1V$, $V_i = 0.026V$, $V_{DD} = 1.5V$, $I_C = 10^{-2} I_0$.

$\omega_{max} = 1/N - 1$,

$\Omega\% = \frac{D - \hat{D}}{U_M} \times 100$,

$\hat{D}\% = \frac{\hat{D}}{U_M} \times 100$

| Input | Output | Performances |
|-------|--------|--------------|
| $I_M$ | $\omega_{min}$ | $\omega_{max}$ | $U_M$ | $D$ | $\hat{D}$ | $\Omega\%$ | $\hat{\Omega}\%$ | $\hat{D}\%$ |
| nA   | mV     | mV           | mV    |     |     |           |               |              |
| 10   | $10^{-5}$ | $\frac{1}{99}$ | 607   | 382 | 297 | 14       | 14000          | 63          |
|      | $10^{-4}$ |                       | 606   | 455 | 230 | 37       | 3700           | 75          |
|      | $10^{-3}$ |                       | 605   | 532 | 182 | 58       | 580            | 88          |
|      | $10^{-2}$ |                       | 604   | 598 | 115 | 80       | 80             | 99          |
| 100  | $10^{-5}$ | $\frac{1}{99}$ | 680   | 456 | 298 | 23       | 23000          | 67          |
|      | $10^{-4}$ |                       | 680   | 530 | 238 | 43       | 4300           | 78          |
|      | $10^{-3}$ |                       | 679   | 604 | 186 | 62       | 620            | 89          |
|      | $10^{-2}$ |                       | 679   | 672 | 121 | 81       | 81             | 99          |
| 1000 | $10^{-5}$ | $\frac{1}{99}$ | 754   | 535 | 302 | 31       | 31000          | 71          |
|      | $10^{-4}$ |                       | 754   | 603 | 241 | 48       | 4800           | 80          |
|      | $10^{-3}$ |                       | 754   | 679 | 188 | 65       | 650            | 90          |
|      | $10^{-2}$ |                       | 754   | 746 | 122 | 83       | 83             | 99          |

$C$ to obtain $D$ and with $\hat{C}$ to obtain $U_M$. Then, $\Omega$ and $\Omega/\omega$ are computed. See Table 1 for the results.

The monotonic behaviors of $\overline{D}$, $D$, $U_M$ and $\Omega$ with respect to $\omega$ are confirmed. We also observe that, for each $I_M$, the decrease in $U_M$ with $\omega$ is insignificant. Since $\Omega/\omega > 1$ for all input resolutions we have considered, the value of $\omega_1$ equals $\omega_{min} = 10^{-5}$. Surprisingly, the amplification ratio $\Omega/\omega$ decreases steeply with $\omega$, at least for the interval $[\omega_{min}, \omega_{max}]$ chosen here. The minimum value is approximately 80 at $\omega_{max}$ and reaches large values of $14 \times 10^3$, $23 \times 10^3$ and $31 \times 10^3$ at $\omega_{min} = 10^{-5}$ (for the three $I_M$, respectively). Indeed, the fact that the crammed lists are processed so efficiently seems to be favorable for applications. On the other hand, in practice it can be important that the winner in output list (located in $[D, U_M]$ interval) should be as close as possible to $U_M$. Unfortunately, the lower $\omega$ the lower $\hat{\Omega}/\omega$. Thus, a balance between $\Omega/\omega$ and $\Omega$ is necessary. The matter merits theoretical investigation.

Based on the results in Table 1, Fig. 6 explains, (for the case $I_M = 100$ nA) how decision levels divide the range $[0, U_M]$. With the increase in $\omega$, the winner placed in the interval $[\overline{D}, U_M]$ is pushed toward its maximum value $U_M$. The 99 losers from the interval $[0, \overline{D}]$ are increasingly crowded with $\omega$, below 18% of $U_M$.

Thus, for the ideal case at $\omega \simeq \omega_{max}$, the gap between the winner and losers is 81%, and the winner is at its highest level between 99% and 100%. The worst case here, at $\omega = 10^{-3} \omega_{max}$, has a gap of 23%, and the winner is at 67%.

The fact that the decision levels computed at $\omega_{min}$ work for the entire interval $[\omega_{min}, \omega_{max}]$ gives a certain flexibility for design. Referring to the example in Fig. 6, the interval of resolution $Q = [10^{-5}, 1/99]$ can be processed with $\overline{D} = 67\%$, $D = 44\%$ and $\Omega = 23\%$, all computed with $\omega = 10^{-5}$. These performances can
Fig. 6  \( N = 100, I_M = 100 \, \text{nA}, \omega_{min} = 10^{-5}, \omega_{max} = 1/99. \) Distribution of decision levels: \( \{D, U_M\} \) — the winners placement, \( \{D, \bar{D}\} \) — the splitting gap (output separation), \( \{0, D\} \) — the losers placement. The output resolution \( \Omega = (\bar{D} - D)/U_M \) = 23, 43, 62, 81% for the four cases.

partially improve if we divide \( Q \) into the two intervals \( Q^a = [10^{-5}, 10^{-3}] \) and \( Q^b = [10^{-3}, 1/99] \). For \( Q^a \), we use the above parameters computed with \( \omega = 10^{-5} \). For \( Q^b \), we compute \( \bar{D} = 89\%, D = 27\% \) and \( \Omega = 62\% \) with \( \omega = 10^{-3} \). Thus, if we pay the price of changing \( \bar{D} \) and \( D \) at an intermediate point, the lists from the second interval are processed much more accurately.

7 Summary of Results

The following “design scenario” summarizes the paper results:

Give the circuit in Fig. 1 with \( 2N \) identical \( MOS \) having the parameters \( I_0, V_T, k, V_t, V_{DD} \). Give an infinite set \( L \) of input currents with \( N, I_M \) and \( \omega \) be known.

We aim to solve the following three issues:

1. to establish two levels \( \bar{D} \) and \( D \) of the output voltages in such a way that for any list in \( L \) at the input, the first two largest outputs \( U_w \) and \( U_l \) are split by \( \bar{D} \) and \( D \): \( U_M \geq U_w \geq \bar{D} > D > U_l \geq 0 \);
2. to be able to control \( \bar{D} \) and \( D \) such that \( \bar{D} \) is as close as possible to the maximum voltage \( U_M \), and the output resolution \( \Omega = (\bar{D} - D)/U_M \) increases with its input correspondent \( \omega_{min} \);
3. to find consistent conditions for operating in subthreshold for all \( MOS \)

We found, respectively, the following answers:

1. \( \bar{D} = U_1(\bar{C}) \) from (40), \( D = U_2(C) \) from (49) and \( U_M = U_1(\bar{C}) \) from (23) where \( C, \bar{C} \) and \( \bar{C} \) are three special lists in \( L \);
2. \( \bar{D} > D \) if (52) is met; \( \Omega \) monotonically increases with \( \omega \) — see (63); \( \Omega/\omega > 1 \) — see (64). Examples show that \( \Omega/\omega \) is much higher than 1.
3. Mild restrictions (24)-(27) and (52) on \( V_T, V_{DD}, I_M, \Delta = \omega I_M \) and \( I_C \);
8 Conclusion

The above article considers the WTA subthreshold circuit as a rank separator and claims originality both as a subject and as a mathematical treatment. It deals with finding two levels of decision that separate the winner from losers and that depend semi-analytically on the input parameters. This is found by the analytical solution of two optimization problem with inequality constraints. A performance criterion of design interest is established. It is about the correspondence between the density of the input list of currents (“input resolution”) and the density of the output list voltages (“output resolution”). Detailed numerical examples motivate and verify the theory.

To conclude, a new idea for WTA output control was presented. It is about finding two levels that separate the winner from losers and allow the precise design and the calculation of the split performance. For this purpose, the paper formulates and rigorously solves two optimization problems with inequality constraints.

Data Availability Data sharing not applicable to this article as no datasets were generated or analyzed during the current study.

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Appendix A

For proving our Theorems, we use a simpler form for writing the models (18) and (19). It requires the following notations:

\[ i_j = \frac{I_j}{I_0}, \quad i_C = \frac{I_C}{I_0}, \quad i = (i_1, i_2, \ldots, i_N), \quad i_{\sigma j} = \frac{I_{\sigma j}}{I_0}, \quad i_{\sigma} = (i_{\sigma 1}, i_{\sigma 2}, \ldots, i_{\sigma N}), \]

\[ i_M = \frac{I_M}{I_0}, \quad \delta = \frac{\Delta}{I_0}, \quad c_{jm} = \frac{C_{jm}}{I_0} = (N - j) \delta, \quad c_{jM} = \frac{C_{jM}}{I_0} = i_M - (j - 1) \delta, \]

\[ \bar{c} = (c_{1m}, c_{2m}, c_{3m}, \ldots, c_{Nm}), \quad \bar{c} = (c_{1M}, c_{2M}, c_{3M}, \ldots, c_{NM}). \]

\[ v = \exp \left( \frac{V}{V_t} \right), \quad x = \exp \left( -k \frac{V}{V_t} \right) = v^{-k}, \quad u_j = \exp \left( \frac{U_j}{V_t} \right), \quad d = \exp \left( \frac{V_{DD}}{V_t} \right), \]

\[ v_T = \exp \left( -k \frac{V}{V_t} \right), \quad u = (u_1, u_2, \ldots, u_N), \quad u_{\sigma} = (u_{\sigma 1}, u_{\sigma 2}, \ldots, u_{\sigma N}). \]

If \( a \neq 0 \) is a real number, we assign \( \text{sgn} (a) = +1 \) when \( a > 0 \) and \( \text{sgn} (a) = -1 \) when \( a < 0 \).

With

\[ g (x, i) = \left( x^{1/k} - d^{-1} \right) \sum_{j=1}^{N} (1 - i_j x)^{-k} \]  \hspace{1cm} (A1)
our model (18) + (19) becomes

\[
 u_j = (1 - i_j x)^{-1}, \quad j \in \overline{1,N} \\
 i_C = g(x,i)
\]  

(A2)  

(A3)

The properties \( V \in (V_0 (I_{\sigma 1}), V_T] \) from (28) and \( V_T < V_{DD} \) from (24) translate into

\[
d^{-k} \leq v_T^k \leq x < i_{\sigma 1}^{-1} \leq j \in \overline{1,N}
\]  

(A4)

where \( i_{\sigma 1} \) is the highest of \( i_j \).

In (A4), \( x \) is the solution of the scalar equation (A3). Our subsequent proofs need its derivatives. We differentiate both parts of (A3) with respect to \( i_p, \ p \in \overline{1,N} \), take into account that \( i_C \) is constant and obtain

\[
\frac{\partial x}{\partial i_p} = -kx \left( x^{1/k} - d^{-1} \right) (1 - xi_p)^{(-k-1)}/S
\]  

(A5)

where

\[
S = S_1 + S_2
\]  

(A6)

and

\[
S_1 = \sum_{j=1}^{N} \frac{1}{k} \left( \frac{1}{k} - 1 \right) (1 - xi_j)^{-k}
\]  

(A7)

\[
S_2 = \sum_{j=1}^{N} k i_j \left( x^{1/k} - d^{-1} \right) (1 - xi_j)^{-k-1}
\]  

(A8)

Appendix B

Here, we prove (30) and (31) in Theorem 1.

For the input currents \( I_1, I_2, \ldots, I_N \), let \( U_1, U_2, \ldots, U_N \) be the output voltages. With notations from Appendix A, we have

\[
\frac{\partial u_j}{\partial i_p} = I_0 \left( \exp \frac{U_j}{V_t} \right) \frac{\partial U_j}{\partial I_p}
\]

such that

\[
sgn \left( \frac{\partial U_j}{\partial I_p} \right) = sgn \left( \frac{\partial u_j}{\partial i_p} \right)
\]

when \( u_j \neq 0 \).

Thus, for any \( j \not\in \sigma N \), by using (A2), we obtain

\[
sgn \frac{\partial U_j}{\partial I_p} = sgn \frac{\partial}{\partial i_p} (xi_j) = sgn E_{jp}
\]  

(B1)
where
\[
E_{jp} = \frac{\partial i_j}{\partial i_p} S - k i_j \left(x^{1/k} - d^{-1}\right) \left(1 - x_i p\right)^{-k-1} \quad (B2)
\]

Here, \(x\) is the solution of (A3) with currents \(i_1, i_2, \ldots, i_N\), and \(S\) is that in (A6)–(A8).
For \(j \neq p\) and \(j \neq \sigma N\), (B2) immediately gives \(E_{jp} = 0\). Thus, (30) comes from (B1).
For \(j = p\), again from (B2) and by using (A6), we obtain \(E_{jj} > 0\). Thus, (31) comes from (B1) with \(j = p\).

**Appendix C**

Here, we show that the function \(\varepsilon \rightarrow U_1 (C_1m + \varepsilon B, C_2m + \varepsilon B, \cdots)\) (from the proof of Theorem 2) is monotonically increasing. With \(A = \frac{\partial U_1}{\partial \varepsilon} (C_1m + \varepsilon B, C_2m + \varepsilon B, \cdots)\) and \(b = \frac{B}{k}\), we obtain \(\text{sgn} A = \text{sgn} \frac{\partial U_1}{\partial \varepsilon} (c_1m + \varepsilon b, c_2m + \varepsilon b, \cdots)\) and from (A2) \(\text{sgn} A = \text{sgn} \frac{\partial}{\partial \varepsilon} [x (c_1m + \varepsilon b)]\), where \(x\) is the solution of (A3) with \(c_jm + \varepsilon b\) as currents. Further on, \(\text{sgn} A = \text{sgn} \left[ bx + b (c_1m + \varepsilon b) \sum_{j=1}^{N} \frac{\partial x}{\partial i_j} \right]\). After replacing \(\frac{\partial x}{\partial i_j}\) with (A5), we get

\[
\text{sgn} A = \text{sgn} \frac{bx}{S} \sum_{j=1}^{N} (1 - x_i j)^{-k-1} \left[ \frac{1}{k} x^{\left(\frac{i}{j} - 1\right)} (1 - x_i j) - k \left(x^{\frac{1}{k}} - d^{-1}\right) (i_1 - i_j) \right]
\]

By using \(i_j = c_jm + \varepsilon b\), \((1/k) \geq k\) and \(x^{-1} > i_1\) from (A4), we find \(\text{sgn} A = +1\).

**Appendix D**

Here, we show that the function \(\varepsilon \rightarrow U_2 (\varepsilon)\) from the proof of Theorem 3 is monotonically decreasing.

With notations in Appendix A and the same reasoning as in Appendix C, we have \(A = \frac{\partial U_2}{\partial \varepsilon} (C_1M - \varepsilon B, C_2M - \varepsilon B, C_3m, C_4m, \cdots)\) and \(\text{sgn} A = \text{sgn} \frac{\partial}{\partial \varepsilon} [(c_2M - \varepsilon b) x]\), where \(x\) is the solution of (A3) with \(c_1M - \varepsilon b, c_2M - \varepsilon B, c_3m, \ldots, cNm\) as currents.

Further on, \(\text{sgn} A = \text{sgn} \left[-bx + (c_2M - \varepsilon b) \frac{\partial x}{\partial i_j}\right] = \text{sgn} \left[-bx - b (c_2M - \varepsilon b) \sum_{j=1}^{2} \frac{\partial x}{\partial i_j}\right]\). After inserting \(\frac{\partial x}{\partial i_j}\) from (A5), we easily obtain \(\text{sgn} A = -1\).
Appendix E

Here, we prove Theorem 4. See notations in Appendix A. According to Theorem 2, \( D \) is the first component of solution \( U(\bar{C}) \) of (18) + (19), where \( \bar{C} = (C_1, C_2, C_3, \ldots, C_N) \). In terms of “small letter” notation, \( \bar{c} = (c_1, c_2, \ldots, c_N) \) yields \( \bar{x} \) via (A3) and \( u_j = (1 - \bar{x}c_j)^{-1} \) via (A2). Thus,

\[
\begin{align*}
    i_C &= \left(\bar{x}^{1/k} - d^{-1}\right) \left[u_1^k + \sum_{j=2}^{N} \left(1 - c_j \bar{x}\right)^{-k}\right] \tag{E1}
\end{align*}
\]

Now, from (A4) we have \( \bar{x} \leq c_1^{-1} = [(N - 1) \delta]^{-1} \) and (E1) gives

\[
\begin{align*}
    i_C &\leq (N - 1)^{-1/k} \left(1 - c_1 \bar{x}\right)^{-1/k} \left[u_1^k + \sum_{j=2}^{N} \left(j - 1\right) \left(N - 1\right)^{-k}\right] \tag{E2}
\end{align*}
\]

Note that \( u_1 = \exp\left(\frac{D}{V_t}\right) \) from Theorem 2. On the other hand, \( D \) is the second component of the solution \( U(\bar{C}) \) of (18) + (19). In terms of “small letter” notation, \( \bar{c} = (c_1, c_2, c_3, \ldots, c_N) \) yields \( \bar{x} \) via (A3) and \( u_2 = \left(1 - c_2 \bar{x}\right)^{-1} \) from (A2). We use (A4) to obtain \( \bar{x} \leq c_1^{-1} = i_1^{-1} \). Then,

\[
\begin{align*}
    u_2 &\leq \left(1 - \frac{i_1 - \delta}{i_1^M}\right)^{-1} \tag{E3}
\end{align*}
\]

where \( u_2 = \exp\left(\frac{D}{V_t}\right) \). Now, to obtain \( D > D_{\bar{C}} \), we have to have \( u_1^k > \bar{u}_2^k \), where \( u_1^k \) is the lower bound of \( u_1^k \) derived from (E2) and \( \bar{u}_2^k = \delta^{-k}i_1^M \) is the upper bound of \( u_2^k \) from (E3). This results in (52).

Appendix F

We prove Theorem 5. See notations in Appendix A. Proving \( \frac{\partial D}{\partial \omega} > 0 \) means showing \( \frac{\partial U_1}{\partial \omega}(\bar{C}) > 0 \), equivalent to \( \frac{\partial u_1}{\partial \omega}(\bar{c}) > 0 \) where \( \bar{c} = (c_1, c_2, \ldots, c_N) \). By (A2), it is sufficient to prove \( \frac{\partial}{\partial \omega} (c_1 x) > 0 \), where \( x \) is the solution of (A3) with \( \bar{c} \) as currents.

It comes out that \( \frac{\partial}{\partial \omega} (c_1 x) = (N - 1) i_1 x + (N - 1) \omega i_m \sum_{j=1}^{N} \frac{\partial x}{\partial i_j} (N - j) i_M \) and with (A5), we obtain the result.
Proving \( \frac{\partial D}{\partial \omega} < 0 \) means showing that \( \frac{\partial U_2}{\partial \omega} (\xi) < 0 \) or \( \frac{\partial U_2}{\partial \omega} (\xi) < 0 \), where \( \xi = (c_{1M}, c_{2M}, c_3, \ldots, c_{NM}) \). By (A2), it is sufficient to prove \( \frac{\partial U_M}{\partial \omega} (c_{2M} x) < 0 \).

By using (A5) here, we straightforwardly obtain the result. To prove \( \frac{\partial U}{\partial \omega} < 0 \) means to show that \( \frac{\partial U_1}{\partial \omega} (\hat{C}) < 0 \), where \( \hat{C} = (C_{1M}, C_{2m}, C_3, \ldots) \). Then, \( \frac{\partial U_1}{\partial \omega} = \frac{\partial U_1}{\partial I_1} (\hat{C}) \frac{\partial C_{1M}}{\partial \omega} + \sum_{j=2}^{N} \frac{\partial U_1}{\partial I_j} (\hat{C}) \frac{\partial C_{jm}}{\partial \omega} \). Now, \( \frac{\partial C_{1M}}{\partial \omega} = \frac{\partial I_M}{\partial \omega} = 0 \) and \( \frac{\partial U_1}{\partial I_j} (\hat{C}) < 0 \) for \( j \geq 2 \) by (30) Theorem 1. Then, with \( \frac{\partial C_{jm}}{\partial \omega} = (N - j) I_M > 0 \), we get \( \frac{\partial U_1}{\partial \omega} < 0 \).

Appendix G

Here, we prove (64) in Theorem 5.

If we show that

$$\Omega(1/N - 1) > 1/N - 1 \quad \text{(G1)}$$

then (64) comes from the (left) continuity of \( \Omega \) in \( 1/N - 1 \). Translated in small letter notation, (64) is

$$u_1 (\xi) > u_2 (\xi) [u_1 (\hat{\xi})]^{\omega} \quad \text{(G2)}$$

For \( \omega = 1/N - 1 \), we denote by \( c \) the common value \( \xi = \xi = \hat{C} = (i_M, i_M N^{-2} N^{-1}, i_M N^{-3} N^{-1}, \ldots, 0) \). If \( x \) is the solution of \( i_C = g(x, c) \), we have \( u_1 (\hat{C}) = u_1 (c) = 1/(1 - xi_M), u_2 (\xi) = u_2 (c) = 1/(1 - xi_M), u_1 (\hat{\xi}) = u_M = u_1 (c) = 1/(1 - xi_M) \). Thus, (G2) reduces to \( (1 - xi_M) N^{-2} N^{-1} < 1 - x N^{-2} N^{-1} i_M \), which is the well-known Bernoulli inequality.

References

1. M. Akbari, T.I. Chou, K.T. Tang, An adjustable 0.3 V current winner-take-all circuit for analogue neural networks. Electron. Lett. 57(18), 685–687 (2021)
2. A.G. Andreou, K.A. Boahen, P.O. Pouliquen, A. Pavasovic, R.E. Jenkins, K. Strohbehn, Current-mode subthreshold MOS circuits for analog VLSI neural systems. IEEE Trans. Neural Netw. 2(1), 205–213 (1991)
3. V. Brajovic, T. Kanade, Computational sensor for visual tracking with attention. IEEE J. Solid State Circuits 33(8), 1199–1207 (1998)
4. S.T. Chandrasekdran, A. Jayaraj, V.E.G. Karnam, I. Banerjee, A. Sanyal, Fully integrated analog machine learning classifier using custom activation function for low resolution image classification. IEEE Trans. Circuits Syst.-I Regul. Pap. 68(3), 1023–1033 (2021)
5. E. Chicca, F. Stefanini, C. Bartolozzi, G. Indiveri, Neuromorphic electronic circuits for building autonomous cognitive systems. Proc. IEEE 102(9), 1367–1388 (2014)
6. R.L. Costea, C.A. Marinov, A consistent model for Lazzaro winner-take-all circuit with invariant subthreshold behavior. IEEE Trans. Neural Netw. Learn. Syst. 27(11), 2375–2385 (2016)

7. S.P. De Weerth, T.G. Morris, Analog VLSI circuits for primitive sensory attention. Proc. IEEE Int. Symp. Circuits, Syst. 6, 507–510 (1994)

8. A. Fish, V. Milrud, O. Yadid-Pecht, High-speed and high-precision current winner-take-all circuit. IEEE Trans. Circuits Syst. II, Exp. Br. 52(3), 131–135 (2005)

9. G. Haessing, D.G. Lesta, G. Lenz, R. Benosman, P. Dudek, A mixed-signal spatio-temporal signal classifier for on-sensor spike sorting. IEEE Int. Symp. Circuits Syst., ISCAS (2020)

10. H. Hung-Yi, T. Kea-Tiong, T. Zen-Huan, C. Hsin, A low-power, high-resolution WTA utilizing translinear-loop pre-amplifier. Int. Joint Conf. Neural Netw. (2010)

11. G. Indiveri, A current-mode hysteretic winner-take-all network with excitatory and inhibitory coupling. Analog Integr. Circuits Signal Process. 28, 279–291 (2001)

12. G. Indiveri, Y. Sandamirkaya, The importance of space and time for signal processing in neuromorphic agents. IEEE Signal Process. Mag. 17–27 (2019)

13. M. Kim, C.M. Twigg, Rank determination by winner-take-all circuit for rank modulation memory. IEEE Trans. Circuits Syst. II—Express Br. 63(4), 326–330 (2016)

14. J. Lazzaro, S. Rycek, M.A. Mahowald, C.A. Mead, Winner-take-all networks of O(N) complexity, in Advances In Neural Information Processing Systems, vol. 1, ed. by D.S. Touretzky (C.A. Morgan Kaufmann, San Mateo, 1989), pp. 703–711

15. S. Liu, J. Kramer, G. Indiveri, T. Delbruck, R. Douglas, Analog VLSI: Circuits and Principles (MIT Press, Cambridge, 2002)

16. W. Lohmiller, P. Gassert, J.J. Slotine, Deep min max networks. IEEE Confer. Decis. Control 60, 2929–2934 (2021)

17. D.G. Luenberger, Linear and Nonlinear Programming, 2nd edn. (Addison-Wesley, 1984)

18. C.A. Marinov, B.D. Calvert, Performance analysis for a K-Winners-Take-All analog neural network: basic theory. IEEE Trans. Neural Netw. 14, 766–780 (2003)

19. C.A. Marinov, B.D. Calvert, Sorting with dynamical systems of neural type. Math. Rep. 55(4), 333–342 (2003)

20. C.A. Marinov, J.J. Hopfield, Stable computational dynamics for a class of circuits with O(N) interconnections capable of KWTA and rank extractions. IEEE Trans. Circuits Syst. I, Reg. Pap. 52, 949–959 (2005)

21. N. Massari, M. Gottardi, Low power WTA circuit for optical position detector. Electron. Lett. 42(24), 1373–1374 (2006)

22. C.A. Mead, Neuromorphic electronic systems. Proc. IEEE 78(10), 1629–1636 (1990)

23. E. Rahiminejad, M. Saben, R. Lotfi, M. Taherzadeh-Sani, F. Nabki, A low-voltage high-precision time-domain winner-take-all circuit. IEEE Trans. Circuits Syst.: Express Br. 67(1), 4–8 (2020)

24. S. Ramakrishnan, J. Hasler, Vector-matrix multiply and winner-take-all as an analog classifier. IEEE Trans. Very Large Scale Integr. (VLSI) Syst. 22(2), 353–361 (2014)

25. S. Sezgin Gunay, E. Sanchez-Sinencio, CMOS winner take all circuits: a detail comparison. IEEE Symp. Circuits Syst. 41–44 (1997)

26. A. Sarje, P. Abshire, Mismatch compensation in winner take all (WTA) circuits. Proc. Midwest Symp. Circuit Syst. 168–170 (2009)

27. J.A. Starzyk, X. Fang, CMOS current mode winner-take-all circuit with both excitatory and inhibitory feedback. Electron. Lett. 29(10), 908–910 (1993)

28. G. Sundararajan, C. Winstead, A winner-take-all circuit with improved accuracy and tolerance to mismatch and process variations. Proc. IEEE Symp. Circuits Syst. 265–268 (2013)

29. B.P. Tan, D.M. Wilson, Semiparallel rank order filtering in analog VLSI. IEEE Trans. Circuits Syst.—II Analog Digit. Signal Process. 48(2), 198–205 (2001)

30. Y. Tsividis, Mixed Analog Digital VLSI Devices and Technology (World Scientific, 2002)

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