Studies of Low-Frequency Power Amplifier Class D with Operational Amplifiers and Transistors

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Abstract: This paper presents studies of Low-Frequency Power Amplifier class D with operational amplifiers and transistors. Their features have been presented and a selected circuit is described. The obtained results have been presented using MultiSIM software – oscillograms in specific nodes of the circuit, amplitude-frequency and phase-frequency responses and their parameters, and results of carried out Fourier and Parameter Sweep analyzes. Simulation results could be used in carrying out the optimization of its characteristics and qualitative parameters, as well as present and expected practical results. The results of performed experimental studies of implemented low-frequency power amplifier class D are presented also.

Keywords: Low-Frequency Power Amplifier Class D, Studies, Qualitative Parameters

1. Introduction

The development of electronics is inextricably linked to the use of amplification equipment in all its frequency and power ranges. This necessitates an increase of the useful output power and an increase of the efficiency. Low-frequency power amplifiers class D have low dissipation power and heat dissipation, small size and price, minimum power consumption in idle mode. The main advantage of class D amplifiers is the high efficiency, which can reach 90 ÷ 98% [7].

Low-frequency power amplifiers class D have been in existence for more than a few decades and are currently used mainly at low frequencies and high powers, i.e. for subwoofer management, and more rarely for amplifying middle and high frequencies of the sound range due to the significant distortions associated with the imperfections of the switching technology used. They are also used as low frequency amplifiers in modern television receivers, in audio- and home cinema systems, GSM communications, and more [10].

2. Representation

Class D amplifiers operate in switch mode with two states of the active element: unstopped, when a maximum current flows through the negligible low impedance of the saturated final transistor for a very short time interval with minimum losses, and clogged - when the current is approximately zero and the voltage drop is almost equal to the supply voltage without losses on the transistor. The main losses on the active element are mainly due to the time of impulse formation, i.e. when switching from one to the other state. If the duration of the fronts is reduced to zero, the efficiency will approach up to 100% [1]. This is the case when the active electronic element is replaced by an ideal switch. Such an idealized mode is not achievable due to the presence of: a minimum voltage on the saturated transistors \(U_{CE_{sat}}\) in the fully unstopped state, the reverse current in clogged state and the impossibility for the switching time to be infinitely small [6]. This time is in the range of one (or less) to tens of microseconds. To make possible the usage of the most efficient mode of operating the transistors, the switch, it is necessary to use a certain type of modulation. Of the possible modes for modulation in this mode of operation of the devices, the pulse width modulation (PWM) [3] is the most convenient in which the pulse duration varies according to the amplitude of the input modulation signal.
2.1. A Circuit of Low-Frequency Power Amplifier Class D with Operational Amplifiers and Transistors

A circuit of Low-Frequency Power Amplifier (LFPA) class D with operational amplifiers (OA) and transistors is presented in Figure 1. It contains four preliminary stages with operational amplifiers, two driver stages built with push-pull connected bipolar transistors, final MOSFET transistors designed for class D operation and a simplified configuration of the output low-pass (LC) filter used. The circuit is suitable for simulation study and practical implementation, and the results obtained illustrate its principle of operation.

The input stage is implemented with a non-inverting voltage repeater with OA1 (U1C) as the gain ratio is $A_{UF} \approx 1$. The second OA2 (U1D) is also connected as a non-inverting amplifier with $A_{UF} = 1 + \frac{R4}{R3}$, and the capacitor C1 serves as blocking between the two input stages. These two input stages perform the function of buffer amplifiers.

OA3 (U1A) has a complex function by performing a “summing” of its inverting input (adder-integrator) of the input signal with the output PWM signals formed at the output of the final stage (node 13) via the inverse feedback $R7$ as a result of which the output voltage is triangular in shape. Its voltage gain is determined by the ratio of the values of $R8$ and $R5$, and the capacitor $C2$ also provides a frequency correction for the OA. OA4 (U1B) is a non-inverting voltage comparator with positive feedback (saturated amplifier) realized with resistor $R10$ connected between its output and non-inverting input. The two stages, comprising OA3 and OA4, realize the implementation of the pulse width modulation and together they form a PWM-controller.

The final MOSFET transistors Q7 and Q8, coupled by OD schemes, of the P and N type respectively, form a semiconductor circuit.

The output low-pass filter is realized with the L1 - C3 elements.

The output amplified modulated voltage with PWM is applied to the inverting input of OA3 and the base of the transistors Q1 and Q2 respectively via the resistors $R7$ and $R12$ performing the functions of interstage parallel inverse feedbacks by voltage.

The formed width-modulated pulses at the output OA4 (U1B) unstop sequentially the transistors Q1-Q2, Q3-Q4 - Q5-Q6 in the presence of an input (modulating) signal and because the potentials $U_{GS}$ of the final MOSFETs Q7 and Q8 are increasing, the latter are also unstopped.

2.2. Simulation Studies of Low-Frequency Power Amplifier Class D with Operational Amplifiers and Transistors

The principle diagram (circuit) of the LFPA class D with the operational amplifiers and transistors from Figure 1 is introduced into the MultiSIM module of the software Circuit Design Suite for which simulation studies have been performed. The set parameters of the input sinusoidal signal through the functional generator used are: frequency 1 kHz, amplitude $1V_{p}$ and displacement of the DC current level -0.

Figure 2 illustrates how OA3 performs a “summing” of its inverting input (adder-integrator) of the input signal with the output PWM signals formed at the output of the final stage (node 13) via the inverse feedback $R7$, as a result of which the output voltage is triangular in shape and follows the change of the input continuous signal.

In Figure 3 is presented the oscillogram of the signal entering the transistor bases Q1 and Q2 which have the function of forming the amplitude of the PWM high-frequency signal. The signal is slightly increased compared to that of node 7 since the R12 returns a part of the output amplified PWM signal. The pulses of the OA4 output have a duty factor of about 50% with a constant amplitude in the field of passing of the input signal through the zero level. At the maximum value of the input signal, there is a minimum pulse width and amplitude reduction, and at the minimum - the inverse. A rebound of the front and rear impulse faces is seen on the oscillogram of the output signal in Figure 3.
In Figure 4, the signal was already been amplified and enters the base of the driver transistors Q3 and Q4. The PWM signal is displaced at a constant current level (node 9), which has a displacement Y pos. (div): -4.8 on the oscillogram in the “upper” arm of the circuit. The rectangular pulses have the same amplitude for the negative side of the input sinusoidal signal unlike the oscillograms in node 7 and Figure 3.

**Figure 2.** Input Signal (in Green) and Output Signal of OA3 (in Red) – Node 5.

**Figure 3.** Input Signal (in Green) and Base Signal of Transistors Q1 and Q2 (in Red) - Node 8.

**Figure 4.** Input Signal (in Green) and Base Signal of Transistors Q3 and Q4 (in Red) - Node 9.

In Figure 5 is given the oscillogram of amplified signal of the drains of the final MOSFET transistors Q7 and Q8. This is a PWM signal that combines the impulses of the final MOSFET transistors, where the signals are with constant amplitude and compensated by the two arms distortions of overshoots of the front and rear of the impulse faces.

The output signal of the LFPA with operational amplifiers and transistors is with low frequency and contains high frequency comprising components, resembling a high frequency carrier oscillation and a signal shape close to the sinusoidal one and is phased at 180° to the input continuous signal.

From the carried out DC analysis (DC Operation Point) of the LFPA class D has been established that the DC current potentials in the individual nodes have values in the range of tens of µV and since the OA4 and the transistors Q1 ÷ Q8 are in switch mode, there are no potentials for establishing the circuit in DC mode.

**Figure 5.** Input Signal (in Green) and Draine Signal of Transistors Q7 and Q8 (in Red) - Node 13.

**Figure 6 and Figure 7** present the obtained results of the performed Alternating Current analysis (AC Frequency) for the LFPA – respectively amplitude- and phase- frequency responses. From Figure 6 the parameters of the LFPA are set: lower $f_b = 10$ Hz, high $f_h = 28.75$ kHz cut-off frequencies and transmitted frequency band (bandwidth) $\Delta f = f_h - f_b = 28.74$ kHz.

On the shown phase-frequency response (Figure 7) is seen a transition with a phase shift of -180° to +180°, i.e. at 360° of the output signal for frequencies between 389 and 507 Hz. This could satisfy the condition of phase angle balance and the LFPA class D circuit to become a oscillator.

The results of the obtained Fourier Analysis are presented in Figure 8 where in tabular form are presented the DC component, number of harmonics, full harmonic distortion factor (THD) as well as the real and imaginary part of the individual harmonics and their normalized values.

With the thus obtained values of the amplitudes of the individual harmonic components from the spectrum of the output signal of the LFPA class D (Figure 8, Magnitude) the coefficient of non-linear distortions is determined for which the obtained value is
The obtained value of $k = 0.6123\%$ is small since the amplitude of the first harmonic component is significantly greater than that of the others, which determines that the LFPA class D in Figure 1 is high quality - Hi-Fi.

The relative difference between the $k$ value and the THD parameter (Figure 8) is

$$
\varepsilon = \frac{k - \text{THD}}{\text{THD}} = \frac{0.6123 - 0.6158}{0.6158} = -0.00568\%.
$$

Using the Parameter Sweep Analysis in Figure 9 and Figure 11 is shown the influence of the values of the elements R7, R12 (from inverse feedback) and C2 (introducing low-frequency correction of OA3) on the Amplitude-frequency response of the LFPA class D with operational amplifiers and transistors, respectively.

It is seen from Figure 9, that the value of the R7 resistor of the inverse feedback changes the gain ratio (the amplitude of the output signal), the slope of the decrease of the Amplitude-frequency response and the values of the $f_b$, $f_h$ and $\Delta f$. The obtained parameters of the LFPA class D from Figure 1 for some of the set values of the R7 resistor are presented in Table 1.
Figure 9. Parameter Sweep Analysis Relative to the R7 Value.

Figure 10. Parameter Sweep Analysis Relative to the R12 Value.

Figure 11. Parameter Sweep Analysis Relative to the C2 Value.

Table 1. Qualitative Parameters of the LFPA Class D at Different Values of the R7 Resistor of the Inverse Feedback:

| Value of R7, kΩ | $U_0$, V | $A_U$ | $f_b$, Hz | $f_h$, kHz | $\Delta f$, kHz |
|----------------|----------|-------|-----------|------------|----------------|
| 10             | 4,54     | 12    | 10,3      | 27,3       | 27,3           |
| 30             | 13,6     | 38    | 10        | 28          | 28             |
| 50             | 22,7     | 64    | 10        | 28,5        | 28,5           |
| 100            | 45,4     | 128   | 10        | 32          | 32             |

After graphically constructing the dependencies $A_U$, $f_b$, $f_h$ and $\Delta f$ in function of the value of the R7 resistor of the inverse feedback of LFPA class D has been found that:

a) the dependence of the gain ratio $A_U$ on the value of R7 is linear;

b) smaller R7 values change insignificantly the value of $f_b$, and larger ones do not affect;

c) the dependencies of $f_b$ and $\Delta f$ are almost linear and coincide, since $f_h$ determines the transmitted bandwidth.

The change in the values of the resistor R12 and the capacitor C2 have an impact on the Amplitude-frequency response of the LFPA class D only in the high frequency range, for frequencies above 200 kHz, outside the transmitted bandwidth. It is established that their influence is “opposite” i.e. the increase of the R12 value expands the fall of the Amplitude-frequency response in high frequency range and the increase of the C2 value narrows it.

2.3. Experimental Studies of the Practical Implemented Low-frequency Power Amplifier Class D with Operational Amplifiers and Transistors

Table 2 shows the DC voltages other than 0 V measured at the specified control nodes of the LFPA class D circuits at a supply voltage of $\pm U_{cc}$ 30 V and no input signal.

Table 2. DC Voltages in Respective Nodes of the LFPA Class D.

| Node № | 5 | 6 | 7 | 8 | 9 | 10 | 11 | 12 | 15 |
|--------|---|---|---|---|---|----|----|----|----|
| $U$, V | -0,1 | -0,1 | 0,74 | 0,72 | 25,6 | -25,8 | 25,8 | -26 | -0,07 |
The obtained amplitude (AP) and amplitude-frequency (AFR) characteristics of the LFPA class D are presented in Figure 12 and Figure 13 respectively, and the parameters determined by them – in Table 3 and Table 4, respectively.

Figure 12 shows that the implemented LFPA class D amplifies in the same way at the three operating frequencies from the performed experimental studies - \( f_1 = 0.1 \text{ kHz}, f_2 = 0.5 \text{ kHz} \) and \( f_3 = 1 \text{ kHz} \), but the most linear is the dependence at medium frequencies (1 kHz) where the dynamic range D takes the smallest value.

From Figure 13 it has been established that LFPA class D has steep slopes of AFR at low frequency. The gain factor for medium frequencies in the bandwidth is characterized by pulsations. The widest bandwidth is realized at the amplitude of the input signal \( u_{i3} = 0.3 \text{ V} \). In this case, the bandwidth is the widest as \( \Delta f \approx 38 \text{ kHz} \).

From the parameters presented in Table 4 of the AFR of the LFPA class D it is established that the lower cut-off frequency \( f_b \) has the same value of 20 Hz and the value of \( f_h \) changes depending on the amplitude of the input signal as it receives the highest value at the average amplitude of the input signal. For \( u_{i3} = 0.6 \text{ V} \) the transmitted bandwidth is the narrowest and it is in the order of 19 kHz. It is established from the values obtained for \( \Delta f \) (Table 4) that the implemented LFPA class D is Hi-Fi.

The maximum input voltage \( u_{\text{max}} \), which LFPA class D can amplify without distortion, is the same for lower and medium frequencies, and for higher ones its value slightly decreases. The sensitivity threshold of the circuit is the same for the operating frequencies studied and is in the order of 35 mV.

Table 3. Qualitative Parameters of the LFPA Class D, Determined by AP.

| Parameter | \( f_1 = 0.1 \text{ kHz} \) | \( f_2 = 0.5 \text{ kHz} \) | \( f_3 = 1 \text{ kHz} \) |
|-----------|----------------|----------------|----------------|
| \( A_U \) | 26.3 | 27.1 | 26.8 |
| \( u_{\text{max}}, \text{ V} \) | 0.68 | 0.68 | 0.5 |
| \( u_{\text{min}}, \text{ V} \) | 0.035 | 0.035 | 0.035 |
| D | 19.4 | 19.4 | 14.2 |

In Figures from 14 to 23 are shown the timing diagrams obtained by means of a digital oscilloscope in the mentioned control nodes of the LFPA class D and the shape of the signals before and after the low-pass filter is shown in the time diagrams in Figure 24 (node 13) and Figure 25 (node 15) respectively.

Oscillograms are captured at the frequency of the input signal \( f = 1 \text{ kHz} \).

In Figure 14 and Figure 15 are shown the timing diagrams of the input harmonic signal supplied by the functional generator MCP SG2110 DDS used. Because OU1 is a repeater the signals have the same amplitude.
The type of signal in node 4 is shown in Figure 16 - OU3 performs the function of an adder-integrator as the two signals are sent to its non-inverting input: the continuous input and the high frequency PWM from the output via the inverse feedback circuit, implemented with the R7 resistor.

The timing diagram of the signal after the adder-integrator (OU3, node 5) is shown in Figure 17. It has a triangular shape and has an operating frequency greater than 100 kHz. Rectangular pulses are formed at the output of the comparator - OU4 and their type is shown on the oscillogram in Figure 18. As a result of the presence of the R11 resistor and inverse feedback implemented with R12, Figure 19 shows the type of signal of the transistor bases Q1 and Q2 where the impulse mark/space ratio is greater than 50%.

From the timing diagrams in nodes 9 and 10 (Figures 20 and 21), it was found that the control and also the output signals for the two pairs of complementary transistors have a different mark/space ratio - greater than and less than 50%, respectively.

From the timing diagrams of Figure 22 and Figure 23 the difference in the control signals for the final MOSFET transistors is detected. They show that transistor Q7 has been unstopped during most of the PWM signal period at the output of the LFPA class D compared to Q8.

The output signal of LFPA class D, which is also an output of low-pass filter, is presented on the oscillogram in Figure 25. It has a sinusoidal shape and the presence of high frequency components is also established.
In Figures 26 ÷ 29 are shown the timing diagrams of the output PWM signal in node 13 of the LFPA class D at different amplitude of the continuous input signal and different input frequency. Using the digital oscilloscope RIGOL-DS1102E, the impulse parameters were measured, the values of which are presented in Table 5. It is found that at different amplitude of the input signal, the values of the parameters of the PWM signals (the pulses in node 13) are different, as is also different the “high” operating frequency. For the second value of the input voltage $U_{i2} = 0,3$ V at $f = 1$ kHz the impulse parameters:

- $T$ – period of impulse;
- $T_{on}$ – time of pulse rise;
- $T_{off}$ – time of pulse decrease;
- $T_{r}$ – duration (time) of the inversion rebound of the impulse;
- $\delta_{f}$ – rebound of the back front of the impulse take the highest values.
At a lower frequency of the input signal $f_2 = 100$ Hz at the same voltage, the operating frequency of the PWM signals decreases and the values of the individual pulse parameters ($T$, $T_{on}$, $T_{off}$, $T_r$, and $\delta_f$) increase.

### Table 5. Measured Parameters of the Output PWM Signal of the Drains of the Final MOSFET Transistors - Node 13.

| $f_2 = 1$ kHz | $U_{i1}$ | $U_{i2}$ | $U_{i3}$ | $U_{i3}$ |
|--------------|---------|---------|---------|---------|
| $U_{on}$     | 0.1 V   | 0.3 V   | 0.6 V   | 0.6 V   |
| $T_{on}$     | 3,32 µs | 5,24 µs | 2,4 µs  | T       |
| $T_{off}$    | 320 ns  | 336 ns  | 264 ns  | T_{on}  |
| $T_r$        | 40 ns   | 232 ns  | 128 ns  | T_r     |
| $\delta_f$   | 12,8%   | 12,4%   | 9,6%    | $\delta_f$ |

### 3. Conclusion

The presented schematics and the obtained simulation results explain the principle of operation and illustrate the operation of the LFPA class D shown in Figure 1, implemented with widespread and widely applied in practice discrete elements with low cost – operational amplifiers and transistors. They could be used to optimize its qualitative indicators - parameters and characteristics, and also provide expected practical results.

The experimental studies of the implemented LFPA class D with operational amplifiers and transistors present its qualitative parameters and characteristics and illustrate the type of signals in the individual nodes of the circuit as well. From them, information can be obtained about the causes of the accompanying LFPA class D deficiencies and to be done improvement of the technology used and they should be reduced to a minimum.

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