Analytic analysis for effects of input initial phase on input-output dynamics of memristor

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Abstract The analytic expressions of input-output dynamic parameters of memristor (i.e., state variable, memristance, and output response) with respect to initial phase of sinusoidal input are obtained by using Homotopy Analysis Method (HAM). Furthermore, a new exponent, called Response Time Delay (difference) between the Maximum Values of input and output—RDMV-IO, is proposed to rapidly analyze the memristor input-output dynamics, under different initial phases. Employing RDMV-IO, we further reveal the intrinsic relationship between initial phase and dynamics. The studies are verified by using Mathematica simulations adopting nonlinear dopant drift memristor model coupled with window function.

key words: memristor, input-output dynamics, initial phase, analytic, HAM
Classification: Integrated circuits

1. Introduction

Since the successful development of the Hewlett-Packard (HP) prototype device [1], memristor has drawn a great deal of research interests [2]. It has been widely used in embedded memory [3, 4], neurobiology [5, 6], artificial intelligence [7], neural networks [8, 9], etc., due to the following advantages: fast speed [10], high density [11], and low power consumption [12, 13]. Memristor is a category of nano-devices with nonlinear input-output dynamics. The memristor input-output dynamics are the transient evolutions of dynamic parameters, such as state variable, memristor resistance (referred to as memristance), and output response. The dynamics depend greatly on the state variable that is controlled by the history of input excitation [1], due to the fact that the state variable is a core parameter of memristor which controls the other dynamic parameters. Therefore, the input-output dynamics is significantly affected by the amplitude, frequency, and initial phase of input excitation [14].

Many studies [1, 15, 16, 17, 18, 19] have focused on the effects of the amplitude and frequency of sinusoidal input on memristor input-output dynamics. However, by definition, the initial phases of the inputs are ignored or assumed to be 0° in order to simplify these studies.

Even worse, the commonly used numerical simulations in these studies may cause serious truncation and rounding errors [20]. By contrast, analytic analysis is capable of overcoming these errors and enabling ones to implement deep theoretical investigations and performance predictions of memristor [19]. Moreover, the analytic expressions can be conveniently integrated into EDA tools, which facilitate the commercial applications of memristor [18].

Unfortunately, there are few analytic studies in the effect of initial phase on the memristor input-output dynamics so far. Elashkar has investigated the influences of the initial phase of sinusoidal input on the transient memristance and I-V Pinched Hysteresis Loop (PHL) of memristor, and has derived the analytic expression of transient memristance [21]. However, besides the transient memristance, the analytic expressions of other dynamic parameters (i.e., state variable, average state variable, average memristance, and output response), as functions of the initial phase, are also considerably important for theoretical analyses of evaluations of the memristor input-output dynamics. In fact, these expressions were not given in [21]. Additionally, Elashkar has adopted a memristor model without coupling a nonlinear window function for memristance analyses [21], resulting in the analyses did not fully exhibit actual physical characteristics. Liu has explored the curves of transient memristance with various initial phases in [22], where the used memristor models are based on the simple piece-wise linear and cubic mathematical models rather than the actual physical model. Besides, the analytic expressions of dynamic parameters were not derived in [23], limiting the practical applications of the results.

To address the above limitations, Homotopy Analysis Method (HAM) [23, 24] is employed in this paper to analytically study the effects of the initial phase of sinusoidal input on memristor input-output dynamics. Without sacrificing the generality, the widely used HP physical model [1] coupling with the nonlinear window function [25] is adopted for representing actual physical
characteristics. To be specific, firstly, HAM is used to solve the state equation of HP physical model under different input initial phases for obtaining approximate analytic solution of state variable. This is because the state variable is a core parameter of HP memristor that controls the other dynamic parameters, and hence the input-output dynamics. Then, the solved state variable is employed to derive the approximate analytic expressions of average state variable, transient memristance, average memristance, and I-V PHL. Finally, based on the traditional I-V PHL area and the newly proposed Response Time Delay (difference) between Maximum Values of input and output (RDMV-IO), we further reveal the intrinsic relationship between the input initial phase and the memristor input-output dynamics. All the solved parameters above have the advantages of closed-form expression and symbolic computation due to the HAM. To the best of the authors’ knowledge, this is the first paper that HAM is applied to analyze the effects of the initial phase of sinusoidal input on the memristor input-output dynamics.

2. Study effects of initial phase on state variable $x$ by HAM

\[
x_3(t, \theta_0, h) = x_0 + \sum_{m=1}^{3} x_m(t, \theta_0, h)
\]

\[
= 1 + 1024\gamma_1 - 1024\gamma_1\cos(\omega t)
+ (\gamma_3 h^2 + 3\gamma_3 + 43008\gamma_2 + 3\gamma_3)\cos(\theta_0) + 512\gamma_1\cos(2\theta_0) + 14336\gamma_2\cos(3\theta_0)/3
\]

\[
- (\gamma_3 h^2 + 3\gamma_3 + 43008\gamma_2 + 3\gamma_3)\cos(\omega t + \theta_0) - 1024\gamma_1\cos(\omega t + 2\theta_0) - 28672\gamma_2\cos(\omega t + 3\theta_0)
\]

\[
+ 14336\gamma_2\cos(2\omega t + \theta_0) + 512\gamma_1\cos(2\omega t + 2\theta_0) + 14336\gamma_2\cos(2\omega t + 3\theta_0)
\]

\[
- 14336\gamma_2\cos(3\omega t + 3\theta_0)/3
\]

We first study effects of initial phase on state variable $x$ by HAM because $x$ is the core parameter of memristor that controls the other dynamic parameters (such as voltage $V_M$, memristance $R_M$). Based upon the original physical model of HP memristor [1], the port equation, memristance, and state equation of memristor with respect to input initial phase can be expressed by

\[
V_M(t, \theta_0) = R_M(t, \theta_0) \times I_M(t, \theta_0)
\]

\[
R_M(t, \theta_0) = R_{ON} x(t, \theta_0) + R_{OFF}[1 - x(t, \theta_0)]
\]

\[
= x_N(t, \theta_0)(1 - \alpha) R_{ON} + \alpha R_{ON}
\]

\[
\frac{dx(t, \theta_0)}{dt} = \frac{\mu_V R_{ON}}{D^2} f[x(t)]
\]

where the state variable $x_N(t, \theta_0) = w(t, \theta_0)/D \in [0,1]$ and input current $I_M(t, \theta_0) = A \sin(\omega t + \theta_0)$, $\theta_0$ is the initial phase. For representing the actual physical characteristics, 1) the Joglekar window function (J-window) $f[x(t)] = 1 - (2x - 1)^2P$ [25], where $P$ is a positive integer, was introduced in (1), ensuring $x \in [0, 1]$ and overcoming the boundary effect; 2) $P = 2$ was chosen in this work for the effect of nonlinear ion drift in the doped layer [1]. For clarity, all the definitions and values of physical parameters of HP memristor and the input used in simulations are summarized in Table I.

In our previous work, a HAM-based analytic modeling methodology for memristor was proposed [26], in which HAM is exploited for solving the state equation of memristor to obtain the $N$-order analytic approximate solution $x_N(t, \theta_0, h)$ of state variable $x(t)$, where $N \in Z$ is the approximation order (taking into account the approximate accuracy and simulation efficiency, we chose $N = 3$ in this work), $h \in R$ is the convergence-control parameter that used to accelerates the convergence for solving $x_N(t, \theta_0, h)$. The method for solving $h$ will be explained later in this section. Note the initial phase $\theta_0$ of input is assumed to be $0^\circ$ to simplify the analyses in [26] and original paper of HP memristor [1], whereas in this work it is an unknown whose effects on the memristor input-output dynamics will be systematically studied.

Based on our modeling methodology, $x_N(t, \theta_0, h)$ with J-window (we chose $N = 3$ and $P = 2$, as explained above) has the following analytic expression:

\[
\gamma_1 = x_0(x_0 - 1)(h + 3/2)(x_0^2 - x_0 + 1/2)
\]

\[
\cdot (x_0 - 1/2)^3\xi h^2
\]

\[
\gamma_2 = x_0(x_0 - 1)(x_0^2 - x_0 + 1/2)(x_0 - 1/2)^2
\]

\[
\cdot (x_0^4 - 2x_0^3 + 3x_0^2/2 - x_0 / 2 + 1/28)\xi^3 h^3
\]

\[
\gamma_3 = 16x_0(x_0 - 1)(x_0^3 - x_0 + 1/2)\xi
\]

where $\xi = \mu_V R_{ON} A / D^2 \omega$

\[
\gamma_1, \gamma_2, \gamma_3, \text{ and } \xi \text{ are variables used for simplification of Eq. (4)}.
\]

The average $N$-order analytic approximate solution of $x_N(t, \theta_0, h)$ over a cycle is given by

\[
x_N(t, \theta_0, h) = \frac{\int_0^{2\pi/\omega} x_N(t, \theta_0, h) dt}{2\pi/\omega}
\]

In Eq. (4), the convergence-control parameter $h$ is identified as a variable need to be solved. To derive the
complete analytic expression of Eq. (4), the optimal $h$ corresponding to different $\theta_0$, which used to accelerates the convergence of approximation, is solved by minimizing the discrete squared residual error $E_{\text{res}}(\theta_0, h)$ [23, 26]. The solved $h$ are listed in Table II.

As seen from Eq. (4), the initial phase has significant effects on the mostly cosinoidal terms of state variable expression, i.e., $\theta_0$ determines the evolution of state variable. To verify the validity of the above theoretical analysis, we performed the analytic simulations based on Eqs. (4)–(9) and compared it with numerical simulations. Note in this work all the analytic simulations were implemented by using the Mathematica [27], while the numerical simulations were performed by using the widely used fourth-order Runge–Kutta method.

Fig. 1 shows the comparisons of evolutions of transient state variables $x$ and the accompanying average state variable $\bar{x}$ in four normalized input cycles. For the convenience of comparisons, each cycle has the same initial state variable $x_0$. The cycles are drawn for $0^\circ$, $90^\circ$, $180^\circ$, and $270^\circ$ initial phases, respectively. Each initial phase corresponds to a normalized cycle. The simulation results in Fig. 1 shows that:

1) our analytic simulations match the above theoretical analysis and the numerical simulations well, denoting the high accuracy of the solved analytic expression Eqs. (4)–(9). The associated Maximum Relative Error (MaxRE), Mean Relative Error (MRE), and Root Mean Square Error (RMSE) between the analytic and numerical simulation is $8.3\%$, $2.9\%$, and $0.013$.

2) the variations in $x$ and relevant $\bar{x}$ are the largest for $\theta_0 = 0^\circ$ and are the smallest for $\theta_0 = 180^\circ$, because the applied input current is firstly negative for $\theta_0 = 180^\circ$, resulting in $x$ decreases from $x_0 = 0.1$ and then reaches its boundary “0” that makes $x = 0$ and stay unchanged. While in case of $0^\circ$, the input is positive and moderate, ensuring $x$ operates in a normal range and does not reach boundary “0” or “1”, i.e., $x \in (0, 1)$. The variations in $x$ and relevant $\bar{x}$ for $90^\circ$ and $270^\circ$ are mirror symmetrical, because the inputs of memristor have the opposite polarities but the same amplitudes.

To exploit the advantages of analytic analysis as mentioned in Introduction, in the following sections, we
performed simulations all based on our obtained analytic expressions having high accuracy (such as Eqs. (4) and (9), as shown from $x$ and $\bar{x}$ in Fig. 1), instead of the numerical simulations.

3. Effects of initial phase on $x$-controlled dynamic parameters

It is known from Section 2 that state variable $x$ is the core of HP memristor that controls the other dynamic parameters. So that in this section, we studied the effects of initial phase $\theta_0$ on other dynamic parameters by using $x$ and the theoretical analyses in Section 2. This study is the foundation of input-output dynamics analysis in Section 4 and Section 5.

Based on Eqs. (1), (2), and (4), we derive the three order analytic solution $V_M^3(t, \theta_0, \omega)$ and $R_M^3(t, \theta_0, \omega)$ of output response $V_M(t)$ and memristance $R_M(t)$, respectively

$$V_M^3(t, \theta_0, \omega) = I_M(t, \theta_0) \times [x_3(t, \theta_0, \omega)R_{ON} + \alpha(1 - x_3(t, \theta_0, \omega))R_{ON}]$$ (10)

$$R_M^3(t, \theta_0, \omega) = x_3(t, \theta_0, \omega)R_{ON} + \alpha(1 - x_3(t, \theta_0, \omega))R_{ON}$$ (11)

Then, according to Eqs. (4) and (11), the average $R_M^3(t, \theta_0, \omega)$ over a cycle is given by

$$\overline{R_M^3(t, \theta_0, \omega)} = \frac{\int_0^{2\pi/\omega} R_M^3(t, \theta_0, \omega) \, dt}{2\pi/\omega}$$ (12)

Fig. 2 shows the evolutions of the $x$-controlled dynamic parameters of memristor, i.e., the output voltage $V_M^3$ [see Fig. 2(a), (b), (c), and (d), based on Eqs. (4) and (10)], memristance $R_M^3$ and average memristance $\overline{R_M^3}$ [see Fig. 2(e), (f), (g), and (h), based on Eqs. (11), (12)], and $I_M - V_M$ PHL [see Fig. 2(i), (j), (k), and (l), based on Eqs. (4) and (10)], under four different initial phases. Note that when the initial phase is $0^\circ$ and $180^\circ$, the corresponding dynamic parameters have the maximum and minimum values, respectively, similar to $x$ and $\bar{x}$ in Fig. 1. We have found that the initial phase $\theta_0$ has significant effects on $x$ and $\bar{x}$ from Fig. 1. Fig. 2 further illustrates that $x$ under different $\theta_0$ directly controls the evolutions of the other dynamic parameters. In other words, the effects of $\theta_0$ are transmitted to the other dynamic parameters through $x$. Because of this, we come to a conclusion that the initial phase $\theta_0$ also has significant effects on the dynamic parameters.

4. Effects of initial phase on $I_M - V_M$ PHL area

The method that analyze dynamics with dynamic parameters presented in Section 3 is direct and effective. However, the workload of this traditional method is too heavy, a simple method that only use one parameter, referred to as $I_M - V_M$ PHL area, was proposed to represent memristor input-output dynamics [28, 29]. So in this section, we discussed the effects of initial phase on the $I_M - V_M$ PHL area.

According to the classical fingerprints of memristor [29], the PHL area, defined as the area enclosed by the
closed $I_M - V_M$ loop, is positively correlated with memristor input-output dynamics [28]. For instance, as PHL area tends to 0, the nonlinear input-output dynamics will disappear, i.e., the memristor will convert to a traditional linear resistor. The PHL area can be expressed by

$$ S = \oint V_M^N(t) dI_M(t) $$

where $\Gamma$ is the closed $I_M - V_M$ loop as shown in Fig. 2(i), (j), (k), and (l). Substituting the given $I_M(t, \theta_0)$ into (13), the analytic expression of PHL area is

$$ S(\theta_0) = 2 \int_0^{\pi/\omega} V_M^N(t, \theta_0) A_\omega \cos(\omega t) \, dt $$

Combing Eqs. (1), (2), and (11), we can obtain the analytical expression of Eq. (14). Then the quantitative analysis in Fig. 3 shows that the PHL area increases first and then decreases with the increasing initial phase, demonstrating a U-shaped curve. This is because as $\theta_0$ increases in $[0^\circ, 180^\circ]$, the variation in $x$ decreases due to the input that is positive, leading to the decrease in variation in output and, thus, that in $I_M - V_M$ PHL area, and vice versa in $[180^\circ, 360^\circ]$. Since PHL area is positively correlated with memristor input-output dynamics, the observation in Fig. 3 indicates $\theta_0$ and the dynamics also have a U-shaped relation. Note that when $\theta_0$ is $0^\circ$ (or $360^\circ$) and $180^\circ$, the corresponding PHL areas have the minimum and maximum values, denoting the strongest and the weakest input-output dynamics, respectively.

5. RDMV-IO

Although using the $I_M - V_M$ PHL area to analyze the memristor input-output dynamics is indeed a simpler method compared with using dynamic parameters, this method is quite inconvenient and easy to cause error for practical measurement (which we shall explain bellow). Thus, we propose a novel method here for analyzing the input-output dynamics by the Response Time Delay (difference) between Maximum Values of input and output in one cycle (RDMV-IO), a graphical interpretation of RDMV-IO is shown in Fig. 4(a).

Clearly, the measurement of RDMV-IO is more convenient than that of PHL area. This is because we only need to measure the times corresponding to the maximum values of the input and output, and then simply subtract these two times to achieve RDMV-IO extraction. Nevertheless, measuring the PHL area requires not only real-time measurement of a large number of transient values of the input and output, but also the storage of these values. Finally, mathematical software is needed to numerically calculate the specific $I_M - V_M$ PHL area. Therefore, analyzing input-output dynamics by RDMV-IO is an easy-to-measure method.

To verify the validity of RDMV-IO, we performed the simulation as shown in Fig. 4(b). It is clear from Fig. 4(b) that as the initial phase increases, the normalized RDMV-IO ($|RDMV-IO|/T \times 100\%$) increases firstly and then decreases, similar to the PHL area-initial phase curve in Fig. 3. This observation suggests that RDMV-IO, like PHL area, is also positively correlated with the input-output dynamics of memristor. Therefore, with the conveniences of RDMV-IO mentioned before, we can rapidly and easily analyze the effects of initial phase on the input-output dynamics of memristor. The physical mechanism of RDMV-IO may be contributed to the moving time of doped layer, microscopically, due to drift or diffusion time of ion [30].

6. Discussion

After fabricating memristor, its input-output dynamics depend only on input signals of applications. Therefore, the analysis method in this paper can be employed for evaluation and prediction of memristor performance, which is particularly useful for applications that need to enhance dynamics (the larger RDMV-IO the better) or to avoid the disappearance of dynamics (i.e., RDMV-IO=0).

It should be noted that, although our research object
is the HP memristor with J-window under sinusoidal input current, the analytic analysis method of the memristor input-output dynamics can be easily extended to other types of novel memory devices, window functions, and inputs (e.g., triangular voltage), due to the generality of HAM [23, 24] to solve Nonlinear Differential Algebraic Equation regarding core parameter of device (such as the state variable in HP memristor).

7. Conclusion

In summary, we have analytically analyzed the effects of initial phase of sinusoidal input on memristor input-output dynamics by employing the HAM and the newly proposed RDMV-IO. We found that the input initial phase has significant effects on the dynamics parameters. Based on these results, we further reveal that the memristor input-output dynamics are affected by the increasing input initial phase, demonstrating a U-shaped relationship.

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