Optimal QRAM and improved unitary synthesis by quantum circuits with any number of ancillary qubits

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Abstract

As a cornerstone for many quantum linear algebraic and quantum machine learning algorithms, QRAM aims to provide the transformation of $|0^n⟩ → |i⟩ |ψ_i⟩$ for all $i ∈ \{0, 1\}^k$ for the given $n$-qubit states $|ψ_i⟩$. In this paper, we construct a quantum circuit for implementing QRAM, with depth $O(n + k + \frac{m^2}{n^{k+1}})$ and size $O(2^{n+k})$ for any given number $m$ of ancillary qubits. These bounds, which can also be viewed as time-space tradeoff for the transformation, are optimal for any integer parameters $m$, $k ≥ 0$ and $n ≥ 1$.

Our construction induces a circuit for the standard quantum state preparation (QSP) problem of $|0^n⟩ → |ψ⟩$ for any $|ψ⟩$, pinning down its depth complexity to $\Theta(n + 2^n/(n+m))$ and its size complexity to $\Theta(2^n)$ for any $m$. This improves results in a recent line of investigations of QSP with ancillary qubits.

Another fundamental problem, unitary synthesis, asks to implement a general $n$-qubit unitary by a quantum circuit. Previous work shows a lower bound of $Ω(n + 4^n/(n + m))$ and an upper bound of $O(n 2^n)$ for $m = Ω(2^n/n)$ ancillary qubits. In this paper, we quadratically shrink this gap by presenting a quantum circuit of depth of $O\left(n 2^{n/2} + \frac{2^{2n/3} k^2}{m^{3/2}}\right)$.

1 Introduction

Quantum algorithms use quantum effects such as quantum entanglement and coherence to process information with the efficiency beyond any classical counterparts can achieve. In the past decade, many quantum machine learning algorithms [BWP*17] share a common subroutine of quantum state preparation (QSP), which loads a $2^n$-dimensional complex-valued vector $v = (v_x : x ∈ \{0, 1\}^n)γ ∈ \mathbb{C}^{2^n}$ to an $n$-qubit quantum state $|ψ⟩ = \sum_{x \in \{0, 1\}^n} v_x |x⟩$. These include quantum principle component analysis [LMR14], quantum recommendation systems [KP17], quantum singular value decomposition [RSML18], quantum linear system algorithm [HHL09, WZP18], quantum clustering [KLLP19, KL20], quantum support vector machine [RML14], etc. Quantum state preparation is also a key step in many Hamiltonian simulation algorithms [BCC+15, LC17, LC19, BCK15].

Some of these quantum machine learning algorithms, such as quantum linear system algorithm [WZP18], quantum recommendation systems [KP17] and quantum $k$-means clustering [KLLP19], need an oracle that can coherently prepare many states: $|i⟩ |0^n⟩ → |i⟩ |ψ_i⟩$, for all $i ∈ \{0, 1\}^k$. We shall refer to this as the controlled quantum state preparation (CQSP) problem. The QSP and CQSP problems are also used in quantum walk algorithms such as the one by Szegedy [Sze04] and by MNRS [MNRS11]. Given a general $N × N$ state transition probability matrix $P = [P_{xy}]_{x,y∈[N]}$, quantum walk algorithms often call three subroutines: Setup, Check and Update. The Setup procedure needs to prepare state $\sum_x \sqrt{π(x)} |x⟩$, where $π$ is the stationary distribution of $P$. The Update procedure needs to realize $|x⟩ |0^{\log N}⟩ → |x⟩ \sum_y \sqrt{P_{xy}} |y⟩$, a typical CQSP problem.

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The CQSP problem can also be viewed as quantum random access memory (QRAM) with quantum content [GLM08b, GLM08a]. QRAM aims to provide the transformation of $|i⟩|0^n⟩ → |i⟩|ψ_i⟩$ for all $i \in \{0, 1\}^k$, where $|ψ_i⟩$’s are states in $|0⟩, |1⟩)^{\otimes n}$ or in $(C^2)^{\otimes n}$, depending on whether the QRAM stores classical or quantum information as its content. The aforementioned quantum algorithms usually assume an efficient implementation of QRAM with classical content, and the hope is to have hardware device to realize this. Despite some conceptual designs, working QRAM devices are yet to be convincingly demonstrated, even for a small scale. In this paper, we address the related and fundamental question of implementing the general QRAM with quantum content by standard quantum circuits, and show tight depth and size bounds for it.

In a different vein, quantum algorithms can be represented as a unitary, which need to be implemented by quantum circuits for a digital quantum computer to run the algorithm. What is the minimum depth and size that any unitary operation can be compressed to? This paper also address this Unitary synthesis (US) problem by presenting a parametrized quantum circuit that can implement any given unitary operation.

In all these CQSP/QRAM, QSP and US problems, we hope to find quantum circuits as simple as possible for the sake of efficiency of execution and physical realization. Standard measures for quantum circuits include depth, size (i.e. the number of gates), and number of qubits. The depth of a circuit corresponds to time, and the number of qubits to space. Rapid advancement of the number of qubits provide opportunities to trade space for time, and indeed it has been found that ancillary qubits are useful in compressing the circuit depth for many tasks including CQSP/QRAM, QSP and US. It is a fundamental question to pin down the time-space tradeoff, or in circuit complexity language, the depth-qubit number tradeoff, for both quantum state preparation and general unitary synthesis problems.

**QRAM and Quantum state preparation** Much previous work focuses on efficient QRAM by a hardware device, except for few that consider to realize QRAMs by quantum circuits [PPR19,dVDAPdS20, DMGM20]. QSP, in contrast, has been extensively studied. Bergholm et al. presented a QSP circuit with $2^{n+1} - 2n - 2$ CNOT gates and depth $O(2^n)$, without ancillary qubits [BVMS05]. Plesch and Brukner [PB11] improve the number of CNOT gate to $\frac{23}{8}2^n - 2\frac{5}{2}n + \frac{5}{2}$ for even $n$, and $\frac{115}{96}2^n$ for odd $n$. Ref. [BVMS05] also gives a depth upper bound of $\frac{23}{8}2^n$ for even $n$ and $\frac{115}{96}2^n$ for odd $n$. The best result was obtained in [STY’21], where the authors achieve the depth $O(2^n/n)$, which is optimal.

Zhang et al. [ZYY’21] presented a QSP circuit of depth $O(n^2)$, by using $O(4^n)$ ancillary qubits, but the circuit involves measurement and the probability of successfully generating the target state is only $Ω(1/(\max|v|^22^n))$.

The best previous result on QSP for general $m$ is by [STY’21], where the authors presented a quantum circuit of depth $O\left(n + \frac{2^n}{n+m}\right)$ and size $O(2^n)$ for $m = O\left(\frac{2^n}{n\log n}\right)$ or $m = Ω(2^n)$, which is asymptotically optimal. For $m \in \left[Ω\left(\frac{2^n}{n \log n}\right), O(2^n)\right]$, they proposed a QSP circuit of depth $O(n \log n)$, which is only $O(\log n)$ off from the lower bound $Ω\left(\max\left[n, \frac{2^n}{n+m}\right]\right)$. Later, Rosenthal independently constructed a QSP circuit of depth $O(n)$ using $O(n2^n)$ ancillary qubits [Ros21]. This year, [ZLY22] gave yet another proof of the $O(n)$ depth upper bound using $O(2^n)$ ancillary qubits. Both [Ros21] and [ZLY22] did not give results for general $m$.

A related study is to prepare a quantum state in the unary encoding $\sum_{k=1}^{2^n} v_k |e_k⟩$ instead of binary encoding $\sum_{k=1}^{2^n} v_k |k⟩$ in [JDM*’21], where $e_i \in \{0, 1\}^{2^n}$ is the vector with the $k$-th bit being $1$ and all other bits being $0$. The binary encoding quantum state preparation is more efficient than unary encoding, because binary encoding QSP utilizes $n$ qubits but unary encoding utilizes $2^n$ qubits. In [JDM*’21], Johri et al. prepared a unary encoding quantum state by a circuit of depth $O(n)$. Moreover, by encoding $k$ to a $d$-dimensional tensor ($k_1, k_2, \ldots, k_d$), they extended the QSP circuit construction and obtained circuit depth $O\left(\frac{2^{2n-n/d}}{d}\right)$. If $d = n$, their encoding of $k$ is binary encoding and the depth upper bound is $O(2^n)$.

In this paper, we first give new quantum circuit constructions for CQSP, i.e. QRAM with quantum content.
Theorem 1 (CQSP/QRAM). For any integers \( k, m \geq 0 \), \( n > 0 \) and any quantum states \(| \psi_i \rangle : i \in \{0, 1\}^k \), the following controlled quantum state preparation

\[ |i \rangle |0^n \rangle \rightarrow |i \rangle |\psi_i \rangle, \forall i \in \{0, 1\}^k \]

can be implemented by a quantum circuit consisting of single-qubit and CNOT gates of depth \( O(n + k + \frac{\max\{n, m\}}{n+k+m}) \) and size \( O(2^{n+k}) \) with \( m \) ancillary qubits. These bounds are optimal for any \( k, m \geq 0 \).

Taking \( k = 0 \), this immediately implies the following result for QSP.

Theorem 2 (QSP). For any \( m \geq 0 \), any \( n \)-qubit quantum state \(| \psi_i \rangle \) can be generated by a quantum circuit, using single-qubit gates and CNOT gates, of depth \( O(n + \frac{\max\{n, m\}}{n+m}) \) and size \( O(2^n) \) with \( m \) ancillary qubits. These bounds are optimal for any \( m \geq 0 \).

These bounds match the known lower bounds of circuit depth and size for QSP: \( \Omega\left(\max\{n, \frac{m}{n+m}\}\right) \) for depth [ZWH21, STY+21] and \( \Omega(4^n) \) for size [SMB04]. Thus we completely characterize the depth and size complexity for QSP with any number of ancillary qubits.

Unitary synthesis. For general unitary synthesis, Barenco et al. constructed a circuit involving \( O(n^3 4^n) \) CNOT gates [BBC+95]. Knill reduced the circuit size to \( O(n4^n) \) in [Kni95], which was further improved by Vartiainen et al. [VMS04] and Mottonen and Vartiainen [MV05] to \( O(4^n) \), the same order as the lower bound of \( \left\lceil \frac{4^n}{4} - 3n - 1 \right\rceil \) for the number of CNOT gates [SMB04].

These results assume no ancillary qubits. When there are \( m \) ancillary qubits available, Ref. [STY+21] presented a quantum circuit for \( n \)-qubit general unitary synthesis of depth \( O(n2^n + \frac{\max\{n, m\}}{n+m}) \), and also proved a depth lower bound \( \Omega\left(n + \frac{4^n}{n+m}\right) \). Hence, their circuit depths are asymptotically optimal when \( m = O\left(\frac{2^n}{n}\right) \), and leave a gap of \( \Omega\left(n + \frac{4^n}{m}\right), O\left(n2^n + \frac{4^n}{m}\right) \) when \( m = \Omega\left(\frac{2^n}{n}\right) \). By using Grover search in a clever way, Rosenthal improved the depth upper bound to \( O(n2^{n/2}) \) with \( m = \Theta(n4^n) \) ancillary qubits [Ros21], but did not give results for smaller \( m \).

For general unitary synthesis, based on cosine-sine decomposition and Grover search, we optimize the circuit depth for general unitary synthesis.

Theorem 3 (Unitary synthesis). For any \( m \geq 0 \), any \( n \)-qubit unitary \( U \in \mathbb{C}^{2^n \times 2^n} \) can be implemented by a quantum circuit with \( m \) ancillary qubits, using single-qubit gates and CNOT gates, of depth \( O\left(n2^{n/2} + \frac{\min\{2^n, 4n\}}{m}\right) \) when \( m = \Omega\left(\frac{2^n}{n}\right) \). In particular, the depth is \( O(n2^{n/2}) \) when \( m = \Theta(n4^n) \).

This result improves the one in [Ros21] is two-fold. First, to achieve the same minimum depth of \( O(n2^{n/2}) \), we need fewer ancillary qubits: we need \( m = \Theta(n4^n) \) compared to \( m = \Theta(n4^n) \) used in [Ros21]. Second, our method works for any \( m \) as opposed to the one in [Ros21] which needs \( m = \Theta(n4^n) \) many ancillary qubits.

Organization. The rest of this paper is organized as follows. In Section 2, we introduce notation and review some previous results. In Section 3, we will present a quantum circuit for (controlled) quantum state generation using arbitrary number of ancillary qubits. Then we will show a quantum circuit for general unitary synthesis in Section 4.

2 Preliminary

Notation. Let \( [n] \) denote the set \( \{1, 2, \cdots, n\} \). All logarithms \( \log(\cdot) \) are base 2 in this paper. For any \( x = x_1 \cdots x_k \in \{0, 1\}^k, y = y_1 \cdots y_t \in \{0, 1\}^t, xy \) denotes the \( (s + t) \)-bit string \( x_1 \cdots x_k y_1 \cdots y_t \in \{0, 1\}^{s+t} \).

The \( n \)-qubit state \(|i\rangle = |0^{n-1} i_n \rangle \in (|0\rangle, |1\rangle)^{\otimes n} \) is the binary encoding of \( i \) satisfying \( i = \sum_{j=0}^{n-1} i_j \cdot 2^j \).
Quantum gates and circuits  An n-qubit gate/unitary is a $2^n \times 2^n$ unitary operation on n qubits. The identity unitary is usually denoted by $I_n$. The X gate is the single-qubit gate that flips the basis $|0\rangle$ and $|1\rangle$. Single-qubit gates are known to have the following factorization.

Lemma 4 ([NC02], Corollary 4.2). Any single-qubit gate $U$ can be written as $U = e^{i\alpha}AXBXC$ for some $\alpha \in \mathbb{R}$ and some single-qubit gate $A, B$ and $C$ satisfying $ABC = I_1$.

A CNOT gate acts on two qubits, one control qubit and one target qubit. The gate flips the basis $|0\rangle$ and $|1\rangle$ on the target qubit, conditioned on the control qubit is on $|1\rangle$. A quantum circuit on n qubits implements a unitary transform of dimension $2^n \times 2^n$. A quantum circuit may consist of different types of gates. One typical set of gates contains all 1-qubit gates and 2-qubit CNOT gates. This is sufficient to implement any unitary transform. For notational convenience, we call this type of quantum circuits the standard quantum circuits.

A subset of circuits is CNOT circuits, which are the ones consisting of 2-qubit CNOT gates only.

A Toffoli gate is a 3-qubit CCNOT gate where we flip the basis $|0\rangle, |1\rangle$ of (i.e. apply X gate to) the third qubit conditioned on the first two qubits are both on $|1\rangle$. Namely, there are two control qubits and one target qubit. This can be extended to an n-fold Toffoli gate, which applies the X gate to the $(n+1)$-th qubit conditioned on the first n qubits all being on $|1\rangle$. This n-fold Toffoli gate can be implemented by a standard quantum circuit of linear depth and size without ancillary qubits [Gid15], and of logarithmic depth and linear size if a linear number of ancillary qubits are available [BDHC19].

Lemma 5. An n-fold Toffoli gate can be implemented by a standard quantum circuit of $O(n)$ depth and size without using any ancillary qubit, and to $O(\log n)$ depth and $O(n)$ size using $O(n)$ ancillary qubits.

A non-standard quantum circuit model is QAC$_f^0$ circuit. A QAC$_f^0$ circuit is a quantum circuit with one-qubit gates, unbounded-arity Toffoli

$$|x_1, \ldots, x_k, b\rangle \rightarrow |x_1, \ldots, x_k, b \oplus \bigoplus_{i=1}^k x_i\rangle,$$
and fanout gates

$$|b, x_1, \ldots, x_k\rangle \rightarrow |b, x_1 \oplus b, \ldots, x_k \oplus b\rangle.$$  

**QRAM/CQSP, QSP, and US problems** The QRAM or Controlled Quantum State Preparation (CQSP) problem is: Given $2^k$ quantum states $|\psi_i\rangle$, realize the transformation of

$$|i\rangle|0^m\rangle \rightarrow |i\rangle|\psi_i\rangle, \forall i \in \{0, 1\}^k.$$  

We sometimes write $(k, n)$-CQSP to emphasize the parameters. The Quantum State Preparation (QSP) problem can be formulated as follows. Given a complex vector $v = (v_0, v_1, v_2, \ldots, v_{2^n-1})^T \in \mathbb{C}^{2^n}$ with $\sqrt{\sum_{k=0}^{2^n-1} |v_k|^2} = 1$, generate the corresponding $n$-qubit quantum state

$$|\psi_v\rangle = \sum_{k=0}^{2^n-1} v_k |k\rangle,$$

by a quantum circuit from the initial state $|0\rangle^\otimes m$, where $\{|k\rangle : k = 0, 1, \ldots, 2^n - 1\}$ is the computational basis of the quantum system. We sometimes call a quantum circuit for quantum state preparation a QSP circuit. The general Unitary Synthesis (US) problem is: Given an $n$-qubit unitary $U$, find a quantum circuit to implement it.

In all these problems, we hope to find circuits as simple as possible, and standard measures for quantum circuits include depth, size (i.e. the number of gates), and number of qubits. The depth of a circuit corresponds to time, and the number of qubits to space. For many information processing tasks quantum circuits include depth, size (i.e. the number of gates), and number of qubits. The depth of a circuit to implement it.

Similarly, we call an $(n + m)$-qubit quantum circuit $C$ implements an $n$-qubit unitary $U$ using $m$ ancillary qubits if

$$C \left(|0\rangle^\otimes m |0\rangle^\otimes m\right) = |\psi\rangle |0\rangle^\otimes m.$$  

Similarly, we call an $(n + m)$-qubit quantum circuit $C$ implements an $n$-qubit unitary $U$ using $m$ ancillary qubits if

$$C \left(|\psi\rangle |0\rangle^\otimes m\right) = (U |\psi\rangle) \otimes |0\rangle^\otimes m,$$  

for any $n$-qubit state $|\psi\rangle$.

**Uniformly Controlled Unitary (UCU)** Let $S = \{s_1, \ldots, s_k\}$, $T = \{t_1, \ldots, t_\ell\}$ and $S \cap T = \emptyset$. A uniformly controlled unitary $V^S_T$ consists of $2^k$ controlled unitary operations, where $S$ is the index set of the control qubits, and $T$ is the index set of target qubits. The $2^k$ multiple-controlled unitary operations are conditioned on distinct basis states of the $k$ control qubits; see Figure 2 for the circuit representation of $V^S_T$, where $U$ is a shorthand for the collection of $U_0, U_1, \ldots, U_{2^\ell-1}$. To make the sizes of $S$ and $T$ explicit, we sometimes call $V^S_T$ a $(k, \ell)$-UCU. The matrix representation of $V^S_T$ is

$$V^S_T = \begin{pmatrix}
    U_0 & U_1 & \cdots & U_{2^{\ell-1}} \\
    U_0 & U_1 & \cdots & U_{2^{\ell-1}} \\
    \vdots & \vdots & \ddots & \vdots \\
    U_0 & U_1 & \cdots & U_{2^{\ell-1}}
\end{pmatrix} \in \mathbb{C}^{2^{2^k} \times 2^{2^\ell}},$$

where $U_0, U_1, \ldots, U_{2^{\ell-1}} \in \mathbb{C}^{2^2 \times 2^\ell}$ are unitary matrices. If $S = \emptyset$, $V^S_T$ is a just an $\ell$-qubit unitary operation. If $\ell = 1$, the UCU is also called uniformly controlled gate (UCG), and we refer to $k$-UCG for $(k, 1)$-UCU.
Ref. [STY+21] gives the following size and depth upper bounds of a general UCG.

**Lemma 6.** Given m ancillary qubits, any n-qubit UCG \( V_{[n]}^{[n-1]} \) can be implemented by a standard quantum circuit of size \( O(2^n) \) and depth \( O(n + \frac{2^n}{m+n}) \).

The following framework of a QSP circuit was given in [GR02, KP17].

**Lemma 7.** The QSP problem can be solved by \( n \) UCGs of growing sizes, \( V_{[n]}^{[n-1]} \cdots V_{[2]}^{[1]} V_{[1]}^{[0]} \).

### Decomposition of n-qubit quantum gate

Based on cosine-sine decomposition, any \( n \)-qubit unitary can be decomposed into two \((1, n-1)\)-UCUs and one \((n-1)\)-UCG in [PW94],

\[
U = \begin{pmatrix} V_1' & V_1'' \\ \C & \S \\ -\S & \C \\ \end{pmatrix} \begin{pmatrix} V_2' \\ \V_2'' \\ \end{pmatrix},
\]

where \( U \in \mathbb{C}^{2^n \times 2^n}, V_1', V_1'', V_2', V_2'' \in \mathbb{C}^{2^{n-1} \times 2^{n-1}} \) are unitary matrix, \( C, S \in \mathbb{C}^{2^{n-1} \times 2^{n-1}} \) are diagonal matrices whose diagonal elements are \( \cos \theta_1, \cos \theta_2, \ldots, \cos \theta_{2^{n-1}} \) and \( \sin \theta_1, \sin \theta_2, \ldots, \sin \theta_{2^{n-1}} \), respectively. The circuit representation of cosine-sine decomposition is shown in Figure 3.

![Figure 3: Cosine-sine decomposition of an n-qubit quantum gate in the language of UCU.](image)

### 3 Asymptotically optimal circuit depth for (controlled) quantum state preparation

Now we give a more detailed implementation and analyze the correctness and cost of the quantum circuit. First, we will use the following copying circuit many times so we single it out as a lemma.

**Lemma 8** ([STY+21]). For any \( x = x_1 x_2 \ldots x_n \in \{0, 1\}^n \), a unitary transformation \( U_{\text{copy}} \) satisfying

\[
|x\rangle |0^{mn} \rangle \xrightarrow{U_{\text{copy}}} |x\rangle |x\rangle |x\rangle \cdots |x\rangle,
\]

\( m \) copies of |x\rangle

can be implemented by a CNOT circuit of depth \( O(\log m) \) and size \( O(mn) \).

The next lemma says that a special type of UCU can be implemented efficiently.
Lemma 9. For all \( i \in \{ p \} \) and \( x \in \{ 0, 1 \}^q \), suppose that \( U_i^x \) is a single-qubit gate and let \( L^x = \bigotimes_{i=1}^p U_i^x \). Then for any \( m \geq pq \), the unitary \( \sum_{x \in \{ 0, 1 \}^q} \langle x | x \rangle \otimes L^x \) can be implemented by a standard quantum circuit of depth \( O(\log p + q + \frac{pq^2}{m}) \) and size \( O(pq) \) with \( m \) ancillary qubits.

Proof. For any \( x \in \{ 0, 1 \}^q \) and \( y = y_1 \cdots y_p \in \{ 0, 1 \}^p \), unitary \( \sum_{x \in \{ 0, 1 \}^q} \langle x | y \rangle \otimes L^x \) can be realized as follows.

\[
\begin{align*}
|x\rangle \langle y| \otimes |0^m\rangle \\
= |x\rangle \left( \bigotimes_{i=1}^p \langle y_i | \langle 0^q \rangle_R \right) |0^{m-pq}\rangle \\
\xrightarrow{U_{\text{copy}}} |x\rangle \left( \bigotimes_{i=1}^p \langle y_i | x_i \rangle_R \right) |0^{m-pq}\rangle \\
\xrightarrow{\bigotimes_{x=1}^p \sum_{x \in \{ 0, 1 \}^q} U_i^x \otimes |x\rangle_R \langle y \rangle_R} |x\rangle \left( \bigotimes_{i=1}^p U_i^x | y_i \rangle \langle x_i | R \right) |0^{m-pq}\rangle \\
\xrightarrow{U_{\text{copy}}^\dagger} |x\rangle \left( \bigotimes_{i=1}^p U_i^x | y_i \rangle \langle 0^q | R \right) |0^{m-pq}\rangle \\
= |x\rangle L^x | y\rangle \otimes |0^m\rangle
\end{align*}
\]

The first \( pq \) ancillary qubits are divided into \( p \) registers, which are labelled as register \( R_1, R_2, \ldots, R_p \). Based on Lemma 8, we make \( p \) copies of \( |x\rangle \), using a quantum circuit of depth \( O(\log p) \) and size \( O(pq) \) in Eq. (2). For every \( i \in \{ p \} \), we apply a UCU \( \sum_{x \in \{ 0, 1 \}^q} U_i^x \otimes |x\rangle_R \langle y \rangle_R \) on \( |y\rangle \). All these \( q \)-UCGs act on different qubits, so they can be implemented in parallel. In Eq. (3), we implement \( p \) UCGs in parallel and each UCG is assigned to \( \frac{m-pq}{p} \) ancillary qubits. According to Lemma 6, Eq. (3) can be realized by a quantum circuit of depth \( O(q + \frac{pq^2}{m}) \) and size \( p \cdot O(2^q) = O(p^2q) \).

In Eq. (4), we restore register \( R_1, R_2, \ldots, R_p \) by the inverse circuit of Eq. (2), of depth \( O(\log p) \) and size \( O(pq) \). The total depth is \( 2 \cdot O(\log p) + O(q + \frac{pq^2}{m}) = O(\log p + q + \frac{pq^2}{m}) \) and the total size is \( 2 \cdot O(pq) + O(p^2q) = O(p^2q^2) \).

This lemma extends to general UCU: as long as each \( U_i \) in a UCU has a shallow circuit implementation, then the UCU can be easily implemented.

Lemma 10. For all \( x \in \{ 0, 1 \}^q \), suppose that \( W^x \) can be implemented by a standard \( p \)-qubit quantum circuit of depth \( d \). Then for any \( m \geq pq \), the \((q,p)\)-UCU \( \sum_{x \in \{ 0, 1 \}^q} \langle x | x \rangle \otimes W^x \) can be implemented by a standard quantum circuit of depth \( O(\log p + dq + \frac{dpq^2}{m}) \) and size \( O(dpq^2) \) with \( m \) ancillary qubits. In particular, if we have sufficiently many ancillary qubits, then the circuit depth for a \((q,p)\)-UCU is \( O(\log p + dq) \).

Proof. Similar to Lemma 9, we first make \( p \) copies of \( |x\rangle \) and un-copy these at the end. Between these two steps, we proceed layer by layer for the \( d \) layers of the \( W^x \) circuits. In each layer, we use the method of Lemma 9 to handle the single-qubit gates, and use Lemma 5 to handle the \( q \)-controlled CNOT gates i.e. \((q+1)\)-fold Toffoli gates.

The cost is analyzed as follows. The copy and un-copy steps take depth \( O(\log p) \), size \( O(pq) \), and \( pq \) ancillary qubits. In each layer, all the single-qubit gates can be handled in parallel, as we have one copy of \( |x\rangle \) for each of them. Same as Lemma 9, these single-qubit gates take depth \( O(q + \frac{pq^2}{m}) \), size \( O(p^2q^2) \), and \( m - pq \) ancillary qubits. Since the ancillary qubits are restored to \( |0\rangle \), they can be reused in the next layer. Each CNOT layer takes \( O(q) \) depth and size, without using ancillary qubits. And again these \( q \)-controlled CNOT gates i.e. \((q+1)\)-fold Toffoli gates, can be paralleled as we have one copy of \( |x\rangle \) for each of these Toffoli gates. Putting all these costs together gives the claimed bounds. \( \square \)
3.1 Rosenthal’s quantum state preparation framework

In [Ros21] Rosenthal presents a QAC\(_f\) circuit of depth \(O(n)\) with \(O(n2^n)\) ancillary qubits for \(n\)-qubit QSP. As mentioned in [Ros21], this result suffices to yield a standard quantum circuit for QSP, with depth \(O(n)\) and \(O(n2^n)\) ancillary qubits. Indeed, each \(k\)-qubit Toffoli or fanout gate can be simulated by a standard quantum circuit of depth \(O(\log k)\) with \(O(k)\) ancillary qubits (Lemma 5). However, the QAC\(_f\) circuit needs \(O(n2^n)\) ancillary qubits, which is out of our parameter regime of \(m \in \left(\frac{2^n}{n \log n}, o(2^n)\right)\).

Next we will analyze the QAC\(_f\) circuit and see how to make it suitable for any \(m = \Omega(2^n/n^2)\). Let us first review Rosenthal’s framework. In the following, we will use \(\epsilon\) to denote the empty string.

Let \(\{0, 1\}^n \ni z \mapsto \{0, 1\}^n \cup \{\varepsilon\}\) denote the set of \(\{0, 1\}\) strings of length at most \(n\), and \(\{0, 1\}^{<n} \ni z \mapsto \{0, 1\}^i \cup \{\varepsilon\}\) denote the set of \(\{0, 1\}\) strings of length at most \(n-1\). For any \(x = x_1x_2 \cdots x_n \in \{0, 1\}^n\), let \(x_{\ell_i}\) denote the \(i\)-bit string \(x_1x_2 \cdots x_i\) and \(x_{r_i}\) denote the \((i-1)\)-bit string \(x_1x_2 \cdots x_{i-1}\). Let \(R(\alpha)\) denote a single-subbit gate \(R(\alpha) = \begin{pmatrix} 1 & \epsilon^{i\alpha} \\ \epsilon^{-i\alpha} & 1 \end{pmatrix}\) for any \(\alpha \in \mathbb{R}\), which puts a phase of \(\alpha\) on \(|1\rangle\) basis.

Let \(|\psi_x\rangle = \sum_{z \in \{0, 1\}^{<n}} v_x(z) \{|x\rangle\}\) denote the target quantum state. For all \(x \in \{0, 1\}^{<n}\), let \(|x\rangle\) denote the length of \(x\). Define \((n - |x|)\)-qubit states \(|\psi_x\rangle\) \((0 \leq |x| < n)\) recursively by the equations:

\[
|\psi_x\rangle = |\psi_\varepsilon\rangle \quad \text{and} \quad |\psi_x\rangle = \begin{cases} \beta_y |0\rangle |\psi_{x_0}\rangle + \beta_{x_1} |1\rangle |\psi_{x_1}\rangle, & \text{if } |x| \leq n - 2, \\
\beta_{x_0} |0\rangle + \beta_{x_1} |1\rangle, & \text{if } |x| = n - 1,
\end{cases}
\]

For all \(x \in \{0, 1\}^{<n}\), further define a one-qubit quantum state

\[
|\phi_x\rangle = \beta_{x_0} |0\rangle + \beta_{x_1} |1\rangle. \quad (5)
\]

It can be verified that \(v_x = \prod_{i = 1}^{n} \beta_{x_{\ell_i}}\) for all \(x \in \{0, 1\}^n\).

Next let us define a leaf function \(\ell : \{0, 1\}^{\{0, 1\}^{<n}} \rightarrow \{0, 1\}^n\) in the following way: Identify the input index set \(\{0, 1\}^{<n}\) with the vertices of the complete binary tree, with each interior vertex \(x\) having the left and right children \(x_0\) and \(x_1\), respectively. The root corresponds to the empty string \(\varepsilon\). Given an input \(z\), \(\ell(z)\) is the leaf that the following walk from the root lead to: at any interior node \(x\), move to the left or right child if \(z_x = 0\) or 1, respectively. It can be verified that

\[
\ell(z)_j = \bigvee_{i = 1}^{n} \left[ x_{t_i} = t_i \right], \quad \forall j \in [n], \quad \text{where} \quad x_{t_i} = t_i = \begin{cases} 1 & \text{if } x_{r_i} = t_i, \\
0 & \text{if } x_{r_i} \neq t_i.
\end{cases}
\]

Also define a corresponding \((2^n + n - 1)\)-qubit unitary transformation \(U_\ell\) by

\[
U_\ell |z, a\rangle = |z, a \oplus \ell(z)\rangle, \quad \forall z \in \{0, 1\}^{\{0, 1\}^{<n}}, \quad \forall a \in \{0, 1\}^n.
\]

In the rest of this section, \(R_e\) is a one-qubit register for each \(x \in \{0, 1\}^n\), and \(S\) is an \(n\)-qubit register.

The QSP algorithm in [Ros21] can be summarized as follows.

**Lemma 11.** Any \(n\)-qubit quantum state \(|\psi\rangle\) can be generated by the following three steps:

1. \(|0\rangle_{R_e} \rightarrow |\phi_x\rangle_{R_e},\) for all \(x \in \{0, 1\}^{<n}\).
2. Apply \(U_\ell\) to \(\bigotimes_{x \in \{0, 1\}^{<n}} |\phi_x\rangle_{R_e} \otimes |0\rangle_S\).
3. Apply \(\Gamma^\dagger\), where \(\Gamma\) is any unitary satisfying

\[
|t\rangle_S \bigotimes_{x \in \{0, 1\}^{<n}} |0\rangle_{R_e} \xrightarrow{\Gamma^\dagger} |t\rangle_S \bigotimes_{x \in \{0, 1\}^{<n}} \begin{cases} |t\rangle_{R_e} & \text{if } x = t_{\ell_i} \text{ for some } i \in [n], \\
|\phi_x\rangle_{R_e} & \text{otherwise},
\end{cases} \quad \forall t \in \{0, 1\}^n.
\]

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For the correctness please refer to [Ros21], and here we focus on the implementation and the corresponding analysis in a way suitable for our later circuit construction.

The first step of the algorithm consists of single-qubit rotations on $2^n - 1$ qubits, and thus naturally has depth 1 and size $2^n - 1$. We denote by $L_i^\ell$ this step of operation, where the superscript emphasizes that the gate parameters depend on the target vector $v \in \mathbb{C}^{2^n}$.

As shown in [Ros21], the second step can be implemented by a QAC$_f^0$ circuit on $O(n2^n)$ qubits, which transfers to a standard circuit of depth $O(n)$ and size $O(n2^n)$, with $O(n2^n)$ ancillary qubits. We also note that this second step is independent of the target vector. We denote by $C_i^\ell$ the circuit of this step, where the absence of superscript $v$ emphasize the independence of the target state.

The third step, though also of depth $O(n)$ and size $O(n2^n)$ with $O(n2^n)$ ancillary qubits, unfortunately depends on the target vector $v$. This brings us some difficulty for small $m$, and we will show how to handle it next.

### 3.2 Implementation: separating depth and dependence

In this section we will show how to implement the third step in Rosenthal’s algorithm in such a way that (1) it has a constant number of rounds, some deep and some shallow, (2) deep rounds have depth $O(n)$, but are independent of the target vector $v$, (3) shallow rounds each have depth 1, and depend on $v$. This separation of depth and dependence is useful for our later construction of efficient circuits. The circuit and these conditions are formalized in the following lemma.

**Lemma 12.** A unitary transformation $\Gamma^\ell$ satisfying Eq.(7) can be implemented by a standard quantum circuit of the following form

$$\Gamma^\ell = C_5 L_5^\ell C_4 L_4^\ell C_3 L_3^\ell C_2 L_2^\ell C_1^\ell.$$  

Here each $L_i^\ell = \bigotimes_{k=1}^{n} U_i^{\ell}$ is a depth-1 circuit consisting of $s_i = O(2^n)$ single-qubit gates with $U_i^{\ell}$ determined by $\psi_v$. $C_1^\ell$, $C_2$, $\ldots$, $C_5$ are all independent of $\psi_v$; $C_1^\ell$, $C_4$, and $C_5$ are circuits of depth $O(n)$ and size $O(n2^n)$, and $C_2$ and $C_3$ are circuits of depth $O(1)$ and size $O(2^n)$.

**Proof.** We first introduce notation $C_{S_v}^i(V)$: For any $y = y_1 \cdots y_\ell \in \{0, 1\}^{\leq n}$, $C_{R_s}^i(V)$ is a unitary operation acting on an $n$-qubit register $S$ (the first $n$ qubits) and 1-qubit register $R_s$ (the last qubit) as follows

$$C_{R_s}^i(V) \overset{\text{def}}{=} |y\rangle \langle y| \otimes I_{n-\ell} \otimes V + \sum_{y' \in \{0, 1\}^{\leq n} \setminus \{y\}} |y'\rangle \langle y'| \otimes I_{n-\ell} \otimes I_1,$$

The unitary $C_{R_s}^i(V)$ makes the following transformation

$$|i\rangle_S |0\rangle_{R_s} \rightarrow |i\rangle_S V^{[i_{\leq \ell} = y]} |0\rangle_{R_s}, \text{ where } [i_{\leq \ell} = y] = \begin{cases} 1, & \text{if } i_{\leq \ell} = y, \\ 0, & \text{if } i_{\leq \ell} \neq y. \end{cases} \quad (8)$$

By introducing an ancillary qubit called register $A$, $C_{R_s}^i(V)$ can be implemented by the quantum circuit in Figure 4. In the quantum circuit, the single-qubit gate $A, B, C, R(\alpha)$ satisfy $V = e^{i\alpha}AXBXC$ and $ABC = I_1$ (Lemma 4).

According to Figure 4, we can rewrite the circuit of $C_{R_s}^i(V)$:

$$C_{R_s}^i(V) = W_1^y (I_n \otimes A \otimes R(\alpha)) (I_n \otimes CNOT_{R_s}^A) (I_n \otimes B \otimes I_1) (I_n \otimes CNOT_{R_s}^A) (I_n \otimes C \otimes I_1) W_1^y,$$  

Because any $n$-qubit Toffoli gate can be implemented by a quantum circuit of depth $O(n)$ based on Lemma 5, $W_1^y, W_2$ can be implemented by a quantum circuit of depth $O(n)$. Unitary $D_1^i, D_2^i$ consists of a single-qubit gates and $D_3^i$ consists of 2 single-qubit gates. The total depth of $C_{R_s}^i(V)$ is $O(n)$. It is worth mentioning that in the circuit construction of $C_{R_s}^i(V)$, only $D_1^i, D_2^i, D_3^i$ depend on unitary $V$. 

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Figure 4: Quantum circuit for $C_{R_0}^{S,N}(V)$. Register $A$ is an ancillary qubit. Single-qubit gate $A, B, C, R(\alpha)$ satisfy $V = e^{\alpha A}XBXC$ and $ABC = I_1$.

Now we start the circuit construction of $\Gamma$. For all $x \in \{0, 1\}^n$, let $U_x$ denote a single-qubit gate satisfying $U_x|0\rangle = |\phi_x\rangle$. First, we implement the following transformation $\Gamma_x^{S,R_x}$ on register $S$ and $R_x$ by the method in Figure 4:

$$|t\rangle_S |0\rangle_{R_x} \rightarrow |t\rangle_S \begin{cases} |t_x\rangle_{R_x}, & \text{if } x = t_{c_i} \text{ for some } i \in [n], \\ |\phi_x\rangle_{R_x}, & \text{otherwise}, \end{cases} \forall t \in \{0, 1\}^n.$$  

This needs depth and size $O(n)$ with 1 ancillary qubit:

$$|t\rangle_S |0\rangle_{R_x} \rightarrow |t\rangle_S |\phi_x\rangle_{R_x} |0\rangle_A \quad \text{(depth 1, size } 1)$$  

$$|t\rangle_S \rightarrow |t\rangle_S |0\rangle_{R_x} \quad \text{(depth } O(n), \text{ size } O(n))$$  

$$|t\rangle_S \rightarrow |t\rangle_S |\phi_x\rangle_{R_x} \quad \text{(depth } O(n), \text{ size } O(n))$$

Base on the construction of $\Gamma_x^{S,R_x}$, we can now implement $\Gamma$ by a quantum circuit of depth $O(n)$ and size $O(n2^n)$ with $(n + 1)(2^n - 1)$ ancillary qubits. To compress the depth, we first make a copy of $|t\rangle$ for each $x \in \{0, 1\}^n$.

$$|t\rangle_S \begin{cases} |t_x\rangle_{R_x} |0\rangle_{R_x} |0^{(n+1)(2^n-1)}\rangle, & \text{if } x = t_{c_i} \text{ for some } i \in [n], \\ |\phi_x\rangle_{R_x} |0\rangle_{R_x} |0^{(n+1)(2^n-1)}\rangle, & \text{otherwise}. \end{cases} \quad \text{(14)}$$

$$|t\rangle_S \begin{cases} |t_x\rangle_{R_x} |0\rangle_{R_x} |0^n\rangle_{S_x} |0\rangle_{A_x}, & \text{if } x = t_{c_i} \text{ for some } i \in [n], \\ |\phi_x\rangle_{R_x} |0\rangle_{R_x} |0^n\rangle_{S_x} |0\rangle_{A_x}, & \text{otherwise}. \end{cases} \quad \text{(15)}$$

$$|t\rangle_S \begin{cases} |t_x\rangle_{R_x} |0\rangle_{R_x} |0^{(n+1)(2^n-1)}\rangle, & \text{if } x = t_{c_i} \text{ for some } i \in [n], \\ |\phi_x\rangle_{R_x} |0\rangle_{R_x} |0^{(n+1)(2^n-1)}\rangle, & \text{otherwise}. \end{cases} \quad \text{(16)}$$

$$|t\rangle_S \begin{cases} |t_x\rangle_{R_x} |0\rangle_{R_x} |0^n\rangle_{S_x} |0\rangle_{A_x}, & \text{if } x = t_{c_i} \text{ for some } i \in [n], \\ |\phi_x\rangle_{R_x} |0\rangle_{R_x} |0^n\rangle_{S_x} |0\rangle_{A_x}, & \text{otherwise}. \end{cases} \quad \text{(17)}$$

$$= |t\rangle_S \begin{cases} |t_x\rangle_{R_x}, & \text{if } x = t_{c_i} \text{ for some } i \in [n], \\ |\phi_x\rangle_{R_x}, & \text{otherwise}. \end{cases} \quad \text{(18)}$$

$$= |t\rangle_S \begin{cases} |t_x\rangle_{R_x}, & \text{if } x = t_{c_i} \text{ for some } i \in [n], \\ |\phi_x\rangle_{R_x}, & \text{otherwise}. \end{cases} \quad \text{(19)}$$
Here all the three transformation steps have depth \(O(n)\) and size \(O(n2^n)\), by Lemma 8 and the analysis in Eq.(11)-(13).

For every \(U_\kappa^\dagger\), \(C_{R_\kappa}^{S_{x,v}}(U_\kappa^\dagger)\) can be represented as

\[
C_{R_\kappa}^{S_{x,v}}(U_\kappa^\dagger) = W_1^\dagger D_2^3 U_\kappa^\dagger CNOT_{R_\kappa}^{A_x} D_2^2 U_\kappa^\dagger CNOT_{R_\kappa}^{A_x} D_2^3 U_\kappa^\dagger W_1^\dagger, \tag{18}
\]

as discussed in Eq. (9). \(D_1^1, D_2^2, D_3^1\) are single-qubit gates respectively and \(D_3^1\) consists of 2 single-qubit gates which are determined by \(U_\kappa^\dagger\) (or by target quantum state \(|\psi_v\rangle\)). \(W_1^\dagger, W_2^\dagger\) are quantum circuits of depth \(O(n)\), independent of \(|\psi_v\rangle\). As discussed above, \(\Gamma\) is represented as

\[
\begin{align*}
\Gamma &= U_{\text{copy}}^\dagger \left( \bigotimes_{x \in \{0,1\}^n} \Gamma_{x,R_\kappa}^{S_{x,v}} \right) U_{\text{copy}}, \\
&= U_{\text{copy}}^\dagger \left( \bigotimes_{x \in \{0,1\}^n} \left( C_{R_\kappa}^{S_{x,v}}(X) W_2^\dagger D_2^3 U_\kappa^\dagger CNOT_{R_\kappa}^{A_x} D_2^2 U_\kappa^\dagger CNOT_{R_\kappa}^{A_x} D_2^3 U_\kappa^\dagger W_1^\dagger U_{\text{copy}} \right) \right) U_{\text{copy}}, \\
&= U_{\text{copy}}^\dagger \left( \bigotimes_{x \in \{0,1\}^n} \left( C_{R_\kappa}^{S_{x,v}}(X) W_2^\dagger \bigotimes_{x \in \{0,1\}^n} D_2^3 U_\kappa^\dagger \bigotimes_{x \in \{0,1\}^n} CNOT_{R_\kappa}^{A_x} \right) \right) U_{\text{copy}}, \\
&= U_{\text{copy}}^\dagger \left( \bigotimes_{x \in \{0,1\}^n} \left( C_{R_\kappa}^{S_{x,v}}(X) W_2^\dagger \bigotimes_{x \in \{0,1\}^n} D_2^3 U_\kappa^\dagger \bigotimes_{x \in \{0,1\}^n} CNOT_{R_\kappa}^{A_x} \right) \right) U_{\text{copy}}.
\end{align*}
\]

The conclusion for the decomposition of \(\Gamma^\dagger\) then follows. For the cost analysis: According to Eq. (18), \((L_x^1)^\dagger, (L_x^2)^\dagger, (L_x^3)^\dagger, (L_x^4)^\dagger\) are depth-1 circuits consisting of \(O(2^n)\) single-qubit gates, which are determined by target state \(|\psi_v\rangle\). According to Lemma 8, Figure 4, Eq. (13) and (18), \((C_1^\dagger)^\dagger, (C_4^\dagger)^\dagger\) and \((C_5^\dagger)^\dagger\) are quantum circuits of depth \(O(n)\) and size \(O(n2^n)\). Based on Eq. (18), \((C_2)^\dagger, (C_3)^\dagger\) are quantum circuits of depth \(O(1)\) and size \(O(2^n)\). This completes the proof.

Now letting \(C_1 = C_1^\dagger C_1^\dagger\), we get the following result.

**Lemma 13.** Any \(n\)-qubit quantum state \(|\psi_v\rangle\) can be generated by a quantum circuit \(\text{QSP}_{\psi_v}\) using single-qubit gates and CNOT gates, of depth \(O(n)\) and size \(O(n2^n)\), with \(O(n2^n)\) ancillary qubits. The QSP circuit can be written as

\[
\text{QSP}_{\psi_v} = C_5 L_4^v C_4 L_3^v C_3 L_2^v C_2 L_1^v C_1 L_1^v.
\]

Each \(L_i^v = \bigotimes_{k=1}^{s_i} U_{ik}^v\) is a depth-1 circuit consisting of \(s_i = O(2^n)\) single-qubit gates, and \(L_i^v\) is determined by \(|\psi_v\rangle\). \(C_1, C_4\) and \(C_5\) are circuits of depth \(O(n)\) and size \(O(n2^n)\), and \(C_2\) and \(C_3\) are circuits of depth \(O(1)\) and size \(O(2^n)\). For any \(i \in \{5\}, C_i\) is independent of \(|\psi_v\rangle\).

### 3.3 Quantum circuit for (controlled) quantum state preparation

Next, we will use Lemma 13 to efficiently realize the controlled quantum state preparation. Let us fix a constant \(c\) in the size upper bound of \(L_i^v\) in Lemma 13, i.e. \(s_i \leq c \cdot 2^n\). The next lemma is a restatement of the upper bound part of Theorem 1.

**Lemma 14.** For any \(k \geq 0\) and quantum states \(|\psi_i\rangle : i \in \{0,1\}^k\), the following controlled quantum state preparation

\[
|i\rangle |0^n\rangle \rightarrow |i\rangle |\psi_i\rangle, \hspace{0.5cm} \forall i \in \{0,1\}^k,
\]

can be implemented by a standard quantum circuit of depth \(O \left( n + k + \frac{2^{n-k}}{n+k+m} \right)\) and size \(O \left( 2^{n+k} \right)\) with \(m\) ancillary qubits.
Therefore, the controlled quantum state preparation can be implemented as

\[
\sum_{i \in \{0,1\}^k} |i\rangle \otimes QS_P_i
\]

For all \(i \in [n]\), UGC \(V^{[k+i-1]}_{n+i}\) can be implemented by a quantum circuit of depth \(O \left( k + i + \frac{2k+i}{m+i} \right) \) and size \(O(2^{k+i})\) by Lemma 6, using \(m\) ancillary qubits. Therefore, the depth and size of this CQSP circuit are \(\sum_{i=1}^{n} O \left( k + i + \frac{2k+i}{m+i} \right) = O \left( \frac{m+k}{m+n+k} \right) \) and \(\sum_{i=1}^{n} O \left( 2^{k+i} \right) = O \left( 2^{n+k} \right)\), respectively.

**Case 2:** \(m = \Omega(2n^{\kappa}(n+k)^2)\). We will show the quantum circuit for CQSP in two sub-cases: \(t \geq \lceil 4 \log(n+k) \rceil \) and \(t < \lceil 4 \log(n+k) \rceil \).

**Case 2.1:** \(t \geq \lceil 4 \log(n+k) \rceil \). Then \(m \geq \max(2cn2^n, k2^n)\) for any constant \(c > 0\). For all \(i \in [0,1]^k\), suppose \(|\psi_i\rangle = \sum_{j=0}^{2n-1} v_j^n |j\rangle\). Let \(QS_P_i\) denote a QSP circuit with \(m_1 = cn2^n\) ancillary qubits as guaranteed by Lemma 13 to prepare \(|\psi_i\rangle\), which can be represented as

\[
QS_P_i = C_5L_3C_4L_4C_3L_3C_2L_2C_1L_1.
\]

Here each \(L_r = \bigotimes_{j=0}^{s_r} U_{r,j}^{i_r}\) is a depth-1 circuit consisting of \(s_r = O(2^n)\) single-qubit gates, and \(L_r\) is determined by \(|\psi_i\rangle\). For \(r \in [5]\), \(C_r\) is an \((n+m_1)\)-qubit circuit of depth \(O(n)\), which is independent of \(|\psi_i\rangle\). Note that the task in the statement of this lemma is nothing but the UCU of \(\{QS_P_i\}\), which can be implemented by applying \(\sum_{i \in [0,1]^k} |i\rangle \langle i| \otimes QS_P_i\). This operator can be decomposed as follows.

\[
\sum_{i \in [0,1]^k} |i\rangle \langle i| \otimes QS_P_i = \sum_{i \in [0,1]^k} |i\rangle \langle i| \otimes (C_5L_3C_4L_4C_3L_3C_2L_2C_1L_1) = \prod_{r=5}^{1} \left( L_r \otimes C_r \right) \left( \sum_{i \in [0,1]^k} |i\rangle \otimes L_r^i \right).
\]

where the notation \(\prod_{r=5}^{1} A_r\) means to multiply the matrices \(A_r\)'s in the order of \(A_5A_4A_3A_2A_1\). The second equation above holds because, when viewed as matrices, the equation is just a block diagonal matrix multiplication:

\[
\text{diag}(C_5L_3C_4L_4C_3L_3C_2L_2C_1L_1, \ldots, C_5L_3^{2^n-1}C_4L_4^{2^n-1}C_3L_3^{2^n-1}C_2L_2^{2^n-1}C_1L_1^{2^n-1}) = \text{diag}(C_5, \ldots, C_5) \times \text{diag}(L_3^0, \ldots, L_3^{2^n-1}) \times \text{diag}(L_4^0, \ldots, L_4^{2^n-1}) \times \cdots \times \text{diag}(L_1^0, \ldots, L_1^{2^n-1}).
\]
and size $O\left(2^n \times 2^k\right) = O(2^{n+k})$, with the $m - m_1$ ancillary qubits. For $r \in [5]$, every $C_r$ is a quantum circuit of depth $O(n)$ and size $O(n^2)$. Putting everything together and noting $m \geq 2cn^2$, we can implement $\sum_{i=0}^{1} \left|i\right\rangle\left\langle i\right| \otimes QS P$, by a quantum circuit of depth $O\left(n + k + \frac{2n^3k}{m - 2cn^2}\right)$ and size $O\left(2^{n+k} + n^2\right) = O(2^{n+k})$, with $m$ ancillary qubits.

**Case 2.2** $t < [4\log(n + k)]$. Define $n$-qubit quantum state $|\psi_i\rangle = \sum_{i=0}^{2n-1} v_{r,i} |\tau\rangle$ and $|\psi_i^{(s)}\rangle = \sum_{i=0}^{2n-1} v_{s,i}^{(s)} |\eta\rangle$, where $s \leq n$ and $v_{s,i}^{(s)} = \sqrt{\sum_{p=0}^{2n-1} |v_{s+p,i}^{(s)}|^2}$. Our construction consists of two steps. In the first step, we implement a $[4\log(n + k)]$-qubit CQSP, using $m = \Omega(2^{n+k}/(n + k)^2)$ ancillary qubits:

$$\text{CQSP1} : \ |i\rangle |0^n\rangle \rightarrow |i\rangle |\psi_i^{(s)}\rangle |0^{n+k} - [4\log(n + k)\rangle, \forall i \in \{0, 1\}^k,$$

where $s = [4\log(n + k)] - k$. Note that $k$ and $s$ satisfy $m > \max\{2cs^2, k^2\}$, thus similar to the Case 2.1 above, we can implement Eq. (22) by a circuit of depth $O(\log(n + k))$ and size $O((n + k)^4)$. In the second step, we implement an $(n + k)$-qubit CQSP using $m$ ancillary qubits:

$$\text{CQSP2} : \ |i\rangle |\eta\rangle |0^{n+k} - [4\log(n + k)\rangle \rightarrow |i\rangle |\eta\rangle |\psi_i,0^n\rangle, \forall i \in \{0, 1\}^k, \eta \in \{0\} \cup [2^t - 1],$$

where $|\psi_{i,\eta}\rangle \equiv \sum_{p=0}^{2n+k} v_{p,2n+k-\left[4\log(n + k)\right]} |\eta,0^n\rangle |\phi_p\rangle$. Eq. (23) is a CQSP, in which the number of controlled qubits $[4\log(n + k)]$ satisfying $m > \max\{2c(n + k - [4\log(n + k)])2(n + k - [4\log(n + k)])2(n + k - [4\log(n + k)]\}$. Therefore Eq. (23) can be implemented in the same way as in Case 2.1, such that the depth and size are $O\left(n + k + \frac{2n^3k}{m + n^2}\right)$ and $O(2^{n+k})$, respectively. It can be verified that the CQSP operator can be implemented by CQSP2. CQSP1 and the depth and size are $O\left(n + k + \frac{2n^3k}{m + n^2}\right)$ and $O(2^{n+k})$, respectively.

The paper [STY+21] presents an $n$-qubit QSP circuit with $m$ ancillary qubits. For $m \in \left[0, O\left(\frac{2^n}{n \log n}\right)\right] \cup \left[\Omega(2^n), +\infty\right)$, the circuit depth for $n$-qubit quantum state preparation is optimal. However, if $m \in \left[\omega\left(\frac{2^n}{n \log n}\right), O(2^n)\right]$, there still exists logarithmic gap between the upper and lower bounds of QSP circuit depth. Since our Theorem 1 gives a unified construction that works for any $k$, including $k = 0$, we obtain Theorem 2, which closes the gap left open in [STY+21].

**Remarks**

1. In [PB11], it was shown that any $n$-qubit quantum states are determined by $2^n - 1$ free parameters omitting a global phase. In Theorem 1, an $(n + k)$-qubit CQSP is defined by $2^k \cdot 2^n - 1 = 2^{n+k} - 1$ free parameters. Thus by a similar argument for depth lower bound of the quantum state preparation in [PB11], we can get a depth lower bound for $(k, n)$-qubit CQSP is $\Omega\left(\frac{2n^3k}{m + n^2}\right)$ using $m$ ancillary qubits. Moreover, the same as the proof of Lemma 37 in [STY+21], we can also obtain a depth lower bound $\Omega(n + k)$ by the light cone argument. Combining the two results above, the depth lower bound for CQSP is $\Omega\left(n + k + \frac{2n^3k}{m + n^2}\right)$. Therefore, the circuit depth in Theorem 1 is optimal.

2. If the number of controlled qubits $k$ in Theorem 1 is 0, the CQSP degenerates to standard QSP. Therefore, we can obtain an optimal QSP circuit as in Theorem 2.

4 Circuit depth optimization of general unitary synthesis

The following oracle is used in the circuit constructions in [Ros21].
Definition 15 (Oracle $O_U$ of $U$). Let $U = [u_{x,y}]_{x,y \in \{0,1\}^n} \in \mathbb{C}^{2^n \times 2^n}$ denote a general $n$-qubit unitary operator. Let vector $u_x \in \mathbb{C}^{2^n}$ denote the $x$-th column of $U$ and $|u_x\rangle = \sum_{y \in \{0,1\}^n} u_{x,y} |y\rangle$ is the corresponding $n$-qubit quantum state. The following unitary transformation $O_U$ is defined as the $U$-oracle:

$$|x\rangle |0^n\rangle \xrightarrow{O_U} |x\rangle |u_x\rangle,$$

for all $x \in \{0,1\}^n$.

We can directly apply Theorem 1 to obtain the following circuit construction for the oracle.

Lemma 16. For any $m \geq 0$ and $U \in \mathbb{C}^{2^n \times 2^n}$, the $U$-oracle $O_U$ and its inverse $O_U^\dagger$ can each be implemented by a standard quantum circuit of depth $O(n + \frac{4^n}{m})$ and size $O(4^n)$ with $m$ ancillary qubits.

Remarks
1. Ref. [Ros21] gives a construction for $O_U$ and $O_U^\dagger$ with $O(n)$ depth using $m = \Theta(n4^n)$ ancillary qubits. In comparison, our Theorem 1 only needs $m = \Theta(4^n/n)$ ancillary qubits to achieve $O(n)$ depth, and works for any $m \geq 0$.

2. Since Theorem 1 is tight for all values of parameters $(n,k,m)$, the bounds in Lemma 16 are also optimal.

Lemma 17 ([Ros21]). For any $m \in [n, O(4^n/n)]$, any $n$-qubit unitary $U$ can be implemented by $\ell = O\left(\frac{n}{m} \right)$ many queries to oracle $O_U$ or $O_U^\dagger$:

$$(U |\phi\rangle) |0\rangle^0m = C_\ell O_U^{\dagger} C_{\ell-1} O_U^{\dagger} C_{\ell-2} O_U^{\dagger} \cdots C_2 O_U^{\dagger} C_1 (|\phi\rangle |0\rangle^0m),$$

for all $n$-qubit states $|\phi\rangle$,

where $O_U^{\dagger}$ denotes either the oracle $O_U$ or $O_U^\dagger$, and each $C_i$ is a standard quantum circuit of depth $O(m)$ and size $(m)$, and is independent of $U$.

By Lemma 16 and 17, for any $m \in [n, O(4^n/n)]$, any $n$-qubit unitary can be implemented by a standard quantum circuit of depth $O(2n^2) \cdot \left(\log m + n + \frac{4^n}{(m-n)n}\right) = O\left(n2^{-n/2} + \frac{2n^2}{m}\right)$ If the number of ancillary qubits is large, this bound improves the previous depth bound of $O(n2^n)$ in [STY+21]. Next we will show how to further improve the circuit depth for the parameter regime $\Omega(2^n) \leq m \leq O(4^n)$ by cosine-sine decomposition and the following UCU.

Lemma 18. Let $T = [n-k+1, n-k+2, \ldots, n]$ and $S = [n] - T$ for any $k \in \{2, 3, \ldots, n\}$. For any $m \in [n, O(4^n/n)]$, any $(n-k,k)$-UCU $V^S_T$ can be implemented by a quantum circuit of depth $O\left(n2^{k/2} + \frac{n^2 + k}{m}\right)$ and size $O\left(m2^{k/2} + 2n^3k/2\right)$, with $m$ ancillary qubits.

Proof. We will implement $V^S_T$ by an $(n+m)$-qubit quantum circuit. We label the first $n$ qubits as $1, \ldots, n$, and the ancillary qubits as $n+1, n+2, \ldots, n+m$. According to Lemma 17, any $k$-qubit unitary $U_s \in \mathbb{C}^{2^s \times 2^s}$ acting on qubits $[n-k+1, n-k+2, \ldots, n]$ can be implemented by $O(2^{k/2})$ queries to the $2k$-qubit oracles $O_U$ and $O^\dagger_U$. Using the notation $O^{\dagger}_U$ to denote oracle $O_U$ or $O^\dagger_U$, we have that for all $x \in \{0,1\}^{n-k}$,

$$(U_s |\phi\rangle) |0\rangle^0m = C_\ell O^{\dagger}_U C_{\ell-1} O^{\dagger}_U \cdots C_2 O^{\dagger}_U C_1 (|\phi\rangle |0\rangle^0m),$$

for all $n$-qubit states $|\phi\rangle$.

where $\ell = O(2^{k/2})$ and $C_1, \ldots, C_\ell$ are depth-$O(\log m)$ and size-$O(m)$ quantum circuits independent of $U_s$. Any $n$-qubit uniformly controlled gate $V^S_T$ can thus be implemented as follows:

$$V^S_T = \sum_{x \in \{0,1\}^{n-k}} |x\rangle |x\rangle \otimes U_s,$$

$$= \sum_{x \in \{0,1\}^{n-k}} |x\rangle \langle x| \otimes \left[C_\ell O^{\dagger}_U C_{\ell-1} O^{\dagger}_U \cdots C_2 O^{\dagger}_U C_1\right],$$

$$= [I_{n-k} \otimes C_1] \left[\sum_{x \in \{0,1\}^{n-k}} |x\rangle \langle x| \otimes O^{\dagger}_U\right] \cdots [I_{n-k} \otimes C_2] \left[\sum_{x \in \{0,1\}^{n-k}} |x\rangle \langle x| \otimes O^{\dagger}_U\right] [I_{n-k} \otimes C_1].$$
where we switched the summation and multiplication again because of the block diagonal matrix as for Eq. (20). Now we implement $\sum_{x \in \{0,1\}^{n-1}} |x\rangle \langle x| \otimes O_{U_x}^{(i)}$.

For every $U_x$, let $|U_{x,y}\rangle$ denote the $y$-th column of $U_x$ and $QS P_{x,y}$ denote a $k$-qubit QSP circuit which satisfies $QS P_{x,y}|0^k\rangle = |U_{x,y}\rangle$ for all $y \in \{0,1\}^k$. According to Lemma 7, any $QS P_{x,y}$ can be decomposed into $k$ UCGs, i.e.,

$$QS P_{x,y} = \prod_{i=k}^{1} V_{T_i}^{S_i}(x, y),$$

where $S_i = \{n+1, \cdots, n+i-1\}$ and $T_i = \{n+i\}$ for all $i \in [k]$ ($S_1 = \emptyset$). The oracle $O_{U_x}$ can be represented as

$$O_{U_x} = \sum_{y \in \{0,1\}^{i}} |y\rangle \langle y| \otimes QS P_{x,y},$$

$$= \sum_{y \in \{0,1\}^{i}} |y\rangle \langle y| \otimes \prod_{i=k}^{1} V_{T_i}^{S_i}(x, y),$$

$$= \prod_{i=k}^{1} \sum_{y \in \{0,1\}^{i}} |y\rangle \langle y| \otimes V_{T_i}^{S_i}(x, y). \quad (25)$$

In Eq. (25), $\sum_{y \in \{0,1\}^{i}} |y\rangle \langle y| \otimes V_{T_i}^{S_i}(x, y)$ is a $(k+i-1)$-UCG. We denote it as $V_{T_i}^{S_i}(x)$, where $L_i = \{n-k+1, n-k+2, \ldots, n+i-1\}$ for all $i \in [k]$. Therefore, $O_{U_x}$ can be decomposed into

$$O_{U_x} = \prod_{i=k}^{1} V_{T_i}^{S_i}(x). \quad (26)$$

If $m \leq 2(n+ck)2^k$, based on Eq. (26) we have

$$\sum_{x \in \{0,1\}^{n-1}} |x\rangle \langle x| \otimes O_{U_x} = \sum_{x \in \{0,1\}^{n-1}} |x\rangle \langle x| \otimes \prod_{i=k}^{1} V_{T_i}^{S_i}(x) = \prod_{i=k}^{1} \sum_{x \in \{0,1\}^{n-1}} |x\rangle \langle x| \otimes V_{T_i}^{S_i}(x). \quad (27)$$

For every $i \in [k]$, $\sum_{x \in \{0,1\}^{n-1}} |x\rangle \langle x| \otimes V_{T_i}^{S_i}(x)$ is an $(n+i-1)$-UCG, whose control qubit set is $[n+i-1]$ and target qubit set is $[n+i]$. We use $V_{T_i}^{[n+i]}$ to denote it. According to Lemma 6, $V_{T_i}^{[n+i]}$ can be implemented by a quantum circuit of depth $O(n+i + \frac{2ni}{m})$ and size $O(2^{n+i})$. Therefore, the total depth Eq. (27) is $\sum_{i=1}^{k} O(n+i + \frac{2ni}{m}) = O(2^{n+k}/m)$ and the total size is $\sum_{i=1}^{k} O(2^{n+i}) = O(2^{n+k})$.

If $m > 2(n+ck)2^k$, $\sum_{x \in \{0,1\}^{n-1}} |x\rangle \langle x| \otimes O_{U_x}$ can be regarded as a controlled quantum state preparation, which has $n$ controlled qubits and $k$ target qubits. Hence, by Lemma 14, we can implement it by a circuit of depth $O(n+k + \frac{2n^k}{m+2n^k}) = O\left(n+k + \frac{2n^k}{m}\right)$ and size $O\left(2^{n+k}\right)$.

Combining the above two cases, we can implement $\sum_{x \in \{0,1\}^{n-1}} |x\rangle \langle x| \otimes O_{U_x}$ by a circuit of depth $O\left(n+k + \frac{2n^k}{m}\right)$ and size $O\left(2^{n+k}\right)$. Therefore by Eq. (24), for any $m \in \{n, O(4^n/n)\}$, unitary $V_T^S$ can be realized by a circuit of depth $O\left(2^{k/2}\right) \otimes O\left(n+\frac{2n^k}{m}\right) + O\left(2^{k/2}\right) \otimes O\left(\log m\right) = O\left(n^{2^{k/2}} + \frac{2^{n+k}}{m}\right)$ and size $2^{k/2} \otimes O\left(2^{n+k}\right) + 2^{k/2} \otimes O\left(m\right) = O\left(m2^{k/2} + 2^{n+3k/2}\right).$ \hfill \Box

Remarks.

1. Extension of UCG. In [STY+21], it was shown that any $n$-UCG can be implemented by a standard circuit of depth $O(n + 2^n/(m+n))$. Lemma 18 generalizes this result to any $k$. 

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2. Tightness. In [SMB04], it was shown that any \( n \)-qubit unitary are determined by \( 4^n - 1 \) free parameters omitting a global phase. Because UCU \( V^n_S \) in Lemma 18 are defined by \( 2^{n-k} \) different \( k \)-qubit unitaries, it is determined by \( 2^{n-k} \cdot 4^k - 1 = 2^{n+k} - 1 \) free parameters. Similar to the depth lower bound for general unitary in [STY+21], given \( m \) ancillary qubits, the depth lower bound for UCU \( V^n_T \) is \( \Omega \left( \frac{2n^k}{n+m} \right) \). Moreover, we can also obtain a depth lower bound \( \Omega(n) \) by light cone. This proof of is the same as the proof of depth lower bound for quantum state preparation in [STY+21]. Combining the two results above, giving \( m \geq 0 \) ancillary qubits, the depth lower bound for UCU \( V^n_T \) is \( \Omega \left( n + \frac{2n^k}{n+m} \right) \). When \( k = O(1) \), the depth in Lemma 18 is asymptotically optimal. The case for general \( k \) is left as an interesting open question.

**Theorem 19 (Restatement of Theorem 3).** For any \( m \geq 0 \), any \( n \)-qubit unitary \( U \) can be implemented by a quantum circuit with \( m \) ancillary qubits, using single-qubit gates and CNOT gates, of depth

\[
\begin{aligned}
&O\left( n^{2^n} + \frac{2^n}{m} \right), & \text{if } m = O\left( \frac{2^n}{n^2} \right), \\
&O\left( n^{2^n/2} + \frac{n^{2^n/2}}{m^{1/2}} \right), & \text{if } m = \Omega\left( \frac{2^n}{n^2} \right),
\end{aligned}
\]

and size

\[
\begin{aligned}
&O\left( 4^n \right), & \text{if } m = O\left( \frac{2^n}{n} \right), \\
&O\left( n^{1/2} \frac{2n^n/2}{m^{1/2}} \right), & \text{if } m \in \Omega\left( \frac{2^n}{n} \right), O\left( 4^n / n \right), \\
&O\left( 2^{2n^n/2} \right), & \text{if } m = \Omega\left( 4^n / n \right).
\end{aligned}
\]

**Proof.** Assume that \( m = O(4^n/n) \), otherwise we only use this many ancillary qubits. Let \( D_n(k, m) \) and \( S_n(k, m) \) denote the circuit depth and size, respectively, of a general \( n \)-qubit UCU \( V^{[n-k]}_{m-[n-k+1,...,n]} \) with \( m \) ancillary qubits. Especially, \( D_n(n, m) \) and \( S_n(n, m) \) denote the depth and size of an \( n \)-qubit unitary \( U \). According to cosine-sine decomposition in Figure 3, for every \( k \in [n] \) we have

\[
D_n(n, m) = 2D_n(n - 1, m) + D_n(1, m) \quad \text{(Eq.1)}
\]

\[
= 2D_n(n - 1, m) + O\left( n + \frac{2^n}{m} \right) \quad \text{(Lemma 6)}
\]

\[
= 2^{n-k} D_n(k, m) + O\left( n2^{n-k} + \frac{2^{n-k}}{m} \right) \quad \text{(by recursion)}
\]

and

\[
S_n(n, m) = 2S_n(n - 1, m) + S_n(1, m) \quad \text{(Eq.1)}
\]

\[
= 2S_n(n - 1, m) + O\left( 2^n \right) \quad \text{(Lemma 6)}
\]

\[
= 2^{n-k} S_n(k, m) + \left( 2^{n-k} - 1 \right) \times O\left( 2^n \right) \quad \text{(by recursion)}
\]

\[
= 2^{n-k} S_n(k, m) + O\left( 2^{2n-k} \right).
\]

Now we use Lemma 18, \( D_n(k, m) = O\left( n^{2^{n/2}} + \frac{2^{n+\frac{1}{2}}}{m} \right) \) and \( S_n(k, m) = O\left( m2^{k/2} + 2^{n+3k/2} \right) \). Hence, for any \( k \in [n] \) we have

\[
D_n(n, m) = 2^{n-k} \times O\left( n^{2^{n/2}} + \frac{2^{n+\frac{1}{2}}}{m} \right) + O\left( n^{2^{n-k}} + \frac{2^{2n-k}}{m} \right) = O\left( n^{2^{n-k}} + \frac{2^{2n-k}}{m} \right),
\]

\[
S_n(n, m) = 2^{n-k} \times O\left( m2^{k/2} + 2^{n+3k/2} \right) + O\left( 2^{2n-k} \right) = O\left( m2^{n+k/2} + 2^{2n+k/2} \right).
\]
From this, we can obtain

\[
D_n(m) = O\left(\min_{k \in [n]} \left\{ n^{2n+\frac{k}{2}} + \frac{2^{2n+\frac{k}{2}}}{m} \right\} \right) = \begin{cases} 
O\left(n^{2n} + \frac{n^2}{2}\right), & \text{if } m = O\left(\frac{n^2}{2}\right), \\
O\left(n^{2n/2} + \frac{n^{3/2}m^{1/2}}{m^{1/2}}\right), & \text{if } m = \Omega\left(\frac{n^2}{2}\right).
\end{cases}
\]

The corresponding size is

\[
S_n(m) = \begin{cases} 
O\left(4^n\right), & \text{if } m = O\left(\frac{n^2}{2}\right), \\
O\left(n^{1/2}2^{3n/2}m^{1/2}\right), & \text{if } m = \Omega\left(\frac{n^2}{2}\right).
\end{cases}
\]

These give the desired depth upper bound \(O(n^{2n/2})\) and size upper bound \(O(2^{5n/2})\). □

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