On-Chip Voltage-Controlled Oscillator Based on a Center-Tapped Switched Inductor Using GaN-on-SiC HEMT Technology

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Abstract: This study presents a voltage-controlled oscillator (VCO) in a cross-coupled pair configuration using a multi-tapped switched inductor with two switch-loaded transformers in 0.5 µm GaN technology. Two switch-loaded transformers are placed at the inner and outer portions of the multi-tapped inductor. All the switches are turned off to obtain the lowest sub-band. The outer transformer with three pairs of switches is turned on alternately to provide three sub-band modes. A pair of switches at the inner transformer provide a high-frequency band. Two switch-loaded transformers are turned on to provide the highest sub-band. Six modes are selected to provide a wide tuning range. The frequency tuning range (FTR) of the VCO is 27.8% from 3.81 GHz to 8.04 GHz with a varactor voltage from 13 V to 22 V. At a 1 MHz frequency offset from the carrier frequency of 4.27 GHz, the peak phase noise is −119.17 dBc/Hz. At a power supply of 12 V, the output power of the carrier at 4.27 GHz is 20.9 dBm. The figure of merit is −186.93 dB because the VCO exhibits a high output power, low phase noise, and wide FTR. To the best of the author’s knowledge, the FTR in VCOs made of GaN-based high electron mobility transistors is the widest reported thus far.

Keywords: voltage-controlled oscillator; GaN-on-SiC; frequency tuning range

1. Introduction

A voltage-controlled oscillator (VCO) is the most ubiquitous element in fully integrated transceivers to generate a tunable local carrier as a local oscillator for up and down frequency conversion in phased array or radar systems [1,2]. To effectively drive multiple mixers in a phased array or provide sufficient detection range for transmitters, AlGaN/GaN high electron mobility transistors (HEMT) are promising candidates for high power at radio-frequency applications. VCOs are the basic element of fifth-generation wireless communication, which can be used in base stations and military applications. The inherent material properties of wide bandgap GaN enable high-power handling capability and better microwave noise performance for low-noise amplifiers and VCOs [3,4]. Furthermore, GaN HEMT on a high-thermal-conductivity SiC substrate provides better reliability for high-power operation. GaN-based monolithic microwave integrated circuits (MMICs) are indispensable in the development of next-generation wireless communication systems.

Research on the design of a high-power MMIC oscillator in GaN HEMT technology has been reported. Fixed-frequency oscillators have been reported to provide a low phase noise (PN) instead of frequency tuning range (FTR) and high efficiency [5,6]. With the demand for the increased capacity of wireless communication systems, VCOs are required to have a wide bandwidth or multiple frequency bands. The most commonly used LC tank in VCOs for frequency tuning is the varactor, which has a low PN and low power consumption. An 8.5–9.5 GHz VCO was designed using 0.15 µm GaN-on-SiC HEMT technology with 31.8 dBm $P_{out}$, 11.1% FTR and −101 dBc/Hz PN at the offset of 1 MHz [7]. A common gate-frequency...
balanced Colpitts VCO operated at 6.45–7.55 GHz was fabricated in 0.25 µm GaN HEMT on silicon carbon substrate with 15% FTR, 2 dBm $P_{out}$, and $-132$ dBc/Hz PN at a 1 MHz offset [8]. A common-source single-end VCO with a feedback network was fabricated using 0.25 µm GaN-on-SiC HEMT technology with 400 MHz FTR, 1.06 dBm $P_{out}$, and $-122.5$ dBc/Hz PN at the offset of 1 MHz [9]. A power VCO based on a T-type lumped-element feedback delay line topology and Class E power amplifier with a frequency from 9.348 GHz to 9.46 GHz was prototyped with 0.25 µm GaN HEMT on SiC, which has 27.9 dBm $P_{out}$ and $-121.62$ dBc/Hz PN at the offset of 1 MHz [10]. Although the VCO provides high power and low PN, its FTR is limited by the varactor because the doping profile of GaN HEMT is determined by the epitaxy of transistors. Moreover, the quality factor (Q-factor) of the varactor degrades rapidly with frequency, resulting in a narrow bandwidth. Regarding the wide frequency band, multiband selection is a good approach to extend the bandwidth. Switched capacitor banks or switched inductors are commonly used to obtain a wide tuning range for VCOs [11,12]. However, switched capacitor or switched inductor arrays for GaN technology are hardly applicable because of the large parasitic capacitance and on-resistance of their switches and the large space that they occupy. Wideband multimode magnetically coupled multicore VCOs, which combine the tuning range with multiple modes, have been proposed. However, coupled multicore results in lossy coupled resonators and additional power consumption [13,14].

Tuning frequency via magnetic field has been proposed to increase the tuning range by band selection for single-core VCOs [15,16]. Magnetic tuning is performed by changing the inductance of the primary coil based on the tunable elements in the secondary coil to reduce the significant parasitic effect of the tunable elements, enabling DC noise isolation for the tuning element. In this work, we present a VCO in a cross-coupled pair configuration involving a multi-tapped switched inductor with two switch-loaded transformers. Two switch-loaded transformers providing overlapped tuning ranges are located at the inner and outer portions of the multi-tapped inductor. The tuning range of the fabricated switched inductor VCO is 3.81–5.04 GHz (27.8%). The lowest PN is $-119.17$ dBc/Hz at a 1 MHz offset at 4.27 GHz, and the highest $P_{out}$ of 20.9 dBm is obtained at 4.8 GHz using a power supply of 12 V.

2. Circuit Design

2.1. Switched Inductor

Figure 1 shows the configuration of a conventional loaded transformer-based variable inductor. $L_p$ and $L_s$ represent the primary and secondary coils of the transformer, respectively. $C_v$ and $R_v$ are the variable capacitor and resistor, respectively. Furthermore, $k$ is the coupling factor between coils. When a signal passes through the primary coil, a current is induced in the secondary coil, causing it to generate an induced magnetic field. The induced magnetic field of the secondary coil is opposite to the magnetic field of the primary coil, thus canceling the magnetic field. Thus, the inductance of the primary coil changes, resulting in multiple frequency bands. The variable inductor can be calculated using the input impedance $Z_{in}$, which is written as follows [17]:

$$Z_{in} = j\omega L_p - \frac{(j\omega)^2 M^2}{Z_{out} + j\omega L_s}$$  (1)

where $M$ is the mutual inductance and $Z_{out}$ is written as follows:

$$Z_{out} = \frac{R_v}{1 + j\omega C_v R_v}$$  (2)
By substituting Equation (2) into Equation (1), the equivalent inductance and resistance of the loaded transformer can be derived as follows:

\[
L_{eq} = L_p \frac{R_o^2[1 - \omega^2C_sL_s(1 - k^2)]^2 + \omega^2L_s^2(1 - k^2)^2}{R_o^2(1 - \omega^2C_sL_s)[1 - \omega^2C_sL_s(1 - k^2)] + \omega^2L_s^2(1 - k^2)}
\]

(3)

and

\[
R_{eq} = L_p \frac{R_o^2[1 - \omega^2C_sL_s(1 - k^2)]^2 + \omega^2L_s^2(1 - k^2)^2}{k^2R_oL_s}
\]

(4)

The inductance can be tuned by loading adjustable components, such as varactors, resistors, or switches. The varactor-loaded and switch-loaded components are usually used for transformer-based variable inductors. In this work, a switched inductor is obtained using two switch-loaded transformers to provide overlapping multibands to achieve a wide bandwidth. Figure 2 depicts the equivalent circuit of the switch-loaded transformer at switch on/off. Transistor as a switch can be modelled as R and C in a parallel circuit for both states. \(Z_{out}\) at the ON and OFF states can be expressed as \(R_{ON} + 1/(j\omega C_{ON})\) and \(R_{OFF} + 1/(j\omega C_{OFF})\), respectively, where \(R_{OFF}, C_{OFF}, R_{ON}\), and \(C_{ON}\) are the equivalent circuit parameters for the switched transistor at the ON and OFF states. Notably, \(R_{ON}\) is usually smaller than \(1/(j\omega C_{ON})\) and \(R_{OFF}\) is usually larger than \(1/(j\omega C_{OFF})\). \(C_{ON}\) and \(R_{OFF}\) can be neglected. \(L_{eq}\) at the ON state can be assumed when \(R_{ON}\) is approaching zero. Substituting \(Z_{out}\) into Equation (3), the first term in denominator and numerator of Equation (3) can be neglected and \(L_{eq}\) at the ON state can be written as follows:

\[
L_{eq,ON} \approx L_p \frac{\omega^2L_s^2(1 - k^2)^2}{\omega^2L_s^2(1 - k^2)} \approx L_p \left(1 - k^2\right)
\]

(5)

\(L_{eq}\) at the OFF state can be assumed if \(R_{ON}\) is approaching infinite. Therefore, the second term in denominator and numerator can be neglected; \(L_{eq}\) at the OFF state can be written as follows:

\[
L_{eq,OFF} \approx L_p \frac{R_o^2[1 - \omega^2C_{OFF}L_s(1 - k^2)]^2}{R_o^2(1 - \omega^2C_{OFF}L_s)[1 - \omega^2C_{OFF}L_s(1 - k^2)]} \approx L_p \left(1 + \frac{\omega^2L_sC_{OFF}k^2}{1 - \omega^2L_sC_{OFF}}\right)
\]

(6)

![Figure 1](image-url)
Figure 2. Equivalent circuit of the switch-loaded transformer at switch on/off.

However, the coil area occupied a large space on the chip, resulting in the limitation of the number of transformers. To avoid area expansion and increased bandwidth, two secondary coils are placed inside and outside the primary coil. In this work, the secondary coils are placed at the inner and outer portions of the multi-tapped inductor to provide compact size. Figure 3 shows the proposed layout of the multi-tapped switched inductor with two switch-loaded transformers. The center coil, which is a multi-tapped inductor, is considered the primary coil. Then, more switches are added at inner and outer portions of coils (two secondary coils) to increase the number of sub-bands. The switches are placed at three corners of outer coil and connected between the outer coil and the ground. Only one switch was connected between the edge of the inner coil and the ground. Symmetrical configuration and differential operation can reduce the common mode effects and improve the phase noise. Band selection is used based on the coupling length of the primary and secondary coils. A sub-band is generated when all switches are turned off (Mode 1), which has the largest inductance and the lowest frequency. Three sub-bands are created by turning on the pair of switches on both sides of the outer coil alternately when the switch at the inner coil is turned off (Modes 2–4). The inductance decreases sequentially from Mode 2 to Mode 4 because the coupling length increases accordingly. Mode 5 operates when the switch at the inner coil is turned on and the switches at the outer coil are turned off. The inner coil dominates the coupling ratio to obtain small inductance. The inductance at Mode 5 is smaller than outer coil operates because of its longer coupling length. Only the longest coupling length of the switch (QSW3/QSW6) at the outer coil is turned on when operating with the inner coil (Mode 6). The highest frequency band is obtained because of lowest inductance when inner and outer coupling are both operating. Six modes of sub-bands are selected for the switched inductor. Multiband is created by two switch-loaded transformers to provide continuous sub-bands for width bandwidth. Table 1 summarizes the six modes of sub-bands generated by the switches.

Table 1. Six modes of sub-bands for the switched inductor.

| Mode | QSW1/QSW5 | QSW2/QSW6 | QSW3/QSW7 | QSW4/QSW8 |
|------|-----------|-----------|-----------|-----------|
| Mode 1 | OFF | OFF | OFF | OFF |
| Mode 2 | ON | OFF | OFF | OFF |
| Mode 3 | OFF | ON | OFF | OFF |
| Mode 4 | OFF | OFF | ON | OFF |
| Mode 5 | OFF | OFF | OFF | ON |
| Mode 6 | OFF | OFF | ON | ON |
2.2. Design of Switched-Inductor VCO

The topology used conventional cross-coupled VCO involving a switched inductor with two switch-loaded transformers. The LC tank consists of an inductor with two switch-loaded transformers and two varactors (Cv1,2). The losses in the LC tank were compensated by using transistors (Q1,2). C1 and C2 are DC block capacitors because GaN HEMT operates at negative gate bias. R1 and R2 provide a stable gate bias for the core circuit. The widths of the cross-coupled pair of transistors and switch-loaded transistors (Qsw1 ~ Qsw8) are 4 × 100 and 6 × 100 μm, respectively. Two 4 × 100 μm GaN HEMTs were modified by connecting the sources and drains as two-port devices as two varactors (Cv1 and Cv2). The switched inductor VCO was biased at V_D = 12 V, V_G = −1.2 V, I_D = 260–300 mA, and V_tune = 13–22 V. The gate bias of the switch-loaded transistors was 0 V and −5 V for the ON and OFF states, respectively. To ensure balanced signals of the cross-coupled pair in the core circuit, an air bridge is used in GaN MMIC to form a metal crossing to achieve symmetry. Backside ground of wafer and via-hole connection from frontside to backside was used to improve self-heating effect and obtain high gain. The circuit is designed and verified by Advance Design System (ADS) Momentum Electromagnetic simulation to consider signal interference and parasitic effects. The inductor provides coarse tuning via switch-loaded transformers for band selection. Fine-tuning is performed using varactors. The oscillation frequency (f_0) of VCO is determined as follows:

\[
f_0 = \frac{1}{\sqrt{L_{eq} C_{eq}}} \tag{7}
\]

where L_eq is the equivalent inductance of the LC tank. Here, C_eq includes the varactor capacitance and the parasitic capacitance contributed by cross-coupled pair configuration. Figure 4 depicts the circuit architecture of the designed switched inductor VCO. In this work, the VCO has six continuous sub-bands using a switched inductor. The high power and low PN of VCO are achieved by using highly symmetrical interconnects to balance differential signals.
The switched inductor VCO was characterized using a spectrum analyzer for spectral density and a signal source analyzer for phase noise measurements. Figure 5 shows a photograph of the VCO circuit based on a switched inductor loaded with two transformers. The VCO was implemented using 0.5 μm GaN HEMT on SiC technology with a chip size of 2.2 × 2 mm² from Transcom, Inc. (Tainan County, Taiwan.) Figure 6 shows the simulated and measured FTR for each sub-band with $V_{\text{tune}}$ varying from 13 V to 22 V. Six continuous sub-bands generate the entire tuning range. Within each sub-band, an FTR of 0.53–0.83 GHz was achieved by the varactors with an FTR of 11.1% to 19.4%. The total tuning range of the proposed VCO was obtained with the oscillation frequency varying from 3.81 GHz to 5.04 GHz, which results in an FTR of 27.8%. The tuning frequency versus each switch mode with $V_{\text{tune}}$ varying from 13 V to 22 V is also shown in the figure. Notably, coarse frequency tuning was dominated by mode numbers, whereas fine-tuning was controlled using varactors, particularly when $V_{\text{tune}} > 18$ V. Although the outer coil is larger than the inner coil, the coupling lengths of Modes 3–5 are still too close, resulting in a nonlinear crossover between Modes 3–5. All the modes overlap to provide a wide continuous frequency range when $V_{\text{tune}}$ varies from 13 V to 22 V. The tuning range of the simulated and measured results were similar. The difference between the simulated and measured results is due to parasitic effects and the accuracy of the model, particularly in high voltage. Figure 7 shows the measured PN from 1 kHz to 10 MHz offset for each mode. The PN is higher when the switch is turned on because the Q-factor of the inductor is degraded. Mode 1 has less PN because all the switches are in the OFF state. Figure 8a shows the lowest PN of $-119.17$ dBc/Hz at 1 MHz offset from the carrier frequency of 4.27 GHz. Q$_{\text{SWs}}$ 3/4 (Q$_{\text{SWs}}$ 6/8) are turned on at Mode 6, resulting in a high PN. The worst PN of $-109.17$ dBc/Hz at the offset of 1 MHz from the carrier frequency of 5.04 GHz is detected at Mode 6 because two switches are turned on. The average PN is $-114.6 \pm 5.3$ dBc/Hz at 1 MHz offset for the carrier frequency varying from 3.81 GHz to 5.04 GHz. Figure 9 shows the $P_{\text{out}}$ for all the states. For the output power measurement, one of the output ports is connected to a spectrum analyzer for output power measurement and the other output ports...
port is connected by a 50-ohm resistance for balance. The maximum $P_{\text{out}}$ of 20.9 dBm at Mode 5, including 2.9 dB cable loss, is shown in Figure 8b. A large $P_{\text{out}}$ at Modes 1 and 5 is observed because the coupling path connected to the ground is far from $V_{\text{DD}}$. Conversely, Mode 2 has the lowest $P_{\text{out}}$ compared with the other modes because the coupling length is close to $V_{\text{DD}}$, resulting in energy coupling to the ground when SWs 1/4 are turned on. The average $P_{\text{out}}$ for all the modes was 19.4 ± 2.8 dBm. Similar to PN, $P_{\text{out}}$ was degraded when the switched transistor was turned on. A high $P_{\text{out}}$ of VCO indicates a low phase noise, which can be explained using Leeson’s formula, as follows [18]:

$$L(\Delta \omega) = 10 \log \left\{ \frac{2^{FKT}}{P_{\text{sig}}} \left[ 1 + \left( \frac{\omega_0}{2Q\Delta \omega} \right)^2 \right] \left[ 1 + \frac{\Delta \omega_{1/3}}{\Delta \omega} \right] \right\} \quad (8)$$

where $L(\omega)$ is single side band PN density at the $\Delta \omega$ offset of $\omega_0$ carrier frequency, $P_{\text{sig}}$ is the output power, $F$ is PN figure, $Q$ is the Q-factor of LC tank, $\Delta \omega_{1/3}$ is the 1/3 corner frequency, $K$ is the Boltzman constant, and $T$ is the temperature. The performance of the proposed VCO and those reported in previous literature [7,9,10,19] are summarized in Table 2. The figure of merit ($\text{FOM}_T$), which is employed to investigate their advantages, is defined as follows [20]:

$$\text{FOM}_T = 20 \log \left( \frac{f_0}{f_{\text{offset}}} \frac{\text{TR}\%}{10} \right) - L\{f_{\text{offset}}\} + 10 \log \left( \frac{P_{\text{out}}}{P_{\text{DC}}} \right) \quad (9)$$

where $L(f_{\text{offset}})$ is the measured PN at the $f_{\text{offset}}$ offset of $f_0$ carrier frequency. TR\%, $P_{\text{DC}}$, and $P_{\text{out}}$ are the percentage of FTR, dc power consumption, and output power, respectively. A large $\text{FOM}_T$ corresponds to an oscillator with high output power, wide bandwidth, and low PN. The fabricated VCO is compared with most state-of-the-art works. The highest $\text{FOM}_T$ was obtained in this work using a wide tuning range, high power, and low PN. Switch inductance by transformer loading provides a wide tuning range for millimeter-wave VCOs, particularly for high-power operation.

Figure 5. Photograph of the switched inductor VCO.
Figure 6. Simulated and measured frequency tuning range versus (a) varactor biases and (b) selection modes for the switched inductor VCO.

Figure 7. Measured phase noise versus (a) varactor biases and (b) selection modes for the switched inductor VCO.

Figure 8. Measured (a) PN at the carrier frequency of 4.27 GHz and (b) $P_{\text{out}}$ at 4.8 GHz of the switched inductor VCO.
Table 2. Comparison of MMIC GaN HEMT VCO.

| Ref. | Technology | \( f_0 \) (GHz) | Tuning Range (%) | PN @ 1 MHz (dBc/Hz) | \( P_{out} \) (dBm) | FOM_T |
|------|------------|-----------------|-----------------|---------------------|-------------------|--------|
| [7]  | 0.15 \( \mu \)m HEMT GaN-on-SiC | 9 | 1 GHz (11.1%) | -101 | 20.2 | -174 |
| [9]  | 0.25 \( \mu \)m HEMT GaN-on-SiC | 7.28 | 400 MHz (5.5%) | -122.5 | 1.06 | -182.9 |
| [10] | 0.25 \( \mu \)m HEMT GaN-on-SiC | 7 | 1.1 GHz (15.7%) | -132 | 27.9 | -179.1 |
| [19] | 0.25 \( \mu \)m HEMT GaN-on-SiC * | 2.66 | 145 MHz (5.6 %) | -129 | 17.55 | -177.9 |
| This Work | 0.5 \( \mu \)m HEMT GaN-on-SiC | 4.42 | 1.23 GHz (27.8%) | -119.17 | 20.9 | -186.9 |

* including power amplifier.

4. Conclusions

In this study, a switched inductor was tuned by two switch-loaded transformers for a VCO in a cross-coupled pair configuration. Using the proposed frequency tuning scheme, the VCO was realized in 0.5 \( \mu \)m GaN HEMT on SiC technology with design goals for the maximum FTR, low PN, and high \( P_{out} \). The measured results show that the VCO achieved an FTR of 27.8% from 3.81 GHz to 8.04 GHz with a maximum \( P_{out} \) of 20.9 dBm. With a 1 MHz frequency offset, the PN varied from -109.2 dBc/Hz to -119.17 dBc/Hz across the six sub-bands. The proposed VCO has a strong potential to be integrated into fifth-generation wireless base stations and military systems.

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