On-chip microwave photonic beamformer circuits operating with phase modulation and direct detection

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Abstract: We propose and experimentally demonstrate the working principles of two novel microwave photonic (MWP) beamformer circuits operating with phase modulation (PM) and direct detection (DD). The proposed circuits incorporate two major signal processing functionalities, namely a broadband beamforming network employing ring resonator-based delay lines and an optical sideband manipulator that renders the circuit outputs equivalent to those of intensity-modulated MWP beamformers. These functionalities allow the system to employ low-circuit-complexity modulators and detectors, which brings significant benefits on the system construction cost and operation stability. The functionalities of the proposed MWP beamformer circuits were verified in experimental demonstrations performed on two sample circuits realized in Si$_3$N$_4$/SiO$_2$ waveguide technology. The measurements exhibit a 2 × 1 beamforming effect for an instantaneous RF transmission band of 3–7 GHz, which is, to our best knowledge, the first verification of on-chip MWP beamformer circuits operating with PM and DD.

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1. Introduction

Microwave photonic (MWP) beamformers for RF phased array antennas have stimulated a fast-growing surge of interest in the past decades from both scientific and industrial communities [1–19]. In particular, promising applications for airborne and spaceborne communications and large-scale-antenna-based radio astronomy have shown the added value of and need for microwave photonic technologies [20]. By harnessing the advantageous nature of photonics, MWP beamformers allow the pursued system features such as large instantaneous RF bandwidth, light weight, low loss, high system compactness, beam pattern reconfigurability, and seamless beam-steering to be achieved simultaneously [21, 22]. Nevertheless, it is believed that MWP beamformers, just like the other promising MWP functionalities, can only become a widely-deployed and commercially competitive solution as against the conventional all-electronics ones when the full integration of the MWP systems has been successfully achieved [23, 24]. The integrated method is known to be most effective way to bring down the system costs to a level allowing for volume production. A more fundamental aspect is that it improves system stability and predictability, which is essential for the success in meeting the ever-rising performance requirements. In the context of MWP beamformer, integrating the photonic functionalities in the chip level will not only enable precise light manipulation at the wavelength scale, but also make it possible to create very
complex systems for large-scale phased array antennas, where a great number of devices and dense control are required [20]. Next to the beamformer [8-17], a significant number of other key MWP functionalities have also been demonstrated in the recent years using different integration technologies [24]. Some salient works include reconfigurable filters [25-27], arbitrary waveform generators [28, 29], integrators [30, 31], differentiators [32, 33], frequency discriminators [34, 35], and tunable phase shifters and delay lines [36–43].

In our previous works, we have proposed and demonstrated a number of integrated beamformer circuits, which are realized in low-loss, high-index-constrast Si3N4/SiO2 planar waveguides (TriPleX waveguide technology) [10, 12, 14, 16, 17, 20, 23, 44]. These on-chip beamformer circuits implement a number of key-functionalities such as broadband beamforming networks employing ring resonator-based delay lines [10, 41], optical sideband filters [14, 23, 44], separate carrier phase tuning [40], and (de)multiplexers operating in both RF and optical regimes [17]. All these functionalities are designed to be continuously tunable by incorporating thermo-optical tuning elements in the circuits. This enables the proposed beamformers to perform arbitrary beamforming and seamless beam-steering, which are difficult to achieve using the conventional all-electronics beamformers. In addition to this, more advanced functionalities such as multi-beam and multi-RF band beamforming are also possible to be implemented in the beamformer circuits by exploiting signal processing schemes based on the frequency periodicity of the waveguide devices and simultaneous use of multiple optical wavelengths [16, 17].

For the previously demonstrated MWP beamformers, the antenna signals are typically up-converted into the optical domain by means of optical intensity modulation using Mach-Zehnder modulators or electroabsorption modulators. In general, intensity modulators require, in practice, bias-voltage adjustment and stabilization for optimum performance. Besides, their implementations usually involve a trade-off between device performance and complexity. All this may lead to critical risks for the realization of fully integrated MWP beamformers, particularly when an array of multiple modulators is required in receive antennas. In contrast, phase modulators feature not only high modulation efficiency and bias-free operation, but also low device complexity thereby reducing risks in device realization. Calling upon this fact and the aim of low system complexity, it is therefore worth investigating the implementation of MWP beamformers operating with phase modulation (PM) and direct detection (DD).

Here we propose and experimentally verify two on-chip MWP beamformer circuits which allow for such PM/DD scheme. The circuits incorporate two major signal processing functionalities: one is a 2 × 1 beamforming network employing ring resonator-based delay lines which enables seamless beam-steering for broadband antennas; the other is an optical sideband manipulator (OSM) which renders the circuit output equivalent to those of intensity modulation. The implementation of the OSM functionality enables the use of simple direct detection in the system instead of high-device-complexity coherent detection. In Section 2, the working principles of the MWP beamformers operating with PM and DD are explained. Section 3 describes the architectures of the beamformer circuits and the parameter settings for operation. In Section 4, the experimental demonstrations of the circuit functionalities are presented. The conclusions and discussions are given in Section 5.

2. System principle

The proposed MWP beamformer system incorporating the PM/DD scheme is schematically depicted in Fig. 1. The signal processing schemes applied are conceptually illustrated in Fig. 2, where the amplitude and phase spectra of the signals are shown at different points throughout the scheme to illustrate the working of the system. As depicted in Fig. 1, beamforming is considered using the simplified model of a two-element phased array receive antenna. Here, we use the far-field plane-wave assumption for the incident RF signal. In this example, the signal incident angle with respect to the normal of the antenna plane is indicated by θ, with which the lower antenna element receives a wavefront of the incident RF signal with a time delay Δτ (determined by Δτ = sin(θ)d/c0 with d the inter-element distance and c0 the speed of light in air) as against the upper one as shown in Fig. 1. We use s(t) and s(t−Δτ)
general slope, the sidebands (the upper sideband is subject to this phase shift in the illustration; besides, the two sidebands feature anti-symmetrical phases with respect to the optical carrier, which is \(|\Delta\phi|\) with a transfer function approach is to remove one of the sidebands from the optical signal by using a spectral filter then equivalent to the spectrum characteristic of intensity modulation [45, 46]. The other linear phase characteristic resulting from the antenna delay \(\Delta \tau\), with which the phase slope scales. To achieve the beamforming functionality [11, 14, 50], the optical signals at point C and D are required to be synchronized in both phase and delay, so that at point E they will add up constructively for the entire signal band. To this end, a tunable optical time delay line (TTD) and an optical phase shifter (OPS) are used in the beamformer circuit as shown in Fig. 1. Assuming that both the TTD and OPS have optical bandwidths to cover the entire modulation spectrum, then the optical signals at point C and D can be made identical when the TTD provides a delay equal to \(\Delta \tau\) and OPS removes the carrier phase difference between the two signal paths. Next to the question on the phase, the relative amplitudes between different signal paths are also a determining factor for the beamforming functionality [11, 14, 50]. In practice, this can be addressed by implementing the signal combiner (between point C/D and E in Fig. 1) using an arbitrary-ratio tunable coupler [23, 44], which also compensates the loss difference between the signal paths.

Although constructively combined as shown in the inset E of Fig. 2, the optical signal at point E is still in the phase modulation format. This means that the desired RF signal cannot yet be recovered by means of direct detection at that point, because the two phase modulation sidebands will contribute to the RF output in anti-phase status and therefore cancel out each other. To render the output of the beamformer circuit detectable by means of direct detection, or in other words, equivalent to that of intensity modulation, a dedicated processing unit is added in serial to the signal synchronizing and combining functionalities, namely a so-called optical sideband manipulator (OSM). Basically, this OSM can perform its function via two alternative approaches as illustrated in the inset F-case 1 and F-case 2 of Fig. 2. In F-case 1, the approach is to provide an additional phase shift of \(\pi\), as indicated by \(\Delta \phi_{\text{OSM}}(f)\), to one of the sidebands (the upper sideband is subject to this phase shift in the illustration; besides, the general slope \(\Delta \tau'\) indicates an extra delay introduced by the device). As a result of this, the two sidebands feature anti-symmetrical phases with respect to the optical carrier, which is then equivalent to the spectrum characteristic of intensity modulation [45, 46]. The other approach is to remove one of the sidebands from the optical signal by using a spectral filter with a transfer function \(|\Delta H_{\text{OSM}}(f)|\) as shown in F-case 2 [47-49]. This way, only the remaining sideband contributes to the RF output, which prevents the RF cancellation effect and therefore makes direct detection viable. However, unlike that for the first approach, the detected RF output in this case has an additional loss of 3 dB because half of the signal power is lost by filtering. On the other hand, this filter approach has a benefit of a significantly lower optical bandwidth requirement for the TTD, since the TTD only needs to have a delay bandwidth to cover one sideband and the optical carrier rather than the entire modulation spectrum. As will be explained later, this may lead to lower device complexity when ring resonators are used to construct the TTD.

In principle, the order of the aforementioned processing functionalities can be inverted. This means that the proposed beamformer circuit can also be applied to transmit antennas when the modulators are replaced by the detectors and vice versa. Although so far we considered only the simplest scenario of beamforming with two antenna elements, the concept of such a beamformer with the PM/DD scheme also applies to large-scale phased array antennas consisting of \(N\) \((N>2)\) antenna elements. For example, an \(N \times 1\) combining circuitry can be constructed using multiple \(2 \times 1\) combiners arranged in a binary-tree topology. The TTD and OPS functionalities can be equipped to every signal path to facilitate a flexible beamformer which supports arbitrary beamforming for 2-D phased array antennas [14, 17, 20]. However, from the perspective of device realization and operation, upscaling the
beamformer also raises a number of challenges. Larger antenna dimensions mean longer delays required on chip, which cause higher device insertion loss because the optical transmission loss scales with the effective delay path length. This accents more the need of a low-loss integration platform. For the receive antennas, the on-chip realization of an array of high-transfer-efficiency modulators is seen to be essential for the system operation, not only because of the questions on the performance and cost, but more fundamentally the integration of this with the beamformer circuit preserves the inter-channel phase relation which enables coherent optical processing as required in the system principle. Although not yet mature, some significant progress for this has been made in the development of monolithic or hybrid photonic integration technologies [20]. Moreover, as the beamformer becomes more complex with more functional units incorporated, the dynamic control of it becomes also a concern. This requires then devising a dedicated algorithm, which can be implemented either by means of real-time computing or switching between different settings of a look-up table, depending on the applications.

Fig. 1. A schematic of the MWP beamformer system associating the PM/DD scheme for a 2-element phased array receive antenna. The devices indicated in the filled blocks are envisioned to be integrated in the chip level using a monolithic or hybrid integration platform.

Fig. 2. Conceptual illustrations of the signal amplitude and phase spectra at different position points in the system shown in Fig. 1 (A: the optical signal spectrum is considered to be the reference, having a flat phase characteristic for simplicity; B: the optical signal spectrum including a linear phase characteristic caused by the antenna delay $\Delta t$; C and D: the optical signals in both beamformer channels are synchronized in delay as well as optical carrier phase, by means of the tunable delay line (TTD) and optical phase shifter (OPS) as indicated in Fig. 1; E: two beamformer channels are combined constructively; F-case 1: phase-modulation to intensity-modulation (PM-to-IM) conversion by means of a ring resonator-based modulation transformer (MT), which introduces an additional phase shift of $\pi$ to one of the sidebands (the upper sideband in this illustration) so that the signal spectrum is an equivalent to that of intensity modulation; F-case 2: PM-to-IM conversion based on an optical sideband filter (OSBF), which removes one of the sidebands and makes direct detection viable).
3. Beamformer circuits

To implement the processing functionalities of the beamformer described above, we consider two architectures of waveguide circuits, which are depicted in Fig. 3(a) and 3(b). The circuits are constructed using only three basic building blocks, which are tunable couplers (in this case $2 \times 2$ Mach-Zehnder couplers), ring resonators, and phase shifters. The tunable coupler can be used in the circuits as a signal combiner/splitter or switch, where the coupling coefficient $\kappa$ can be varied by changing the optical phase shift difference between the two arms [23, 44]. A practical way to achieve an additional optical phase shift $\phi$ is to modify the refractive index over a length of waveguide, such as via incorporating a thermo-optical or electro-optical tuning element. As depicted in Fig. 3(c), a fully-tunable ring resonator can be constructed by incorporating one arm of the Mach-Zehnder coupler in a closed loop and adding two such tuning elements to obtain tunability of $\kappa$ and $\phi$. This tunability allows us to compensate for the fabrication tolerance and achieve arbitrary delay settings as the delay value (which is proportional to the quality factor [51]) and the resonance frequency of the ring resonator can be varied by changing $\kappa$ and $\phi$, respectively [12, 41, 44]. For the implementation of TTD and OPS functionalities, we chose here cascades of ring resonators and independent phase tuning elements, respectively. Our earlier works have demonstrated that they are able to perform desired functionalities for broadband signals and are particularly good candidates for constructing compact circuits [12, 14]. As the two options to realize an OSM as described above, circuit architecture 1 implements the phase-shift approach, using a single ring resonator serving as a phase-to-intensity modulation transformer (MT) [45, 46], and circuit architecture 2 implements the spectral-filtering approach, using an optical sideband filter (OSBF) based on a ring resonator-assisted Mach-Zehnder interferometer (RAMZI) structure [44, 47-49].

![Fig. 3. (a) beamformer circuit architecture 1 including a ring resonator-based modulation transformer (MT); (b) beamformer circuit architecture 2 including an optical sideband filter (OSBF) based on ring resonator-assisted Mach-Zehnder interferometer structure; (c) the structure breakdown of a fully-tunable ring resonator.](image-url)
To give a concrete example of circuit design, we consider here a $2 \times 1$ beamforming functionality for an RF signal specification: a center frequency of 5 GHz, an instantaneous bandwidth of 4 GHz (3–7 GHz), and a maximum delay difference of 0.3 ns between the two antenna elements. Based on the system principle shown in Fig. 1, the ideal frequency responses of the TTD and OSM for both circuit architectures can be defined accordingly, which are the dotted lines in Fig. 4. To prove of concept, we consider using the Si$_3$N$_4$/SiO$_2$ waveguide technology as described in [44] to construct the circuits. This allows the TTDs to be constructed using a cascade of ring resonators with a free spectral range (FSR) of 25 GHz, which features device compactness and low propagation loss (systematically $\leq$ 0.15 dB/cm) simultaneously. Based on the bandwidth properties investigated in [45-49], FSRs of 10 GHz and 20 GHz are chosen for the MT and OSBF, respectively, such that their operation passbands are sufficiently wide to accommodate the specified RF signal. In the context of signal processing, one can use $z$-transforms to describe the transfer functions of the waveguide circuits and calculate the corresponding frequency responses [51]. Then, by approximating the calculated frequency responses of the implemented TTDs and OSMs to the ideal ones, one can determine the needed number of cascaded ring resonators and the circuit parameters ($\kappa$ and $\phi$) to achieve the desired beamforming functionality. An example of the approximation is depicted in solid lines in Fig. 4, for which the $z$-transforms and circuit parameter setting for circuit architecture 1 and 2 are listed in Table 1. Here, the power transmission coefficient $r = 0.96$ is used for calculation, which corresponds to the low-loss Si$_3$N$_4$/SiO$_2$ waveguide (0.15 dB/cm) and has negligible effect on the circuit functionalities. The optical carrier frequency is regarded as the frequency reference for setting the circuit frequency responses. For circuit architecture 1, we calculate that 6 ring resonators are needed to allow the TTD to fulfill the delay bandwidth requirement, which is, in this case, a bandwidth $\geq$ 14 GHz to cover the entire spectrum of the optical signal including the carrier and both upper and lower sidebands. In coordination, the MT with a FSR of 10 GHz introduces a phase shift of $\pi$ to the lower sideband with respect to the carrier and upper sideband. This implements the signal processing scheme “F-case 1” in Fig. 2. For circuit architecture 2, a smaller number of ring resonators, namely 4 ring resonators in this case, is sufficient for the TTD to satisfy the delay bandwidth requirement, since the lower sideband will be removed from the signal spectrum by filtering and the actual signal bandwidth reduces thereby to 7 GHz. The OSBF with a FSR of 20 GHz is characterized by a passband with a bandwidth of 10 GHz, a stopband 6 GHz, and a transition band 2 GHz. This matches the specification of the optical signal spectrum and implements the signal processing scheme “F-case 2” in Fig. 2.

![Fig. 4.](image-url) The graphs in (a) and (b) depict the calculated true time delay (TTD) and optical sideband manipulator (OSM) responses (solid lines) of circuit 1 and 2, respectively, which are set to approximate the ideal responses (dotted lines) for the $2 \times 1$ beamforming functionality. The circuit parameter setting for the approximation is given in Table 1. The blue sketches indicate the relative spectral positions of the carrier and the upper and lower sidebands (USB, LSB) of the optical signals.
Table 1. List of \( z \)-transforms and parameter settings of the proposed beamformer circuits

| Architecture 1 (Fig. 3(a)) | Architecture 2 (Fig. 3(b)) |
|---------------------------|---------------------------|
| **TTD**                   | **OSBF (FSR 20 GHz):**    |
| 6 ring resonators (FSR 25 GHz): | \( H_{\text{OSBF}}(z) = \frac{\sqrt{1 - \kappa_2 \kappa_4}}{1 - e^{-j\phi_2} r^2 \sqrt{1 - \kappa_2}} \) |
| \( H_{\text{TTD}}(z) = \prod_{n=1}^{6} \frac{1 - \kappa_n z^{-1}}{1 - e^{-j\phi_n} z^{-1}} \sqrt{1 - \kappa_n} \) | \( H_{\text{OSBF}}(z) = \frac{\sqrt{1 - \kappa_2 \kappa_4}}{1 - e^{-j\phi_2} r^2 \sqrt{1 - \kappa_2}} \) |
| \( \kappa_1 = 0.55; \kappa_2 = 0.62; \kappa_3 = 0.63; \kappa_4 = 0.63; \kappa_5 = 0.62; \) | \( \kappa_9 = 0.52; \kappa_{10} = 0.59; \kappa_{11} = 0.59; \kappa_{12} = 0.52; \) |
| \( \kappa_6 = 0.55; \phi_1 = 0.56 \pi; \phi_2 = 0.34 \pi; \phi_3 = 0.12 \pi; \) | \( \phi_9 = -0.02 \pi; \phi_{10} = -0.20 \pi; \phi_{11} = -0.40 \pi; \phi_{12} = -0.58 \pi. \) |
| \( \phi_4 = 0.12 \pi; \phi_5 = 0.34 \pi; \phi_6 = 0.56 \pi. \) | \( \kappa_7 \text{ and } \kappa_{13} \text{ to vary the inter-channel amplitude ratio; } \phi_7 \text{ and } \phi_{13} \text{ to synchronize the carrier phase.} \) |

4. Experimental verification

To experimentally demonstrate the working of the proposed beamformer circuits, two sample circuits with the architectures shown in Fig. 3 were used, which are parts of a reconfigurable complex microwave photonic signal processor chip fabricated in a low-loss, high-index-contrast \( \text{Si}_3\text{N}_4/\text{SiO}_2 \) waveguide technology (TriPleX [23, 44]). The chip has multiple optical inputs and outputs, which allows us to get access to the circuits of question independently. To enable full operation, the chip has been fully packaged with electrical connections to all the tuning elements and fiber pigtailed at both input and output sides, a photo of which is shown in Fig. 5(a). The setups to characterize the circuits and to verify the beamforming functionality are shown in Fig. 5(b) and 5(c), respectively. The circuit parameters \((\kappa \text{ and } \phi)\) are configured via the on-chip thermo-optical tuning elements implemented using resistor-based heaters [23, 44]. A scalable 12-bit voltage control system (from SATRAX B.V.) was used to drive multiple heaters simultaneously. The other major components in the setups include a 1.55 \( \mu \text{m} \) wavelength single-frequency CW laser (Gooch & Housego, EM4-253-80-057) driven by a low-noise current controller (ILX Lightwave, LDX-3620), a Mach-Zehnder intensity modulator (Avanex, PowerLog FA-20), two phase modulators (EO space, PM-5K-10), an RF detector (Discovery semiconductor, DSC-R401HG), and an RF vector network analyzer (Agilent, NA5230A PNA-L).
4.1 Beamformer circuit setting

As the first step, the setup in Fig. 5(b) is used to characterize the frequency responses of the beamformer circuits by means of the so-called phase-shift method [44]. Here, a probe-signal-modulated optical carrier is frequency-swept across a full free spectral range of the circuit under test, and the frequency response in the optical domain is acquired from the detected probe signal. In our case, a probe signal of 50 MHz generated by the RF network analyzer was chosen to achieve satisfying measurement resolution and accuracy simultaneously. The beamformer circuits were configured to match the calculated frequency responses as shown in Fig. 4. The measurements of the achieved frequency responses for the two circuits are presented in Fig. 6 and 7, respectively, where the calculated responses are also depicted showing an excellent agreement. As shown in Fig. 5(a) and 6(a), the TTDs of circuit 1 and 2 provide actual bandwidths of 16 GHz and 8 GHz, respectively, for the delay value of 0.3 ns (the beamformer channel without TTD is used as delay and loss reference). This satisfies the delay bandwidth requirements in order to perform signal synchronization between the two beamformer channels (delay bandwidth ≥ 14 GHz for circuit 1; ≥ 7 GHz for circuit 2). The additional loss due to the equivalent delay path length is seen to be about 1.2 dB, which is consistent with a waveguide propagation loss of 0.15 dB/cm. The reduced transmission of the TTD may cause an initial offset of the signal amplitude ratio between different beamformer channels. However, in practice this offset can be compensated at the tunable signal combiners (κ_7 and κ_13 in Fig. 3), and therefore will not affect the beamforming functionality of the circuit. It can be seen in Fig. 6(b) and 7(b) that also the achieved MT and OSBF responses are in excellent agreement with the calculations. In addition, the transmission loss of the MT and the dispersion in the OSBF passband are also shown in the measurement results, which both have insignificant values in the frequency regions covering the optical signal and therefore should guarantee satisfying performances with negligible signal distortion.

![Fig. 6. Measured (solid) and calculated (dashed) frequency responses of circuit 1: (a) group delay and power transmission of the delay line (TTD) consisting of a cascade of 6 ring resonators, where the beamformer channel without TTD is regarded as delay and loss reference; (b) phase shift and power transmission of the modulation transformer (MT).](image1)

![Fig. 7. Measured (solid) and calculated (dashed) frequency responses of circuit 2: (a) group delay and power transmission of the delay line (TTD) consisting of a cascade of 4 ring resonators, where the beamformer channel without TTD is regarded as delay and loss reference; (b) power transmission of the optical sideband filter (OSBF) and the dispersion in the OSBF passband (the dispersion in the OSBF stopband is skipped for simplicity as it is not of relevance to the device functionality).](image2)
4.2 Demonstration of the beamformer system

With the circuit settings as described, the second step was demonstrating the desired beamforming functionality, for which we used the experimental setup in Fig. 5(c). An input RF signal sweeping from 10 MHz to 10 GHz is generated by the network analyzer and equally split to be employed as the driving signals for the two phase modulators in the beamformer system. In this setup, an RF delay $\Delta_{\text{RF}}$ between the two driving signals is applied to simulate an according inter-antenna-element delay, while all other RF and optical connections for the two beamformer channels were made equal in length. First, the beamformer circuit 1 configured as in Fig. 6 is applied in the setup, and the optical carrier is tuned to the center of the group delay and power transmission spectrum (zero frequency in Fig. 6). Since this experimental setup uses several fiber connections between the laser and beamformer circuit chip instead of the desired full chip-level integration, special care was taken with mechanical and thermal stability in order to maintain the carrier phase relation between the beamformer channels.

With the setup ready, the RF-to-RF transmission of the two beamformer channels was first measured to verify the achievement of the desired system RF bandwidth of 4 GHz, namely the specified antenna reception band ranging from 3–7 GHz. For this measurement, only one RF drive was connected to its modulator at a time. The measurement results are depicted in Fig. 8(a). As explained earlier, the coupler $\kappa_7$ was adjusted such that the two beamformer channels have equal amplitude. The envelope profile of the measured RF-to-RF transmissions agrees with the theory regarding the bandpass property of the PM/DD scheme employing a ring resonator-based MT [46]. The RF transmission envelope of the beamformer channel with TTD (Ch1) has a good symmetric profile with respect to the center frequency of the RF passband, similar to that of the other channel without TTD (Ch2). This indicates that the optical carrier is positioned at the center of the delay band of TTD as desired, so that the two sidebands of the optical signal experience equal delays and losses and therefore avoid an additional RF fading at the detection as the effect of sideband imbalance.

Next, two RF drives, RF1 and RF2, were connected to the modulators simultaneously, and a RF delay $\Delta_{\text{RF}} = 0.3$ ns was applied to RF2 to simulate the signal reception delay difference between the two antenna elements. In this case, $\Delta_{\text{RF}} = \Delta_{\text{TTD}}$, so that the two beamformer channels were delay-synchronized. After matching the optical carrier phases between the two beamformer channels, the $2 \times 1$ beamforming effect, namely an RF transmission gain of 6 dB, was achieved in agreement with theory [28]. This is depicted in Fig. 8(b). To further verify the correct operation of the beamformer circuit, an intentionally deviating delay was chosen between the two beamformer channels, $\Delta_{\text{diff}} = \Delta_{\text{RF}} - \Delta_{\text{TTD}} = 0.67$ ns, which corresponds to microwave signals being incident on the antenna plane at a different angle than the beamformer is set for. Theoretically, this will result in frequency-periodic nullings in the RF transmission spectrum at the beamformer output [50], where the frequency period is determined by $\Delta f = 1/\Delta_{\text{diff}} = 1.5$ GHz. A measurement on this was performed and exhibited agreement with the theory, the result of which is depicted in Fig. 8(c).

Likewise, the similar functionality of beamformer circuit 2 was also demonstrated, the results of which are depicted in Fig. 9. The measurement results in Fig. 8 and 9 prove the principles of the proposed MWP beamformer system operating with PM and DD, and verify the on-chip implementations of the proposed beamformer circuits.
Fig. 8. Measured RF-to-RF transmissions of the demonstrator setup with beamformer circuit 1, normalized to the maximum transmission of Ch2: (a) only one RF drive is connected to its modulator at a time, which shows the system passband covers the desired 4 GHz bandwidth (3–7 GHz); (b) both RF drives are connected to the modulators and the two beamformer channels are delay-synchronized, which shows the $2 \times 1$ beamforming effect, namely a 6 dB gain in the passband; (c) an additional delay difference of 0.67 ns is introduced between the two beamformer channels, which causes periodic nullings with a frequency interval of 1.5 GHz as expected from theory.

Fig. 9. Measured RF-to-RF transmissions of the demonstrator setup with beamformer circuit 2, normalized to the maximum transmission of Ch2: (a) only one RF drive is connected to its modulator at a time, which shows the system passband covers the desired 4 GHz bandwidth (3–7 GHz); (b) both RF drives are connected to the modulators and the two beamformer channels are delay-synchronized, which shows the $2 \times 1$ beamforming effect, namely a 6 dB gain in the passband; (c) an additional delay difference of 0.67 ns is introduced between the two beamformer channels, which causes periodic nullings with a frequency interval of 1.5 GHz as expected from theory.

5. Conclusions

In this paper, we have presented a novel microwave photonic beamformer system which operates with phase modulation and direct detection. The system can be constructed using two different on-chip beamformer circuit architectures. Both circuits consist of a beamforming network employing ring resonator-based delay lines and an optical sideband manipulator. The optical sideband manipulator can be implemented using a modulator transformer based on a ring resonator, or using an optical sideband filter based on a resonator-assisted Mach-Zehnder interferometer structure. To verify the principles in an experimental demonstration, a beamformer demonstrator has been presented in the most basic case, a two-element phased array receive antenna with instantaneous frequency band from 3–7 GHz, which exhibits the expected $2 \times 1$ beamforming functionality. The demonstration clearly shows the 6 dB signal enhancement for delay-synchronized reception and frequency-selective signal suppression for delay-deviated reception, which agrees well with the theory. The proposed MWP beamformer features simple device complexity, robustness, and scalability. In addition to this, its constitution promises a low-risk realization of fully integrated MWP beamformer systems, which is the key feature for system cost reduction in future development.
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