Impedance Modeling and Analysis of Medium-Frequency Oscillation Caused by VSC-HVDC Connected to Local Weak Grid and DFIG-Based Wind Farms

Kun Sun, Wei Yao *, Cai Yan and Jinyu Wen

State Key Laboratory of Advanced Electromagnetic Engineering and Technology, School of Electrical and Electronic Engineering, Huazhong University of Science and Technology, Wuhan, China

In the northwest of China, a strategy to transmit the wind-thermal–bundled power from the local grid and doubly fed induction generator (DFIG)–based wind farms through a voltage source converter–based HVDC (VSC-HVDC) can be widely applied. However, since the local grid is usually weak, a new type of electrical oscillation in the medium-frequency region may occur in the sending-end converter (SEC) of VSC-HVDC with PQ-control. The mechanism of this oscillation caused by the interaction between the DFIG, local grid, and SEC is not entirely understood. In this study, the sequence impedance model of the sending-end converter (SEC) of VSC-HVDC with the PQ-control outer loop and PLL is derived with the explicit analytic expression, and then, the oscillation mechanism is explored based on the intuitive analysis of the system impedance frequency characteristics. Compared with the subsynchronous oscillation (SSO) caused by the DFIG or power inverter, the theoretical analysis shows that this medium-frequency oscillation (MFO) mainly originates from the SEC due to its negative damping effect between about 100 and 200 Hz. In addition, the impact of the system controller parameters and operating conditions of the DFIG, local grid, and SEC on the oscillation characteristics is analyzed in detail. Finally, the correctness of the theoretical analysis is validated by time-domain simulation.

Keywords: sending-end converter, local weak grid, doubly fed induction generator, impedance model, stability analysis, medium-frequency oscillation

1 INTRODUCTION

In Northwest China, large-scale wind power is generally bundled with local thermal power and then transmitted to the load centers in the eastern coast, as the wind power is fluctuant and intermittent while the thermal power is controllable (Sun et al., 2017; Gao et al., 2018). Recently, the VSC-HVDC technology is developing rapidly due to its advantage in the wind power integration (Flourentzou et al., 2009; Yang et al., 2018; Chen et al., 2020; Sun et al., 2020; Xiong et al., 2021). Therefore, the strategy to transmit the large-scale wind power through VSC-HVDC in the local grid will be widely applied. However, the proposed transmission mode may face potential stability issues induced by the power electrical converters, especially given that the local grid in Northwest China is usually weak.
Several oscillation incidents related to VSC-HVDC have occurred worldwide. In 2013, an SSO around 20 Hz occurred in the Nanao VSC-HVDC project in China and led to the outage of the integrated DFIG-based wind farms (Lyu et al., 2018). In addition, a 250–350 Hz current oscillation was observed in the North Sea VSC-HVDC project in Germany from the converter to the grid (Christoph et al., 2015; Zhou et al., 2019). Similarly, for the proposed system, the interaction between the DFIG-based wind farms, local grid, and VSC-HVDC may cause a new type of electrical oscillation issue, which is worthy of further study.

Currently, the impedance method is suitable for the stability issue caused by the grid-connected units due to its simple mathematical expression and stability criterion (Cespedes and Sun, 2014). There have been a lot of studies concentrating on the stability issue of the DFIG-based wind farms and VSC-HVDC based on the impedance method. For instance, the effect of the control parameters on the stability of the grid-connected converter is investigated in Wang et al. (2017), Wen et al. (2017), Xu et al. (2019), and Wang et al. (2020). However, in the above references, only the inner loop and PLL are considered in the modeling, while the influence of the outer loop is ignored. Thus, the accuracy of the stability analysis is doubtful. Recent references (Khazaei et al., 2018; Liu and Xie, 2019; Zhou et al., 2020) take the outer loop into account as well. However, the impedance model with the outer loop is normally given in matrix format instead of the explicit analytic expression, which is not clear and intuitive when it comes to illustrating the impedance characteristics. In addition, all of the above references only consider the outer loop of a power inverter instead of a converter. In Liu et al. (2021), Liu and Xie (2017), Miao (2012), Viento and Sun (2018), Koralewicz et al. (2020), Yan et al. (2019), Yang et al. (2020), SSO existing in the DFIG-based wind farms interfaced with the series-compensated line or AC grid is investigated, and the impact of the various factors on SSO is analyzed. However, the influence of the integrated DFIG on the oscillation over the supersynchronous frequency region is not considered. Besides, few references consider the stability of the proposed system which transmits wind power in combination with thermal power through the SEC.

In particular, the interaction between the DFIG and VSC-HVDC is deeply analyzed in author’s previous work (Sun et al., 2019). However, the system stability will be affected in a different way when the wind-thermal–bundled power is transmitted from the local grid and DFIG-based wind farms to the SEC with PQ-control instead of the amplitude–frequency control. Since the local grid is integrated and the control strategy of the SEC is changed, the interaction mechanism will be more complicated in the proposed transmission mode. The similar system of a VSC-HVDC connected to the DFIG-based wind farm or the thermal power plant has been preliminarily analyzed in Chen et al. (2018), Liu et al. (2018), and Zou et al. (2018). However, they mainly focus on SSO or high-frequency oscillation issues, while MFO is not considered. In addition, the control strategy of the SEC in the proposed transmission mode is different from that in the above studies since the output power of the wind-thermal–bundled system should be controlled by the SEC (Yogarathinam et al., 2017) and then transmitted to the power grid. Thus, the outer loop of the SEC cannot be ignored since it reflects the effect of the control strategy. In short, the stability of the proposed transmission mode is not yet systematically comprehended. Therefore, this work aims to explore the interaction between the DFIG, local grid, and SEC with PQ-control in the medium-frequency region.

The main contributions of this study are summarized as:

- The potential MFO risk of the SEC with PQ-control in the medium-frequency region is analyzed when it transmits the wind-thermal–bundled power from the local grid and DFIG-based wind farms.
- To analyze the interaction mechanism in the proposed transmission mode, the sequence impedance model of the SEC with the PQ-control outer loop and PLL is derived with the explicit analytic expression, which is clear and intuitive when it comes to illustrating the impedance characteristics.
- The coupling between the inner loop, outer loop, and PLL of the SEC is analyzed according to the derived impedance models, and their influence on the system stability can be intuitively found based on the analytic impedance expression and the corresponding impedance frequency characteristics. Compared with the power inverter, the mechanism and characteristics of MFO caused by the SEC are different.
- The impact of the local grid strength and the integration of DFIG-based wind farms on the system stability is analyzed as well. Compared with SSO caused by the DFIG, the controller parameters and operating conditions of the DFIG have a different influence on this MFO which mainly originates from the SEC.

The rest of this article is organized as follows. The structure and oscillation phenomenon of the system is investigated in Section 2. Section 3 derives and validates the sequence impedance models of the SEC, the DFIG, and the local grid. In Section 4, the mechanism explanation of the oscillation is revealed based on the derived models, and the impact of various factors on the oscillation is analyzed. Section 5 validates the correctness of the theoretical analysis by time-domain simulation. Finally, conclusions are drawn in Section 6.

# 2 STRUCTURE AND OSCILLATION PHENOMENON OF THE SYSTEM

The configuration of the investigated system is briefly introduced in this section. Then, time-domain simulation and spectrum analysis are conducted to analyze the potential electrical oscillation in the system.

## 2.1 System Configuration

The investigated system shown in Figure 1 contains three parts: the local grid with a thermal power unit, the DFIG-based wind farms, and the VSC-HVDC system. Both the thermal power and wind power are stepped up to 110 kV and then transmitted to the main grid via VSC-HVDC. In order to keep
the transmission power stable, the constant power control is used in the SEC, while the receiving-end converter (REC) regulates the DC voltage. Since the wind power is fluctuant while the thermal power is controllable, the local thermal power keeps the total output power constant by means of its self-regulation (Xiao et al., 2016; Yogarathinam et al., 2017; Shah et al., 2018). Besides, the thermal power unit also maintains the stable AC voltage and frequency on the point of common coupling (PCC).

Initially, the wind farms contain 200 DFIG wind turbines (WTs), which run in the maximum power point tracking mode (GE 1.5 MW). The capacity of the VSC-HVDC is 500 MW. Normally, it requires the thermal power unit to stabilize the fluctuation of full wind power with at least 40% rated loads. Therefore, the capacity of the local thermal power unit is set to 500 MW. To simplify the modeling, the equivalent aggregated model of the DFIG is adopted (Kunjumuhammed et al., 2017; Peng et al. 2020), since the impact of DFIG integration on the system is of concern instead of the internal action among the turbines.

### 2.2 Oscillation Phenomenon

A detailed simulation model of the system shown in Figure 1 is established with a switching device model of the VSC-HVDC in MATLAB/Simulink, associated with the SimPowerSystems Blockset. To investigate the potential oscillation risk existing in the system, two scenarios are analyzed. The system parameters are listed in Tables 1–3.

#### 2.2.1 Scenario 1: The Effect of the Outer Loop of the SEC

Initially, the wind farms are not integrated. The short circuit ratio (SCR) of the local grid is set to 6.8, and the output active power of VSC-HVDC is set to 0.5 pu. At \( t = 6 \) s, the SCR decreases to 5.1. To analyze the effect of the PQ-control outer loop of the SEC, the simulation models both with and without the outer loop are adopted. Figure 2A shows the active power response on the PCC.

Since the local grid strength decreases, it can be found that the oscillation emerges with the outer loop considered, while the system is still stable with the outer loop ignored. Thus, the outer loop of the SEC influences the system stability.

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**TABLE 1 | Parameters of VSC-HVDC.**

| Parameter            | Value  | Parameter | Value  |
|----------------------|--------|-----------|--------|
| VSC capacity         | 500 MW | Base MVA  | 100 MW |
| Rated voltage        | 110 kV | \( K_p/K_s \) | 0.2/50 pu |
| DC voltage           | 200 kV | \( K_p/K_s \) | 0.3/100 pu |
| DC capacitor         | 1,250 μF | \( K_p/K_s \) | 60/1,400 pu |
| \( L_1 \)            | 8 mH   | \( P_{ref}/Q_{ref} \) | 0.5/0 pu |

**TABLE 2 | Parameters of the DFIG.**

| Parameter | Value | Parameter | Value |
|-----------|-------|-----------|-------|
| Rated power | 1.5 MW | \( r_s \) | 0.0023 pu |
| Rated voltage | 690 V | \( L_s \) | 0.16 pu |
| Rated rotor speed | 1.2 pu | \( r_r \) | 0.0016 pu |
| DC capacitor | 0.01 F | \( L_r \) | 0.18 pu |
| DC voltage | 1150 V | \( L_m \) | 2.9 pu |

**TABLE 3 | Parameters of synchronous machine and AC line.**

| Parameter | Value | Parameter | Value |
|-----------|-------|-----------|-------|
| Rated capacity | 500 MW | \( T'_{d} \) | 8 s |
| Rated voltage | 13.8 kV | \( T'_{d} \) | 0.03 s |
| \( X_d \) | 1.8 pu | \( T'_{q} \) | 0.4 s |
| \( X_q \) | 1.7 pu | \( T'_{q} \) | 0.05 s |
| \( X_d \) | 0.3 pu | \( J_{eq} \) | 6.5 s |
| \( X_q \) | 0.55 pu | AC line length | 25 mile |
| \( X'_{d} \) | 0.25 pu | \( R_{line} \) | 0.1855 Ω/mile |
| \( X'_{q} \) | 0.25 pu | \( L_{line} \) | 1.685 mH/mile |
2.1 The wind speed at 10 m/s. At 

3.1 Impedance Model of the SEC With PLL and Current Loop

3.1.1 Model With PLL and Current Loop

In this section, the sequence impedance model of the sending-end converter (SEC) with the power loop and PLL is derived in detail before the integration and reduces to 142 Hz after the integration.

Therefore, the oscillatory behavior of the system can be influenced by the parameters’ variation of the SEC, the DFIG, and the local grid. The frequency region of the electrical oscillation is about 100–200 Hz, which belongs to MFO.

3 SYSTEM MODELING AND STABILITY CRITERION

In this section, the sequence impedance model of the sending-end converter (SEC) with the power loop and PLL is derived in detail with the explicit analytic expression and validated. Then, the impedance models of the DFIG and the local AC grid are built as well.

3.1 Impedance Model of the SEC With PQ-Control

3.1.1 Model With PLL and Current Loop

The control method of the SEC and REC is discussed in Section 2.1, and the control structure of the SEC is depicted in Figure 1.

Since the impact of the REC on the stability is small due to the isolation provided by the DC link, only the impedance model of the SEC needs to be built in detail. The control of the SEC is based on the dq reference frame, in which the d-axis is orientated to the PCC voltage $U_{pcc}$ and the q-axis leading the d-axis by 90°.

The voltage equation of the SEC in the dq frame is expressed as

$$
\begin{align*}
U_{pccd} &= U_{cd} - sL_{f} i_{cd} + \omega_{1} L_{f} i_{cq}, \\
U_{pccq} &= U_{cq} - sL_{f} i_{cq} - \omega_{1} L_{f} i_{cd},
\end{align*}
$$

(1)

where $U_{c}$ represents the output voltage and current of the SEC, the subscripts “$d$” and “$q$” denote the d-axis and q-axis components, $s$ is the differential operator, $\omega_{1}$ is the base frequency, and $L_{f}$ is the filter inductance of the converter. Note that the influence of the filter capacitance is ignored since it only dominates in the middle–high frequency region.

The small-signal model of the current controller of the SEC can be described as

$$
\begin{align*}
\Delta U_{cdref} &= H_{c}(s)(\Delta i_{cdref} - \Delta i_{cd}) + \Delta U_{pccd} - \omega_{1} L_{f} \Delta i_{cq}, \\
\Delta U_{cqref} &= H_{c}(s)(\Delta i_{cqref} - \Delta i_{cq}) + \Delta U_{pccq} + \omega_{1} L_{f} \Delta i_{cd},
\end{align*}
$$

(2)

where the subscript "ref" denotes the reference, and $K_{pc}$ and $K_{ic}$ are the proportional and integral gains of the current controller $H_{c}(s)$ of the SEC. According to the control loop, $U_{cd} = U_{cdref}$ and $U_{cq} = U_{cqref}$ when the DC dynamics is not considered since it is normally stable.

Then, the influence of the PLL dynamics is taken into account. PLL is used for phase tracking of $U_{pcc}$. However, when the system is disturbed by a small-signal perturbation, the phase $\theta$ of $U_{pcc}$ cannot be synchronized accurately and an angle deviation $\Delta \theta$ is introduced due to the PLL dynamics. Thus, the PLL output angle $\theta_{PLL}$ can be expressed as $\theta_{PLL} = \theta + \Delta \theta$. Therefore, the system has two dq frames during the transient process of PLL: one is the system dq frame defined by $\theta$, while the other is the controller dq frame defined by $\theta_{PLL}$. Note that the variables in the controller dq frame are denoted with the superscript "PLL." The small-signal disturbance relationship of the converter voltage and current between the two frames have been derived in detail in Wang et al. (2017), Wen et al. (2017), and Sun et al. (2019)

$$
\begin{align*}
\Delta U_{cdq}^{PLL} &= \Delta U_{cdq} + G_{pd}^{PLL}(s)\Delta U_{pccdq}, \\
\Delta I_{cdq}^{PLL} &= \Delta I_{cdq} + G_{ip}^{PLL}(s)\Delta U_{pccdq}.
\end{align*}
$$

(3)

For simplicity, the voltage and current are expressed in matrix form: $U_{cdq} = [U_{cd} U_{cq}]^{T}$, $i_{cdq} = [i_{cd} i_{cq}]^{T}$. The transfer function matrices $G_{pd}^{PLL}(s)$ and $G_{ip}^{PLL}(s)$ in Eq. 3 are defined as

![Figure 2](image-url) | Oscillation phenomenon. (A) The effect of the outer loop of the SEC. (B) The effect of the integration of the DFIG.
where $T_{pll} = H_{pll}(s)/(s + U_{pcc}H_{pll}(s))$ and $K_{pll}$ and $K_{i}^{pll}$ are the proportional and integral gains of the PLL controller $H_{pll}(s)$ of the SEC.

According to Figures 1–3, the small-signal circuit model of the SEC with the current loop and PLL is obtained and shown in the blue and green blocks of Figure 3. $G_{c}(s)$ is the transfer function matrix of the current controller, $G_{d}(s)$ is the transfer function of the decoupling gain of the current controller, and $G_{r}(s)$ represents the impedance of the filter

$$G_{c}(s) = \begin{bmatrix} H_{c}(s) & 0 \\ 0 & H_{d}(s) \end{bmatrix}, \quad G_{d}(s) = \begin{bmatrix} 0 & \omega_{1}L_{f} \\ -\omega_{1}L_{f} & 0 \end{bmatrix}, \quad G_{r}(s) = \begin{bmatrix} sL_{f} & -\omega_{1}L_{f} \\ \omega_{1}L_{f} & sL_{f} \end{bmatrix}.$$  (5)

According to Figure 3, the small-signal propagation path of the converter current in the current loop can be described as

$$\Delta I_{cdq} = \Delta I_{cdqref} - \left(\Delta I_{cdq} + G_{pll}(s)\Delta U_{pccdq}\right) \cdot \left(G_{c}(s) + G_{d}(s) - G_{pll}^{u}(s)\Delta U_{pccdq} - \Delta U_{pccdq}\right)G_{r}(s)^{-1}.$$  (6)

### 3.1.2 Model With Power Loop

Then, the influence of the outer loop with PQ-control is taken into account. The small-signal model of the power controller of the SEC can be described as

$$\begin{cases} \Delta I_{cdqref} = H_{c}(s)(\Delta P_{ref} - \Delta P) \\ \Delta I_{cdqref} = H_{c}(s)(\Delta Q_{ref} - \Delta Q) \end{cases}.$$  (7)

where $K_{ps}$ and $K_{is}$ are the proportional and integral gains of the power controller $H_{c}(s)$ of the SEC and $\Delta P_{ref}$ and $\Delta Q_{ref}$ are equal to zero in small-signal analysis since they are constant.

The small-signal analysis is applied to the measured power, which is defined as $P = U_{pcc}I_{cdq} + U_{pcc}I_{cq}$, $Q = U_{pcc}I_{cdq} - U_{pcc}I_{cq}$. Notice that the steady-state q-axis terminal voltage $U_{pcc} = 0$. Then, the small-signal reference current of the converter can be obtained as follows

$$\begin{cases} \Delta P = U_{pcc}I_{cdq} + \Delta U_{pcc}I_{cdq} + \Delta U_{pcc}I_{cq} \\ \Delta Q = U_{pcc}I_{cdq} + \Delta U_{pcc}I_{cdq} - \Delta U_{pcc}I_{cq} \end{cases}.$$  (8)

In addition, the small-signal propagation path of the power loop is depicted in the red block of Figure 3, where the power is expressed in matrix form: $\Delta \textbf{PQ} = [\Delta \textbf{P} \Delta \textbf{Q}]^T$. In combination with Eqs. 8, 9, the reference current can be derived as

$$\begin{cases} \Delta I_{cdqref} = -G_{c}(s)\left[U_{pcc}H_{i}(s) + G_{pll}^{u}(s)\Delta U_{pccdq}\right] + G_{r}(s)\left(U_{pcc}I_{cdq} + \Delta U_{pcc}I_{cdq}\right) \\ \Delta I_{cdqref} = -G_{c}(s)\left[U_{pcc}H_{i}(s) + \Delta U_{pcc}I_{cdq} - \Delta U_{pcc}I_{cq}\right] \end{cases}.$$  (9)

where $G_{c}(s)$ is the transfer function matrix of the power controller and $G_{r}(s)$ is the transfer function of the current

$$G_{r}(s) = \begin{bmatrix} H_{r}(s) & 0 \\ 0 & H_{s}(s) \end{bmatrix}, \quad G_{c}(s) = \begin{bmatrix} \ell_{c} & \omega_{c} \ell_{c} \end{bmatrix}.$$  (10)

Then, the dq impedance of the SEC seen from the terminal is derived by combining Eq. 6

$$Z_{sec}(s) = -\left[\left(1 + U_{pcc}G_{c}(s)\right)\left(G_{c}(s) + G_{d}(s) + G_{i}(s)\right)\right]$$

$$\cdot \left[1 + G_{c}(s)G_{c}(s)G_{i}(s) + \left(1 + G_{c}(s)G_{c}(s)G_{i}(s)\right)G_{pll}^{u}(s) + \left((1 + U_{pcc}G_{c}(s))G_{i}(s) + G_{d}(s)\right)G_{pll}^{i}(s)\right]^{-1}.$$  (11)

However, the expression of the dq impedance model of the SEC is hard to understand since it has four components and each one does not contain an explicit physical meaning. Instead, to
analyze intuitively and conveniently, the dq impedance model of the SEC can be transformed into the sequence impedance model by combining Eq. 12 and the following equation (Shah and Parsa, 2017)

\[
Z_{pq}(s) = \begin{bmatrix}
1 & 1 \\
-j & j
\end{bmatrix}^{-1} Z_{dq}(s) \begin{bmatrix}
1 & j \\
1 & -j
\end{bmatrix},
\]

where \(Z_{pq}(s)\) and \(Z_{dq}(s)\) represent the \(2 \times 2\) impedance matrixes in the sequence-domain and dq-domain. The \(2 \times 2\) sequence impedance of the SEC can be obtained by substituting \(Z_{vsc}(s)\) in Eq. 12 with \(Z_{dq}(s)\). The off-diagonal elements of \(Z_{pq}(s)\) represent the coupling between the positive and the negative components, and their influence on the stability is well analyzed in Zhang et al. (2019), which found that the model without off-diagonal elements is very close to the detailed model over the supersynchronous frequency range, whereas some over pessimistic conclusions are exhibited at the low-frequency range. Liu et al. (2021) also pointed out that the frequency coupling effect caused by the off-diagonal elements mainly influences the analysis of SSO. Therefore, the off-diagonal elements are not considered in this study since the investigated MFO region is much higher than the SSO range.

In addition, since the system stability under the balanced condition is considered in this study, the positive sequence impedance model of the SEC is derived as

\[
Z_{vsc}(s) = \frac{1}{1 - \frac{1}{j} \left[ H_{r}(s - j\omega_{1}) - j\omega_{1} L_{r} \right]} \left[ 1 + U_{pcd} H_{r}(s - j\omega_{1}) \right] + s L_{c}
\]

According to Eq. 14, the sequence impedance of the SEC has an explicit analytic expression, which is intuitive and convenient for further analysis. The denominator of Eq. 14 mainly reflects the impact of the PLL dynamics. \(I_{e}\) is a negative value as a converter. Obviously, the current loop is directly coupled with the power loop in the numerator and directly coupled with PLL in the denominator. The power loop and PLL are decoupled in the denominator in the condition that \(U_{pcd} = 0\), and thus, the measured power is the same in both the controller dq frame and the system dq frame.

### 3.2 Impedance Model of the DFIG

The control structure of the DFIG is depicted in Figure 1. The RSC regulates the power flow between the stator and the grid, while the grid-side converter (GSC) regulates the DC voltage. Some simplifications are taken into account. For instance, the influence of the GSC and the excitation branch can be ignored since their magnitudes are sufficiently large in the investigated frequency region. Then, the positive sequence impedance model of the DFIG can be expressed as (Miao, 2012; Vieto and Sun, 2018; Sun et al., 2019; Liu et al., 2021)

\[
Z_{dlq}(s) = \left( r_{s} + H_{r}(s - j\omega_{1}) - jK_{i} \right) \left[ S_{dq}(s) + s L_{s} + r_{s} + s L_{s} \right] + \left( 1 - \frac{1}{j} \left( r_{d} H_{r}(s - j\omega_{1}) \right) \left[ S_{dq}(s) + U_{lated} H_{r}(s - j\omega_{1}) \right] \right) T_{pll-rc}(s - j\omega_{1}),
\]

where \(K_{pr}\) and \(K_{ir}\) are the proportional and integral gains of the RSC current controller \(H_{r}(s)\). Besides, \(T_{pll-rc}(s) = H_{pll-rc}(s) / (s + U_{alted} H_{pll-rc}(s))\) and \(K_{pll-rc}\) is the proportional gain of the PLL controller \(H_{pll-rc}(s)\) of the RSC. The slip of the rotor is expressed as \(s_{imp}(s) = (s - j\omega_{1}) / s\) and \(\omega_{1}\) is the rotor angular frequency. The rotor slip is a variable and can be expressed as a transfer function with the frequency. Besides, definitions of other parameters in the equation are explained in the above references.

### 3.3 Impedance Model of the Local Grid

The impedance of the local grid includes the internal impedance of the synchronous generator (SG) and the line impedance. The line impedance is expressed as \(Z_{line}(s) = R_{line} + jX_{line}\). The impedance model of the SG has been well derived in Huang et al. (2021), which is expressed as

\[
\begin{align*}
Z_{d}(s) &= \omega_{1} \left( \begin{array}{c}
X_{d} - X_{d}^{r} \\
T_{d}s + a_{d}
\end{array} \right) G(s) + X_{d}^{r}
\end{align*}
\]

\[
\begin{align*}
Z_{q}(s) &= \omega_{1} \left( \begin{array}{c}
X_{q} - X_{q}^{r} \\
T_{q}s + a_{q}
\end{array} \right) G(s) + X_{q}^{r}
\end{align*}
\]

where \(G(s) = 1 / \left[ (T_{d} + T_{d}^{*})^{2} + (T_{d} + T_{d}^{*})s + 1 \right] \) and \(a_{d} = (X_{d} - X_{d}^{r}) / (X_{d}^{*} - X_{d}^{r})\), and \(a_{q} = (X_{q} - X_{q}^{r}) / (X_{q}^{*} - X_{q}^{r})\). \(\omega_{1}\) is the rotor speed and \(T_{d}, T_{q}, T_{d}^{*}, T_{q}^{*}\) are the transient and subtransient time constants in the dq frame. \(X_{d}, X_{d}^{*}, X_{q}, X_{q}^{*}\) are the stator, transient, and subtransient reactances in the dq frame. The positive sequence impedance of the SG is derived as \(Z_{aq}(s) = j(Z_{d}(s) + Z_{q}(s)) / 2\). Therefore, \(Z_{grid}(s) = Z_{line}(s) + Z_{aq}(s)\). However, according to the spectrum analysis in Section 2, the investigated MFO region of the system significantly exceeds the low-frequency oscillation, which refers to the dynamics of the generator sets (Baltas et al., 2021). Therefore, the influence of the generator on this new type of electrical oscillation can be ignored. In addition, \(Z_{aq}(s)\) is relatively small compared with \(Z_{line}(s)\) in the investigated MFO frequency region, especially when the local grid is weak (Wang et al., 2019). Therefore, only the impedance of the transmission line is considered in this study for the sake of simplified analysis. That is, \(Z_{grid}(s) = Z_{line}(s)\). This assumption is proved in the next part.

### 3.4 Validation and Stability Criterion

The verification and comparison of the impedance models are performed in Figure 4. The positive sequence impedance of the SEC is measured from 0 to 200 Hz with 40 points using the method of injecting current. A perturbation current (about 2% of rated current) at the measured frequency points is applied, and the related voltage is measured. The harmonic voltage and current in different frequency points are analyzed using the DFT tool. Thereby, the impedance is calculated by dividing the voltage by current at each frequency point. The operating condition for the measurement is given in Tables 1–3. As shown in Figures 4A,B, the impedance models of the SEC with or without PQ-control have been validated. It can be found that the PQ-control will influence the magnitude of \(Z_{vsc}(s)\) and the frequency of the abrupt change in the phase angle. As shown in Figure 4C, compared with the impedance
model of the power inverter derived in Zhou et al. (2020), the risk region of the SEC is shifted from the supersynchronous frequency region to the MFO region. As shown in Figure 4D, the impedance of the SG can be ignored in the MFO region due to its magnitude being relatively small compared with $Z_{\text{line}}(s)$ with $\text{SCR} = 5.1$ in the MFO region.

Thereby, to investigate the interaction mechanism, the equivalent circuit model of the whole system is established by aggregating the impedance model of each part on the PCC, as shown in Figure 5A. In small-signal impedance analysis, the local grid is normally modeled as the Thevenin equivalent circuit, while the SEC and the wind farm are modeled as the Norton equivalent circuit. According to the impedance network model proposed in Liu et al. (2018), the system impedance can be aggregated by the parallel connection of wind farm impedance and AC grid impedance and then connected in series with SEC impedance. As shown in Figure 5B, the resistance and reactance of the whole system can be represented by the sum of each part.

The stability criterion based on the second-order RLC series resonance circuit has been widely applied in recent works (Miao, 2012; Liu and Xie, 2017) to assess the system stability. By combining with this criterion and the derived impedance models, the oscillation mechanism and characteristics can be
analyzed based on the impedance-frequency curves, which are more intuitive and convenient compared with the Nyquist plots

\[
\begin{align*}
R_{\text{sys}} &= \text{Re}\left(Z_{\text{dfig}}(s) + Z_{\text{vsc}}(s)\right) > 0 \\
X_{\text{sys}} &= \text{Im}\left(Z_{\text{dfig}}(s) + Z_{\text{vsc}}(s)\right) = 0,
\end{align*}
\]

where \( R_{\text{sys}} \) and \( X_{\text{sys}} \) are the system resistance and reactance.

4 MECHANISM AND CHARACTERISTICS ANALYSIS

According to the equivalent impedance model of the system built in Section 3, the impact of local grid strength and SEC parameters on the system stability are analyzed in this section. Then, the effect of the integration of the DFIG-based wind farms on the oscillation is analyzed as well.

4.1 Local Grid Strength

To analyze the impact of local grid strength on the electrical oscillation, the variation of the SCR from 5.1 to 1.7 is analyzed under the same operating condition as Scenario 1 in Section 2.2 without the SEC outer loop and the integrated DFIG. The impedance-frequency curves of the grid and the SEC are depicted in Figure 6A. It can be found that both the resistance and reactance of the SEC follow a parabolic trend increase with the negative value. Note that in Figure 6A and the following figures, the grid reactance is represented by its opposite number \(-X_{\text{grid}}\) to better find out the frequency intersection point, while the grid resistance is still \( R_{\text{grid}} \). According to Eq. 17, when the system equivalent reactance \( X_{\text{sys}} = 0 \) (\( X_{\text{vsc}} = -X_{\text{grid}} \)), the system will be stable when the system equivalent resistance \( R_{\text{sys}} = R_{\text{vsc}} + R_{\text{grid}} > 0 \). When reducing the SCR of the local grid, the grid resistance follows a small increase, while the grid reactance follows a significant increase and thus the intersection point for \( X_{\text{vsc}} = -X_{\text{grid}} \) is shifted to the left, which means the resonant frequency of the system is reduced. Then, the system equivalent resistance (\( R_{\text{sys}} = R_{\text{vsc}} + R_{\text{grid}} \)) changes from positive to negative at the corresponding resonant frequency. In Figure 6A, the system equivalent resistance decreases from \( R_{\text{sys}1} \) to \( R_{\text{sys}3} \). Therefore, the system stability is weakened with the reduction of the SCR. The fast uptrend of \( R_{\text{vsc}} \) and \( X_{\text{vsc}} \) in the investigated MFO region plays a key role in the oscillation, while the reduction of the SCR has an incentive effect.

4.2 Controller Parameters of the SEC

4.2.1 PLL Parameters of the SEC

According to Eq. 14, since the power loop is directly coupled with the current loop instead of PLL in the numerator, the term \((1 + H_{1}(s - j\omega_{1}))\) can be regarded as a disturbance term to the current loop and thus can be ignored in the analysis of the PLL parameters. Besides, the decoupling term can also be ignored for its small influence. Then, the impedance model of the SEC can be simplified as

\[
Z_{\text{vsc}}(s) = -\frac{H_{1}(s - j\omega_{1}) + sL_{1}}{1 - \frac{1}{2}\left(I_{c}H_{1}(s - j\omega_{1}) + U_{\text{grid}}\right)T_{\text{PLL}}(s - j\omega_{1})}
\]

The numerator of Eq. 18 represents the impedance of the SEC without considering the influence of PLL. According to Eq. 19, it

FIGURE 6 | Impact of SCR on the impedance-frequency curves. (A) \( Z_{\text{vsc}}(s) \) and \( Z_{\text{grid}}(s) \) and impact of \( K_{\text{pPLL}} \) on the impedance-frequency curves. (B) \( Z_{\text{grid}}(s) \); (C) \( Z_{\text{vsc}}(s) \); and (D) \( Z_{\text{vsc}}(s) \) and \( Z_{\text{grid}}(s) \).
can be found that the resistance of the SEC will be positive without the influence of PLL since \(K_{pc} > 0\). The phase angle of \(Z_{\text{rec,nonp}}(s)\) will be less than \(-90^\circ\) because of the existence of the resistance term \(K_{pc}\)

\[
Z_{\text{rec,nonp}}(s) = K_{pc} - \left( \frac{K_c}{\omega - \omega_1} \right).
\]  

(19)

Then, the denominator of Eq. 18 which represents the influence of PLL is analyzed. Since \(K_{ppl} \gg K_{pc}/(\omega - \omega_1)\) in the investigated MFO region, the integral gain of PLL can be ignored. Besides, \(U_{pcd} = 1\) is considered in the further analysis. Thus, the denominator of Eq. 18 can be simplified as

\[
Z_{ppl}(s) = \frac{1}{2} \frac{K_{ppl}^2(1 - i, K_{pc}) + K_{ppl} i, K_{pc} + 2(\omega - \omega_1)^2}{K_{ppl}^2 + (\omega - \omega_1)^2} + \frac{1}{2} \frac{K_{ppl}^2 i, K_{pc} + K_{ppl}(1 + i, K_{pc})(\omega - \omega_1)^2}{(\omega - \omega_1) \left[ K_{ppl}^2 + (\omega - \omega_1)^2 \right]},
\]

(20)

\[
\angle Z_{ppl}(s) = \arctan \frac{K_{ppl}^2 i, K_{pc} + K_{ppl}(1 + i, K_{pc})(\omega - \omega_1)^2}{(\omega - \omega_1) \left[ K_{ppl}^2 + (\omega - \omega_1)^2 \right]}.
\]

(21)

According to Eq. 20, both \(\text{Re}(Z_{ppl}(s))\) and \(\text{Im}(Z_{ppl}(s))\) follow a parabolic trend increase with the increase of \(K_{ppl}\). Therefore, the magnitude of \(Z_{ppl}(s)\) has a positive correlation with \(K_{ppl}\). According to Eq. 21, since the coefficients of \(K_{ppl}^2\) and \(K_{ppl}\) in the numerator are larger than the denominator, \(\angle Z_{ppl}(s)\) also has a positive correlation with \(K_{ppl}\). By contrast, \(\angle Z_{ppl}^{-1}(s)\) has a decreasing trend and may cause the phase angle of \(Z_{\text{rec}}(s)\) to be over \(-90^\circ\) in the investigated MFO region. Therefore, it can be found that the negative damping effect of the SEC is mainly caused by the influence of PLL.

The increase of \(K_{ppl}\) from 20 to 120 pu is analyzed under the same operating condition as in Section 4.1, with SCR = 1.7. The corresponding influence on the impedance-frequency curves of \(Z_{ppl}(s)\) and \(Z_{\text{rec}}(s)\) is illustrated in Figures 6B,C. Obviously, the variational tendency of the curves aligns with the above analysis. In addition, both the resistance and reactance-frequency curves of the SEC and the local grid are depicted in Figure 6D. With the increase of \(K_{ppl}\), the resistance of the SEC changes from positive to negative, while the negative value of the reactance of the SEC becomes smaller. Thus, the system resonant frequency is reduced, and the system resistance at the corresponding resonant frequency decreases from \(R_{s1}\) to \(R_{s3}\), which is changed from positive to negative. That means the larger the \(K_{ppl}\) the more significant the negative effects of PLL on the system stability.

### 4.2.2 Current Loop Parameters of the SEC

According to Eq. 18, \(Z_{\text{rec}}(s)\) can be expressed as the quotient of \(Z_{\text{rec,nonp}}(s)\) and \(Z_{ppl}(s)\), and the influence of the SEC current loop is represented in both of them. First, the influence of \(K_{pc}\) is analyzed. According to (19), with \(K_{pc}\) increases nearby the critical points of the oscillation, \(\text{Re}(Z_{\text{rec,nonp}}(s))\) is increased and thus the strength of the system damping while \(\text{Im}(Z_{\text{rec,nonp}}(s))\) remains unchanged. According to Eq. 20 and Eq. 21, \(\text{Re}(Z_{ppl}(s))\) decreases linearly while \(\text{Im}(Z_{ppl}(s))\) increases linearly with the increase of \(K_{pc}\), which means \(\angle Z_{ppl}(s)\) becomes more positive while \(\angle Z_{ppl}^{-1}(s)\) becomes more negative. Thus, the influence of \(K_{pc}\) on \(Z_{\text{rec,nonp}}(s)\) and \(Z_{ppl}(s)\) is opposite. However, the influence of \(K_{pc}\) on \(Z_{ppl}(s)\) is represented in the term \((L, K_{pc} + 1)\) and thus the variation of \(K_{pc}\) has small influence on this term. Therefore, the influence of \(K_{pc}\) is mainly reflected in \(Z_{\text{rec,nonp}}(s)\) instead of \(Z_{ppl}(s)\).

The increase of \(K_{pc}\) from 0.25 to 0.35 pu is analyzed under the same operating condition as in Section 4.1, with SCR = 1.7 and \(K_{ppl} = 60\) pu. The corresponding influence on the impedance-frequency curves of \(Z_{\text{rec,nonp}}(s)\), \(Z_{ppl}(s)\) and \(Z_{\text{rec}}(s)\) is illustrated in Figures 7A–C. In addition, both the resistance- and reactance-frequency curves of the SEC and the local grid are depicted in Figure 7D. According to Figure 7C, with \(K_{pc}\) increases nearby the critical points of the oscillation, \(\angle Z_{\text{rec}}(s)\) changes from more than \(-90^\circ\) to less than \(-90^\circ\), which means that the resistance of the SEC changes from negative to positive. Correspondingly, the reactance of the SEC changes very little since the sine function is around the peak zone. Thus, the system resistance at the corresponding resonant frequency becomes positive. In Figure 7D, the system equivalent resistance increases from \(R_{s1}\) to \(R_{s3}\) due to the increase of SEC resistance with \(K_{pc}\), which also validates the above analysis. Hence, the system stability has a positive correlation with \(K_{pc}\).

In addition, the influence of \(K_{ic}\) is analyzed. According to Eq. 19, with \(K_{ic}\) increases, \(\text{Im}(Z_{\text{rec,nonp}}(s))\) is decreased while \(\text{Re}(Z_{\text{rec,nonp}}(s))\) remains unchanged. By comparison with that, the influence of \(K_{ic}\) on \(Z_{ppl}(s)\) can also be ignored and thus \(Z_{ppl}^{-1}(s)\) is regarded as constant. Note that \(\text{Re}(Z_{\text{rec,nonp}}(s))\) remains unchanged during the variation of \(K_{ic}\); thus, the resistance of the SEC is only determined by \(Z_{ppl}^{-1}(s)\) which can be expressed as \(Z_{ppl}^{-1}(s) = R_{ppl} - jX_{ppl}\), where \(R_{ppl}, X_{ppl} > 0\). Therefore, \(Z_{\text{rec}}(s)\) can be expressed as

\[
Z_{\text{rec}}(s) = [H_{s}(s - j\omega_1) + sL_{f}](R_{ppl} - jX_{ppl})
\]

\[
= R_{ppl}K_{pc} - X_{ppl}\left( \frac{K_{c}}{\omega - \omega_1} - \omega L_{f} \right) - \left[ X_{ppl}K_{pc} + R_{ppl}\left( \frac{K_{c}}{\omega - \omega_1} - \omega L_{f} \right) \right].
\]

(22)

According to Eq. 22, the increase of \(K_{ic}\) leads to the decrease of both the resistance and reactance of the SEC and thus causes the higher system resonant frequency. Then, the system resistance at the corresponding resonant frequency is reduced. Thus, the system stability has a negative correlation with \(K_{ic}\).

### 4.2.3 Power Loop Parameters of the SEC

The term \((1 + H_{s}(s - j\omega_1))\) is reconsidered when focusing on the effect of the power loop of the SEC. As discussed before, only the influence of the power loop on \(Z_{\text{rec,nonp}}(s)\) needs to be analyzed since it has no effect on \(Z_{ppl}(s)\)

\[
Z_{\text{rec,nonp}}(s) = H_{s}(s - j\omega_1)\left( 1 + H_{s}(s - j\omega_1) \right) + sL_{f}
\]

\[
= K_{pc}\left( 1 + K_{ppl} \right) - \frac{K_{c}K_{a}}{(\omega - \omega_1)^2} - \left( \frac{K_{c}\left( 1 + K_{ppl} \right) + K_{ppl}K_{pc}}{\omega - \omega_1} - \omega L_{f} \right).
\]

(23)
Obviously, the influence of the outer loop and inner loop on the impedance of the SEC is coupled directly. Considering \((\omega - \omega_l)^2 K_{pc} K_{ps} > K_{ac} K_{ai}\) at the investigated MFO region, it can be found that the resistance of the SEC is improved slightly due to the consideration of the power loop at the same frequency. However, the reactance of the SEC becomes more negative and thus the resonant frequency point is shifted to the right, and the system resistance is reduced at the new resonant frequency. Then, further analysis of the interaction between the current loop and the power loop is provided. According to Eq. 23, when \(K_{pc}\) or \(K_{ps}\) increases, the resistance of the SEC becomes more positive while the reactance becomes more negative, and thus, the system stability is improved. \(K_{ps}\) increasing from 0.05 to 0.35 pu is analyzed under the same operating condition as in Section 4.1, with the SEC outer loop and SCR = 5.1. The corresponding influence on the resistance- and reactance-frequency curves of the SEC and the local grid is illustrated in Figure 8A. The system equivalent resistance increases from \(R_{sys1}\) to \(R_{sys3}\) due to the increase of SEC resistance with \(K_{ps}\). The variational tendency of the curves aligns with the above analysis.

In addition, when \(K_{ac}\) or \(K_{ai}\) increases, both the resistance and reactance of the SEC become more negative. Note that the reduction of the reactance is in favor of the system stability, while the reduction of the resistance has an opposite effect. Since \((\omega - \omega_l) > (1 + K_{pc})K_{ai}\), the variation of the resistance is larger than that of the reactance with \(K_{ai}\) increases, which means that the system resistance at the corresponding resonant frequency becomes negative and thus weakens the system stability. Likewise, the influence of \(K_{ac}\) is similar to that of \(K_{ai}\). Besides, the coefficient of \(K_{pc}\) and \(K_{ac}\) in Eq. 23 is \((1 + K_{ps})\), while the coefficient of \(K_{ps}\) and \(K_{ai}\) is \(K_{pc}\). Thus, the influence of the power loop is lower than that of the current loop because \((1 + K_{ps}) > K_{pc}\). This analysis is verified by comparing Figures 8A,B, which show that the influence of \(K_{pc}\) ranges from 0.2 to 0.3 pu, with \(K_{ps} = 0.2\) pu.

### 4.3 Operating Condition and Controller Parameters of the DFIG

#### 4.3.1 Number of Grid-Connected DFIG-WTs

The integration of the DFIG-based wind farms is not considered in the above subsections when focusing on the influence of the control parameters of the SEC. In this part, the effect of the number of grid-connected DFIG-WTs on the system stability is analyzed. According to Eq. 15, since the PLL dynamics of the DFIG mainly reflects the nearby fundamental frequency (Vieto and Sun, 2018; Sun et al., 2019), it can be neglected during the analysis in the investigated MFO region. Besides, \(K_{pr}\) is dominant in the RSC current control loop, while \(K_{sr}\) is less important and can be ignored in the following analysis. Thus, the equivalent resistance and reactance of the multiple DFIG-WTs can be simplified as (Sun et al., 2019)

\[
R_{dfig} = \frac{r_s + K_{sr}}{nS_{np}} + \frac{r_s}{n}, \quad X_{dfig} = \frac{\omega (L_s + L_r)}{n}, 
\]

where \(n\) represents the number of DFIG-WTs. According to Eq. 24, the DFIG represents as a resistance-inductance since the rotor slip \(S_{np} = (s - j\omega_l)/s\) is positive in the investigated MFO region. It is contrary to the SSO mechanism that the DFIG represents as an inductance in series with a negative resistance in the SSO.
frequency region. Compared with SSO caused by the DFIG, the frequency of the electrical oscillation studied in this article is about 100–200 Hz, which is a kind of MFO. The negative damping of the system is induced by the SEC instead of the DFIG, which means that the mechanism of this electrical oscillation is totally different from that of SSO caused by the DFIG (Sun et al., 2019).

According to Figure 5, the integration of the DFIG is equivalent to inserting an impedance on the PCC, which is in parallel with the local grid. The parallel impedance is still resistive and inductive in the investigated MFO region, which can be expressed as

\[ Z_{\text{dfig}}(s)|Z_{\text{grid}}(s) = \frac{-X_{\text{grid}}X_{\text{dfig}} + jX_{\text{grid}}R_{\text{dfig}}}{R_{\text{dfig}} + j(X_{\text{grid}} - X_{\text{dfig}})} = \frac{X_{\text{grid}}X_{\text{dfig}} + jR_{\text{dfig}} + X_{\text{grid}}R_{\text{grid}} + X_{\text{dfig}}^2}{R_{\text{dfig}} - (X_{\text{grid}} + X_{\text{dfig}})^2}, \] (25)

where \( R_{\text{grid}} \) is neglected due to its small value. According to Eq. 25, since \( X_{\text{dfig}} \gg R_{\text{dfig}} \) in the investigated MFO region, the reduction of \( \text{Re}(Z_{\text{dfig}}(s)|Z_{\text{grid}}(s)) \) is much larger than \( \text{Im}(Z_{\text{dfig}}(s)|Z_{\text{grid}}(s)) \) with the increase of \( n \). Thus, the resonant frequency point of the system shifts to the right, and the corresponding system resistance changes from negative to positive. The influence of \( n \) increasing from 180 to 220 is analyzed in Figure 9A under the same operating condition as Scenario 2 in Section 2.2, with SCR = 1.7. The system equivalent resistance increases from \( R_{\text{sys1}} \) to \( R_{\text{sys3}} \) due to the increase of SEC resistance with frequency. It can be concluded that the larger the \( n \) nearby the critical points of the oscillation, the more positive the effect of DFIG-WTs on the system stability. In addition, since \( R_{\text{dfig}} \) and \( X_{\text{dfig}} \) are the inverse proportional function of \( n \), the influence of \( n \) on the system is significant when \( n \) is not large.

4.3.2 Wind Speed
According to Eq. 24, the resistance of the DFIG can be expressed as \( (r_r + K_{pr})/nS_{\text{lip}} + r_r/n \). Contrary to the SSO mechanism of the DFIG, the rotor slip \( S_{\text{lip}} = (s - j\omega_t)/s \) is positive in the investigated MFO region. With the increase of the wind speed, the rotor speed \( \omega_t \) is increased while the rotor slip decreases. Thus, the rotor resistance \( (r_r + K_{pr})/nS_{\text{lip}} \) is larger when the wind speed is high, which means that the DFIG will provide more positive damping for the whole system. The influence of wind speeds in the range of 9.5–10.5 m/s on the system impedance is analyzed in Figure 9B. The system equivalent resistance changes from \( R_{\text{sys1}} \) to \( R_{\text{sys3}} \), which is increased slightly due to the increase of \( \text{Re}(Z_{\text{dfig}}(s)|Z_{\text{grid}}(s)) \) with wind speed. Therefore, the increase of the wind speed leads to better system stability.

4.3.3 Current Loop Parameters of the RSC
With the increase of \( K_{pr} \), the rotor resistance \( (r_r + K_{pr})/nS_{\text{lip}} \) is increased directly since the rotor slip is positive in the investigated MFO region, which means that the DFIG will provide more positive damping for the whole system when \( K_{pr} \) is relatively large. The influence of \( K_{pr} \) in the range of 0.45–0.75 pu on the system impedance is analyzed in Figure 9C. The system equivalent resistance changes from \( R_{\text{sys1}} \) to \( R_{\text{sys3}} \), which is increased slightly due to the increase of \( \text{Re}(Z_{\text{dfig}}(s)|Z_{\text{grid}}(s)) \) with \( K_{pr} \). Therefore, the influence of \( K_{pr} \) is similar to that of the wind speed, which is helpful for the system stability.

4.3.4 PLL Parameters of the DFIG
In this part, the influence of PLL parameters of the DFIG is reconsidered based on the impedance model in Eq. 15. The influence of \( K_{\text{ppll}} \) increasing from 20 to 120 pu on the system impedance is illustrated in Figure 9D. Note that when the bandwidth of PLL increases with \( K_{\text{ppll}} \), the equivalent resistance of the system increases slightly from \( R_{\text{sys1}} \) to \( R_{\text{sys3}} \), while the reactance almost remains unchanged in the investigated MFO region. By combining Figures 9A–D, the change of the system impedance caused by the PLL parameters of the DFIG is much smaller than other factors. Thus, it is not an efficient method to improve the system stability by adjusting \( K_{\text{ppll}} \) of the DFIG.

5 SIMULATION VERIFICATION
The impact of the controller parameters and the operating condition of the system on the oscillation are investigated in
Section 4. The time-domain simulations are conducted to validate the correctness of the theoretical analysis.

5.1 Local Grid Strength
The impact of the local grid strength is analyzed under the following operating condition: the number of grid-connected DFIG-WTs is 200, the wind speed is 10 m/s, SCR of the local grid is 3.4, and other system parameters are the same as in Section 2.2. Figure 10A shows the output active power responses on the PCC for a sudden change of the SCR from 1.2 to 2.5 at $t = 6$ s. Obviously, the oscillation state changes from convergent to divergent with the decrease of the SCR. Therefore, the system stability has a positive correlation with the local grid strength. The simulation results align with the analysis in Section 4.1.

FIGURE 9 | Impact of different factors on $Z_{vsc}(s)$ and $Z_{grid}(s)$. (A) $n$; (B) wind speeds; (C) $K_{pr}$; and (D) $K_{pall}$.

FIGURE 10 | Output power varies with (A) SCR, (B) $K_{pr}$, (C) $K_{pall}$, and (D) $K_{pall}$. 
5.2 Control Parameters of the SEC
The impact of the PLL, current loop, and power loop of the SEC on the oscillation characteristics is analyzed under the same operating condition as in Section 4.1, with a sudden change of the SCR from 3.4 to 1.7 at $t = 6$ s. As shown in Figure 10B, the output active power responses on the PCC with $K_{p_{PLL}}$ in the range of 20–120 pu are measured. With $K_{p_{PLL}}$ increases, the oscillation state changes from convergent to invariable, which means that the oscillation is intensified when $K_{p_{PLL}}$ is large. As can be seen in Figure 10C, the oscillation state changes from divergent to convergent when $K_{pr}$ increases from 0.2 to 0.3 pu, which means that the system stability has a positive correlation with $K_{pr}$. Figure 10D illustrates the simulation results when $K_{p_{PLL}} - r_{sc}$ changes from 20 to 120 pu. When $K_{p_{PLL}} - r_{sc}$ increases, the divergence speed of the oscillation becomes slightly slower. It can be found that $K_{p_{PLL}} - r_{sc}$ has little influence on the oscillation compared with other parameters. In conclusion, the above simulation results align with the analysis in Section 4.3.

5.3 Operating Condition and Controller Parameters of the DFIG
The impact of $n$, wind speed, RSC current loop, and PLL of the DFIG on the oscillation characteristics is analyzed under the same operating condition as in Section 5.2, with other control parameters of the SEC kept unchanged. Figure 11A shows that the output active power responds to the number of grid-connected DFIG-WTs, which ranges from 180 to 220. Obviously, the oscillation is gradually suppressed while the $n$ increases. As shown in Figure 11B, the simulation results with the wind speed changes from 9.5 to 10.5 m/s are depicted. It can be observed that the system stability has a positive correlation with the wind speed. The output active power responses on the PCC with $K_{pr}$ from 0.45 to 0.75 pu are depicted in Figure 11C. As can be seen, the oscillation state changes from divergent to convergent when $K_{pr}$ increases. Figure 11D illustrates the simulation results when $K_{p_{PLL}} - r_{sc}$ changes from 20 to 120 pu. When $K_{p_{PLL}} - r_{sc}$ increases, the divergence speed of the oscillation becomes slightly slower. It can be found that $K_{p_{PLL}} - r_{sc}$ has little influence on the oscillation compared with other parameters. In conclusion, the above simulation results align with the analysis in Section 4.3.

6 CONCLUSION
A SEC with PQ-control faces the potential risk of a new type of electrical oscillation in the medium-frequency region when it transmits the wind-thermal–bundled power from the local grid and DFIG-based wind farms. In this study, the sequence impedance model of the SEC with the explicit analytic expression is derived to help understand the mechanism and characteristics of MFO. This oscillation mainly originates from the SEC, while the local AC grid strength and the integration of DFIG-based wind farms also have an influence on it. With regard to the SEC controllers, the system stability is weakened with the increased proportional gain of the PLL controller. In addition, system stability has a positive correlation with the proportional gains of both the current loop and the power loop. However, the influence of the power loop is not as large as that of the current loop. Regarding the influence of the local grid strength, the oscillation risk increases when the local grid is weak. Contrary to the SSO mechanism of the DFIG, the risk of this oscillation decreases when relatively more DFIG-WTs are integrated at a high wind speed. Besides, system stability has a positive correlation with the proportional gains of the RSC current loop, while the influence of PLL of the RSC is slight. In conclusion, these factors can influence the system resistance at the resonant frequency, and the oscillation may appear due to the negative damping under the undesired operating condition.
conditions and controller parameters of the DFIG, local grid, and SEC. All the theoretical analyses are verified by time-domain simulation.

DATA AVAILABILITY STATEMENT
The original contributions presented in the study are included in the article/Supplementary Material; further inquiries can be directed to the corresponding author.

AUTHOR CONTRIBUTIONS
KS and WY contributed to the conception and design of the study. CY and JW organized case studies. KS wrote the first draft of the manuscript. All authors contributed to manuscript revision and read and approved the submitted version.

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**Conflict of Interest:** The authors declare that the research was conducted in the absence of any commercial or financial relationships that could be construed as a potential conflict of interest.

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