Analysis and Design of Analog Fountain Codes for Short Packet Communications

Wen Jun Lim, Graduate Student Member, IEEE, Rana Abbas, Member, IEEE, Yonghui Li, Fellow, IEEE, Branka Vucetic, Life Fellow, IEEE, Mahyar Shirvanimoghaddam, Senior Member, IEEE

Abstract—In this paper, we focus on the design and analysis of the Analog Fountain Code (AFC) for short packet communications. We first propose a density evolution (DE) based framework, which tracks the evolution of the probability density function of the messages exchanged between variable and check nodes of AFC in the belief propagation decoder. Using the proposed DE framework, we formulate an optimisation problem to find the optimal AFC code parameters, including the weight-set, which minimises the bit error rate at a given signal-to-noise ratio (SNR). Our results show the superiority of our AFC code design compared to existing designs of AFC in the literature and thus the validity of the proposed DE framework in the asymptotically long block length regime. We then focus on selecting the precoder to improve the performance of AFC at short block lengths. Simulation results show that lower precoder rates obtain better realised rates over a wide SNR range for short information block lengths. We also discuss the complexity of the AFC decoder and propose a threshold-based decoder to reduce the complexity.

Index Terms—Analog fountain code (AFC), density evolution, differential evolution optimisation, rateless codes.

I. INTRODUCTION

The third generation partnership project (3GPP) has defined three primary service categories for the fifth generation (5G) of mobile communications [1]. These include enhanced mobile broadband (eMBB), ultra-reliable and low-latency communications (URLLC), and massive machine type communications (mMTC). 5G aims at providing a higher data rate (e.g., up to 10 Gbps for eMBB), shorter end-to-end latency (e.g., ≤ 1 ms for URLLC), higher reliability (e.g., packet error rate (PER) of less than 10−5 for URLLC) and higher energy efficiency (e.g., up to 10 years operation with a single battery for mMTC) [2]. Many new techniques have been proposed to meet these requirements, e.g., massive multiple-input-multiple-output (mMIMO), millimetre wave (mmW) communications, non-orthogonal multiple access (NOMA), short-packet communications, etc. [3], [4]. For example, the authors in [5] demonstrated that NOMA has superior performance in terms of fairness and spectral efficiency compared to orthogonal multiple access (OMA) for low latency downlink in the short packet scenario. In [6], the authors derived the expression for the minimum channel reciprocity that enables the uplink channel training to achieve a higher data rate than the downlink channel training. Results showed that the advantage of uplink channel training in short packet communication with multiple transmit antennas as the channel reciprocity coefficient decreases with the decrease of the block length or the increase of the number of transmit antennas. The authors in [7] proposed the optimal resource allocation to maximize the average data rate in the MIMO systems, which adopts short packet communications. With the emergence of new applications and services, such as extreme URLLC, and entering the era of Beyond 5G (B5G) and eventually moving towards the 6th generation (6G) of wireless communications standard, fundamental breakthroughs in the way we design communications systems are vital in order to meet the requirements of emerging applications.

The channel code design has been always at the heart of any communications standards and still remains an essential part to unleash the full potential of 5G New Radio (NR) services, namely, to guarantee the required latency and reliability requirements at maximum efficiency [2]. It has been shown that by using shorter transmission-time interval (TTI), lower latency can be achieved, which implies using short packets [8]. This is of particular importance for resource-constraint settings, where the available bandwidth, power, and hardware resources, such as antennas, are limited. The design of efficient and robust short packet communications is vital for 5G mMTC and URLLC applications, where the data transferred might be significantly smaller than traditional human-based communications, ranging from a few hundred bits down to a few tens [9], [10]. Modern channel coding techniques have diverged away from the traditional theories of Shannon due to this new shift alongside other challenges imposed by the 5G services, e.g., higher reliability requirements (lower than 10−5) and higher energy efficiency. We refer the readers to the survey paper [8] for a comprehensive review of modern channel coding techniques. In particular, there is a large consensus that traditional channel codes, adopted in current cellular networks, are strictly sub-optimal for short packet communications [8].

The conventional channel coding methods cannot fulfill the requirements of B5G and future 6G, such as large coding...
gain, low-complexity, low-latency, high-throughput, bit-level granularity, and flexible code parameters. Flexibility is required to provide different levels of reliability and latency at various block lengths. On the other hand, bit-level granularity implies ease of generating various rates and lengths with negligible performance degradation. The conventional adaptive modulation and coding which relies on the channel state information (CSI) feedback from the receiver is not suitable for short packet communications due to the significant overhead. Moreover, the signalling is estimated to incur 5–8 ms latency which violates the low latency requirement of URLLC.

self-adaptive channel codes can be effectively used to reduce the end-to-end latency without any CSI at the transmitter side. Existing self-adaptive codes, also known as rateless codes, are either binary in nature with no straight-forward extension to adaptive modulation, or they exhibit significant performance degradation in the finite block length regime. In this paper, we focus on the design of efficient self-adaptive codes for short packet communications, i.e., for message lengths from only a few (≤100) bits up to a few hundred (≤1000) bits.

A. Related Works

The original research on rateless codes, such as LT codes and Raptor codes, have shown that rateless codes can achieve the channel capacity over binary erasure channels. However, all design extensions of rateless codes to noisy channels, e.g., additive white Gaussian noise (AWGN) channel, have shown to be channel dependent. To the best of our knowledge, only a few rateless codes to date are near capacity-achieving over a wide range of signal to noise ratios (SNRs) for an asymptotically long block length. This includes Analog Fountain Codes (AFC) and Spinal codes. These codes, however, show a significant gap to the capacity at short block lengths.

In rateless codes, the rate is determined on the fly and without the need for CSI at the transmitter side. This is particularly favourable in fast varying channels as well as non-reciprocal channels. Recent studies on short AFC have shown impressive results for 5G URLLC in terms of comparable latency to the Polyanskiy-Poor and Verdu (PPV) normal approximation, as well as reliability down to 10^-7. However, the proposed encoding scheme therein, as well as the weight-set design, are based on heuristics rather than a solid analytical framework. In this paper, we aim to bridge this gap in the literature by providing a solid analytical framework and an optimisation framework for AFC. To the best of our knowledge, the only work that has attempted to do so can be found in [20]. Authors in [20] proposed a modified weight selection scheme for short block length AFC. In particular, the extrinsic information transfer (EXIT) chart analysis was modified in [20] to address the issue of performance degradation of EXIT analysis in the finite block length regime. However, the design in [20] relies on approximate densities and the assumption of symmetric Gaussian distribution for the messages exchanged in each iteration of the belief propagation (BP) decoder. These assumptions are not accurate, particularly at low rates. This negatively impacts the optimal code ensembles’ search.

The authors in [23], [24] proposed a weight selection strategy according to channel condition, for reduced complexity, without loss of performance. Several other works mainly focused on the decoder design, where different approaches were proposed to reduce the complexity of the message passing decoder of AFC. The design of AFC weight set in all these works were mostly heuristic and did not provide meaningful insights into the code design for short block lengths. Furthermore, the performance of AFC at short block lengths depends on the precoder. However, the choice of the precoder for AFC has not been studied previously.

B. Main Contributions

Our main contributions in this work are summarized next.

1) A Density Evolution-based Framework to Analyse AFC: A density evolution (DE) framework is proposed to analyse the performance of the BP decoder of AFC in the infinite block length regime. We track the evolution of pdf of log-likelihood ratios (LLR), which are exchanged between variable and check nodes during the BP decoding. Due to the complexity of the variable and check node updating rules, we propose a Monte-Carlo based analysis to derive the pdfs in each iteration of DE. Our results show that the proposed DE framework can precisely approximate the pdf of LLRs in each iteration of the BP decoding.

2) A Differential Evolution Algorithm to Optimise AFC parameters: We use differential evolution to find the optimal weight set of AFC with different degrees by using the proposed DE framework. Simulation results show that by applying the optimised weight set, the performance of AFC in terms of bit error rate is improved compared with the existing designs of AFC in the literature, thus proving the validity of the proposed DE framework.

3) Precode selection for AFC in the short block length regime: The DE framework proposed in this work is valid for the asymptotically long block length. To guarantee the performance at short block lengths, one needs to choose a proper precoder. We will show that the optimised weight set obtained from the DE framework in the asymptotic regime can be directly used in the short block length regime with the help of a precoder with a powerful error-correcting capability to achieve the desired level of reliability. In particular, we consider Bose, Chaudhuri, and Hocquenghem (BCH) codes with an ordered statistics decoder (OSD) and LDPC codes with the BP decoder. We show that low rate precoders offer better reliability at a wide range of SNRs compared to high-rate precoders. We also show that the precoded AFC performs close to the normal approximation benchmark in the finite block length regime over a wide range of SNRs.

4) Threshold-based Decoder for reducing the decoding complexity: We propose a Threshold-based decoder for BCH precoded AFC, where only the soft information from AFC decoder, which deemed to be decoded with high confidence, will be passed to the BCH decoder. This prevents to execute the OSD algorithm in each and every iteration, which lead to the reduced complexity. Our results shows that this approach significantly reduces the decoding complexity with a negligible performance degradation.
TABLE I
SUMMARY OF NOTATIONS.

| Notation | Description |
|----------|-------------|
| $b$      | The information block |
| $k$      | The length of the information block, i.e., $|b| = n$ |
| $R_{\text{pre}}$ | The rate of the precoder |
| $G_{\text{pre}}$ | Generator matrix of precoder |
| $\mathbf{u}$ | The vector of intermediate symbols, i.e., $\mathbf{u} = G_{\text{pre}} \mathbf{b}$ |
| $n$      | The number of intermediate symbols, i.e., $n = k/R_{\text{pre}}$ |
| $\Omega(x)$ | Degree distribution function of AFC, i.e., $\Omega(x) = \sum_i \Omega_i x^i$ |
| $\mathcal{W}$ | Weight set of AFC, i.e., $\mathcal{W} = \{w_1, \cdots, w_M\}$ |
| $\mathcal{W}^+ = \mathcal{W}^-$ | Set of all positive and negative weight coefficients, i.e., $\mathcal{W}^+ = \{w_1, \cdots, w_M\}$, $\mathcal{W}^- = \{-w_1, \cdots, -w_M\}$ |
| $c_i$      | The $i$th AFC coded symbol |
| $d_v$      | Degree of variable nodes in the bipartite graph of AFC |
| $d_c$      | Degree of check nodes in the bipartite graph of AFC |
| $R_{\text{AFC}}$ | Rate of AFC code truncated at length $m$, i.e., $R_{\text{AFC}} = n/m = d_c/d_v$ |
| $M_v \setminus v$ | Set of variable nodes other than $v$ connected to check node $c$ |
| $N_c \setminus e$ | Set of check nodes other than $e$ connected to variable node $v$ |
| $m_{\text{succ}}$ | Number of AFC coded symbols to perform a successful decoding |
| $\mathbb{E}[\cdot]$ | Expectation operand |
| $\gamma$ | Signal-to-noise-ratio (SNR) |
| $\Delta$ | Number of AFC coded symbols sent between decoding attempts |
| $\sigma^2$ | AWGN variance |
| $m^{[w]}_{i\rightarrow v}(w)$ | Message sent from check node $c$ to variable node $v$ along the edge with weight $w$ at the $i$th iteration of BP |
| $m^{[w]}_{i\rightarrow c}(w)$ | Message sent from variable node $v$ to check node $c$ at the $i$th iteration of BP |

C. Paper Organisation

The rest of the paper is organised as follows. In Section II, we explain the encoder and decoder of the precoded AFC code. We then propose the density evolution framework for the analysis and design of AFC in Section III. Section IV presents the differential evolution optimisation framework for the AFC weight-set optimisation. We then study the precoding selection for AFC codes in the short block length regime in Section V. The decoding complexity of AFC and the proposed threshold-based decoder are discussed in Section VI. Finally, conclusions are drawn in Section VII.

II. ANALOG FOUNTAIN CODES

AFC was originally proposed in [15], which is mainly characterised by a weight set, degree distribution function, and the message length. The code is rateless in nature and can generate a potentially limitless number of coded symbols, thus, achieving any desired rate on the fly. In what follows, we explain the encoding and decoding processes of AFC. Table I summarizes notations commonly used in this paper.

A. The Encoder

The precoded AFC is a concatenation of a fixed-rate precoder and the AFC code. An information block of length $k$ bits, denoted by $b$, is first encoded by using a fixed-rate $(n, k)$ code (the precode) of rate $R_{\text{pre}} = k/n$, to generate a codeword of length $n$ bits, denoted by $u$, referred to as intermediate symbols. The generator matrix of the fixed-rate code is denoted by $G_{\text{pre}}$; therefore, we have:

$$\mathbf{u} = G_{\text{pre}} \mathbf{b}. \quad (1)$$

The precode serves as the outer code for the precoded AFC code. Intermediate symbols are modulated by using a BPSK modulation to generate $n$ modulated symbols, $\mathbf{v}_i$, that is

$$\mathbf{v}_i = (-1)^{\mathbf{w}_i}, \text{ for } i = 1, \cdots, n. \quad (2)$$

Next, by using an AFC code a potentially limitless number of AFC coded symbols, also referred to as output symbols, are generated. AFC is mainly characterised by a weight set $\mathcal{W}$ and a degree distribution function $\Omega(x) = \sum_i \Omega_i x^i$. In order to generate an AFC coded symbol, a degree $d$ is drawn based on the degree distribution function $\Omega(x)$. Then $d$ modulated intermediate symbols are randomly selected and linearly combined in the real domain with a set of $d$ real weight coefficients selected from the weight set $\mathcal{W}$. For simplicity, we consider that the degree $d$ is fixed, i.e., $\Omega(x) = x^d$, and the weight set is predefined and given by $\mathcal{W} = \{w_1, w_2, \cdots, w_d\}$. The $i$th AFC coded symbol, denoted by $c_i$, is then given by:

$$c_i = \sum_{j=1}^{d} w_j \mathbf{v}_{i,j}, \quad (3)$$

where $\mathbf{v}_{i,j} \in \mathcal{V}_i$ and $\mathcal{V}_i$ is the set of modulated intermediate symbols that have been chosen to generate the $i$th output symbol. We further assume that $\sum_{i=1}^{n} w_i^2 = 1$, therefore we have $\mathbb{E}[|\mathbf{c}|^2] = 1$, where $\mathbb{E}[\cdot]$ is the expectation operand. Fig. 1 shows the bipartite graph representation of the AFC code truncated at length $m$. We refer to AFC coded symbols and intermediate symbols in the bipartite graph of AFC by check and variable nodes, respectively. The degree of a check (variable) node is defined as the number of variable (check) nodes connected to it in the bipartite graph. A regular AFC has constant variable and check node degrees, $d_v$ and $d_c$, respectively. As can be seen in Fig. 1, each edge of the graph is assigned a real weight from $\mathcal{W}$ and each check node is the real sum of the variable nodes multiplied by their weights assigned to their edges connecting them to that check node.

AFC coded symbols are sent over the channel. Once the receiver received $m_0$ symbols, it performs the decoding. If the decoding failed, it collects $\delta$ additional AFC coded symbols and reruns the decoder. This process will continue until the decoding succeeds. The realised rate of the precoded AFC is given by

$$R = \frac{k}{\mathbb{E}[m_s]} = \frac{R_{\text{pre}} \times n}{\mathbb{E}[m_s]}, \quad (4)$$

where $m_s$ is the number of AFC coded symbols collected to perform a successful decoding.
B. The Decoder

We consider the additive Gaussian noise (AWGN) channel, where the channel output $y_i$ is given by

$$y_i = c_i + n_i, \quad \text{for } i = 1, 2, \cdots, (5)$$

where $n_i$ is AWGN with zero mean and variance $\sigma^2$. The signal-to-noise ratio (SNR), denoted by $\gamma$, is then given by $\gamma = 1/\sigma^2$.

The decoding is performed in two stages. First, the BP decoding algorithm is applied to AFC to find the LLRs of intermediate symbols. We use the BP algorithm initially proposed in [23] and further modified in [29] to decode AFC. Second, the LLRs are passed to the decoder of the precode to find the original $k$ information symbols. If the decoding failed, the BP decoding is repeated with a longer block of AFC symbols which includes newly arrived symbols. The LLRs are passed again to the decoder of the precode. This continues until the decoding succeeded or the maximum number of AFC symbols are sent.

In this paper, we consider both BCH codes and LDPC codes as the precoder. BCH code is a powerful cyclic error-correcting code with a variety of block lengths and corresponding code rates [8]. BCH codes have strong error-correcting capability, which can correct all random patterns of $t$ errors, where $t$ is the design parameter. BCH codes are effective in preventing error floor due to the large minimum Hamming distance [8]. However, there is a shortcoming of BCH codes, which is not flexible enough since block length and information length cannot be selected arbitrarily. For BCH precoded AFC, we use the ordered statistics decoder (OSD), which is computationally complex. LDPC, on the other hand, offers a lower complexity decoder, as BP can be effectively used to decode them. However, LDPC cannot offer the same level of error correction capability as BCH codes, particularly at short block lengths.

C. The Belief Propagation Decoding of AFC

We consider a regular AFC with a constant variable and check node degree, $d_v$ and $d_c$, respectively. In each iteration of the BP decoding, messages are exchanged between the check and variable nodes and vice versa. We use the LLR as the message which is exchanged between nodes in each iteration of the BP algorithm.

Let $m_{c \rightarrow v}(w_v)$ denote the message sent from check node $c$ to variable node $v$ along the edge with weight $w_v$ in the $\ell$th iteration of the BP decoding. It can be calculated as follows:

$$m_{c \rightarrow v}^{(\ell)}(w_v) = \sum_{b_c \in \{-1,1\}} \sum_{v' \in \mathcal{M}_c \setminus v} e^{-\frac{y - w_v - \sum_{v' \in \mathcal{M}_c \setminus v} w_v b_{c'}}{2\sigma^2}} \prod_{v' \in \mathcal{M}_c \setminus v} p_{v' \rightarrow c}^{(\ell - 1)}(b_{v'})$$

and $m_{v \rightarrow c}^{(\ell)}$ denote be the message sent from a variable node $v$ to check node $c$ in the $\ell$th iteration of the BP decoding, which is given by

$$m_{v \rightarrow c}^{(\ell)} = \sum_{c' \in \mathcal{N}_v \setminus c} m_{c' \rightarrow v}^{(\ell - 1)}(w_{v'})$$

where $\mathcal{N}_v \setminus c$ represents the set of check nodes that are connected to variable node $v$ except check node $c$. It is important to note that as can be seen in (6), the message passed from a check node to a variable node depends on the weight associated with the edge connecting them.

The messages are exchanged in an iterative manner between variable and check nodes for a predefined number of iterations or until the decoding achieves convergence. The final LLR value of each variable node after $L$ iterations of the BP decoding is calculated as follows:

$$m_v^{(L)} = \sum_{c \in \mathcal{N}_v} m_{c \rightarrow v}^{(L)}(w_v).$$

These LLRs are then passed to the decoder of the precode to find the original $k$ information symbols.

III. THE DENSITY EVOLUTION ANALYSIS OF AFC

In this section, we focus on the AFC code only without the precoder to better understand the AFC code performance and be able to optimise the code parameters. We will use the density evolution (DE) analysis [30] to evaluate the performance of the BP decoder of AFC. DE has been extensively applied to graph-based codes with static uniformity, which involves tracking the distribution of the messages exchanged along the edges of the bipartite graph in each iteration of the message passing algorithm (MPA) [31].

The DE analysis of AFC is different than that for the binary linear codes and in particular LDPC codes. In LDPC codes, or any binary linear block code, the information symbols are XORed according to the generator matrix to generate coded symbols. In the decoding of LDPC codes, a bipartite graph is generated based on the parity check matrix of the code, where the check operation simply checks whether the XOR of the variable nodes connected to the check node is 0 or not. The DE analysis of LDPC codes is then defined on this graph and the updating rules at variable and check nodes are defined according to these XOR operations [32]. Although the encoding process of AFC can be represented by a bipartite graph, the operation at check nodes is different. As can be seen in Fig. 1 to generate each AFC coded symbols, a set of randomly chosen information symbols, are first BPSK modulated, and then summed using the real weight coefficients (assigned to the edges in the bipartite graph). Furthermore,
similar to the DE analysis of Raptor codes [22], the bipartite graph of AFC is constructed based on the generator matrix of the code. The weighted sum at the check nodes of AFC will make tracking the evolution of the messages in the BP decoder very challenging.

It is important to note that the DE analysis is valid for the asymptotically long block length regime, where the message and codeword lengths go to infinity. In Section V we will focus on the design of the precoder for AFC to optimise the performance in the short block length regime. In fact, the optimised weight set obtained from DE in the asymptotic regime can be used in the short block length regime with the help of a precoder with a powerful error-correcting capability to achieve the desired level of reliability.

A. The Density Evolution Analysis

Without loss of generality, we assume that all incoming messages in a node are independent and identically distributed (i.i.d). This assumption was first considered in [30] and further justified in [33]. Based on this assumption, the bipartite graph can be viewed as a set of independent sub-trees with independently distributed messages, making the analysis more feasible. With probability arbitrarily approaching to 1 when \( n \) goes to infinity, a cycle-free bipartite graph emerged [33]. We would like to emphasise that the assumption is valid for the infinite block length regime. Later in Section V, we will focus on the design of the precoder to optimise the AFC performance at short block lengths.

The other assumption which has been widely considered for the DE analysis, is the all-zero-codeword transmission. For this condition to be met, the output channel LLRs should be symmetric; that is, the bit error rate is independent of the transmitted codeword [34]. However, AFC does not meet this condition as the average power of the output symbols depends on the information sequence. For example, when the all-zero information sequence is being encoded using AFC, each coded symbol will be equal to \( \sum_{i=1}^{d_v} w_i \), which is equivalent to the signal with the highest power assuming that \( w_i \)'s are all positive. For information sequences with some non-zero symbols, some of the coded symbols will have lower power. This results in unequal protection of information sequences. To solve this problem and meet the DE analysis requirements, we adopt the idea of the i.i.d channel adapter [35].

In particular, we slightly modify the encoding process of AFC in [3] by multiplying a binary random number, i.e., +1 and −1, with the weight associated with each edge in the bipartite graph. The \( \ell \)th AFC coded symbols are generated as follows:

\[
c_i = \sum_{j=1}^{d_v} (-1)^{t_j} w_j v_{i,j},
\]

where \( t_j \)'s are uniformly and randomly drawn from set \{0, 1\}. In the rest of the paper, we use this modified encoder unless otherwise specified.

1) DE Check Node Updating Rule: It is important to note that, unlike binary graph-based codes that are mainly characterised by the degree distribution function in the asymptotic block length regime, the AFC analysis should also take into account the weights associated with each edge in the graph. To do so, we assume that the messages passed from check to variable nodes are weight dependent. Let \( m_{c \rightarrow v}^{(\ell)}(w) \) denote the message passed from check node \( c \) to variable node \( v \) along the edge with weight \( w \) in the \( \ell \)th iteration of MPA. Let \( f_{cv}^{(\ell)}(w, m) \) denote the pdf of \( m_{c \rightarrow v}^{(\ell)}(w) \). In DE, we track the evolution of \( f_{cv}^{(\ell)}(w, m) \).

To calculate \( f_{cv}^{(\ell)}(w, m) \), we need to find the pdf of \( m_{c \rightarrow v}^{(\ell)}(w) \) which is derived in (6). Due to the complexity of this equation, it is not straightforward to find \( f_{cv}^{(\ell)}(w, m) \).

To address this, we propose to collect the exchanged messages by random sampling, i.e., by performing Monte-Carlo simulations. In particular, we randomly generate samples of \( y \) assuming that an all zero-codeword is being sent. It is important to note that for DE we consider channel adapters assuming that an all zero-codeword is being sent. It is justified in [33]. Based on this assumption, the bipartite graph can be viewed as a set of independent sub-trees with the weight associated with each edge in the graph.

More specifically, to generate samples of \( p_{w \rightarrow v}^{(\ell)}(b_v) \) from the pdf of \( m_{c \rightarrow v}^{(\ell)}(w) \), we first draw a random number \( m_{c \rightarrow v}^{(\ell)}(w) \) from \( f_{cv}^{(\ell)}(w, m) \). Then by using (7), \( p_{w \rightarrow v}^{(\ell)}(b_v) \) is calculated for \( b_v = 1 \) and \( b_v = -1 \). These will be inserted into (6) to calculate one sample of \( m_{c \rightarrow v}^{(\ell)}(w) \). Once a large number of samples are generated, \( f_{cv}^{(\ell)}(w, m) \) can be approximated by finding the histogram of samples of \( m_{c \rightarrow v}^{(\ell)}(w) \).

2) DE Variable Node Updating Rule: Let \( f_{vc}^{(\ell)}(m) \) denote the pdf of the \( m_{c \rightarrow v}^{(\ell)}(w) \), which is given in (8). As we assume that the messages passed along the edges are independent in an asymptotically long block length regime, we can find \( f_{vc}^{(\ell)}(m) \) as follows:

\[
f_{vc}^{(\ell)}(m) = \frac{1}{2d_c} \sum_{i=1}^{d_v} w_i \prod_{j=1}^{d_c} f_{cv}^{(\ell)}(w, m),
\]

where \( \mathcal{W} = \{ w, -w : w \in \mathcal{W} \} \) is the set of all positive and negative weight coefficients due to channel adapter. This equation follows from the fact that each variable node has \( d_v \) connected check nodes and the edges have weights which are randomly drawn from \( \mathcal{V} \). Since each edge can randomly choose one of the \( 2d_c \) available weight coefficients, we normalise the density by multiplying it with \( 1/2d_c \). Fig. 2 shows the densities \( m_{c \rightarrow v}^{(\ell)} \) at different iterations of the BP decoding for an AFC code with weight set \( \mathcal{W} = \{ 0.5097, 0.4992, 0.4960, 0.4949 \} \), degree \( d_c = 4 \), and rate \( R = 0.5 \), when SNR = 10dB. As can be seen in this figure, the densities are shifting towards right when the iteration number increases.

3) Approximation of the bit error rate using DE: We assume that DE converges after a few iterations and the check to variable node densities converge to \( f_{cv}^{(\infty)}(w, m) \). The
be seen, (13) provides a tight approximation for the BER for an AFC code at different SNRs when a proper precoder is used. We note that due to the weighted sum of (bipolar) information symbols when generating AFC coded symbols, the pdf of the message sent from check to variable nodes cannot be represented in closed form. Instead, we used a sampling approach to find the pdf of the message sent from check to variable nodes, which can be easily implemented to numerically find the BER of AFC codes using (13).

The bit error rate (BER) of the AFC code, denoted by $\epsilon(d_c, d_v, W)$, can then be calculated as follows:

$$
\epsilon(d_c, d_v, W) = \int_{-\infty}^{0} f_v^{(\infty)}(x)dx,
$$

which directly follows from the assumption of all-zero information sequence and that a bit error occurs when the LLR is calculated to be negative. Fig. 3 shows the approximation of BER at different decoding iterations using the DE analysis and simulation results.

IV. AFC WEIGHT SET OPTIMISATION

In this section, we use the DE analysis and define an optimisation problem to minimise the BER of the AFC and find the optimal weight set for a given check node degree $d_c$, variable node degree $d_v$, and channel SNR. The optimisation problem can be summarised as follows:

$$
\min_{W} \epsilon(d_c, d_v, W) \quad \text{s.t.}
$$

$$
C_1 : w_i > 0, \quad \text{for } i = 1, \ldots, d_c,
$$

$$
C_2 : \sum_{i=1}^{d_v} w_i^2 \approx 1,
$$

where $\epsilon(d_c, d_v, W)$ is found via (13), condition $C_1$ is to make sure that all weight coefficients are positive, and condition $C_2$ to make sure the average power of the AFC coded symbols is 1. Traditionally, the optimisation of graph-based channel codes involved maximising the decoding threshold. This is equivalent to minimising the bit error rate, which we consider in this paper for AFC. We further note that we are interested in finding the optimal weight set for a given fixed code degree $d_c$. A more general optimisation can be easily defined to determine the optimal degree distribution function and weight set jointly. This is, however, out of the scope of this paper.

The output of DE analysis is usually the BER or the threshold of the code in terms of noise power. To be able to characterise the messages and run DE we need to set the SNR and the code rate. The methodology used is similar to the approach used for optimising the rateless codes, and in particular Raptor codes [14]. That is the code is analysed at a fixed rate and a given SNR and the degree distribution is accordingly optimised to minimise the BER. Our objective function in (14) is to minimise the bit error rate at a given SNR and code rate, i.e., given $d_c$ and $d_v$. We will show in Section V that the optimised weight set performs reasonably well across all SNRs when a proper precoder is used.

We use the differential evolution algorithm [38] to solve (14). The differential evolution is a simple yet powerful optimisation tool based on the population stochastic search technique. There are three main parameters that control the optimisation algorithm: the scaling factor, crossover probability, and population size. The setting of these three parameters would directly affect the time and performance of the optimisation process. The population of differential evolution may move through a different region of search space to find suitable candidates. Although this approach is time-consuming, it suffices our purpose as we do not aim for efficient optimisation and instead would like to find some good weight sets for AFC.

For the case of AFC, the optimisation process involved the tuning of weight-set with the prefixed rate, $R_{\text{AFC}} = d_c/d_v$, at the given SNR, $\gamma$, to minimise the BER as in (14). Table I shows the optimised AFC weight set for different degree $d_c$, when the rate is $R_{\text{AFC}} = 2$ and $\gamma = 15$dB. In this optimisation, we performed 100 iterations of DE and generated 1000 samples of LLRs to calculated the densities in (13). As for the differential evolution, we considered the population size

![Figure 2](image1.png)

Figure 2: Variable node densities calculated by DE at different decoding iterations for an AFC code with $n = 8000$, $W = \{0.5097, 0.4992, 0.4960, 0.4949\}$, $d_c = 4$, and rate $R = 0.5$, when $\gamma = 10$dB. Solid and dashed curves respectively show DE analytical results and simulation results.

![Figure 3](image2.png)

Figure 3: The BER of AFC at different decoding iterations, when $n = 8000$, $d_c = 4$, $W = \{0.8632, 0.4495, 0.2300, 0.0004831\}$, and rate $R = 0.5$.
of approximately 50, crossover probability of 1, and mutation factor of 0.85.

When optimising the weight set of the AFC using (14), we need to specify the rate and SNR. In other words, the weight set obtained through (14) depend on the rate and channel SNR. For example, when $R_{AFC} = 0.5$ and $\gamma = 5\text{dB}$, the optimised weight set we obtained for $d_c = 4$ is $W_3 = \{0.5097, 0.4992, 0.4960, 0.4949\}$. When $R_{AFC} = 2$ and $\gamma = 15\text{dB}$, the optimised weight set we obtained for $d_c = 4$ will be $W_5 = \{0.8632, 0.4495, 0.2300, 0.0004831\}$. The optimised weight coefficients seem to be closer to each other when optimising AFC at lower SNRs and rates.

Fig. 4 compares the BER performance of AFC with these two weight sets, $W_3$ and $W_5$, when $R_{AFC} = 0.5$. As can be seen, the weight set optimised at a high SNR and higher rate performs better compared to weight set optimised at a low SNR and low rate. This observation provides us a guideline on the input parameter selection for the optimisation process, i.e., the rate and SNR should be as high as possible within the region of investigation to obtain better performance. We also compared the optimised weight sets with some weight sets previously used in the literature (see Table III). Fig. 4 shows the superiority of the optimised weight sets for AFC in terms of BER.

V. Design of AFC for Short Packet Communications

In this section, we evaluate the precoded AFC in the short block length regime. We focus on short messages, from only a few (≤100) bits up to a few hundred (≤1000) bits. This has been widely considered in the literature and standardisation documentations for 5G URLLC. Please note that DE analysis is not applicable in the short blocklength regime. Thus, we instead use the optimised parameter obtained from DE in the long block length, and then further optimise the code performance by tuning the precoder.

In our work, we selected BCH codes and LDPC codes as precoder of AFC. There are two criteria that we consider when select a precoder for AFC: 1) error correction capabilities and 2) decoding complexity. BCH codes are chosen as precoder of AFC for very short message length as it is a very powerful error correcting codes. Although LDPC codes benefits from their low complexity message passing decoder, they do not perform well in the short block length regime. LDPC codes, however, show good performance with low decoding complexity at moderate and high message length. It is worth to note that the proposed framework is compatible with other fixed-rate code. In fact any fixed rate code can be used as a precoder of AFC. The choice of the precoder must be aligned with the one of the criteria aforementioned, if not fulfill both of the criteria. In this case, BCH is chosen for very short message because it has almost the highest minimum Hamming distance and that can be efficiently decoded by OSD decoders. For moderate to long message lengths, we chose LDPC mainly because of their low complexity decoder.

We use the normal approximation (19) as a benchmark for comparing the performance of AFC at short block lengths. For the AWGN channel, the normal approximation for the achievable rate is given by (19):

$$R \approx C - \frac{\sqrt{V}}{n} Q^{-1}(\epsilon) + \frac{\log_2 n}{2n},$$

(15)

where $C = \frac{1}{2} \log_2 (1 + \gamma)$ is the channel capacity, $\gamma$ is the channel SNR, $V = \log_2(2(\gamma + 2)/(2(\gamma + 1)))$ is the channel dispersion, $\epsilon$ is the block error rate, and $Q(x) = \frac{1}{\sqrt{2\pi}} \int_{x}^{\infty} e^{-\frac{t^2}{2}} dx$ is the standard $Q$-function.

A. Block Error Rate of AFC at Fixed Rate

We first investigate the block error rate (BLER) performance of precoded AFC truncated at a fixed block length. In particular, we treat AFC as a fixed-rate code in order to investigate the achievable reliability guarantees. This is essential to understand the effect of the precode rate on the overall performance of AFC codes.

Fig. 5 shows the performance of the precoded AFC when a BCH(127,57) code is used as a precoder. As can be seen in this figure, the optimised weight set ($W_3$ in Table III) outperforms other weight sets previously designed for AFC.
The improvement in terms of BLER is consistent when the AFC is operating at different overall rates. When BCH(63,57) code is used as a precoder, results are consistent with what we observed in Fig. 5 and that the optimised weight set obtained in this paper outperforms weight sets previously designed for AFC.

Fig. 6 shows the comparison between AFC codes precoded with different BCH codes, i.e., BCH(63,57) and BCH(127,57), where the message length is $k = 57$, and at different overall rates. As can be seen, the AFC code with a low-rate precoder achieves a lower BLER. This is mainly because the lower rate precoder is more capable of correcting residual errors that AFC is unable to recover, especially when the SNR is low. The performances of BLER at high SNRs shows the same trend, where the lower rate precoder performs better in terms of BLER at different rates.

Fig. 6 also shows the performance of precoded AFC using different weight sets. It can be observed that the lower rate precode performs better than the high rate precode when using different weight sets with different degrees. It is important to note that when a high rate precode, i.e., BCH(63,57) is being used, the weight set $W_3$ with degree $d_c = 3$ outperforms the other weight sets. However, when the higher rate precode, i.e., BCH(127,57) is being used, the weight set $W_3$ with degree $d_c = 4$ outperforms the other weight sets in different overall rates. This shows that the degree of the AFC code should be chosen carefully, and that depends on the precode rate in order to minimise the BLER.

Simulation results demonstrate that a similar trend is obtained when using a LDPC code as the precoder, as shown in Fig. 7. That is AFC with a low rate precoder outperformed AFC with the high rate precoder in terms of BLER.

**B. Achievable Realised Rate**

We compare the realised rates achievable by the optimised weights versus previously designed weight sets in Table III. For all simulations, we assumed that the receiver attempts decoding every time it receives $\delta = 5$ additional AFC coded symbols. The first decoding attempts occurs when the receiver collects $m_0 = \frac{2k}{\log(1+\gamma)}$ AFC coded symbols. The decoder sends an acknowledgment to the transmitter when the decoding succeeds and accordingly the transmission is terminated.

Results are shown in Fig. 8. It is clear that our optimised weight set $W_3$ is superior in performance to previous AFC weight set designs, with similar degrees, i.e., similar complexity. In particular, for an AFC code precoded with BCH(63,57), our optimised weight set $W_3$ can achieve about 4.35% and 9.68% higher realised rate than weight sets $V_1$ and $V_2$, respectively, in the high SNR regime (around 20dB), and 13.96% and 18.49% higher realised rates, respectively, in the low SNR regime (around 5dB). When compared with benchmark weight sets $V_3$ and $V_4$ (Table III), the performances of the benchmark weight sets are quite close with our optimised weight set $W_3$, but our optimised weight set still demonstrated superiority compared to benchmark weight sets. Our optimised weight set $W_3$ can achieve about 0.84% and 3.37% higher realised rate than weight set $V_3$ and $V_4$, respectively, in the high SNR regime (around 20dB), and 1.49% and 1.66% higher realised rates, respectively, in the low SNR regime (around 5dB).

It is important to note that, in theory, the realised rate is defined for the zero-error transmission. However, due to computational limitations, such a realised rate cannot be calculated in practice. Thus, here we assume that the plotted realised rates correspond to block error rates less than $10^{-4}$.
Fig. 8. The realised rate of precoded AFC using our optimised weight set $W_3$ (Table II), in comparison to previously designed weight sets in the literature (Table III). The precoder used here is the BCH$(63,57)$ code.

Fig. 9. The cdf of the block length for a precoded AFC using our optimised weight set $W_3$ (Table II), in comparison to previously designed weight sets in the literature (Table III). The precoder used here is the BCH$(63,57)$ code.

i.e., for $10^4$ messages, no errors are exhibited. Furthermore, since the transmitted block length varies from one frame to the next, we also plot the cumulative distribution function (cdf) of the block length at different SNRs to understand better what latency guarantees AFC can provide. As shown in Fig. 9, our optimised code exhibits a smaller variance in the block length than previously designed AFC codes. Thus, our optimised weight set can provide better latency guarantees, particularly for delay-sensitive applications with little tolerance for jitters.

Results are shown in Fig. 10, where we consider two precoders, BCH$(63,57)$ code and BCH$(127,57)$. We choose these two precoders, such that they have the same message length but different rates. For AFC, we use the optimised weight set $W_3$ from Table II. As can be seen, the realised rate of AFC with a lower precoder rate is higher than an AFC with a higher rate precoder over a wide range of SNRs. At $\gamma = 20$dB, AFC with BCH$(127,57)$ has a gap of 9.57% to the PPV bound compared to AFC with BCH$(63,57)$, which has a gap of 13.68% to the bound. At $\gamma = 5$dB, AFC with BCH$(127,57)$ is closer to bound, i.e., it has a gap of 7.14% to the bound compared to AFC with BCH$(63,57)$ that has a gap of 9.57% to the bound. Similarly, in Fig. 11 using BCH codes as precoder. That by using a lower precoder rate for AFC, the cdf of the block length has a steeper gradient compared to an AFC with a higher rate precoder. This result further strengthens our claim that lower precoder rate has superior performance compared to higher precoder rate. We do not show the respective results for LDPC precoded AFC due to the space limitation.

It is well established that the degree of the AFC code plays a significant role in its achievable rates, i.e., there is a trade-off between the maximum achievable rate and the allowed encoding/decoding complexity. More specifically, in a noise-free environment, the AFC code can achieve a rate of $d_c$. However, its decoding complexity increases exponentially with $d_c$ as can be clearly inferred from Section VI.

We expect that the check node degree to be the upper bound on the maximum achievable rate when the SNR is sufficiently
high. When the BCH(127,57) code is used as the precoder, the achievable rates are plotted in Fig. 13. On the same graph, the achievable rates for precoded AFC with LDPC(384,192) as precoder are plotted. We consider three different weight sets, $W_1$, $W_3$, and $W_5$ from Table II for AFC code with degree $d_c = 2$, $d_c = 4$, and $d_c = 6$, respectively. As can be seen in Fig. 13 the AFC code with the larger degree achieves a higher realised rate over a wide range of SNRs.

In this work, we only provided the comparisons between our optimised precoded AFC and other designs of AFC available in the literature, instead of comparing with the other state-of-the-art codes, i.e., LDPC codes, Polar codes, and RM codes. This is because these codes are fixed rate codes. They are mainly designed for particular SNRs and at desired code rates. As for other binary rateless codes, such as Raptor codes, they are designed for the binary field and thus they should be equipped with higher order modulation to achieve higher spectrum efficiency when operating over the wireless channel. Thus, it is not fair to compare with our optimised precoded AFC, which is designed for rateless transmission over wireless channel where channel state information is not known at the transmitter.

C. Realised Rate over the Fading Channel

We now consider a fading channel, which is commonly present in vehicular communications. Since AFC is a rateless code, the encoder can send as many coded symbols as required by the receiver to perform a successful decoding. This means that the transmitter can adapt to the channel condition without knowing the channel condition in advance.

We assume a quasi-static Rayleigh fading channel, where the channel remains fixed over the transmission of a message block and varied independently between blocks. Fig. 14 shows the realised rate of the precode AFC, when $W_3$ from Table II is used as the weight set. As can be seen in this figure, AFC can closely approach the finite block length bound across a wide range of channel conditions. This is mainly because each instance of the fading channel can be realised as an AWGN channel, and AFC already performs close to the finite length bound, without channel knowledge at the transmitter side. This also suggests that the AFC will perform well under more complex fading scenarios with correlated fading. Therefore, the code designed over the AWGN channel can be effectively used for the fading scenario. As for multi-path and fast fading scenario, we note that other approaches, like OFDM and interleaving, are required which are beyond the scope of this work. In this paper, we mainly focused on the design of AFC codes and tried to provide a thorough discussion on the effect of code parameters on the performance.

VI. COMPLEXITY ANALYSIS OF AFC DECODING

In this section, we provide a detailed analysis of the complexity of the encoder and decoder of the precoded AFC. The complexity of the encoder is mainly depend on the degree of AFC, $d_c$, as well as the number of coded symbols, $m$. The encoding complexity scale linearly with the block length $m$, since the generation of each AFC coded symbols is independent of other symbols, and that involves the weighted sum of $d_c$ randomly selected information symbols.

A. The complexity of the BP decoder

The BP decoder operates in an iterative manner, and each iteration involves exchanging messages between variable and
check nodes. In particular, during variable node updates, \( nd_v \) messages will be calculated and sent to check nodes. The variable node update operation (8) is simple and only involves the summation of \( d_v - 1 \) incoming messages. Assuming that each summation requires 1 floating point operation (FLOP), the complexity of the entire variable node update round is approximately \( O(nd_v^2) \) FLOPs.

The check node update rule (6) is more complex and involves calculating believes (7). This is mainly because of the weighted edges in the bipartite graph and the real summation at check nodes. In each check node update round, \( md_c \) messages will be calculated and sent to variable nodes. Each calculation involves approximately \((d_c+2) \times 2^{d_c+1}\) FLOPs, assuming that the probabilities \( p_c \) are exchanged between the variable and check nodes. Therefore, the complexity of the entire check node update round is approximately \( O(md_c^22^{d_c+1}) \) FLOPs. Since we have \( md_c = nd_v \) and \( R = n/m \), the overall complexity per information symbol after \( L \) iterations of the BP decoder is approximately given by \( O(LRd_c^2(1 + R2^{d_c+1})) \). It can be seen that the complexity of the decoder increases exponentially with the check node degree, which is common for all message passing decoders.

Since AFC is using a similar decoder as rate compatible modulations (RCM) (29) (in fact rate compatible modulation is a special case of AFC), several approaches proposed in (25) can be used to reduce the complexity of the BP decoder. A common approach is to implement the convolution process in (6) using Fast Fourier Transform (FFT). Another approach is to use the time-domain convolution with the application of Zigzag deconvolution structure or as known as tree-based convolution (25). LLR-based decoder is also another method proposed to reduce the decoding complexity (26). Using LLR offers implementation advantages over using probabilities, because multiplications are replaced by additions and the normalization step is eliminated. Several other approaches have been proposed in (23), (24), (26), which can be applied to AFC codes. A comparison between different decoding approaches for AFC is available in (23).

### B. Threshold-based Decoder for BCH precoded AFC

The precoded AFC requires two decoders, i.e., the BP decoder for AFC and the decoder for precode. The decoding process is carried out in a way that every time that a new set of AFC coded symbols are received, the decoder needs to run both BP and the decoder for precode. This process will stop only when the decoding succeeds. This leads to a huge complexity at the receiver side and accordingly the decoding time increases dramatically. When LDPC codes are used as precode, the complexity of the receiver can be minimised by jointly decoding AFC and LDPC codes using the BP decoder. The complexity of the LDPC decoder is negligible to the AFC decoder since all operations can be performed in the Log-domain and efficient algorithms have been already proposed. The problem becomes challenging when an OSD algorithm is being used for decoding the precode. When a low rate BCH code is used as the precode, we usually need a high-order OSD to achieve a near maximum-likelihood (ML) decoding performance. The complexity, however, increases with \( O(k^\ell) \), where \( k \) is the message length and \( \ell \) is the order of OSD. For a code with minimum Hamming distance \( d_H \), order \( \ell = \lceil d_H/4 + 1 \rceil \) is asymptotically optimal, i.e., it can achieve near-ML performance (27).

To reduce the decoding complexity, we propose a threshold-based decoding algorithm for the precoded AFC code. In this modified algorithm, we pass the soft information to the precode decoder and perform the algorithm only when the average reliability of the soft information is above a predefined threshold value. Further details of this algorithm can be found in Algorithm 1. Details on how to find the threshold value can be found in (41). It is important to note that in Step 9 of Algorithm 1, we check CRCs to verify whether the decoding succeeds or not. This is because the output of the OSD is always a valid codeword. Therefore, we need to add CRC bits to verify whether the output of the OSD is the transmitted codeword or not.

We evaluate the number of times that we need to run the OSD algorithm versus the threshold SNR. Results are shown in Fig. 15 where the average number of OSD instances and the realised rate are plotted versus the threshold SNR for a

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**Algorithm 1: Threshold-based Decoding for BCH**

1. **Inputs:** \( y, G_{\text{pre}}, \delta, m_0, \gamma_{\text{th}} \)
2. **initialisation:** \( m = m_0 \)
3. **while** (CRC check fails) **do**
   4. Request for additional \( \delta \) AFC coded symbols
   5. \( m = m + \delta \)
   6. Perform BP decoding using \( m \) AFC coded symbols
   7. Calculate the average LLR of the output of the BP decoder, \( \mathbb{E}[L] \)
   8. **if** \( \mathbb{E}[L] \geq \gamma_{\text{th}} \) **then**
      9. Perform OSD using \( G_{\text{pre}} \) and \( L \) and check CRCs
   10. **else**
       11. Go to step 4
   12. **end**
4. **end**
5. **return** \( \hat{b} \)

![Fig. 15. Number of OSD instances versus the threshold SNR for the BCH-AFC code at different SNRs when BCH(63,57) is used as a precoder.](image-url)
precoded AFC at different channel SNRs when BCH(63,57) was used as a precoder. As shown in this figure, the number of times that we run OSD is not very sensitive to the threshold value in high SNRs. However, the lower the threshold value, the higher the realised rate. At low SNRs, the threshold value cannot be chosen very small as it significantly increases the number of OSD instances. In fact, one can choose a very low threshold value and get a very high realised rate, but on the other hand, the complexity would significantly increase. It is important to note that if the threshold SNR is chosen very low, every time the receiver receives a new set of AFC symbols, it will run the OSD algorithm, no matter the reliability of AFC decoder outputs is. The proposed decoder can effectively limit the number of OSD instances to almost 1, when a proper threshold value is considered, with a negligible degradation in the realised rate \cite{12}. It is important to note that other approaches can be applied to further reduce the complexity of OSD algorithm as detailed in \cite{11}. This is, however, out of the scope of this work.

VII. CONCLUSION

In this paper, we proposed a density evolution (DE) analysis framework for analog fountain codes. In the DE framework, we tracked the evolution of messages exchanged between the variable and check nodes when decoding an AFC using the belief propagation decoder. Using the proposed framework, we defined an optimisation problem to find the weight set of AFC. Results show that for the asymptotically long message lengths, the optimised weight sets outperform existing weight sets previously designed for AFC. We also studied the design of the precoder for AFC to optimise the performance at short block lengths. We mainly focused on BCH and LDPC codes and showed via simulations that a lower rate precoder could achieve a lower block error rate under the same overall rate and a higher realised rate over a wide range of signal to noise ratios (SNRs). We further showed that our optimised weight sets outperform existing weight sets in the literature at both low and high SNRs in the short block length regime. We further shed lights on how to reduce the decoding complexity of precoded AFC in the rateless setting. The proposed code can be effectively used for rateless transmission of short information sequences, which have applications in many mMTC and URLLC scenarios.

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Wen Jun Lim (Graduate Student Member, IEEE) received the Bachelor of Engineering (Telecommunication) and Master of Engineering Science from University of Malaya, Malaysia, in 2014 and 2017 respectively. He is currently pursuing the Ph.D. degree with the Centre for IoT and Telecommunications, The University of Sydney. His major research interests are channel coding and wireless communications.

Rana Abbas (Member, IEEE) received the B.Sc. degree from the University of Balamand, Lebanon, in 2012, and the M.E. and Ph.D. degrees in electrical engineering from The University of Sydney, in 2013 and 2018, respectively. She is currently a Researcher at the Centre of IoT and Telecommunications, The University of Sydney, and an Innovation Engineer at the Engineering Excellence Group of Laing O’Rourke, Australia. Her research interests include channel coding, random access, information theory, machine-type communications, IoT, and Li-DAR. She was a recipient of the Australian Postgraduate Awards’ Scholarship and the Norman 1 Price Scholarship from the Centre of Excellence in Telecommunications, School of Electrical and Information Engineering, The University of Sydney. She was also a recipient of the Best Paper Award at the IEEE PIMRC, 2018.

Yonghui Li (Fellow, IEEE) received the Ph.D. degree from the Beijing University of Aeronautics and Astronautics, Beijing, China, in November 2002. Since 2003, he has been with the Centre of Excellence in Telecommunications, The University of Sydney, Sydney, NSW, Australia, where he is currently a Professor and the Director of the Wireless Engineering Laboratory, School of Electrical and Information Engineering. His current research interests are in the area of wireless communications, with a particular focus on MIMO, millimeter-wave communications, machine-to-machine communications, coding techniques, and cooperative communications. He holds a number of patents granted and pending in these fields. Dr. Li was a recipient of the Australian Queen Elizabeth II Fellowship in 2008 and the Australian Future Fellowship in 2012. He received the Best Paper Awards from the IEEE International Conference on Communications (ICC) 2014, the IEEE Personal, Indoor and Mobile Radio Communications (PIMRC) 2017, and the IEEE Wireless Days Conferences (WD) 2014. He has served as a Guest Editor for several IEEE journals, such as IEEE JSAC, IEEE Communications Magazine, IEEE IoT Journal, and IEEE Access. He is also an Editor of IEEE Transaction on Communications and IEEE Transaction on Vehicular Technology.

Branka Vucetic (Life Fellow, IEEE) is currently an ARC Laureate Fellow and the Director of the Centre of Excellence for Internet of Things and Telecommunications, The University of Sydney, Sydney, NSW, USA. Her current research work is in wireless networks and the Internet of Things. In the area of wireless networks, she works on ultra-reliable low-latency communications (URLLC) and system design for millimeter-wave frequency bands. In the area of the Internet of Things, she works on providing wireless connectivity for mission-critical applications. Dr. Vucetic is a Fellow of the Australian Academy of Technological Sciences and Engineering and the Australian Academy of Science.

Mahyar Shirvanimoghaddam (Senior Member, IEEE) received the B.Sc. degree (Hons.) from The University of Tehran, Iran, in 2008, the M.Sc. degree (Hons.) from Sharif University of Technology, Iran, in 2010, and the Ph.D. degree from The University of Sydney, Australia, in 2015, all in Electrical Engineering. He is currently a Lecturer with the Centre for IoT and Telecommunications, The University of Sydney. His research interests include coding and information theory, rateless coding, communication strategies for the Internet of Things, and information-theoretic approaches to machine learning. He is a fellow of the Higher Education Academy. He was selected as one of the Top 50 Young Scientists in the World by the World Economic Forum in 2018 for his contribution to the Fourth Industrial Revolution. He received the Best Paper Award for the 2017 IEEE PIMRC, The University of Sydney Postgraduate Award and the Norman I Prize, and The 2020 Australian Award for University Teaching.