A LOW COST POTENTIOSTAT DEVICE FOR MONITORING AQUEOUS SOLUTION

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ABSTRACT

This study developed a new design of a low cost potentiostat circuit device. This device is an alternative electrochemical instrument applied for monitoring heavy metal ion in environmental water. It was developed to alleviate the cost burden of equipment procurement and due to the requirement for in-situ application since the existing commercialize devices are bulky and expensive. The main component of the device consist of electronics configuration of operational amplifier. The device was first modelled and simulated to acquire the design parameters and performance. The potential control algorithm was developed on open-source microcontroller platform. A dummy cell was used to validate the capabilities of the device.

Keywords: Potentiostat, Operational amplifier, Electrochemical, Electronics configuration.

INTRODUCTION

Water pollution is a serious threat to global health [1]. Heavy metal ions (HMI) especially lead (Pb), cadmium (Cd), mercury (Hg), chromium (Cr) and arsenic (As) are highly toxic even in small doses [2]. Industrial activities are the main source of HMI [3]. These HMI are not biodegradable and have a tendency to accumulate in living organisms, causing various diseases and disorders to the nervous, immune, reproductive and gastrointestinal systems [2]. Thus, detection of HMI and determining their quantities is a paramount importance.

Conventional HMI detection technique such as spectroscopic is accurate, but costly, bulky, time consuming and require a well-trained analyst [4], [5]. An electrochemical technique is a reliable alternative to spectroscopic and has gained more popularity as it is more economic, user-friendly, allow a simple procedures and suitable to fabricated on small circuit in the portable form for in-field applications [2].

A potentiostat is a crucial component as the electrochemical analysers. The main functions are to control the applied potential voltage for HMI excitation in solution and to measure the resulting current due to movement of electron in the solution [6]. For potential control process, three electrode system was used as the sensor electrodes; working electrode (WE), reference electrode (RE) and counter electrode (CE) [7]. The WE can be defined as the electrode whose potential is a function of the solution’s concentration [8]. The RE maintain a constant voltage referred to the potential of the hydrogen electrode. Thus, no current is allowed to pass the RE. The CE served to
complete the electric circuit and provided the information of the ion in term of current reading.

In order to analyse and process the information from sensor electrodes, potentiostat is used. Due to the available commercialize potentiostat are expensive, bulky and often exceed the practical needs, researchers tends to build its own using existing information [9]–[16]. Thus, the purpose of this study is to describe the development of a potentiostat device for the purposed of HMI detection.

**METHODOLOGY**

**Concept of Main Potentiostat**

The proposed potentiostat was designed for three electrode system which consist of electronics configuration mainly the operational amplifier and resistor. The potentiostat can be categories into six main components as shown in Figure 1. The potentiostat was first designed and simulate on MATLAB Simscape for design parameters and performance test.

![Image](https://example.com/potentiostat.png)

**Figure 1 Potentiostat modelling.**

Firstly, the applied potential voltage $V_{in1}$ passed through the summing amplifier. The main function of summing amplifier as in Figure 1(a), is to consider and provide the potential voltage, $V_{in2}$ sensed by RE as a feedback signal in potentiostat circuit. This is
to ensure a sufficient excitation voltage supply at the WE due to the applied voltage at WE is always with respect to the RE. The general formulation for voltage summing is as below,

\[ V_{out} = -\left( \frac{R_{s3}}{R_{s1}} V_{in1} + \frac{R_{s3}}{R_{s2}} V_{in2} \right) \] (1)

Since \( R_{s1} = R_{s2} = R_{s3} = 10k\Omega \), the output of summing amplifier can be written as,

\[ V_{out} = V_{in1} + V_{in2} \] (2)

The output produced by voltage summing is in inverted polarity. Thus, an inverter as in Figure 1(b) is used to rectify the polarity. The general formulation of the inverter,

\[ V_{out} = -\frac{R_{i1}}{R_{i2}} V_{in} \] (3)

Since \( R_{i1} = R_{i2} = 10k\Omega \), the output of polarity inverter can be simplified as,

\[ V_{out} = -V_{in} \] (4)

A voltage follower as in Figure 1(c) and (d) provided a high input impedance to prevent significant current drawn that may lead to inaccurate potential voltage reading. The formulation of voltage follower can be written as,

\[ V_{out} = V_{in} \] (5)

The current signal from CE is measured using the transimpedance amplifier or current to voltage converter as in Figure 1(e). The formulation current to voltage converter is shown in Equation (6). The value of \( R_{cv2} \) determined the sensitivity range of the potentiostat. The inverter amplifier Figure 1(f) is cascade with transimpedance amplifier to rectify the output voltage polarity.

\[ V_{out} = iR_{cv2} \] (6)

**Potentiostat Development**

The designed potentiostat was implemented on a breadboard using LM324N Operational Amplifier. The circuit arrangement is shown in Figure 2 consist of three parts, (a) an ATmega328P Arduino Uno microcontroller for signal generator, parameter control and data acquisition. The second part is (b) a digital and analog converter (DAC) including unipolar to bipolar converter (UBC) and bipolar to unipolar converter (BUC). The third part (c) is the main potentiostat circuit as described in Figure 1.

For hardware implementation, the DAC part was applied as a connector between the Arduino Microcontroller and the main potentiostat part. The detail description of DAC is shown in Figure 3. A low pass filter is used to convert the square wave signal generated by microcontroller pulse width modulation (PWM) into an analog voltage. The relation between PWM and desired analog voltage, \( V_{in} \) are stated in Equation (7).
\[ V_{\text{in}} = \text{high voltage of PWM signal } \times \text{duty cycle} \]  \hspace{1cm} (7)

With the cut off frequency, \( f_c \) of,

\[ f_c = \frac{1}{2\pi RC} \]  \hspace{1cm} (8)

The PWM voltage of Arduino Uno board can only provide DAC voltage range from 0V to 5V. Since electrochemical experiment usually required a potential range from -1V to +1V, UBC is used as voltage mapping as shown in Error! Reference source not found., and the equation can be written as below,

\[ V_{\text{out}} = V_{\text{in}} \left( \frac{R_2}{R_1 + R_2} \right) \left( 1 + \frac{R_4}{R_4} \right) - V_2 \frac{R_4}{R_3} \]  \hspace{1cm} (9)

Substituted \( R_1 = 20k\Omega, R_2 = R_3 = 10k\Omega, \) and \( R_4 = 2k\Omega, \) Equation (9) is simplified as,

\[ V_{\text{out}} = 0.4V_{\text{in}} - 1 \]  \hspace{1cm} (10)

Figure 2 Potentiostat development board (a) microcontroller, (b) digital and analog converter and (c) main potentiostat circuit.
Figure 3 Components of digital and analog converter (a) low pass filter, (b) unipolar to bipolar converter and (c) bipolar to unipolar converter.

In Figure 4 shows the output of low pass filter indicated as Vin and the output of UBC indicated as Vout.

![Figure 4 DAC and UBC output.](image)

In order for microcontroller to analyse and process the signal from sensor electrodes, BUC as shown in Figure 3(c) is used to convert the voltage signal range (-1V to +1V) from main potentiostat to microcontroller range (0V to +5V). The formulation of the converter is shown below,

$$V_{out} = \left( V_{in} \frac{R_2}{R_1 + R_2} + V_2 \frac{R_1}{R_1 + R_2} \right) \left( 1 + \frac{R_4}{R_3} \right) \tag{11}$$

Substitute resistance value, rewrite Equation (11) as,

$$V_{out} = 2.5(V_{in} + 1) \tag{12}$$
RESULTS AND DISCUSSION

The capability of the potentiostat device was verified using a dummy cell. The dummy cell is a device replicated the dynamics of electro-analytical cells using passive electrical components such as resistors and capacitors in certain configurations. Linear sweep voltammetry was applied to evaluate the performance of the potentiostat device. Dummy cell of 1kΩ, 2kΩ and 10kΩ were connected between WE and RE and CE.

![Graphs showing resistor polarization curve](image)

Figure 5 Resistor polarization curve using (a) 1kΩ resistor, (b) 2kΩ resistor and (c) 10kΩ resistor.

When a potential is swapped between the leads of a resistor, the measured current can be calculated based on the apply voltage input, $V_{in}$ at the WE and is expected to show a linear response. As seen in Figure 5, the expected behaviour is observed. A similar trend was seen between experimental and theoretical. The polarization resistance was obtained as the inverse value of slop, $m = 1/R_p$ with $R_p = 0.962kΩ$, 2.099kΩ and 10.121kΩ for
dummy cell 1kΩ, 2kΩ and 10kΩ respectively. These values were in a good agreement with the 10% tolerance resistor used. The mean error between experiment and theoretical is small up to 1.30E-05 as shown in Table 1.

| Dummy validation test value, R | Voltage input, Vin | Mean error, e |
|--------------------------------|-------------------|--------------|
| \( R = 1k\Omega \)            | from -1V to +1V    | 2.41E-05     |
| \( R = 2k\Omega \)            | from -1V to +1V    | 2.95E-05     |
| \( R = 10k\Omega \)           | from -1V to +1V    | 1.30E-05     |

**CONCLUSION**

In this study, the low cost (< RM100) potentiostat device was developed using ATmega328P Arduino Uno microcontroller, LM324N operational amplifier, resistors and capacitor. Based on verification test, the designed potentiostat was capable to work as desire with similar trend and small mean error (up to 1.30E-05) between theoretical and experimental.

For further development study, it is highly recommend to improve in the following areas: (1) perform device validation using an actual electrochemical setup, (2) construction of the device using a custom etched printed circuit board and (3) higher portability for use in low-resource settings for researches and potential point-of-use care analysis.

**ACKNOWLEDGEMENTS**

The authors would like to acknowledge Universiti Sains Malaysia (USM) and Ministry of Education (MOE) for providing the financial assistance under RU Top-Down research grant (1001/PAERO/674041) and awarding her USM Academic Staff Training Scheme (ASTS).

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