Measurements of charge carrier mobilities and drift velocity saturation in bulk silicon of \textless 111 \textgreater and \textless 100 \textgreater crystal orientation at high electric fields

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Abstract

The mobility of electrons and holes in silicon depends on many parameters. Two of them are the electric field and the temperature. It has been observed previously that the mobility in the transition region between ohmic transport and saturation velocities is a function of the orientation of the crystal lattice.

This paper presents a new set of parameters for the mobility as function of temperature and electric field for \textless 111 \textgreater and \textless 100 \textgreater crystal orientation. These parameters are derived from time of flight measurements of drifting charge carriers in planar p\textsuperscript{+}n\textsuperscript{+} diodes in the temperature range between -30\degree C and 50\degree C and electric fields of 2\times10\textsuperscript{3} V/cm to 2\times10\textsuperscript{4} V/cm.

Keywords: mobility, silicon, electric field, anisotropy

1. Introduction

The transport properties in bulk silicon have been studied in great detail \[1\]. Precise knowledge of the transport properties is important, as in applications with large drift distances, e.g. radiation sensors, even small uncertainties in the drift velocities can have noticeable effects.

In a radiation sensor ionizing radiation creates electron hole pairs in the silicon bulk material, which drift in the electric field created by an applied voltage and induce a signal in the readout electrode\[1\]. Diffusion processes occur simultaneously causing a lateral spread of the charge carriers and a drift velocity distribution where some charge carriers are accelerated and some are decelerated due to the diffusion. The current induced by each charge carrier is proportional to its drift velocity, which, if diffusive processes are neglected, is given by the product of mobility and electric field. Thus, in order to optimize radiation sensors, precise knowledge of the mobility as function of the electric field is necessary.

Common radiation sensors are built in such a way that drift fields are in the order of 10\textsuperscript{4} V/cm, making the charge collection times of these sensors sensitive to the charge carrier mobility in the transition regime between ohmic and saturation behavior. Especially for sensors damaged by irradiation the charge collection time is an important optimization parameters as charge carriers are trapped and thus degrade the obtained signal.

Parameterizations of charge carrier mobilities can be found in the literature, however the most recent measurements using the time of flight technique have been performed by Canali et al. in 1971 \[2\]. There have been quite a few reviews of this data, the one by Jacoboni et al. \[3\] is especially useful, as it provides a temperature depended parameterization of the drift velocity in \textless 111 \textgreater direction. There have been other reviews incorporating other measurements and proposing different parameterizations \[4, 5, 6, 7\], however either temperature dependence or anisotropy is neglected. For electric field values of 10\textsuperscript{4} V/cm in \textless 111 \textgreater orientated silicon at a temperature of 300 K the aforementioned parameterizations produce values scattered by approximately 10\% (best case), however the scattering increases significantly for different temperatures (57\% for holes at 250 K). None of the mentioned reviews provides a parameterization for the \textless 100 \textgreater direction.

In this work measurements of pulse shapes of p\textsuperscript{+}n\textsuperscript{+} diodes in the temperature range between -30\degree C and 50\degree C with different applied bias voltages are compared to simulations in order to determine a set of parameters, describing the mobility as function of electric field and temperature for both \textless 111 \textgreater and \textless 100 \textgreater orientated silicon.

2. Time of flight method

The time of flight method (sometimes also called transient current technique, TCT) is a well established technique to determine effective trapping times in irradiated silicon diodes \[8, 9\]. It can also be used to investigate charge multiplication or effects of high charge carrier densities in segmented devices \[10, 11\]. Electron hole pairs
are created inside a reverse biased diode either by ionizing radiation or laser light. These charge carriers then drift under the influence of the electric field and induce a current in the readout circuit. The current pulse contains information about the amount of drifting charge and their drift velocity. By using a laser source with appropriate wavelength the expected current pulses for x-rays, heavy ions or minimum ionizing particles can be simulated, allowing the optimization of application specific readout electronics.

Planar p+n+ silicon diodes with <111> and <100>-crystal orientation were investigated using the time of flight method.

In order to measure the mobility of electrons and holes separately we create electron hole pairs by injecting laser light either from the p+ or from the n+ side. The penetration depth of the light is short compared to the thickness of the investigated samples, thus the other type of charge carriers is collected very quickly (within approximately 100 ps).

To evaluate whether effects of high charge carrier densities (also called plasma effects) were present the diameter of the laser spot on the sample was changed by focusing and defocussing. As no changes in the recorded current pulses were observed, high density effects can be considered negligible.

3. Experimental setup

Charge carriers were created with a laser of 660 nm wavelength and the time resolved current pulses of the investigated diode was read out by a Miteq AM-1309 wideband amplifier and a Tektronix DPO 7254 2.5 GHz osciloscope.

The sample was mounted on a substrate that allowed light injection from both sides while providing a stable (± 0.1 K, rms) temperature in the range of 240 K to 340 K and applying the high voltage to the rear side of the diode. The guard ring was grounded.

3.1. Equivalent circuit for SPICE simulations

An equivalent circuit of the setup was used for SPICE simulations (see Figure 2). In the equivalent circuit detector capacitance (9.2 pF or 9.4 pF), signal cable length (3.05 m), bandwidth of the amplifier (1 GHz) and imperfections of the setup (inductances and capacitances) were taken into account. The transfer function of the whole setup has been checked and was found to introduce distortions, thus all simulations have been convoluted with the transfer function (shown in Figure 3).

3.2. Laser properties

The laser system was manufactured by Picoquant GmbH and provided short and intense light pulses with a FWHM < 100 ps. The time resolved pulse structure was taken into account. The transfer function of the whole setup has been checked and was found to introduce distortions, thus all simulations have been convoluted with the transfer function (shown in Figure 3).

3.3. Investigated samples

The investigated samples were planar p+n+ diodes fabricated by CiS Forschungsinstitut für Mikrosensorik und Photovoltaik GmbH. The silicon used is high resistivity n-type diffusion oxygenated float zone silicon with <100> and <111> orientation manufactured by Wacker. The effective doping of the <111> orientation has a peak value of 3x10^19 cm^-3 and a junction depth of 0.5 µm. The n+ implantation has a peak value of 2x10^19 cm^-3 and depth of 1 µm. The samples have very low concentrations of lattice defects, leading to trapping times in the order of milliseconds, which is many orders of magnitude larger than the longest pulse duration recorded in this work (approximately 35 ns). Thus any trapping effects have been considered negligible.

From the measured pad capacitance of 9.25 pF and the pad area of (4.95 mm)^2 we obtain a sample thickness of 280 µm using the standard formula for a parallel plate ca-
Figure 2: Equivalent circuit for SPICE simulations. Dominant elements are the diode capacitance (only 9.2 pF shown) in parallel to the current source (marked by the rectangle), the system inductance (30 nH), the lossy transmission line (3.05 m RG-174 cable) and the band pass filtering properties of the amplifier.

Capacitor without edge effects. The corresponding numbers for the <111> diode are 9.42 pF, (4.93 mm)² and 272 µm. For the measurement of the pad capacitance the capacitance of the guard ring to the backplane and the sensor edge is subtracted by the zero adjustment of the capacitance bridge. The remaining effect of the guard ring is estimated to be well below 1 %. The estimated uncertainty of the diode thickness is ± 2 µm.

Mechanical measurements of the thickness yield somewhat higher values but include ‘dead’ layers like passivation and aluminizations. The accuracy of the final capacitance has the biggest influence on the determination of the thickness, as a change of 0.1 pF changes the measured thickness by approximately 3 µm.

The samples feature an opening on the top side and a grid like aluminum layer on the rear side to allow light injection. The gap between the metalization of diode guard ring is 20 µm wide, the distance between the corresponding implantations is 10 µm. A sketch of the sensor is shown in Figure 1.

4. Measurements

Measurements have been performed for front side illumination (electron signal) and rear side illumination (hole signal). Bias voltages of 100 V, 200 V and 500 V were applied and these measurements were done in temperature steps of 10 K in the range between -30 °C and 50 °C (243.15 K and 323.15 K). A set of measurements and corresponding simulations are shown in Figure 4.

To reduce the random noise component of the system averages 4096 pulses were taken. The remaining noise is no longer random and has an rms value of < 0.4 µA.

5. Simulations

The simulations were done in two steps. At first the expected current pulse was calculated using a custom written transport code, then the resulting current pulse was convoluted with the time structure of the light pulse and with the transfer function of the setup.

The custom transport code is taking charge drift and diffusion into account. It is a 1D calculation on a grid with 100 nm cell size (∆x) with 5 ps time step (∆t).

Above full depletion the electric field inside the diode is linear with its highest value at the p⁺n-junction (∆x = 0) and described by the following equation:

\[ E(x) = \frac{1}{d} \left( U_{bias} + U_{dep} - \frac{2e}{d} U_{dep} \right) \] (1)

Where \( x \) is the position inside the sensor (center of corresponding cell), \( d \) the sensor thickness (280 µm or 272 µm), \( U_{bias} \) the applied bias voltage and \( U_{dep} \) the depletion voltage of the diode.

The normalized charge carrier distribution (\( N_{e,h}(x,t = 0) = \frac{\Delta x}{\lambda_{abs}} \exp(-x^*/\lambda_{abs}) \)) generated by the laser light follows an exponential function with attenuation length \( \lambda_{abs} = 3 \) µm with \( x^* = x \) for front side injection and \( x^* = d - x \) for rear side injection.

The induced current is calculated according to the Ramo theorem [14]. The weighting field of a planar diode is linear and the induced current (normalized to the elementary charge \( e_0 \)) can be approximated as:
In order to avoid problems with charge carriers leaving the diode \((x < 0 \text{ or } x > d)\) and diffusing back into the diode, a charge carrier which reaches the last grid point along the drift direction is removed from the simulation.

The numerical stability of the simulation has been checked by varying the grid size and time step by a factor of up to five. No effect on the pulse shape was observed. Charge conservation was checked and no charge loss was found.

### 5.1. Drift and diffusion

The charge drift and diffusion was simulated using the following basic equations:

\[ I(t+\Delta t) = \sum_x [I_e(x, t+\Delta t) - I_h(x, t+\Delta t)] \] (2)

\[ I_e(x, t+\Delta t) = \frac{x e_0}{\Delta t} (N_e(x, t+\Delta t) - N_e(x, t)) \] (3)

\[ I_h(x, t+\Delta t) = \frac{x e_0}{\Delta t} (N_h(x, t+\Delta t) - N_h(x, t)) \] (4)

#### 5.1.1. Drift

The charge drift and diffusion was simulated using the following basic equations:

\[ \delta x(x) = \pm \mu_{e,h}(x) E(x) \Delta t \] (5)

\[ \sigma_{e,h}^2(x) = \sqrt{2 \mu_{e,h}(x) kT \epsilon_0 \Delta t} \] (6)

\[ N_{e,h}(x, t+\Delta t) = \sum_{x^*} N_{e,h}(x^*, t) C(x, x^*) \] (7)

\[ C(x, x^*) = \frac{1}{2} [A(x, x^*) - B(x, x^*)] \] (8)

\[ A(x, x^*) = \text{erf} \left( \frac{x^* + \delta x(x) - x + 0.5 \ast \Delta x}{\sqrt{2} \sigma_{e,h}^2(x)} \right) \] (9)

\[ B(x, x^*) = \text{erf} \left( \frac{x^* + \delta x(x) - x - 0.5 \ast \Delta x}{\sqrt{2} \sigma_{e,h}^2(x)} \right) \] (10)

Where \(\delta x(x)\) is the distance traveled within one time step \((\Delta t)\). It is negative for holes as they drift into the opposite direction of electrons. Average values (e.g. \(\mu_{e,h}(x)\)) have been calculated by adding the values for the transversed cell and dividing by the number of cells traveled \((\pm 1/\delta x^* \sum_{x^* = x \pm \Delta x^*} \), for \(\delta x^*\) is \(\delta x\) rounded to nearest cell center. \(\delta x\) was determined by adding the drift times for each cell until the total drift time \((t_{drift})\) exceeded the time step when \(n_{cell}\) cells have been transversed, then calculating \(\delta x = \frac{n_{cell} \Delta x}{t_{drift}} \Delta t\). Diffusion is simulated using the Einstein relation \((D_{e,h} = \frac{\mu_{e,h}(x) kT}{\epsilon_0})\).

The number of charge carriers at each grid cell is calculated as the integral of a Gaussian function shifted by \(\delta x\) with \(\sigma = \sqrt{2 D \Delta t}\) from \(x - \Delta x\) to \(x + \Delta x\) (beginning of cell to end of cell).

These shifted Gaussian functions are calculated for each grid cell and summed over all grid cells.

### 5.2. Mobility model

The field dependence of the mobility was parameterized in the following way:

\[ \mu_{e,h}(x) = \frac{\mu_{e,h}^0}{\left(1 + \left(\frac{E(x)}{\nu_{e,h}^{sat}}\right)^{\beta_{e,h}^{sat}}\right)} \] (11)

Where \(\mu_{e,h}(x)\) is the mobility of electrons or holes for a given grid cell \((x < 0 \text{ or } x > d)\) with the electric field \(E(x)\), \(\mu_{e,h}^0\) the ohmic mobility (also called low field mobility), \(\nu_{e,h}^{sat}\) the saturation velocity and \(\beta_{e,h}\) a parameter describing the transition between ohmic behavior and saturation velocity.

The temperature dependence of these parameters was modeled using a simple exponential law:

\[ Parameter_i(T) = Parameter_i(300K) \left( \frac{T}{300K} \right)^{\alpha_i} \] (12)

### 6. Results

To determine the free parameters of the model a modified \(\chi^2\) procedure is used. At first current pulses for all measurements were simulated \((<111>\) and \(<100>\) sample for 100 V, 200 V and 500 V at all temperature steps; 54 pulses in total) and the squares of the difference between measurement and simulation were summed up. This \(\chi^2\) sum was calculated for every set of parameters and the parameters with the smallest \(\chi^2\) are presented in Tables 1 and 2. Errors have been calculated by using the Minuit fitting program \([13]\) of the ROOT package \([16]\). Transients calculated with this set of parameters are shown in Figure 4.
The errors given in Tables 1 and 2 have been determined by Minuit but should be seen as only approximate due to the high non-linearity of the system.

To evaluate the influence of systematic uncertainties in the thickness and depletion voltage the same set of measurements was fitted, assuming ±1 % difference in thickness or ±5 % difference in depletion voltage. The difference in the resulting mobility parameters is within the range of the errors calculated by Minuit.

The penetration depth of 660 nm light changes by about 50 % in the investigated temperature range. Comparison of simulations with the longest and shortest penetration depth and otherwise unchanged parameters showed no significant change in the overall pulse shape, thus the simulation neglected the temperature dependence of the penetration depth.

Optical photons of 660 nm wavelength (1.87 eV) produce so called hot charge carriers as the band gap of silicon is approximately 1.12 eV at room temperature. The simulation assumes charge carriers in thermal equilibrium with the crystal lattice. This is considered to be a good approximation as the thermalization of the hot carriers is usually very fast compared to the pulse duration.

6.1. Fitted mobility parameters

The fitted values of the ohmic mobilities at 300 K reproduce the accepted values [17] within the quoted errors. For the temperature coefficients reported values range from -2.3 to -2.6 for electrons and -2.2 to -2.8 for holes [17]. The best fit value of holes lies within the quoted interval, while the electron value of this work lies just outside the quoted interval (rounded to the second digit it would be -2.3 and
Figure 5: Comparison of different parameterizations of the saturation velocity proposed by other authors. In the publication of Jacoboni et al. [3] two parameterizations are given. The one parameterizing the electron saturation velocity directly has been labeled ‘2nd model’.

thus at the edge of the quoted interval), although it is lower than the value of -2.0 which has been reported by [5].

Saturation velocity parameterizations as function of temperature have been provided by many authors [3, 18, 19], however the values differ by about 20 %. The values for electrons of this work are in the range of values proposed by others, while the values for holes are higher than reported by [3, 18, 19], however values as high as 0.96×10^{-7} cm/s at 300 K have been found by Smith and Frey [20], which is even larger than the value determined in this work. A comparison is shown in Figure 5.

Except from the value of [3] for <111> orientation, no parameterizations of β values as function of temperature and crystal orientation are available.

A comparison of mobilities and resulting drift velocities determined in this work with those proposed by other authors is shown in Figure 6. A distinctive difference in the transition behavior between ohmic mobility and saturation velocity between the different parameterizations is observed. Especially for electrons a lower mobility than predicted in [3, 5] is observed (for both crystal directions). In the transition region hole mobilities in the <111> direction tend towards the values proposed by [3], while the mobilities in the <100> direction tend towards the values proposed by [5]. In the saturation region the wide spread in the proposed velocities (shown in Figure 5) dominates, especially at lower temperatures (not shown).

7. Discussion and Conclusions

A new parameterization of the field and temperature dependence of electron and hole mobilities in bulk silicon was presented for <111> and <100> crystal orientation.

The differences between the values derived from the parameterization presented here and the parameterization of Jacoboni et al. [3] are approximately ±15% in the investigated temperature regime. The differences as function of
electric field are shown in Figure 7. While the parameter-
izations for electrons in the <111> show an almost con-
stant difference of -5%, the other parameterizations show
distinctive differences. The differences to the parameteri-
zations proposed by others [4, 5, 6, 7] are even larger and
are not presented here.

No samples of <110> orientation were available, thus
no parameters for this direction could be determined. The
electron parameters for this orientation provided by Cas-
toldi and Rehak [21] (\(\mu_0^e = 1394 \text{ cm}^2/\text{Vs}, v_{\text{sat}}^e = 1.85 \times 10^6 \text{ cm/s},\)
\(\beta_{\text{e}}^{<110>} = 2.51\)) differ greatly from the parameters pre-

tioned here. However they have been obtained with very
low fields (up to 700 V/cm), in which case the simple pa-

terORIZATION used in this work might need some correc-
tions.

The presented results resolve the ambiguities in the
temperature dependent mobility parameterizations for the
<111> orientation [3, 4, 5, 7] and present a parameteriza-
tion for the <100> orientation. The results obtained enable
more precise simulations of the charge collection in
silicon sensors. The study is part of the investigation of the
plasma effect, which occurs when the density of the
electron hole pairs produced by the radiation exceeds the
bulk doping density of the silicon. Its understanding is of
relevance for the design of silicon pixel sensors for experi-
ments at x-ray free-electron-lasers. It turned out, that the
precision of the literature data on the transport parame-
ters was insufficient to describe the reference data at low
charge carrier densities. Therefore more precise data was
required for a quantitative simulation of the plasma effect
[23].

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