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Research Article

Keywords: Photoelectric encoder, Integrated optics, Sinusoidal error compensation, Particle swarm optimization, Subdivision accuracy.

DOI: https://doi.org/10.21203/rs.3.rs-628854/v1

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Improving the Subdivision Accuracy of Photoelectric Encoder Using Particle Swarm Optimization Algorithm

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Abstract: To improve the subdivision accuracy of a photoelectric encoder and reduce the effects of sinusoidal errors in the signals on the measurement accuracy of the system, we designed an optoelectronic chip to receive grating moiré fringe signals. An amplifier circuit with a hierarchical pipelined architecture was designed, and the photodetector array was matched with the code disk before processing the received signals. Thereafter, a quantitative analysis was performed on the sinusoidal errors in the signals. From the analysis results, a sinusoidal error compensation method based on the particle swarm optimization (PSO) algorithm was developed, and a subdivision error compensation model was established to correct the errors in the signals. Finally, a fast solution for the PSO algorithm was implemented on a field-programmable gate array, and a grating test platform was built for experimental verifications. The results showed that the peak-to-peak subdivision error of the encoder’s photoelectric signal decreased by approximately 60% from 2.98” to 1.13”. Therefore, the scheme proposed in this paper is expected to significantly improve the measurement accuracies of photoelectric encoders.

Keywords: Photoelectric encoder, Integrated optics, Sinusoidal error compensation, Particle swarm optimization, Subdivision accuracy.
1. Introduction

Photoelectric encoders are high-precision sensors used for angle measurements; these devices allow digital angular displacement measurements with optical–mechanical–electrical integration, are widely used in engineering practice, and serve as important components in intelligent processing, satellite communication, servo, and aerospace systems, among others (Yu et al. 2020; Wang 2012; Yang 2012). In certain important applications, such as aerospace technology, the system requirements are demanding not only for encoder angle measurement accuracy and resolution but also size of the system (Ahmadi Tameh et al. 2016; Das and Chakraborty 2018). Traditional photoelectric encoders exhibit several obvious shortcomings. On the one hand, because of limitations imposed by the optical and electrical principles, the encoder requires a complex grating inscription for the code disk; moreover, the photoelectric receiving device design prevents a high degree of integration (Chen et al. 2015; Poulton et al. 2017). On the other hand, factors such as ambient temperature, device aging, and mechanical errors directly affect the quality of the moiré fringe photoelectric signal. The angle measurement accuracy of the photoelectric encoder is mainly limited by the accuracy of the subdivision of the moiré fringe photoelectric signal, which is determined by the signal quality (Das et al. 2016; Su et al. 2015; Yang and Ting 2014; Bajic et al. 2014). Therefore, it is of great significance to develop a miniaturized, high-precision, and high-resolution photoelectric encoder.

In recent years, various research teams from around the world have studied the optical–mechanical structures of photoelectric encoders, extraction of moiré fringe photoelectric signals, electronic processing circuits, and subdivision error compensation for moiré fringe signals. The incremental photoelectric encoder developed by Heidenhain (Germany) for the Galileo Telescope in Italy achieved an angle measurement accuracy of 0.036″ after adjustment using multiple reading heads and correction of the system’s full-circle error (Mancini et al. 2008). NASA has applied optical image recognition technology and developed a 27-bit ultrahigh-resolution Cartesian absolute optical encoder that achieved a high resolution of 0.01″ (Leviton et
Based on the principles of the CMOS sensor and optical mouse, Tresanchez et al. (2010) acquired image information and developed a low-cost, high-resolution angle absolute rotary encoder. Qiao (2015) developed an absolute grating ruler using integrated circuit technology, where the image sensor and photovoltaic array were integrated in a single detector; this scheme realized the receiving position code and moiré fringe signal simultaneously with a single chip and reduced the size of the photoelectric sensing component. Heydemann first proposed a compensation scheme for moiré fringe signals based on least-squares fitting; thereafter, several researchers have continuously improved and optimized this algorithm. Warner et al. (2008) at the US National Optical Astronomy Observatory proposed the periodic error compensation and grating subdivision error correction methods. Hong and Xu (2008) from the Changchun Institute of Optics, Fine Mechanics and Physics at the Chinese Academy of Sciences proposed an error compensation method based on the radial basis function (RBF) and constructed a neural network with good learning and generalization abilities to improve the measurement accuracy of the encoder. Tan et al. (2002) employed the Lissajous figure mapping method to indirectly reduce the effects of sinusoidal deviations on the subdivision errors; however, this method cannot accurately reflect the quality of the signal in the presence of harmonic components. Kaul et al. (2008) performed discrete calibrations on the angle measurement errors and described the error curve using Fourier series expansion; they proposed a harmonic analysis method based on the Fourier transform theory and achieved error compensation for harmonics within the 10th order. The sinusoidal characteristics constitute one of the important indicators of the quality of the moiré fringe photoelectric signal that greatly affect the resolution and accuracy of an encoder. From the optical and mechanical perspectives in practical applications, it would be costly and challenging to reduce the impact of the sinusoidal deviations of signals on subdivision errors. Therefore, this study considers the electrical perspective and address the processing and compensation of photoelectric signals in the presence of sinusoidal deviations, thereby improving the subdivision accuracy of the encoder.

The electrical part of the sensor system is composed of optoelectronic devices and
signal processing circuits. Presently, there are relatively few studies that have reported
the integration of optoelectronic devices with signal processing circuits. With the
development of integrated circuit technology, greater integration of circuits with
optoelectronic devices has become the mainstream trend (Mu et al. 2019). Further, the
existing error compensation schemes for grating moiré fringe photoelectric signals can
only be implemented under specific conditions owing to the complexity of the
algorithm and low operation speed, with most methods relying on postprocessing of the
encoder signals. Thus, automatic compensation cannot be realized when there are
changes in the environmental variables. In this regard, the present work is divided into
two parts: (1) An optoelectronic integrated chip that can be applied to high-precision
photoelectric encoders was developed; an adjustable gain amplifier was also designed
to compensate for the signals in real time and eliminate the influence of the even
harmonics on signals, with reduction in the size of the photoelectric sensing component.
(2) An automatic compensation method for the sinusoidal deviations of the moiré fringe
photoelectric signals was proposed. A compensation model was established for the
subdivision errors caused by the sinusoidal deviations, and the particle swarm
optimization (PSO) algorithm was adopted to solve the waveform parameters. The
automatic compensation method thus helped improve the angle measurement accuracy
of the system.

![Fig. 1](image.png) Structure of the grating angular displacement measurement system

2. Design of the Optoelectronic Integrated Chip

2.1 System architecture
During the actual measurement process, sinusoidal deviations of the moiré fringe signals from the encoders could be induced by factors such as uniformity of the grating code disk, optomechanical assembly, and service environment. The principle by which the signals are read is illustrated in Fig. 2. An LED light source is used to illuminate the main grating, and the optical signal is received by the photodetector array after passing through the indicating grating. Thereafter, the signal is converted to an analog output by a fully differential amplifier (FDA) with adjustable gain such that a signal without the even harmonics and DC level drift can be obtained. The optoelectronic chip designed for the encoder in this work adopts an arrangement where a group of bright/dark codes correspond to four photodetectors. The symbol has a width of 100.9 μm, and the width of its corresponding detector is 25 μm. The detector array in Fig. 2 contains 120 optoelectronic devices, and each photodetector is of size 25×600 μm². A total of 1024 incremental codes were inscribed uniformly along the circumference of the code disk such that the angular separation between two adjacent codes is 0.35°.

![Fig. 2 Principle of reading the incremental code](image)

Fig. 3(a) shows the overall architecture of the designed optoelectronic chip, where the photodetector and processing circuits are integrated to realize accurate processing of the acquired optical signals. Further, symmetry and stability were ensured in the chip circuit for maximal restoration of the measured optical signals (Silva-Martinez et al. 2003; Chen and Hong 2005). The detector array was arranged as noted above: four incremental photoelectric signals with phase differences of 90° were obtained and converted to analog sinusoidal signals before being output by the amplification and
readout circuit. The amplifier circuit has a hierarchical pipelined structure, as shown in Fig. 3(b). In the first stage, a low-noise, high-gain transimpedance amplifier (TIA) was used to convert the nanoampere photocurrent signals into millivolt voltage signals. At the same time, to eliminate the even harmonics and increase the amplitude of the signal, two sets of signals with a phase difference of 180° were output from the chip after passing through the differential amplifier. Given possible differences in the actual light intensities, multiple trim ranges were reserved such that the amplifier gain could be varied by adjusting the resistance. The specific adjustment resistance values are listed in Table 1.

![Fig. 3 Configuration of the designed integrated chip](image)

Table 1 Resistance values in RTI and RX

| Resistance array | RTI | RX |
|------------------|-----|----|
| Value            | 480K| 36K|
| Gain adjustable range | 480K–522K | 3–8 times |

2.2 Operational amplifier design

In this work, a traditional two-stage FDA structure was adopted as the main amplifier, as shown in Fig. 4. To meet the design requirements of low power consumption and low noise, a differential pair tube was selected as the input stage, and a load tube and Miller compensation capacitor were incorporated. The current bias was
introduced by the gates of the mirror current sources M5, M6, and M9 to supply current to each branch.

![Differential amplifier circuit](image)

**Fig. 4** Differential amplifier circuit

P-type metal-oxide semiconductor (PMOS) transistors were used for the input differential pair tube for the amplifier. As the carrier mobility of a PMOS transistor is approximately 2–5 times greater than that of an N-type metal-oxide semiconductor (NMOS) transistor, the 1/f noise of the PMOS transistor is 2–5 times lower than that of the NMOS transistor (Fieque et al. 2011; Tsai et al. 2012). The noise from other PMOS transistors that serve as current sources can be ignored. The noise of the input stages M1 and M3 increase the gain of the second-stage amplifier. The noises of M2 and M4 are equal to those of M1 and M3, respectively. Thus, a 2-time expression was given in the corresponding formula. Since the noise of the output stage is not amplified, it can be ignored. The expression for the first-stage amplifier gain $A_{v1}$ is as follows:

$$A_{v1} = g_{m1} (r_{o1} \| r_{o3})$$

The formula for the total output noise is

$$\overline{V_{noise,out}}^2 = 2g_{m8}r_{o8}^2 \left[ \frac{4kT}{3} \left( g_{m1} + g_{m3} \right) (r_{o1} \| r_{o3})^2 + \frac{K}{C_{ov}WLL_f} r_{o1} \| r_{o3})^2 + \frac{K}{C_{ov}WLL_f} r_{o1} \| r_{o3})^2 \right].$$

The formula for the overall gain of the amplifier is

$$A_v = g_{m1} (r_{o1} \| r_{o3}) g_{m8}r_{o8}$$

The formula for the equivalent noise at the input is
\[
\overline{V_{\text{noise, in}}}^2 = \overline{V_{\text{noise, out}}}^2 = 2 \left[ \frac{8}{3} kT \left( \frac{1}{g_{m1}} + \frac{g_{m3}}{g_{m1}} \right) + \frac{K}{C_{ox} W_1 L_1 f} + \frac{K}{C_{ox} W_3 L_3 f} \right].
\] (4)

According to the structure shown in Fig. 4(b), the input characteristics of the amplifying circuit can be expressed as follows:

\[
i_{\text{PD0}} = I_0 + I_1 \sin \left( 2 \pi ft + 0^\circ \right) = I_0 + I_1 \sin 2\pi ft,
\]

\[
i_{\text{PD2}} = I_0 + I_1 \sin \left( 2 \pi ft + 180^\circ \right) = I_0 - I_1 \sin 2\pi ft,
\]

where \(I_0\) is the common-mode value, and \(I_1\) is the amplitude.

The expressions for the TIAs are as follows:

\[
v_{\text{ol}} = (V_{\text{ref}} + R_I I_0) + R_I I_1 \sin 2\pi ft,
\]

\[
v_{\text{o3}} = (V_{\text{ref}} + R_I I_0) - R_I I_1 \sin 2\pi ft.
\]

By further deriving the output of the SIN pin in Fig. 3(b), the s-domain expression of the system can be obtained as follows:

\[
V_{\text{SIN}} = \frac{R_I R_5}{(R_I C_1 S + 1)(R_3 C_3 S + 1)(R_p C_p S + 1)} (i_3 - i_1).
\] (9)

If the system is expressed as \(H(j\omega) = H_0 e^{j\varphi}\) and input signal is expressed as \(i_w = i_3 - i_1 = -2I_1 \sin \omega t\), the SIN output voltage can be expressed as

\[
v_{\text{SIN}} = V_{\text{ref}} - 2I_1 H_0 \sin (\omega t + \varphi)
\]

where

\[
H_0 = \frac{R_I R_5}{\sqrt{1 + (R_I C_1 \omega)^2} \sqrt{1 + (R_3 C_3 \omega)^2} \sqrt{1 + (R_p C_p \omega)^2}},
\] (10)

\[
\varphi = -\arctan(R_I C_1 \omega) - \arctan(R_3 C_3 \omega) - \arctan(R_p C_p \omega).
\] (11)

By establishing a simulation model according to the above formulas, the gain of the system is obtained as \(A = 93.48 \text{ dB}(v/\text{i})\); at the dominant pole (-3 dB), \(f_{-3 \text{ dB}} = 425 \text{ kHz}\); the input signal is the peak-to-peak value of \(v_{pp} = 1 \text{ V}\) of the SIN output signal at a low frequency of 50 Hz; the input signal is the peak-to-peak value \(v_{pp} =\)
937.55 mV > \frac{1}{\sqrt{2}} = 707 mV of the SIN output at 300 kHz.

2.3 Chip implementation

![Chip implementation image]

The chip was fabricated using a standard 1-poly 3-metal XFAB 0.35 μm commercial CMOS process and had a total area of 3.5×1.8 mm². The layout and photograph of the chip are shown in Fig. 5. The detectors for receiving the incremental signals are arranged in a sector pattern, with an angle of 7.03°. This detector arrangement corresponding to the code disk can avoid errors caused by code-disk contamination and uneven light sources, thereby ensuring adequate quality of the incremental signals. Each photodetector had a size of 25×600 μm². To better guarantee consistency between the two amplifier circuits, the amplifiers were matched, and an array design was adopted for the feedback resistance. In addition, the capacitors and resistors were isolated from each other using dummy components. The index parameters of the incremental signal chain are summarized in Table 2.

| Table 2 Main specifications of the fabricated chip |
|-----------------------------------------------|
| Technology                                    | XFAB 0.35μm,1P3M,CMOS                      |
| Die size                                      | 3.5×1.8 mm²                                |
| Detector Pitch                                | 25 μm                                      |
| Array Size                                    | 120 PD                                     |
| Output                                        | 2 ports (Differential)                      |
3. Principles of PSO and Sinusoidal Error Compensation

3.1 Grating measurement principle and signal error source analysis

It can be seen from Fig. 1 that the grating measurement system is composed of the main grating, an indicating grating, a light source, and a photoelectric receiving device. The relative movements of the main and indicating gratings during measurement generate the moiré fringes, which are then converted to moiré fringe photoelectric signals by the photoelectric receiving device. Finally, the signals carrying the angular position information are converted to weak electrical signals. As described in Section 2, the indicating grating is generally designed as a window form where a one-bit code corresponds to four photodetectors. The initial moiré fringe signal is converted into four photocurrent signals with phase differences of 90°, passed through the signal processing circuit, converted and amplified into a voltage signal, and finally passed through an analog to digital converter (ADC).

During the measurement process, factors such as light source, grating design, optomechanical assembly, signal processing, and service environment may result in sinusoidal errors in the moiré fringe signals, thereby affecting the system’s subdivision accuracy. Therefore, it is necessary to correct and compensate for the sinusoidal errors in the signal to achieve high-precision measurement.

3.2 Principle of moiré fringe photoelectric signal subdivision

The arctangent subdivision (ATS) method is a commonly used digital subdivision approach based on the relationship between the amplitude and phase of the moiré fringe signal. The expression for the ideal grating signal after two-stage differential processing is as follows:
The ATS method obtains the phase of the signal by constructing a tangent function as follows:

\[
    u(\theta) = \begin{cases} 
    \tan \theta = \frac{|A \sin \theta|}{A \cos \theta}, & |A \sin \theta| \leq |A \cos \theta| \\
    \cot \theta = \frac{|A \cos \theta|}{A \sin \theta}, & |A \sin \theta| > |A \cos \theta|
    \end{cases}, \quad \text{(13)}
\]

\[
    \theta = \arctan \left( \frac{A \sin \theta}{A \cos \theta} \right) \quad \text{(14)}
\]

Let \( u'_1(\theta) = |A \sin \theta| \) and \( u'_2(\theta) = |A \cos \theta| \); according to formula (13), the differences between \( u'_1(\theta) \), \( u'_2(\theta) \), and \( u(\theta) \) are shown in Fig. 6.

![Fig. 6 Comparisons among the original signals and constructed function](image)

The function \( u(\theta) \) divides a grating period into eight intervals. By calculating the arctangent of \( u(\theta) \) and the corresponding subdivision values according to the interval number, the relationship between the interval numbers and subdivision values are obtained, as shown in Table 3.

**Table 3** Calculation formulas for the phase intervals

| Interval number | S | Interval number | S |
|-----------------|---|-----------------|---|
| 1               | \( S \times \arctan \frac{A \sin \theta}{A \cos \theta} \) | 5 | \( \frac{S}{2} + \frac{S \times \arctan \frac{A \sin \theta}{A \cos \theta}}{2\pi} \) |
3.3 Error compensation principle and signal waveform construction

The subdivision principle for the grating moiré signal involves solving the angle from two ideal sine and cosine signals. However, the actual output grating moiré signals contain harmonic components. According to Fourier series theory, the grating moiré fringe signal can be expressed as

\[
\sin \theta = \sum_{i=0}^{n} A_i \sin(i\theta) + B_i \cos(i\theta),
\]

where \( A_i \) and \( B_i \) (\( i = 0, 1, 2...n \)) are the amplitudes of the harmonics of the sine and cosine signals, respectively.

The subdivision accuracy and subdivision error are interdependent, that is, the subdivision accuracy can be reflected by the subdivision error and vice versa. Subdivision is realized using ideal sine and cosine signals; however, the actual outputs are not ideal sine or cosine signals. Therefore, there will be a difference between the subdivision value of the actual and theoretical angles, which is the subdivision error. The moiré fringe signal is compensated on the basis of the above analysis. In this work, the angle errors caused by the signal harmonics were quantized, and a lookup table was used to directly compensate for the sinusoidal errors of the angle subdivisions. The specific process is illustrated in Fig. 7.

---

**Fig. 7** Flowchart depicting the angle compensation method
The figure shows that the angle value containing the error can be obtained from the arctangents of the two sampled sine and cosine signals with errors. Then, the angle error compensation lookup table is constructed according to the waveform equations to compensate for the angle. Finally, the compensated angles are subdivided to the accurate subdivision numbers. The actual angle $\theta_m$ is expressed as

$$\theta_m = \arctan \frac{u_{\sin}(\theta)}{u_{\cos}(\theta)}.$$  \hspace{1cm} (16)

The subdivision error $\Delta \theta$ is given by

$$\Delta \theta = \theta_m - \theta.$$  \hspace{1cm} (17)

It can be seen that the sinusoidal error compensation of the grating moiré fringe signal can only be realized by establishing a complete signal waveform equation and solving its position parameters. To construct a lookup table between the actual angles and the corresponding errors, waveform equations must be obtained for the two signals that are to be compensated. Five sets of signals were collected from the measurement platform and were Fourier transformed, as shown in Fig. 8.

![Fig. 8 Spectrum of the grating moiré fringe signal](image)

According to the spectrum in Fig. 8, the 0th, 3rd, 5th, and 7th-order harmonics in the signal account for the largest proportion (approximately 10%) of the fundamental wave, whereas the 2nd, 4th, 6th, 8th, and remaining-order harmonics only account for a small proportion (2%). This result shows that the developed optoelectronic chip can effectively suppress the even harmonic components of the signal. Therefore, the
waveform equation is given by formula (18):

\[
\begin{align*}
\sin \theta' &= A_0 + A_1 \sin \theta + A_3 \sin (3\theta) \\
&\quad + A_5 \sin (5\theta) + A_7 \sin (7\theta) \\
\cos \theta' &= B_0 + B_1 \cos \theta + B_3 \cos (3\theta) \\
&\quad + B_5 \cos (5\theta) + B_7 \cos (7\theta)
\end{align*}
\] (18)

According to the above analysis, it is necessary to solve the five unknown parameters in the two signal waveform equations to eliminate their harmonic components.

3.4 Waveform parameter solution based on the PSO algorithm

Based on the signal waveform equation obtained above, five positional parameters are determined. Considering a sine signal as an example, the PSO algorithm is used for fitting and can be expressed as follows:

\[
u_{\sin}'(\theta) = f \left( A_0, A_1, A_3, A_5, A_7 \right),
\] (19)

where \(A_0, A_1, A_3, A_5,\) and \(A_7\) represent the five unknown parameters in the waveform equation. During each signal period, 128 data points are collected on average. The minimum sum of squared errors of the collected signals are then used as the cost function of the estimated parameters as

\[
e = \min \left\{ \sum_{i=0}^{128} (U_i - f \left( A_0, A_1, A_3, A_5, A_7 \right))^2 \right\},
\] (20)

where the fitness is defined as

\[
\text{fitness} = \sum_{i=0}^{128} (U_i - f \left( A_0, A_1, A_3, A_5, A_7 \right))^2.
\] (21)

When using the PSO algorithm to solve the waveform parameters, the spatial position of each particle is composed of five variables. In the iteration process, updates are performed according to the best position of each particle (\(p_{\text{best}}\)) and the global best position (\(g_{\text{best}}\)). The iteration formula is as follows:

\[
v_{in}(k+1) = w v_{in}(k) + c_1 r_1 \left[ p_{id}(k) - x_{id}(k) \right] \\
+ c_2 r_2 \left[ g_{d}(k) - x_{id}(k) \right],
\] (22)
\[ x_{id}(k+1) = x_{id}(k) + v_{id}(k+1) \]  

where \( k \) is the number of iterations; \( d \) is the dimensionality and represents the number of independent variables; \( r_1 \) and \( r_2 \) are two independent and uniformly distributed random numbers; \( x_{id}(k) \) is the \( d \)th dimensional component of particle \( i \) in the \( k \)th iteration; \( p_{id}(k) \) is the \( d \)th dimensional component of particle \( i \)’s best position vector \( p_{best} \) in the previous \( k \) iterations; \( g_{id}(k) \) is the \( d \)th dimensional component of the swarm’s best position vector \( g_{best} \) in the previous \( k \) iterations; \( v_{id}(k) \) is the \( d \)th dimensional component of particle \( i \)’s current velocity vector in the \( k \)th iteration; \( w \) is the inertial weight; \( c_1 \) and \( c_2 \) are acceleration constants, with values commonly in the range of 0–2.

According to the iteration formula of the PSO algorithm, particles in a given space will gradually approach the position with the smallest fitness value, and the final particle positions with the smallest fitness is the solution to the waveform equation parameters. The specific process of realizing parameter identification using the PSO algorithm is illustrated in Fig. 9.

---

**Fig. 9** Solving the waveform parameters by particle swarm optimization
Field programmable gate arrays (FPGA) offer advantages such as multithreaded operations and strong algorithm implementation capacity. Thus, an FPGA was used herein to implement signal error compensation. In addition, to obtain a more intuitive sinusoidal error compensation, a 10-bit subdivision was performed on the collected moiré fringe signals. The results before and after processing are also compared to verify the effectiveness of the subdivision error compensation algorithm. The key to angle error compensation is to obtain the signal waveform equation and establish the angle compensation lookup table, as noted above. The accuracy of the waveform parameters directly affect the compensation outcome. When solving the signal waveform parameters using the PSO algorithm on an FPGA—given the complexity, fitting accuracy, and time cost of the PSO approach—the algorithm was reasonably simplified while ensuring fitting accuracy by mainly considering the impact of the inertial weight on the result.

The inertial weight \( w \) coordinates the local and global optima. A relatively small value of \( w \) is conducive to the convergence of the algorithm, where the particle search tends to favor the local region; on the contrary, a relatively large value of \( w \) is conducive to escaping the local region, thereby improving the global search ability of the particles. The commonly used linear inertial weight change formula is given as follows:

\[
w = w_{\text{max}} - \frac{w_{\text{max}} - w_{\text{min}}}{\text{iter}_{\text{max}}} \times k
\]

(24)

where \( w_{\text{max}} \) is the maximum inertial weight, \( w_{\text{min}} \) is the minimum inertial weight, \( \text{iter}_{\text{max}} \) is the maximum number of iterations, and \( k \) is the current number of iterations.

Generally speaking, if the number of particles \( N \) in the particle swarm is too small, the algorithm is likely to be trapped in a local optimum; on the contrary, if \( N \) is too large, the algorithm cannot converge easily. Normally, \( N \in [10, 50] \), and for most problems, 10 to 20 particles provide ideal convergence results (Gao et al. 2013). In this study, the number of particles \( N \) was set to 20, maximum number of iterations was set to 300, \( w_{\text{max}} = 0.9 \), and \( w_{\text{min}} = 0.1 \) were used in the calculations. The lookup table for the actual calculated angles \( \theta_m \) and errors \( \Delta \theta \) can be constructed from the obtained waveform parameters and waveform equations, thereby realizing compensation for the sinusoidal
errors. When constructing the angle error lookup table, the first step is to identify the angle errors accurately through the lookup table address (Zhu et al. 2019). The lookup table constructed in this study is shown in Table 4.

| Addr | 0 | 1 | 2 | ... | M-1 | M |
|------|---|---|---|-----|-----|---|
| θₘ₀ | 0 | θ' | 2θ' | ... | (M-1)θ' | Mθ' |
| Δθ  | Δ₀ | Δ₁ | Δ₂ | ... | Δₘ₋₁ | Δₘ |

In the table above, Addr is the lookup table address, θ' is the angle interval of the lookup table, and M is the maximum address of the lookup table. Finally, according to formula (17), the sinusoidal errors in the signals could be compensated using the FPGA.

4. Experiments and Analyses

4.1 Establishment of the experimental platform

To verify the effectiveness of the proposed sinusoidal error compensation scheme, we built a high-precision angular displacement sensor test platform based on the optical continuous closed loop, as shown in Fig. 10. The test system mainly includes the angle standard, autocollimator, photoelectric encoder, ADC module, FPGA module, logic analyzer, and turntable. Among these, the angle standard is a high-precision 23-plane optical polyhedron. The measurement encoder and polyhedron are connected coaxially to ensure that there are no angle transmission errors between the reference and measured objects. The crossline of the autocollimator is maintained consistent with the rotation direction of the photoelectric encoder, and the aiming error of the autocollimator is ±0.05" within a 300" range. The reflecting surface of the polyhedron is parallel to the axis of rotation of the photoelectric encoder. An air-bearing turntable is used to rotate the encoder at small angles, with a minimum step size of 0.1". The turntable has a vertical shaft structure, where the two shaft systems can move independently. The specific parameters of the test platform are listed in Table 5.

| Table 5 | Parameters of the accuracy testing platform |
| Instrument          | Parameter                                                                 |
|---------------------|---------------------------------------------------------------------------|
| Angle standard      | 23-plane optical polyhedron                                              |
| Autocollimator      | Accuracy: 0.03” within the range of ±50”                                  |
|                     | 0.05” within the range of ±300”                                          |
|                     | Resolution: 0.003”                                                       |
|                     | Repeatability error: 0.01”                                               |
| Air-bearing turntable| Angle accuracy: ±0.5”                                                     |
|                     | Angle resolution: 0.2”                                                   |
|                     | Radial movement accuracy: 0.1 μm                                           |
|                     | Axial movement accuracy: 0.1 μm                                           |

A 22-bit absolute photoelectric encoder prototype with a code disk structure, as described in Section 2.1, is used in the test platform. The code disk contains two code channels: a pseudorandom code channel along the outer circle and an incremental code channel along the inner circle. The optoelectronic integrated chip developed in this work is used in the encoder to receive and process the incremental code signals. The ADC module contains a 12-bit AD9238 chip with a maximum sampling frequency of 65 MHz and an effective precision of 11.2 bits. The FPGA used in the platform is an XC7Z020-2CLG400I from XILINX. The ADC chip converts the analog signal outputs from the encoder optoelectronic chip to digital signals, which are then collected by the logic analyzer and transmitted to the FPGA to implement the compensation algorithm.

Fig. 10 Subdivision error detection system of the photoelectric encoder
4.2 Subdivision error correction

In the actual test process, the 23-plane polyhedron and angular displacement encoder are installed coaxially, and the photoelectric autocollimator is used to calibrate the 23-plane polyhedron. When the sight axis of the autocollimator is perpendicular to the working surface of the 23-plane polyhedron, the readings from the angular displacement encoder are recorded and compared with the angles of the autocollimator. The difference between these two values is the error of the encoder. To obtain the corrected results in a more intuitive manner, an oscilloscope is used to display the two-channel output results from the photoelectric encoder, as shown in Fig. 11. It is seen from the figure that the sinusoidal characteristics of the signal are severely affected by the presence of harmonic components. The frequencies of the two output signals are both equal to 100 kHz, with amplitudes of 850 mV and phase difference of 90°.

![Fig. 11 Sampling signal waveform](image)

To subdivide the acquired signals, the 90° and 180° phases of the signal are considered as the starting positions, and an arbitrarily cycle is selected for processing. As noted above, the effective precision of the 12-bit ADC chip used in this work is 11.2 bits. Given the accuracy of the data results, only 10-bit effective data are retained herein. According to the analysis in Section 3, the signal can be divided into eight intervals evenly within one period; thus, 128 points are collected in each interval for processing, and the results are shown in Fig. 12.
Fig. 12 Uncompensated and compensated dynamic subdivision errors at (a) 90° and (b) 180°

For the signal with the 90° starting position, the maximum dynamic subdivision error of the grating moiré signal was +1.79" , minimum was -1.19" , and peak-to-peak error value was +2.98" before correction. After compensation, the maximum dynamic subdivision error was +0.61" , minimum was -0.56" , and peak-to-peak error value was +1.17". For the signal with the 180° starting position, the maximum dynamic subdivision error of the grating moiré signal was +1.64" , minimum was -1.18" , and peak-to-peak error value was +2.82" before correction. After compensation, the maximum dynamic subdivision error was +0.62" , minimum was -0.51" , and peak-to-peak error value was +1.13". The subdivision error was thus reduced by 60%, and the subdivision accuracy improved significantly. By performing Fourier transform on a complete cycle of signals starting at 90° as shown in Fig. 12(a), the results shown in Fig. 13 are obtained.

Fig. 13 Spectrum results of grating moiré signals before and after sinusoidal error compensation

It is seen from the figure that before compensating for the sinusoidal errors, the
grating signal contained obvious 0th, 3rd, 5th, and 7th-order harmonic components. After compensation, the influences of the odd harmonics and DC components in the signal were suppressed effectively, and the overall subdivision error of the signal was reduced significantly. From the above analysis, the compensation scheme proposed in this work for the positive linearity error of the signal is effective, and the subdivision accuracy of the grating moiré fringe signal is improved significantly.

5. Conclusions

To reduce the effects of sinusoidal errors in grating moiré fringe signals on the subdivision accuracy and to improve the subdivision accuracy of the photoelectric encoder, an optoelectronic chip was developed in this study for receiving and processing incremental code signals. The advantage of the proposed method is that a photodetector array matching the incremental code disk is employed, which effectively reduces the errors caused by light diffraction and uneven light sources. A differential amplifier designed with adjustable gain can eliminate the even harmonic components of the signal, thereby improving the signal quality. Further, a subdivision error compensation model and an angle compensation scheme were established for the signal. Then, the PSO algorithm was implemented on an FPGA to solve for the parameters of the sinusoidal waveform equations, which were combined with the subdivision error compensation model. An angle error compensation lookup table was designed to further compensate for the sinusoidal errors in the signal. Finally, the effectiveness of the compensation model was verified through experiments. The experimental results show that the subdivision errors of the encoder's photoelectric signals were reduced by about 60% and that the peak-to-peak values of the subdivision errors decreased from 2.98<sup>°</sup> to 1.13<sup>°</sup>. The subdivision accuracy was also significantly improved. Therefore, the system scheme proposed in this paper can effectively compensate for sinusoidal errors in grating moiré signals, reduce the size of the photoelectric sensor, and improve the angle measurement accuracy of the grating measurement system. In the next study, we
intend to explore integration of the ADC module with the encoder’s optoelectronic chip. Moreover, we expect to implement the algorithm on a DSP chip to develop a complete and highly integrated error compensation system for encoder errors.

**Declarations**

**Funding:** Project of Development and Reform Commission of Jilin Province, China (No. 2019C035-3).

**Conflicts of interest:** The authors have no conflicts of interest to declare that are relevant to the content of this article.

**Availability of data and material:** The datasets generated during and/or analysed during the current study are available from the corresponding author on reasonable request.

**Code availability:** The datasets generated during and/or analysed during the current study are available from the corresponding author on reasonable request.

**Authors' contributions:** All authors contributed equally to this work.

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