Hierarchical Control in Islanded DC Microgrids with Flexible Structures

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Abstract

In this work, we propose a comprehensive three-layered hierarchical control architecture for islanded DC microgrids (DCmG) to achieve a well scheduled and balanced utilization of various resources. Unlike previous contributions, we discuss a top-to-bottom control scheme guaranteeing voltage stability and allowing for generic topologies. Our supervisory control layer comprises a secondary and a tertiary layer and it rests on top of a primary voltage layer. The tertiary layer is governed by an Energy Management System (EMS), which generates optimal power references and decision variables for generation units by solving an MPC problem at every sampling instant. In particular, the generated decision variables take decisions on turning ON/OFF of dispatchable generators, and operation modes of PV generators and batteries. The secondary layer receives power references from the EMS and translates them into appropriate voltage references for the primary layer by solving an optimization problem. We show that a simplified version of the secondary optimization problem is guaranteed to be always feasible. Moreover, since the voltages can only be enforced at the generator nodes, we provide a novel condition to guarantee the uniqueness of the solution for load voltages and power injection of the generation units. This uniqueness condition can be verified at each load node by utilizing local load parameters, and does not require any information about microgrid topology. Notwithstanding that EMS commands can alter the topology of the DCmG, switching on or off some generation nodes, the overall voltage stability is maintained by decentralized primary controllers. The functioning of the proposed architecture is validated via simulations on a modified 16-bus DC system [1].

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1 Introduction

Microgrids (MGs) are small-scale electric networks comprising different devices such as distributed generation units (DGUs) interfaced with power-electronic converters, energy storage units, and loads. MGs can operate in grid-connected and islanded modes, and are compatible with both AC and DC operating paradigms [2][3][4]. In particular, DC microgrids, due to their ability to interface naturally with several renewable energy sources (for instance PV modules), batteries, and electronic loads (various appliances, LEDs, electric vehicles, etc), have gained traction in the recent years [5][6].

The overall control of an islanded DC microgrid (DCmG) is a multi-objective problem spanning different control stages, time scales, and physical layers. For a stable and economic operation of a microgrid, a hierarchical control scheme is generally employed [5][6][7]. The primary control layer, acting at the component level, is responsible for voltage stability, which is crucial for islanded DCmGs. In this work, we consider the reference setting where the DGUs are equipped with decentralized primary controllers designed to track suitable voltage references. To this purpose, several approaches, for example based on droop control [3][5] and plug-and-play control [8][9], have been proposed in the literature.

Primary controllers, however, are unable to account for various operational and economic constraints necessary for continuous and proper functioning of the islanded DCmG. High-level supervisory control architectures are, therefore, necessary to coordinate the voltage references provided to the primary layers. Consensus-based controllers assigning appropriate voltage references to guarantee proportional load sharing and voltage balancing are discussed in [10][11]. Despite their distributed structure, these controllers assume load satisfiability and unsaturated inputs at all times. These limitations can be overcome by exploiting an energy management system (EMS), which can meet specified power and energy objectives while respecting generation constraints and other economic objectives like optimal power dispatch, load sharing, and battery management. Flowchart-based EMS encompassing multiple case scenarios are discussed in [12][13] whereas the use of optimization methods and predictive algorithms to design EMS is investigated in [14][15].

In general, EMS, e.g. based on stochastic or mixed-integer optimization algorithms, utilize power balance equations and, in addition to their capability to consider operational constraints, also provide optimal power set-points [16][17][18][19]. When the primary layer is voltage controlled, the EMS power references need to be translated into suitable voltage references. Such a translation is not straightforward for mGs with meshed topologies and, effectively, requires the solution of power-flow equations. Moreover, considering that the voltages can solely be enforced by the DGUs, a unique voltage equilibrium may fail to exist at the load buses in the presence of nonlinear loads (for example constant power loads) [20].

1.1 Main Contributions

This work presents a comprehensive three-layered hierarchical control architecture for the overall operation and control of an islanded DCmG with arbitrary topologies. Leveraging the availability of grid-stabilizing primary voltage controllers, we propose a supervisory control structure situated atop the primary layer and composed of a secondary and a tertiary layer. An EMS sits at the tertiary level, and facilitates a smooth off-grid operation by providing desired power references of generation units to the secondary layer. The secondary control, acting as an interface between the primary and the tertiary layer, converts the power signals provided by the tertiary level into voltage references, which are tracked by the local voltage regulators. This work assumes all DGUs to be equipped with voltage controllers. The analysis could be also extended
to scenarios where some DGUs are current-controlled \cite{21}, without compromising the validity of the approach. Voltage stability is crucial in islanded DCmGs, for in its absence the voltages may exceed operational thresholds and damage connected loads \cite{6}. The structure and design of primary voltage controllers along with stability certificates and proofs are skipped in this work. A detailed analysis can be found in \cite{22, 9}, which show control design based on the Plug-n-Play paradigm, allowing DGUs to effortlessly enter/leave the DCmG without spoiling overall voltage stability.

Different from \cite{12, 13, 19}, we study an islanded voltage-controlled DCmG with a generic topology in the presence ZIP (constant impedance, constant current, and constant power) loads. In particular, we consider DGUs interfaced with nonrenewable dispatchable resources, batteries, or PV modules. The tertiary layer is equipped with an EMS, which generates optimal power references and boolean decision variables by solving an MPC problem at every sampling instant while taking into account forecasts and system parameters. Furthermore, these decision variables can turn ON/OFF dispatchable DGUs, switch the PV DGUs between Maximum Power Point Tracking (MPPT) and power curtailment modes, and control the operation mode of batteries by either charging or discharging them. In spite of a change in topology that may take place due to EMS commands, the collective voltage stability of the DCmG network is ensured by decentralized primary controllers.

After generating optimal power references, it is necessary to emulate them at the component level, solely receptive to voltage signals. To this aim, we employ a secondary control structure, which utilizes the optimal power references transmitted by the EMS and translates them into appropriate voltage references. This power-voltage conversion at the secondary layer is facilitated by an optimization problem, which is based on the power-flow equations and takes into account the converter and network losses. We prove that this optimization problem, although nonlinear and non-convex, is always feasible if nodal voltages and power injections are not bounded. The existence of a solution to the power-flow equations, necessary for the feasibility of the optimization problem, has been addressed in \cite{23, 24} with fixed DGUs voltages. Nevertheless, the provided conditions for existence can not be used directly as the DGU voltage references are free optimization variables and not known a priori. Furthermore, as a complement, we also provide a necessary condition for the solvability of the stated optimization problem.

We highlight that the voltages can only be enforced at generator nodes and therefore, the uniqueness of load voltages is necessary for attaining the predefined operational objectives. Indeed, if the voltages appearing at the load nodes are different from the ones anticipated by the secondary layer, permissible voltage limits may be violated and consequently, DGUs fail to track the optimal power set-points provided by the EMS. In this respect, we provide a novel condition for the uniqueness of load voltages and DGU power injections. The uniqueness of voltages has also been addressed in \cite{23}, where the deduced condition depends on the generator voltages and the topological parameters of the network. Here, we provide a novel and simpler condition that depends only on local load parameters and can be easily taken into account while designing the DCmG network. Finally, the robustness of the proposed control scheme in the presence of inaccurate generation and load forecasts is tested on a modified 16-bus feeder \cite{1}.

The structure of DCmG along with proposed hierarchical control scheme is described in Section 2. The EMS-based tertiary layer and its interaction with the secondary control layer is detailed in Section 3. The in-depth functioning of secondary layer and related derivations are presented in Section 4. Simulations validating theoretical results are provided in Section 5. Finally, conclusions are drawn in Section 6.

Preliminary results concerning this work have been reported in \cite{25}. The main novel contributions of this work are: (i) design of an EMS, (ii) interface of tertiary and secondary layers, (iii) modeling of different types of DGUs, (iv) detailed proofs of main theorems and propositions.
1.2 Preliminaries and notation

Sets, vectors, and functions: We let \( \mathbb{R} \) (resp. \( \mathbb{R}_{>0} \)) denote the set of real (resp. strictly positive real) numbers. Given \( x \in \mathbb{R}^n \), \( [x] \in \mathbb{R}^{n \times n} \) is the associated diagonal matrix with \( x \) on the diagonal. The inequality \( x \leq y \) for vectors \( x, y \in \mathbb{R}^n \) is component-wise, that is, \( x_i \leq y_i, \ \forall i \in 1, \ldots, n \). For a finite set \( V \), let \( |V| \) denote its cardinality. Given a matrix \( A \in \mathbb{R}^{n \times m} \), \( (A)_i \) denotes the \( i^{th} \) row. The notation \( A > 0 \), \( A \succeq 0 \), \( A > 0 \), and \( A \geq 0 \) represents a positive definite, positive semidefinite, positive, and nonnegative matrix, respectively. Throughout, \( 1_n \) and \( 0_n \) are the \( n \)-dimensional vectors of unit and zero entries, and \( 0 \) is a matrix of all zeros of appropriate dimensions. Given a weighted directed graph \( G(V, E) \), with \( V \) the set of nodes and \( E \) the set of edges, its Laplacian matrix \( L \in \mathbb{R}^{|V| \times |V|} \) is defined as

\[
L = A 1_{|V|} - A,
\]

where \( A \) is the adjacency matrix of \( G \) collecting edges weights and is defined as

\[
a_{ij} = \begin{cases} 
    w_{ij} & \text{if } (i, j) \in E \\
    0 & \text{otherwise}
\end{cases}
\]

Figure 1: Representative diagram of the DCmG network with DGUs and loads.

2 DC microgrid structure and hierarchical control scheme

In this section, we describe the DCmG structure and provide an outline of the hierarchical control structure used to ensure optimal, safe, and uninterrupted operation of the network.

Structure of the DC microgrid: The electric interconnections in an DCmG, comprising multiple DGUs connected to each other via power lines, are modeled as an undirected connected graph \( mG = (V, E) \). \( V \) is partitioned into two sets: \( G \) is the set of DGUs and \( L \) is the set of loads. The edges represent the interconnecting lines of the mG. As shown in Figure 1, each DGU and load is interfaced with the DCmG through a point of common coupling (PCC).

Distributed generation units (DGUs): The DGUs comprise a DC voltage source, a DC-DC converter, and a series RLC filter. Additionally, depending upon the type of DC voltage source, we define \( G_D \) as the set of dispatchable DGUs, \( G_B \) as the set of DGUs interfaced with batteries, and \( G_P \) as the set of DGUs connected to PV panels, where \( G_D \cup G_B \cup G_P = G \).
Tertiary Control (EMS)

Secondary Control (SCPF)

Primary Control

DC Microgrid Network
ZIP Loads

Figure 2: Hierarchical control scheme for DC microgrids.

Load model: Depending upon the type of load, the functional dependence on the PCC voltage changes and the term $I_{L,j}(V_j)$ takes different expressions. Prototypical load models that are of interest include the following:

1. constant-current loads: $I_{L,j} = \bar{I}_{L,j}$,

2. constant-impedance loads: $I_{LZ,j}(V_j) = Y_{L,j}V_j$, where $Y_{L,j} = 1/R_{L,j} > 0$ is the conductance of the $j^{th}$ load, and

3. constant-power loads:

$$I_{LP,j}(V_j) = V_j^{-1}\bar{P}_{L,j}, \quad (1)$$

where $P_{L,j} > 0$ is the power demand of the load $j$.

To refer to the three load cases above, the abbreviations I, Z, and P are often used [26]. The analysis presented in this article will focus on the general case of a parallel combination of the three loads, thus on the case of ZIP loads, which are modeled as

$$I_{L,j}(V_j) = \bar{I}_{L,j} + Y_{L,j}V_j + V_j^{-1}\bar{P}_{L,j}. \quad (2)$$

The net power absorbed by the $j^{th}$ load is given as

$$P_{L,j} = \bar{I}_{L,j}V_j + Y_{L,j}V_j^2 + \bar{P}_{L,j}. \quad (3)$$

2.1 Hierarchical control in DC microgrids

In this work, we propose the hierarchical control architecture depicted in Figure 2. The controller is split into three distinct layers viz. primary, secondary, and tertiary. The secondary and tertiary layers together form the supervisory control layer of the DCmG network.
Each DGUs is equipped with local voltage regulators (not shown in Figure 1) forming the primary control layer. The main objective of these controllers is to ensure that the voltage at each DGU’s PCC tracks a reference voltage $V_i^*$ provided by the supervisory control layer.

**Assumption 2.1** (Stability under primary voltage control). It is assumed that the primary controllers, under constant voltage reference $V_i^*, i \in \mathcal{G}$, achieve offset-free voltage tracking and guarantee the stability of the entire DCmG network. Indeed, if the DGU voltages are not stabilized, they can increase beyond a critical level, resulting in damage to the connected loads. The reader is deferred to [6, 22, 9] and the references therein for further details concerning design of stabilizing primary controllers.

An EMS sits at the tertiary level, and utilizes the forecasts of PV generation $P_{PV}^F$, and loads’ power and current absorption $\bar{P}_L, \bar{I}_L$. At each time step, it measures the nominal PV generation $P_{PV}^o$, the state of charge (SOC) of batteries $S_B$ and the actual power and current absorption of ZIP loads $P_L, I_L$. Solving an MPC optimization problem, the EMS generates optimal power references $P_{G,i}^*, i \in \mathcal{G}$ for the DGUs. In addition, it produces decision variables $\delta_i \in \{0, 1\}, i \in \mathcal{G}$, which can either turn on/off DGUs or change their operation mode. Since the primary layer operates only with voltage references, the secondary control layer translates the power references into appropriate voltage references $V^*$. The detailed structure and functioning of the secondary and tertiary control layers are discussed in Sections 4 and 3, respectively.

We highlight that different layers work at different time scales. In a typical scenario, the primary controllers operates in a range varying from $10^{-6}$ to $10^{-3}$ s, the secondary layer ranges from 100 to 300 s, and the tertiary layer ranges from 5 to 15 mins. At each high level sampling time, the controller provides a reference to its corresponding lower layer.

### 3 Tertiary control layer: the EMS

This section details the functioning of the MPC-based EMS, sitting at the top of the proposed hierarchical structure. The forecasts, parameters, and decision variables are described in Table 1. As a convention, all the power values are defined to be positive if delivered from a DGU. Moreover, the upper and lower bounds of each variable are denoted with superscripts max and min, respectively.

#### 3.1 MPC-based EMS for islanded DCmGs

The MPC-based EMS controller is responsible for energy management and coordination of resources in the islanded DCmG. The core of this controller is a receding horizon optimization problem, which enables load satisfiability, optimal scheduling of dispatchable and storage DGUs, and maximum possible utilization of PV DGUs.

The EMS is formulated as a mixed integer optimization problem, executed at the generic time instant $k$, with a finite prediction horizon $[k, \ldots, k+N]$, where $N$ indicates the number of prediction steps. In the following discussion, the index $i$ is used to define variables and constraints spanning all prediction horizon, i.e. $i \in [0, \ldots, N]$. The MPC-based EMS, at each time step, defines an optimal plan is formulated on power dispatch, storage schedule, and operational modes of the units for the whole prediction horizon. However, only the first sample of the input sequence is implemented and subsequently the horizon is shifted. At the next sampling time, using updated information on forecasts and mG initial condition, a new optimization problem is solved. Next, we describe the EMS in detail.
Table 1: Optimization variables and system parameters for the EMS

| Symbol      | Description                                      |
|-------------|--------------------------------------------------|
| $P_{DH}, P_{CH}$ | Charging and discharging power of the battery [kW] |
| $P_B$       | Power output of battery DGUs [kW]                 |
| $P_D$       | Power output of dispatchable DGUs [kW]           |
| $P_{PV}$    | Power output of PV DGUs [kW]                     |
| $P_{PV}^p$  | Nominal power production of PV DGUs [kW]         |
| $P_{PV}^f$  | Power production forecast of PV DGUs [kW]        |
| $P_L^v$     | Nominal total power absorption for ZIP loads [kW]|
| $I_L^v$     | Current absorption forecast for I component of ZIP loads [kVar] |
| $S_B$       | State of charge (SOC) of battery                 |
| $S_B^0$     | Nominal SOC of battery                           |
| $\eta_{CH}, \eta_{DH}$ | Charging and discharging efficiency of battery |
| $C_B$       | MG battery capacity [kWh]                        |
| $V^o$       | Nominal network voltage [V]                      |
| $\delta_B$  | Operation mode of battery DGU [boolean]          |
| $\delta_D$  | Operation mode of dispatchable DGU [boolean]     |
| $\delta_{PV}$ | Operation mode of PV DGU [boolean]              |
| $V$         | Nodal voltage magnitude [V]                      |
| $I$         | Nodal current magnitude [A]                      |

1. **DGUs**: Based upon the type of voltage source, the DGUs are characterized differently for the EMS.

   a) **Storage DGUs**: For these DGUs, a battery serves as the voltage source. The SOC dynamics of a battery $b \in \mathcal{G}_B$, considering both the charging and discharging efficiencies, are given as

   $$ S_{B,b}(k+1+i) = S_{B,b}(k+i) - \frac{\tau}{C_B,b} \left( \frac{1}{\eta_{DH,b}} P_{DH,b}(k+i) + \eta_{CH,b} P_{CH,b}(k+i) \right), \quad (4) $$

   with battery power output

   $$ P_{B,b}(k+i) = P_{DH,b}(k+i) - P_{CH,b}(k+i). \quad (5) $$

   Since battery DGUs can operate either in charging or discharging mode, the following constraints are stated

   $$ 0 \leq P_{DH,b}(k+i) \leq P_{B,b}^{max}(k+i) \delta_{B,b}(k+i), \quad (6) $$
   $$ 0 \leq P_{CH,b}(k+i) \leq -P_{B,b}^{min}(k+i) (1 - \delta_{B,b}(k+i)), \quad (7) $$

   where $\delta_{B,i} = 1$ indicates discharging mode while $\delta_{B,i} = 0$ represents the charging mode. In order to ensure longevity of batteries, the SOC is constrained between minimum and maximum bounds

   $$ S_{B,b}^{min} \leq S_{B,b}(k+i) \leq S_{B,b}^{max}. \quad (8) $$
The constraints (1)-(8) must hold $\forall i \in [0, \ldots, N - 1]$. To avoid complete charging or discharging of batteries, not ideal for guaranteeing voltage stability and load satisfiability for all possible contingencies, a terminal constraint on the SOC is imposed

$$S_{B,b}(k + N) = S_{B,b}^o + \Delta S_{B,b},$$

where $\Delta S_{B,b}$ is a slack variable introduced to ensure feasibility.

b) Dispatchable DGUs: These DGUs are interfaced with a nonrenewable energy resource, and are required to counteract the intermittency introduced by the renewables. These DGUs generally remain in off state and are turned on when necessary. The operational mode is governed by the variable $\delta_{D,d}, d \in \mathcal{D}_D$, with values 1 and 0 indicating on and off states, respectively. The power produced by the dispatchable DGU lies within a range defined by lower and upper bounds

$$\delta_{D,d}(k + i) P_{D,d}^\text{min} \leq P_{D,d}(k + i) \leq P_{D,d}^\text{max} \delta_{D,d}(k + i), \quad d \in \mathcal{G}_D,$$

defined $\forall i \in [0, \ldots, N - 1]$.

c) PV DGUs: We note that the $p^{th}$ DGU, $p \in \mathcal{G}_P$, has two distinct modes of operation: power curtailment mode and MPPT. In power curtailment mode, the DGU’s power needs to be curtailed in order to respect operational limits whereas the maximum possible power is injected into the grid in MPPT mode. To preserve internal power balance during periods of peak PV generation, power curtailment is sometimes unavoidable. Since, at a given time instant, the EMS utilizes both the nominal PV generation and the forecast, the PV power output is constrained as

$$P_{PV,p}(k) = P_{PV,p}^o(k) - \Delta P_{PV,p}(k), \quad p \in \mathcal{G}_P,$$

$$P_{PV,p}(k + i) = P_{PV,p}^f(k + i) - \Delta P_{PV,p}(k + i), \quad \forall i \in [\ldots, N - 1],$$

where $\Delta P_{PV,p}$ expresses the amount of curtailed power. Indeed, the curtailed power cannot be arbitrary and fulfills the constraints

$$\Delta P_{PV,p}(k) \geq (1 - \delta_{PV,p}(k)) \epsilon,$$

$$\Delta P_{PV,p}(k) \leq (1 - \delta_{PV,p}(k)) P_{PV,p}^o(k),$$

$$\Delta P_{PV,p}(k + i) \geq (1 - \delta_{PV,p}(k + i)) \epsilon, \quad \forall i \in [1, \ldots, N - 1],$$

$$\Delta P_{PV,p}(k + i) \leq (1 - \delta_{PV,p}(k + i)) P_{PV,p}^f(k + i), \quad \forall i \in [1, \ldots, N - 1],$$

where $\epsilon > 0$ is a sufficiently small number and $\delta_{PV,p}$ is a decision variable. The rationale behind constraints (13)-(16) is not only to limit power curtailment but also to enable one to distinguish between the operation modes. Clearly, if $\delta_{PV,p} = 1$, $\Delta P_{PV,p}$ is forced to zero, whereas if $\delta_{PV,p} = 0$, it must be strictly greater than zero and lower than the nominal PV power production. For more details on logic and mixed-integer constraints, the authors refer the reader to [27].

2. Loads: The nominal power absorption of the $l^{th}$ ZIP load, $l \in \mathcal{L}$, is computed at nominal voltage by utilizing the current state of the system for the first time step

$$P_{L,l}^o(k) = \tilde{I}_{L,l}(k)V^o + Y_{L,l}V^{o^2} + \tilde{P}_{L,l}(k), \quad l \in \mathcal{L},$$

while forecasts are used for future time instants

$$P_{L,l}^o(k + i) = \tilde{I}_{L,l}^f(k + i)V^o + Y_{L,l}V^{o^2} + \tilde{P}_{L,l}^f(k + i), \quad \forall i \in [1, \ldots, N - 1].$$
It is worth noticing that $P_{o,l}$ is just an estimate, as net power absorption of ZIP loads depends on the actual DCmG voltages; see (3).

3. Power balance: In an islanded DCmG, the internal power balance must be maintained. Hence, the following constraint is expressed

$$\sum_{b \in D_B} P_{B,b}(k+i) + \sum_{d \in D_D} P_{D,d}(k+i) + \sum_{p \in D_P} P_{PV,p}(k+i) + \sum_{l \in L} P_{o,l}(k+i) = 0,$$

which is stated $\forall i \in [0, \ldots, N-1]$. We highlight that the filter and network losses are neglected at the EMS level.

4. Cost function: The aim is to minimize the cost of satisfying the electrical loads, hence the cost function is

$$J(k) = \sum_{b \in D_B} (∆S_{B,b})^2 w_{S,B} + \sum_{i=0}^{N-1} \sum_{b \in D_B} (P_{B,b}(k+i))^2 w_{B,b}$$

$$+ \sum_{i=0}^{N-1} \sum_{d \in D_D} (P_{D,d}(k+i))^2 w_{D,d} + \sum_{i=0}^{N-1} \sum_{p \in D_P} (∆P_{PV,p}(k+i))^2 w_{PV,p}$$

$$+ \sum_{i=0}^{N-1} \sum_{p \in D_P} (∆P_{PV,p}(k+i) - ∆P_{PV,p}(k+i-1))^2 w_{PV,p}$$

$$+ \sum_{i=0}^{N-1} \sum_{b \in D_B} (∆B,b(k+i) - ∆B,b(k+i-1))^2 w_{B,b}$$

$$+ \sum_{i=0}^{N-1} \sum_{d \in D_D} (∆D,d(k+i) - ∆D,d(k+i-1))^2 w_{D,d},$$

where $w_{S}, w_{P V}, \ldots$ are positive weights. We intend on keeping batteries close to their nominal SOCs and using power curtailment as the last resort. Thus, the weights $w_{S,B}$ and $w_{PV}$ are set to much higher values with respect to others, enabling $∆S_{B,b}$ and $∆P_{PV,p}$ to be nonzero only when necessary for preserving feasibility. The terms $\alpha$, $\beta$ and $\gamma$ are included in the cost to avoid frequent changes in modes of operation of different DGUs.

At every EMS time instant, the following optimization is solved to obtain optimal power set points $\bar{P}_{B,i}, \bar{P}_{D,j}, \bar{P}_{PV,p}$ and decision variables $\delta_{B,i}, \delta_{D,j}, \delta_{PV,p}$.

$$J_{EMS}(k) = \min J(k)$$

subject to (1) – (19).
3.2 Interaction between tertiary and secondary layers

The EMS produces power references as well as decision variables, both of which are passed down to the secondary control layer. The value of these decision variables essentially determines the topology of the DCmG network. This is due to the fact that dispatchable generators can be connected/disconnected from the network based on the value of $\delta_{D,j}$. Moreover, based on the value of $\delta_{PV,p}$, the PV DGUs can either inject maximum power or undergo power curtailment. While injecting maximum power, the PV DGU is governed by MPPT algorithms and automatically alters its output voltage in order to inject maximum power. Thus, in this mode, the DGU operates as a P load injecting power. When the DGU experiences a power curtailment, it injects the requested power and operates as a voltage-controlled DGU.

As mentioned earlier, the EMS power references are not directly perceivable by the primary controllers. Thus, a power-to-voltage translation is performed by the secondary controller by utilizing topology-based power-flow equations (see Section 4). Therefore, at every EMS time instant, the secondary controller uses the decision variables to update the mG topology in order to accommodate the turning ON/OFF of dispatchable generators as well as operation mode of PV DGUs.

Remark 3.1. (Connectivity of the DCmG network) It is assumed that the turning ON/OFF of dispatchable DGUs does not impact the connectivity of the rest of the DCmG network. In other words, addition or removal of a dispatchable DGUs must not split the remainder of the network into two or more disjoint islanded mGs. In case, critical DGUs affecting the connectivity of graph are present in the network, one can restrict their operation modes by adding additional constraints to the EMS optimization problem (see Section 5 for an example).

4 Secondary control based on power-flow equations

The secondary control is designed to make DGUs track the power references provided by the EMS, now denoted as $\hat{P}_G$. We highlight that the decision variables communicated by the EMS at a given sampling instant define the topology of the network over the next EMS sampling period.

Remark 4.1. The secondary layer, operating on a faster time scale in comparison to the EMS, utilizes a fixed topology over an EMS sampling period to perform power-voltage translation. The mG topology is updated when a new set of decision variables is received.

To perform the power-to-voltage translation, such that proper references can be sent to primary controllers, we first start by deducing the equations linking power and voltage. The relation between power and voltage in an islanded DCmG is defined by the power-flow equations dependent on mG parameters and topology.

We let the undirected connected graph $\tilde{m}\tilde{G} = (\tilde{V}, \tilde{E})$ define the topology of the mG for a specified EMS sampling period. The set $\tilde{V}$ is partitioned into two sets: $\tilde{G} = \{1, \ldots, n\}$ is the set of DGUs and $\tilde{L} = \{n+1, \ldots, n+m\}$ is the set of loads. The set $\tilde{G} = \tilde{G}_D \cup \tilde{G}_B \cup \tilde{G}_G$, where $\tilde{G}_D$ is the set of connected dispatchable DGUs, $\tilde{G}_B$ is the set of batteries, and $\tilde{G}_G$ is the set of voltage-controlled PV DGUs. In steady state, the inductances and capacitances can be neglected and the current-voltage relation is given by the identity $I = B\Gamma B^T V = Y V$, where $B \in \mathbb{R}^{(n+m) \times |E|}$ is the incidence matrix of $\tilde{m}\tilde{G}$, $I$ is the vector of PCC currents, $V$ is the vector containing PCC voltages, $\Gamma$ is the diagonal matrix of line conductances, and $Y \in \mathbb{R}^{(n+m) \times (n+m)}$ is the network admittance matrix [28]. On partitioning the nodes into DGUs and loads, the relation can be
rewritten as
\[
\begin{bmatrix}
I_G \\
I_L
\end{bmatrix} = \begin{bmatrix}
B_G R^{-1} B_G^T & B_G R^{-1} B_G^T \\
B_L R^{-1} B_L^T & B_L R^{-1} B_L^T
\end{bmatrix} \begin{bmatrix}
V_G \\
V_L
\end{bmatrix},
\]
(22)

where \(V_G = [V_1, \ldots, V_n]^T\), \(V_L = [V_{n+1}, \ldots, V_{n+m}]^T\), \(I_G = [I_1, \ldots, I_n]^T\), and \(I_L = [I_{n+1}, \ldots, I_{n+m}]^T\). The subscripts \(G\) and \(L\) indicate the DGUs and loads, respectively. Throughout this work, the following assumption is made.

**Assumption 4.1.** The PCC voltage \(V_i\) is strictly positive for all \(i \in \mathcal{V}\).

We remark that Assumption 4.1 is not a limitation, and rather reflects a common constraint in microgrid operation. Notice that, in Figure 1, one end of the load is connected to the PCC and the other to the ground, assumed to be at zero potential by convention. Since the electric current and hence power flows from higher to lower potential, negative references and PCC voltages would reverse the role of loads and make them power generators. In order to ensure power balance in the network, this power would be absorbed by the generators. This, in effect, defeats the fundamental goal of the mG, that is, the satisfiability of the loads by virtue of the power generated by the DGUs. Furthermore, if \(V_i \in \mathbb{R}^N\), then a zero-crossing for the voltages may take place. At zero voltage, the power consumed by the ZIP loads tends to infinity.

Based on the current directions depicted in Figure 1, it is evident that \(I_{L,j}(V_j) = -I_j, \ j \in \mathcal{L}\). Using (2), one can simplify (22) as
\[
I_G = Y_{GG} V_G + Y_{GL} V_L
\]
(23a)
\[
0 = Y_{LG} V_G + Y_{LL} V_L + I_L + [V_L]^{-1} \bar{P}_L,
\]
(23b)
where \(Y_L \in \mathbb{R}^{m \times m}\) is the diagonal matrix of load admittances. The vectors \(\bar{I}_L\) and \(\bar{P}_L\) collect consumptions of I and P loads, respectively. Note that the power \(P_{G,i}, i \in \mathcal{G}\) produced by an individual DGU is the sum of power injected into the network and the filter losses. Equivalently,
\[
P_G = [V_G] I_G + [I_G] R_G \bar{I}_G
\]
(24)
where \(R_G \in \mathbb{R}^{n \times n}\) is a diagonal matrix collecting filter resistances and \(I_G\) is the vector of DGU filter currents. On pre-multiplying (23a) with \([V_G]\), and by using (24), one can rewrite (23) as
\[
f_G(V_G, V_L, P_G) = [V_G] Y_{GG} V_G + [V_G] Y_{GL} V_L
\]
\[
+ [I_G] R_G \bar{I}_G \bar{P}_G = 0,
\]
(25)
\[
f_L(V_G, V_L) = Y_{LG} V_G + Y_{LL} V_L + Y_L V_L
\]
\[
+ Y_L V_L + I_L + [V_L]^{-1} \bar{P}_L = 0.
\]
(26)
The equations (25) and (26) fundamentally depict the power balance and current balance at DGU and load nodes, respectively. These equations depend on the topology-dependent \(Y\) matrix, and are updated once a new set of decision variables is received.

In order to translate the power references into suitable voltage references, the secondary layer solves an optimization problem, whose objective is to minimize the difference between the reference power \(P_G\) and the DGU input power \(P_G\) under the equilibrium relations (25) and (26). We first consider the following simplified version of the optimization problem, where constraints on voltages and generator power are neglected.

11
Secondary Power Flow (SPF):

\[
J_{SPF}(\tilde{P}_G, \tilde{P}_L, \tilde{I}_L) = \min_{V_G, V_L, P_G} ||P_G - \tilde{P}_G||_2 
\]  
(27a)

subject to

\[
f_G(V_G, V_L, P_G) = 0 
\]  
(27b)

\[
f_L(V_G, V_L) = 0 
\]  
(27c)

As noticeable from Figure 2, the SPF layer requires the updated load consumption \((\bar{P}_L, \bar{I}_L)\) and the power references \(\bar{P}_G\) in order to solve (27). We define \(X\) to be the set of all \((V_G, V_L, P_G)\) that satisfy (27b)-(27c) simultaneously. Hereafter, we will discuss necessary and sufficient conditions ensuring that the set \(X\) is nonempty. We start by introducing two preliminary Lemmas.

**Lemma 4.1.** The matrix \(Y_{LL}\) can be written as

\[
Y_{LL} = \hat{Y}_{LL} + \begin{bmatrix} -Y_{LG} \end{bmatrix}, \tag{28}
\]

where \(\hat{Y}_{LL}\) is a Laplacian matrix.

**Proof.** The network admittance matrix \(Y\) is a Laplacian with zero row sum [28]. Matrix \(Y_{LL}\), a submatrix of \(Y\), is symmetric with positive diagonal and non-negative off-diagonal entries. Since the network graph \(G\) is connected, \(Y_{LL}\) has at least one row with strictly positive row sum. \(Y_{LL}\) is a Laplacian matrix with self loops [29] and, therefore, can be written as (28).

**Lemma 4.2.** The matrix \(-(Y_{LL} + Y_L)^{-1} Y_{LG}\) has no rows with all zero entries and is nonnegative.

**Proof.** The matrix \(-Y_{LG}\) is a non-negative matrix and, since the graph is connected, has at least one row with non-zero row sum. The statement of the above Lemma follows from the fact that \(Y_{LL} + Y_L\) is a Laplacian matrix with self loops and its inverse is strictly positive [29].

Next, we show that SPF is always feasible.

**Proposition 4.1.** The feasible set \(X\) is non-empty. In particular, for all \(\bar{P}_L \in \mathbb{R}^m\) and \(\bar{I}_L \in \mathbb{R}^m\), the following statements hold:

1. The equation (27c) is always solvable.
2. The solvability of (27c) implies that (27b) is solvable.

**Proof.** Under Assumption 4.1, the equation (27c) can be written as follows:

\[
[V_L] \tilde{Y}_{LL} V_L + [V_L] Y_{LG} V_G + [V_L] \bar{I}_L + \bar{P}_L = 0, \tag{29}
\]

where \(\tilde{Y}_{LL} = Y_{LL} + Y_L\). Using Banach fixed-point theorem, as shown in [23], it can be proven that for a fixed \(V_G\), a corresponding \(V_L\) solving (27c) exists if

\[
\Delta = ||P^{-1}_{crit} \bar{P}_L||_\infty < 1 \tag{30}
\]

where

\[
P_{crit} = \frac{1}{4} [\bar{V}] \tilde{Y}_{LL} [\bar{V}] \tag{31}
\]

\[12\]
\[
\tilde{V} = -\tilde{Y}_{LL}^{-1} Y_{LG} V_G - \tilde{Y}_{LL}^{-1} \tilde{I}_L.
\] (32)

Different from [23], here \(V_G\) is a free variable. Therefore, for the solvability of (27c), it is enough to show that a \(V_G\) can be always found such that (30) is satisfied for any \(\tilde{I}_L\) and \(\tilde{P}_L\).

Consider \(V_\alpha = \alpha \mathbf{1}_n\), with \(\alpha \in \mathbb{R} > 0\). Therefore,

\[
\tilde{V}_\alpha = -\tilde{Y}_{LL}^{-1} Y_{LG} V_G^\alpha - \tilde{Y}_{LL}^{-1} \tilde{I}_L = \alpha(-\tilde{Y}_{LL}^{-1} Y_{LG} \mathbf{1}_n) - \tilde{Y}_{LL}^{-1} \tilde{I}_L.
\]

Given Lemma 4.2, \((-\tilde{Y}_{LL}^{-1} Y_{LG} \mathbf{1}_n)\) is a positive vector. Hence, there exists an \(\bar{\alpha} \in \mathbb{R}_0^+\) such that \(\tilde{V}_\alpha > 0\) \(\forall \alpha > \bar{\alpha}\).

Considering \(i, j \in \mathcal{L}\), any element \((i, j)\) of the matrix \((P_\alpha^{crit})^{-1}\) can be expressed as follows

\[
(P_\alpha^{crit})^{-1}_{ij} = 4 (\tilde{Y}_{LL})^{-1}_{i,j}/(\tilde{V}_\alpha^i \tilde{V}_\alpha^j).
\] (33)

It is evident that \((P_\alpha^{crit})^{-1}_{ij}\) is inversely proportional to the parameter \(\alpha\), for \(\alpha > \bar{\alpha}\). As a result, it is always possible to increase \(\alpha\) such that (30) is verified for any \(\tilde{P}_L\) and \(\tilde{I}_L\). Consequently, a voltage solution \((V_\alpha^*, L^*)\) of (27c) always exists, proving statement 1.

Regarding statement 2, it is evident that (27b) is linear with respect to \(P_G\). This implies that, for any solution \((V_\alpha^*, L^*)\) of (27c), a corresponding \(P_G^*\) solving (27b) always exists.

Proposition 4.1 guarantees the feasibility of SPF. We now discuss optimality. If SPF achieves the optimal cost \(J_{SPF}^* = 0\), it implies that a voltage solution exists such that the power references \(\bar{P}_G\) are exactly tracked by the DGUs. This condition can not be achieved for any value of \((\bar{P}_L, \bar{I}_L, \bar{P}_G)\). The following proposition, inspired by [30], presents a necessary condition that must hold for \(J_{SPF}^* = 0\). The proof nonetheless is different as DGU filter losses are also taken into account.

**Proposition 4.2.** If the SPF achieves the optimal cost \(J_{SPF}^* = 0\), then

\[
\sum_{i \in \mathcal{D}} \bar{P}_G \geq \sum_{i \in \mathcal{L}} \bar{P}_L - \frac{1}{4} \tilde{I}^T \tilde{Y}_{GG}^{-1} \tilde{I}_L,
\] (34)

where \(\tilde{Y}_{GG} = Y_{GG} - Y_{GL}^T (Y_{LL} + Y_{L}) Y_{GL}\).

**Proof.** Under Assumption 1, equations (27b) and (27c) can be expressed in a single matrix equality as follows

\[
f(V, P_G) = [V]^T \tilde{Y} V + [V]^T \tilde{I} + \begin{bmatrix} [I_G]^T R I_G \\ 0 \end{bmatrix}
+ \begin{bmatrix} -P_G \\ \tilde{P}_L \end{bmatrix} = \mathbf{0}_{n+m},
\] (35)

where \(\tilde{I} = [0^T \quad I_G^T]^T\), and \(\tilde{Y} = Y + \begin{bmatrix} 0 & 0 \\ 0 & Y_L \end{bmatrix}\). To achieve \(J_{SPF}^* = 0\), a solution \((V, P_G)\) to SPF must exist such that \(P_G = \bar{P}_G\) and

\[
f(V, \bar{P}_G) = \mathbf{0}_{n+m}.
\] (36)
On multiplying the above equation by $1^T_n + m$ on both sides, one obtains

$$1^T_n f(V, \bar{P}_G) = V^T \bar{Y} V + V^T \bar{I} + I^T_G R I_G - 1^T_n P_G + 1^T_m \bar{P}_L = 0.$$  \tag{37}

If the solution exists for (35), then, one can also verify (36). Using simple computations, equation (37) can be rewritten as

$$\left(V + \frac{1}{2} \bar{Y}^{-1} \bar{I}\right)^T \bar{Y} \left(V + \frac{1}{2} \bar{Y}^{-1} \bar{I}\right) + I^T_G R I_G \geq 0.$$  \tag{39}

We highlight that $I_G$ is a function of $V$ (see (23a)). This further implies that

$$\frac{1}{4} \bar{I}^T \bar{Y}^{-1} \bar{I} + \sum_{\forall i \in D} \bar{P}_G - \sum_{\forall i \in L} \bar{P}_L \geq 0.$$  \tag{40}

Using standard results on the inverse of block matrices, the expression $\bar{I}^T \bar{Y}^{-1} \bar{I}$ can be simplified as $\bar{I}_L^T (\bar{Y}_{GG})^{-1} \bar{I}_L$, where $\bar{Y}_{GG}$ is the Schur complement of $\bar{Y}$. \hfill \square

**Remark 4.2.** It is highlighted that the necessary condition (34) depends only on the network parameters and load consumption. Therefore, it can be incorporated in the EMS optimization problem as a constraint for the choice of the power references $\bar{P}_G$.

In a real DCmG, the power output $P_G$ is constrained by physical limits of the DGUs. Moreover, the components of the DCmG are designed to operate around the nominal voltage. Hence, both nodal voltages and DGU powers must respect certain constraints, which are not incorporated in the aforementioned SPF. Consequently, we now introduce the following constrained optimization problem with additional operational constraints.

**Secondary Constrained Power Flow (SCPF):**

$$J_{SCPF}(\bar{P}_G, \bar{P}_L, \bar{I}_L) = \min_{V_G, V_L, P_G} \|P_G - \bar{P}_G\|_2$$  \tag{41a}

subject to

$$f_G(V_G, V_L, P_G) = 0$$  \tag{41b}
$$f_L(V_G, V_L) = 0$$  \tag{41c}
$$V_G^{min} \leq V_G \leq V_G^{max}$$  \tag{41d}
$$V_L^{min} \leq V_L \leq V_L^{max}$$  \tag{41e}
$$P_G^{min} \leq P_G \leq P_G^{max}$$  \tag{41f}
Although the feasibility of SPF is always ensured by Proposition 11, this may not be true for SCPF due to the presence of additional constraints (11)-(11). Nevertheless, if the DCmG is properly designed, a feasible solution of SCPF should always exist. In fact, the infeasibility of the SCPF just implies the absence of sufficient power generation to satisfy the load demand and losses in the allowed voltage range.

Next we study the properties of an optimal solution \( x^* = (V_G^*, V_L^*, P_G^*) \) of SCPF, assuming it exists. As mentioned before, the secondary control layer acts as an interface between the EMS (tertiary layer) and the local voltage regulators (primary layer). The voltage \( V_G^* \) obtained from the SCPF is transmitted as a reference to the primary voltage controllers of the DGUs. We highlight that the component \( V_G^* \) of \( x^* \) can be directly imposed since the load nodes are not equipped with voltage controllers and the generators are not controlled to track power references. Therefore, it is important to guarantee that, for a given voltage reference \( V_G^* \), \( P_G^* \) is the power produced by the DGUs and \( V_L^* \) appears at the load nodes. This implies that for a fixed \( V_G^* \), the unique solution satisfying the power flow equation (25)-(26) must be \( V_L = V_L^*, P_G = P_G^* \). We show the uniqueness by means of the following theorem.

**Theorem 4.1.** Consider the solution \( x^* = (V_G^*, V_L^*, P_G^*) \) from the SCPF optimization problem. For a fixed \( V_G^* \), the pair \( (V_L^*, P_G^*) \) is the unique solution of (25)-(26) in the set \( \mathcal{Y} = \{ (V_L, P_G) : V_L > V_L^{\text{min}}, P_G \in \mathbb{R}^n \} \) if

\[
\hat{P}_{L,i} < (V_L^{\text{min}})^2 Y_{L,i}, \quad \forall i \in \mathcal{L}.
\]  

**Proof.** For a fixed \( V_G^* \), the power-flow equations (25)-(26) can be rewritten as

\[
\hat{f}_G(V_L, P_G) = f_G(V_L, V_L, P_G) \bigg|_{V_G = V_G^*} = [V_G^*] Y_{GG} V_G^* + [V_L] Y_{LG} V_L + [I_G] R_G I_G - P_G = 0,
\]

\[
\hat{f}_L(V_L) = f_L(V_G, V_L) \bigg|_{V_G = V_G^*} = Y_{LG} V_G^* + Y_{LL} V_L + Y_L V_L + I_L + [V_L]^{-1} \hat{P}_L = 0.
\]  

We will proceed by analyzing equation (44). Note that \( \hat{f}(V_L^*) = 0 \) since \( V_L^* \) is a feasible solution obtained from the SCPF. Moreover, if the function \( \hat{f}_L(V_L) \) is injective, then \( V_L^* \) is the unique solution of (11).

To show the injectivity of \( \hat{f}_L(V_L) \), we first evaluate its Jacobian with respect to \( V_L \), given as

\[
\mathcal{J}(V_L) = \frac{\partial \hat{f}_L(V_L)}{\partial V_L} = Y_{LL} + Y_L - [V_L]^{-2} \hat{P}_L.
\]  

As stated in [32, Theorem 6], if the Jacobian (45) of the function \( \hat{f}_L(V_L) \) is symmetric and positive definite in a convex region \( \Omega \), then \( \hat{f}_L(V_L) \) is injective in \( \Omega \). Note that \( \mathcal{J}(V_L) \) is symmetric by construction. Moreover, using Lemma 4.1, one can split (45) into

\[
\mathcal{J}(V_L) = \hat{Y}_{LL} + [-Y_{LG} 1_n] + Y_L - [V_L]^{-2} \hat{P}_L,
\]  

15
where \( \hat{Y}_{LL} \succeq 0 \) and \(-Y_{LG}\) is a nonnegative matrix. For \( J(V_L) \) to be positive definite, it is sufficient to show that \( M \succ 0 \). Since \( M \) is a diagonal matrix,

\[
- \sum_{j \in D} Y_{ij} + Y_{L,i} - \hat{P}_{L,i} V_i^{-2} > 0, \quad \forall i \in \tilde{L}.
\]

(47)

We remark that \(- \sum_{j \in D} Y_{ij}\) is positive only if load \(i\) is connected directly to at least one DGU, and is otherwise zero. Hence, if \( \hat{P}_{L,i} < V_i^2 Y_{L,i} \), then (47) is automatically satisfied and consequently \( J(V_L) \succeq 0 \). Using (48), one can deduce that \( f_L(V_L) \) is injective in \( \Omega \) given as

\[
\Omega = \{ V_i : V_i > \sqrt{\frac{\hat{P}_{L,i}}{Y_{L,i}}}, \quad \forall i \in \tilde{L} \}.
\]

Since \( V_i^* \in [V_{L,i}^\text{min}, V_{L,i}^\text{max}] \) and (48) holds, \( V_i^* \) always belongs to \( \Omega \). The uniqueness of \( V_i^* \) in \( \Omega \) follows from the injectivity of \( f_L(V_L) \); moreover, given (42), \( V_i^* \) is unique in \( \mathcal{Y} \). Consequently, considering that \( f_G(V_L^*, P_G^*) = 0 \), it is evident that \( P_G = P_G^* \) is the unique solution of (43) if \( V_G = V_G^* \) and \( V_L = V_L^* \).

Remark 4.3. (Condition (42) and stability) The uniqueness condition (42) essentially limits the power consumption of P loads. As shown in [9], due to the negative impedance introduced by the P loads, their power consumption \( P_{L,i} \) is dominated by \( V_i^* \), \( Y_{L,i} \), \( \forall i \in \tilde{L} \) in order to guarantee stability. Since \( V_i^* \) is the solution of SCPF, \( V_i^* \geq V_{i}^\text{min} \), by satisfying (42), one can simultaneously guarantee the uniqueness of load voltages and the stability of the DCmG.

Remark 4.4. The use of a multi-layered hierarchical control scheme is a well-established concept for the overall operation of a mG [10]. In the context of islanded DCmGs, supervisory control structures with different functionalities are explored in [3, 12, 13, 19]. However, these contributions are restricted to a specific topology, do not consider the interface with the primary layer, or disregard the stability of the DCmG. Besides the incorporation of generic topologies changing over time and the seamless integration of multiple control layers, this work considers both overall mG stability and optimal resource allocation at the same time. Moreover, the secondary control layer can easily be interfaced with any EMS that generates power references.

5 Numerical Results

In this section, we aim to show the performance of the proposed hierarchical control scheme via simulation studies conducted in MATLAB. We consider a 16-bus DC feeder in meshed stand-alone configuration [1], equipped with three battery DGUs, two dispatchable DGUs, a PV DGU, and ten ZIP loads (see Figure 3). The DGUs are interfaced with synchronous Buck converters and controlled by the primary voltage controllers studied in [9]. We highlight that turning off dispatchable DGUs at nodes 1 and 2 simultaneously splits the mG into two separate DCmGs (see Figure 3), and can be circumvented by adding the simple constraint \( \delta_{D,1}(k) + \delta_{D,2}(k) \geq 1 \) to the EMS optimization problem (21). The loads are standard ZIP, and their power and current absorption follow three different daily profiles denoted by subscripts \(a\), \(b\), and \(c\) (depicted in Figure 4). The DCmG is operated at a nominal voltage \( V^0 = 100 \text{ Volts} \) with nodal voltages lying between \( V_{L,i}^\text{min} = 0.9 V^0 \) and \( V_{L,i}^\text{max} = 1.1 V^0 \). The DGU parameters utilized by the EMS are given in Table 2.
Figure 3: DCmG based on the modified 16-bus feeder [1]. The letters $D$, $B$, and $PV$ denote dispatchable, battery, and PV DGUs, respectively. The letter $L$ indicates loads with subscripts $a$, $b$, and $c$ defining different consumption patterns.

| DGU | ($P_{\min}^D$, $P_{\max}^D$) | $C^B$ | $(\eta_{CH}, \eta_{DH})$ | ($S_{\min}^B$, $S_{\max}^B$) | $S_B^{\circ}$ |
|-----|-----------------------------|------|-----------------------|-------------------------|---------|
| D1  | (+10, +80) | - | - | - | - |
| D2  | (+10, +80) | - | - | - | - |
| B3  | (-40, +40) | 150 | (0.9, 0.9) | (0.1, 0.9) | 0.5 |
| B4  | (-50, +50) | 150 | (0.9, 0.9) | (0.1, 0.9) | 0.6 |
| B5  | (-60, +60) | 250 | (0.9, 0.9) | (0.1, 0.9) | 0.4 |

Table 2: DGU parameters used by the EMS.

Figure 4: Actual current and power absorption of DCmG loads. Each of the 10 DCmG loads corresponds to one of the three profiles shown above.

The MPC-based EMS schedules the optimal power set-points of DGUs every 15 minutes, using a prediction horizon of 5 hours, i.e. $N = 20$. The secondary layer runs with a sampling time of 3 minutes with the goal of tracking the received power references despite the aforementioned load variations. In the ensuing discussion, we describe the behavior of various mG components controlled by the proposed hierarchical controller over a span of 24 hours.

**Dispatchable DGUs:** As shown in Figure [5] DGUs D1 and D2 track the power references provided by the EMS. During the day, when PV generation starts picking up (see Figure [5], the
EMS turns off DGU D1 to ensure economic optimality and maintain mG power balance. DGU D2, although producing minimum permissible power during the period of peak PV generation, remains operational throughout the day in order to maintain connectivity of the DCmG.

**Battery DGUs**: In Figure 6, it can be noticed that battery DGUs follow power references provided by the EMS. Abrupt charging and discharging, and frequent switching between these two modes work to the detriment of battery’s longevity, and are prevented by the EMS. As for the SOC, reported in Figure 6, they evolve respecting the operational constraints. Moreover, the EMS tries to store surplus energy during periods of peak PV generation (see Figure 8). This energy is released in the last part of the day during which the PV generation declines.

**PV DGU**: As reported in Figure 8, so as to be consistent with a real operation scenario, the simulations have been conducted with a mismatch between nominal PV generation and forecasts. At a sampling instant, the EMS utilizes the nominal PV generation and the forecast not only to generate power references but also to decide whether to operate the PV DGU in MPPT or power curtailment mode. As seen from Figure 8, the power injected by the PV generators into...
Figure 7: Power output by battery DGUs.

Figure 8: Nominal generation, PV generation forecast, EMS power reference, generated power for DGU PV6.
the DCmG tracks the EMS power references. Notice that the PV DGU operates in MPPT mode during the first and the last hours of the simulation, whereas it curtails power during the central part of the day. Clearly, a power curtailment is inevitable considering that the SOCs are going to hit their upper bound, DGU D1 is injecting minimum power, and DGU D2 is nonoperational.

Figure 9: Load power forecasts and net power absorption for different load nodes.

**Loads:** The load power forecasts used by the EMS and the net power absorption for nodes 8, 11, and 16 are shown in Figure 9. One can observe that the forecasts are fairly different from the actual power absorption. This stems from the fact that EMS forecasts are deduced using inaccurate current and power profiles (see Figure 4 for actual current and power absorption) at nominal voltage. Even if exact profiles were available to the EMS a priori, the forecasts would not coincide with net power absorbed by the loads. This is because the net power absorbed by a load depends on PCC voltage, which is generated by the secondary layer only after EMS power references are received.

Finally, we highlight that, during the simulation, the condition (42) always holds for all load nodes, ensuring the uniqueness of solution for load voltages and the perfect tracking of DGUs power injections. The secondary control layer manipulates the voltage references of the DGUs every three minutes, and maintains the voltages in the allowed range, as shown in Figure 10. As a consequence of new power references received from the EMS, a clear change in voltages can be
observed every 15 minutes. In figure 11 we show the performance of primary voltage controllers when dispatchable DGU D2 is turned off by the EMS. Indeed, the transients quickly die out and voltages are forced back to desired reference values.

6 Conclusions

In this work, we proposed a top-to-bottom hierarchical control structure for an islanded DCmG. Our supervisory controller resting atop a primary voltage layer comprises secondary and tertiary layers. By utilizing an MPC-based EMS at tertiary layer, optimal power references are generated. The secondary layer translates these power signals into voltage references for the primary layer. More specifically, the voltage references are generated by solving an optimization
problem at the secondary layer, which can incorporate practical operational constraints. Furthermore, we studied the well-posedness of the secondary optimization problem by discussing its feasibility and deduced a novel condition for the uniqueness of generator voltages and DGU power injections. Lastly, we demonstrated multiple layers of our hierarchical controller working in tandem to achieve desired objectives on a 16-node mG.

Future work will target the development of an EMS that enables the mG to work in grid-connected mode, and a secondary control layer that does away with the dependence on mG topology. Further developments can also focus on solving the proposed optimization problem in a distributed and efficient manner.
References

[1] J. Li, F. Liu, Z. Wang, S. H. Low, and S. Mei, “Optimal power flow in stand-alone DC microgrids,” IEEE Transactions on Power Systems, vol. 33, no. 5, pp. 5496–5506, Sep. 2018.

[2] A. La Bella, S. Negri, R. Scattolini, and E. Tironi, “A two-layer control architecture for islanded AC microgrids with storage devices,” in 2018 IEEE Conference on Control Technology and Applications (CCTA). IEEE, 2018, pp. 1421–1426.

[3] Q. Shafiee, J. M. Guerrero, and J. C. Vasquez, “Distributed Secondary Control for Islanded Microgrids : A Novel Approach,” IEEE Transactions on Power Electronics, vol. 29, no. 2, pp. 1018–1031, 2014.

[4] M. Babazadeh and H. Karimi, “A robust two-degree-of-freedom control strategy for an islanded microgrid,” IEEE Transactions on power delivery, vol. 28, no. 3, pp. 1339–1347, 2013.

[5] T. Dragičević, X. Lu, J. C. Vasquez, and J. M. Guerrero, “DC microgrids part I: A review of control strategies and stabilization techniques,” IEEE Transactions on Power Electronics, vol. 31, no. 5, pp. 4876–4891, 2016.

[6] L. Meng, Q. Shafiee, G. Ferrari-Trecate, H. Karimi, D. Fulwani, X. Lu, and J. M. Guerrero, “Review on control of DC microgrids and multiple microgrid clusters,” IEEE Journal of Emerging and Selected Topics in Power Electronics, vol. 5, no. 3, pp. 928–948, 2017.

[7] A. Bidram and A. Davoudi, “Hierarchical structure of microgrids control system,” IEEE Transactions on Smart Grid, vol. 3, no. 4, pp. 1963–1976, Dec 2012.

[8] A. Martinelli, P. Nahata, and G. Ferrari-Trecate, “Voltage stabilization in MVDC microgrids using passivity-based nonlinear control,” in 2018 IEEE Conference on Decision and Control (CDC), Dec 2018, pp. 7022–7027.

[9] P. Nahata, R. Soloperto, M. Tucci, A. Martinelli, and G. Ferrari-Trecate, “A passivity-based approach to voltage stabilization in islanded DC microgrids with ZIP loads,” Tech. Rep., 2017. [Online]. Available: https://infoscience.epfl.ch/record/253266?ln=en

[10] M. Tucci, L. Meng, J. M. Guerrero, and G. Ferrari-Trecate, “Stable current sharing and voltage balancing in dc microgrids: A consensus-based secondary control layer,” Automatica, vol. 95, pp. 1 – 13, 2018.

[11] S. Trip, M. Cucuzzella, X. Cheng, and J. Scherpen, “Distributed averaging control for voltage regulation and current sharing in dc microgrids,” IEEE Control Systems Letters, vol. 3, no. 1, pp. 174–179, Jan 2019.

[12] M. Kumar, S. C. Srivastava, and S. N. Singh, “Control strategies of a DC microgrid for grid connected and islanded operations,” IEEE Transactions on Smart Grid, vol. 6, no. 4, pp. 1588–1601, July 2015.

[13] T. Dragičević, J. M. Guerrero, and J. C. Vasquez, “Supervisory control of an adaptive-droop regulated dc microgrid with battery management capability,” IEEE Transactions on Power Electronics, vol. 29, no. 2, pp. 695–706, Feb 2014.
[14] A. La Bella, S. R. Cominesi, C. Sandroni, and R. Scattolini, “Hierarchical predictive control of microgrids in islanded operation,” *IEEE Transactions on Automation Science and Engineering*, vol. 14, no. 2, pp. 536–546, 2017.

[15] M. Marzband, F. Azarinejadian, M. Savaghebi, and J. M. Guerrero, “An optimal energy management system for islanded microgrids based on multiperiod artificial bee colony combined with markov chain,” *IEEE Systems Journal*, vol. 11, no. 3, pp. 1712–1722, 2017.

[16] C. A. Hans, P. Sopasakis, A. Bemporad, J. Raisch, and C. Reincke-Collon, “Scenario-based model predictive operation control of islanded microgrids,” in *2015 54th IEEE Conference on Decision and Control (CDC)*, Dec 2015, pp. 3272–3277.

[17] A. Parisio, E. Rikos, and L. Glielmo, “Stochastic model predictive control for economic/environmental operation management of microgrids: An experimental case study,” *Journal of Process Control*, vol. 43, pp. 24–37, 2016.

[18] S. R. Cominesi, A. La Bella, M. Farina, and R. Scattolini, “A multi-layer control scheme for microgrid energy management,” *IFAC-PapersOnLine*, vol. 49, no. 27, pp. 256–261, 2016.

[19] A. Iovine, T. Rigaut, G. Damm, E. De Santis, and M. D. Di Benedetto, “Power management for a dc microgrid integrating renewables and storages,” *Control Engineering Practice*, vol. 85, pp. 59–79, 2019.

[20] A. S. Matveev, J. E. Machado, R. Ortega, J. Schiffer, and A. Pyrkin, “On the existence and long-term stability of voltage equilibria in power systems with constant power loads,” *arXiv preprint arXiv:1809.08127*, 2018.

[21] R. Han, M. Tucci, A. Martinelli, J. M. Guerrero, and G. Ferrari-Trecate, “Stability analysis of primary plug-and-play and secondary leader-based controllers for DC microgrid clusters,” *IEEE Transactions on Power Systems*, vol. 34, no. 3, pp. 1780–1800, May 2019.

[22] M. Tucci, S. Riverso, and G. Ferrari-Trecate, “Line-independent plug-and-play controllers for voltage stabilization in DC microgrids,” *IEEE Transactions on Control Systems Technology*, vol. 26, no. 3, pp. 1115–1123, May 2018.

[23] J. W. Simpson-Porco, F. Dörfler, and F. Bullo, “Voltage collapse in complex power grids,” *Nature communications*, vol. 7, p. 10790, 2016.

[24] S. Taheri and V. Kekatos, “Power flow solvers for direct current networks,” *arXiv preprint arXiv:1807.03936*, 2018.

[25] A. La Bella, P. Nahata, and G. Ferrari-Trecate, “A supervisory control structure for voltage-controlled islanded DC microgrids,” in *58th IEEE Conference on Decision and Control*, 2019.

[26] P. Kundur, *Power System Stability and Control*. McGraw-Hill, 1994.

[27] A. Bemporad and M. Morari, “Control of systems integrating logic, dynamics, and constraints,” *Automatica*, vol. 35, no. 3, pp. 407 – 427, 1999.

[28] F. Dörfler, J. W. Simpson-Porco, and F. Bullo, “Electrical networks and algebraic graph theory: Models, properties, and applications,” *Proceedings of the IEEE*, vol. 106, no. 5, pp. 977–1005, 2018.
[29] F. Dörfler and F. Bullo, “Kron reduction of graphs with applications to electrical networks,” IEEE Transactions on Circuits and Systems I: Regular Papers, vol. 60, no. 1, pp. 150–163, 2013.

[30] S. Sanchez, R. Ortega, R. Grino, G. Bergna, and M. Molinas, “Conditions for existence of equilibria of systems with constant power loads,” IEEE Transactions on Circuits and Systems I: Regular Papers, vol. 61, no. 7, pp. 2204–2211, 2014.

[31] T.-T. Lu and S.-H. Shiou, “Inverses of 2 by 2 block matrices,” Computers and Mathematics with Applications, vol. 43, no. 1, pp. 119 – 129, 2002.

[32] D. Gale and H. Nikaido, “The Jacobian matrix and global univalence of mappings,” Mathematische Annalen, vol. 159, no. 2, pp. 81–93, 1965.