Letter

Frequency Offset Tolerant Synchronization Signal Design in NB-IoT

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Abstract: Timing detection is the first step and very important in wireless communication systems. Timing detection performance is usually affected by the frequency offset. Therefore, it is a challenge to design the synchronization signal in massive narrowband Internet of Things (NB-IoT) scenarios where the frequency offset is usually large due to the low cost requirement. In this paper, we firstly proposed a new general synchronization signal structure with a couple of sequences which are conjugated to remove the potential timing error that arises from large frequency offset. Then, we analyze the suitable sequence for our proposed synchronization signal structure and discuss a Zadoff–Chu (ZC) sequence with root 1 as an example. Finally, the simulation results demonstrate that our proposed synchronization signal can work well when the frequency offset is large. It means that our proposed synchronization signal design is very suitable for the massive NB-IoT.

Keywords: Internet of Things; synchronization signal; timing detection; large frequency offset

1. Introduction

Internet of Things (IoT) is a network that aims to connect all the devices in the world and makes the information exchange between them easily. With the rapid development of wireless communication technologies, the IoT has been realized step by step in recent years. Over 60% of the IoT application scenarios are massive narrowband IoT (NB-IoT), whose characteristics include massive access devices, transmission delay tolerance and low cost, such as smart meters, eHealth, and so on [1–5]. The low-cost requirement means that cheap/inaccurate crystal oscillators are used, which leads to a large frequency offset between the transmitter and the receiver. It poses challenges to the design of the downlink synchronization signal which is used to obtain the downlink timing. In a traditional Long Term Evolution (LTE) system, the primary synchronization signal (PSS) and the secondary synchronization signal (SSS) are used to achieve the downlink timing [6,7]. PSS detection, as the very first process, usually suffers from the large frequency offset. The frequency offset will destroy the ‘perfect’ correlation of the Zadoff–Chu (ZC) sequence used by the PSS, which could lead to a timing error even if there is no noise [7]. Then, the differential correlator or partial correlator is usually used to remove or diminish the effects of the frequency offset on timing detection [8].

In this paper, we firstly proposed a new general synchronization signal structure with two PSS sequences in an adjacent period which are conjugated to remove the potential timing error caused by the frequency offset. Then, we analyze the suitable sequence for our proposed structure and analyze a special ZC sequence as an example. Finally, simulation results are given to verify our proposed design.
2. System Model

Assume that the synchronization signal in time domain is \( x[n] \), \( n = 0, 1, \cdots, N - 1 \) at the transmitter, where \( N \) is the length of the synchronization sequence. Assuming there is one receiving antenna, the received signal at the device can be written as follows:

\[
y[n] = hx[n - d] + w[n]
\]

where \( h \sim CN(0, \sigma^2_h) \) is the channel gain, unknown but assumed to be constant over the PSS transmission duration, \( d \) is the number of samples corresponding to the signal propagation time between the transmitter and the receiver, and \( w[n] \sim CN(0, \sigma^2_w) \) is the white Gaussian noise.

When considering the frequency offset, the received signal at the device can be rewritten as follows:

\[
y[n] = hx[n - d]e^{j2\pi n \Delta f / f_s} + w[n],
\]

where \( f_s \) is the Nyquist sampling rate without a consideration of the sampling offset and \( \Delta f \) is the frequency offset between the transmitter (base station) and the receiver (device).

At the receiver, a local sequence which is a copy of the transmitter synchronization sequence is used to detect the right receiving start time by calculating the correlation between the received signal and the local sequence. The output of the direct correlator can be written as follows [9]:

\[
z(k) = \frac{1}{N} \sum_{n=0}^{N-1} y[n+k]x^*[n]
\]

When the phase change caused by the frequency offset is large in the signal duration, e.g., \( 2\pi N \Delta f / f_s > \pi \), the M-part correlator or differential correlator will be used to replace the direct correlator [8]. Just as the name implies, the purpose of the M-part correlator is to divide the sequence into M parts while doing correlation operations. \( M \) is decided by the maximum phase shift, and the relationship can be written as follows:

\[
2\pi N \Delta f / (f_s \cdot M) < \varphi
\]

where \( \varphi \) is the threshold to limit the phase change in each part; its value depends on its specific applied environment. Then, we can work out the number of segments:

\[
M = \left\lceil \frac{2\pi N \Delta f}{\varphi f_s} \right\rceil
\]

\( \lceil \cdot \rceil \) is the integer-valued function. Obviously, the maximum phase in each part will decrease as the number of segments increase, which means stronger frequency offset tolerance. The output of the M-part correlator can be written as follows:

\[
z(k) = \frac{1}{N} \sum_{m=0}^{M-1} \sum_{n=\frac{N}{M}m}^{N/(M+1)-1} y[n+k]x^*[n]
\]

Obviously, the processing gain will decrease approximately \( 10 \log_{10} M \) dB for the M-part correlator due to the non-coherent combination among the different parts compared to the direct correlator.

The output of the differential correlator can be expressed as follows [10]:

\[
z(k) = \frac{1}{N - k_d} \sum_{n=0}^{N-1-k_d} y[n+k_d+k]y^*[n+k]x^*[n+k+k_d]x[n]
\]
Although the differentiation operation can remove the frequency offset perfectly, there will be at least a 3 dB loss in the signal-to-noise ratio (SNR) due to the fact that the noise is amplified by the multiplication between two received samples [5]. Therefore, these two methods are usually used when the frequency offset is large.

Then, a maximum likelihood estimate (MLE) is used to estimate the right timing, and it can be expressed as follows [9]:

$$\hat{k} = \arg\max_k |z(k)|$$

(8)

3. Conjugated-Sequences-Based Timing Structure

Our proposed synchronization signal consists of a couple of sequences which are conjugated. When we ignore the noise and the channel gain (constant in one detection process), the output of the direct correlator in (3) can be rewritten as follows:

$$|z_1(\Delta k_1, \Delta f)| = \left| \frac{1}{N} \sum_{n=0}^{N-1} x[n + k_1 - d]e^{j2\pi(n+k_1)\Delta f/f_s}x^*[n] \right|$$

$$= \left| \frac{1}{N} \sum_{n=0}^{N-1} x[n + \Delta k_1]x^*[n]e^{j2\pi n \Delta f/f_s} \right|$$

(9)

where $\Delta k_1 \triangleq k_1 - d$, $k_1$ is the index of the direct correlator output with local sequence $x[n]$.

The other synchronization sequence in our design is $x^*[n]$, $n = 0, 1, \cdots, N-1$. The corresponding correlator output can be expressed as follows:

$$|z_2(\Delta k_2, \Delta f)| = \left| \frac{1}{N} \sum_{n=0}^{N-1} x^*[n + k_2 - d]x[n]e^{j2\pi(n+k_2)\Delta f/f_s} \right|$$

$$= \left| \frac{1}{N} \sum_{n=0}^{N-1} x[n]x^*[n - \Delta k_2]e^{j2\pi n \Delta f/f_s} \right|$$

(10)

where $\Delta k_2 \triangleq d - k_2$, $k_2$ is the index of the direct correlator output with local sequence $x^*[n]$.

Obviously, when $\Delta k_1 = \Delta k_2$

$$|z_1(\Delta k_1, \Delta f)| = |z_2(\Delta k_2, \Delta f)|$$

(11)

without considering the data adjacent to the synchronization sequence, that is

$$x[n] = 0, \text{ } n \geq N \text{ or } n < 0$$

(12)

It means if the timing estimated from the correlator with local sequence $x[n]$ is

$$\hat{k}_1 = \Delta \hat{k}_1 + d$$

the timing estimated from the correlator with local sequence $x^*[n]$ must happen at

$$\hat{k}_2 = d - \Delta \hat{k}_2 = d - \Delta \hat{k}_1$$

(14)

where $\Delta \hat{k}_1$ and $\Delta \hat{k}_2$ are the timing estimation errors which are the same according to (11).

Therefore, the right timing can be estimated according to

$$\hat{k} = \frac{\hat{k}_1 + \hat{k}_2}{2}$$

(15)

which can remove the potential reducible timing error caused by the large frequency offset which is difficult to avoid [11].
In the current LTE system, the synchronization signal is periodic, so we let the two PSS sequences in the adjacent period utilize the \( x[n] \) and \( x^*[n] \), respectively, as shown in Figure 1. When the timing error caused by the frequency offset is small (for the device with an expensive crystal oscillator), it needs to search just one of the PSS signals rather than two for the purpose of a quick search, low complexity, and compatibility. In addition, it can maintain the peak-to-average power ratio (PAPR) property of the original sequence \( x[n] \) without the need of PAPR reduction techniques [12–15].

\[
\sum_{n=0}^{N-1} |x[n]|^2 = \sum_{n=0}^{N-1} |x^*[n]|^2
\]

where \( \sum_{n=0}^{N-1} |x[n + \Delta k]| = \sum_{n=0}^{N-1} |x^*[n]| \) is the energy of the synchronization signal. The Equation (16) holds if and only if

\[
x[n + \Delta k] = C x[n] e^{\frac{2 \pi n \Delta f}{f_s}}
\]

where \( C \) is constant.

For the consideration of good PAPR, the amplitude of \( x[n] \) should be constant (\( |C| = 1 \)). We assume

\[
x[n] = e^{\frac{j \pi}{M} \sum_{i=0}^{K-1} a_i n^i}
\]

Substituting (8) into (17) yields

\[
\sum_{i=0}^{k-1} a_i (n + \Delta k_1)^i = \sum_{i=0}^{k-1} a_i n^i + 2n \Delta f / f_s
\]

By comparing the coefficient of \( n^i \) on both sides of the equation, we can obtain

\[
x[n] = e^{j \pi (a_2 n^2 + a_1 n + a_0)}.
\]
Then, substituting (20) into (19) yields

\[
|z_1(\Delta k_1, \Delta \lambda, a_2)| = \frac{1}{N} \left| \sin \left( \frac{\pi \lambda (N - |\Delta k_1|)}{N} + \pi a_2 \Delta k_1 (N - |\Delta k_1|) \right) \right| \tag{21}
\]

where \(\Delta \lambda = \Delta f / \Delta f_s\) is the frequency offset normalized to the subcarrier spacing \(\Delta f_s\). According to the property of the sine function, (21) can be rewritten as

\[
|z_1(\Delta k_1, \Delta \lambda, a_2)| = \frac{1}{N} \left| \sin \left( \frac{\pi \lambda (N - |\Delta k_1|)}{N} + \pi \xi \Delta k_1 (N - |\Delta k_1|) \right) \right| \tag{22}
\]

where \(\xi = a_2 - [a_2], [\cdot] \) is the integer-valued function. It means the correlator output only depends on the fractional part of coefficient \(a_2\) that corresponds to the sequence. For ease of discussion, we can only consider the value of \(a_2 \in [0, 1)\).

From the previous analysis, we know that the ZC sequence is one of the sequences that satisfies the constraints and is suitable for our proposed PSS design. It can be expressed as [17]

\[
x[n] = e^{-\pi \mu (n+1) / N}, \quad n = 0, 1, 2, \ldots, N - 1
\]

where \(N\) is the length of the ZC sequence, \(\mu = 0, 1, 2, \ldots, N - 1\) is the root of the ZC sequence and \(\Delta k_1 = \mu / N\).

Apparently, the correlator outputs of the ZC sequences with different roots are different. In addition, the low reduction of maximum output of the correlator under large frequency offsets is beneficial to our proposed method. Figure 2 shows an example of the maximum correlator output of different ZC sequences (e.g., \(N = 131\)) under different frequency offsets, where different colors represent ZC sequences with different roots. The maximum output of the correlator is defined as

\[
z_{\text{max}}(\Delta \lambda, a_2) = \max_{\Delta k_1} |z_1(\Delta k_1, \Delta \lambda, a_2)| \tag{24}
\]

From Figure 2, we can see that the ZC sequence with root \(\mu = 1\) is most insensitive to the frequency offset. When \(\mu = 1\), (22) can be rewritten as

\[
|z_1(\Delta k_1, \Delta \lambda, \frac{1}{N})| = \begin{cases} 
\frac{1}{N} \left( \frac{(N - |\Delta \lambda|)}{N} \right) & \Delta k_1 = -\Delta \lambda \\
\frac{1}{N} \left| \sin \left( \frac{\pi (\Delta \lambda + \Delta k_1)(N - |\Delta k_1|)}{N} \right) \right| & \text{others}
\end{cases}
\]

Figure 3 shows the correlator output in (25) under different frequency offsets and different timing offsets with \(\mu = 1\). We can see that there is no timing error when the frequency offset is small (e.g., \(\Delta \lambda < 0.5\)). However, when the frequency offset increases, the timing error is present according to the criterion defined in (8). However, the timing error can be removed easily by our proposed design. In addition, by comparing Figures 2 and 3, we can see that the minimum of the maximum correlator output decreases slowly when the frequency offset \(\Delta \lambda\) increases step by 1. This is because the gap of the timing offset \(\Delta k_1\) corresponding to the adjacent peaks is one which is always true with \(\mu = 1\) for arbitrary \(N\). Therefore, the ZC sequence with \(\mu = 1\) is suitable for our proposed conjugated-sequences-based timing structure.
When $\Delta \lambda$ is because the gap of the timing offset (e.g., $\mu = 1$), it is easy to show that
$$z_1(\Delta k_1, \Delta \lambda, \frac{1}{N}) > \left| z_1(\Delta k_1 + 1, \Delta \lambda - 1, \frac{1}{N}) \right|$$
(26)

According to (25) and Figure 3, we can know that the minimum of the maximum correlator output happens at the cross point corresponding to the adjacent timing offset. When $\Delta k_1 \geq 0$, $\Delta \lambda \in [-\Delta k_1 - \frac{1}{2}, -\Delta k_1 + \frac{1}{2}]$, it is easy to show that
$$\left| z_1(\Delta k_1, \Delta \lambda, \frac{1}{N}) \right| > \left| z_1(\Delta k_1 + 1, \Delta \lambda - 1, \frac{1}{N}) \right|$$
(26)

and, according to (25). When $\Delta k_1 \leq 0$, $\Delta \lambda \in [\Delta k_1 - \frac{1}{2}, \Delta k_1 + \frac{1}{2}]$, it is also easy to show that
$$\left| z_1(\Delta k_1, \Delta \lambda, \frac{1}{N}) \right| > \left| z_1(\Delta k_1 - 1, \Delta \lambda + 1, \frac{1}{N}) \right|$$
(27)
According to (26), (27) and Figure 4, we can get that at the given maximum frequency offset \( \Delta \lambda_{\text{max}} \), the minimum of the maximum correlator output satisfies

\[
 z_{\text{min}} > z_1(-[|\Delta \lambda_{\text{max}}|], [|\Delta \lambda_{\text{max}}|] + 0.5, \frac{1}{N}) = \frac{1}{N} \sin \left( \frac{\pi (N-|\Delta \lambda_{\text{max}}|)}{2N} \right) \sin \left( \frac{\pi}{N} \right) \tag{28}
\]

The current narrowband PSS (NPSS) in NB-IoT is about 0.8 ms in time domain [18], so the subcarrier spacing in our proposed PSS is 1/0.8 ms = 1.25 kHz and \( N = 131 \) for the justice comparison. When the maximum frequency offset is 40 kHz, which is equal to 20 ppm at the carrier frequency 2 GHz, the maximum detection energy loss is approximately

\[
10 \log_{10} \left( \frac{\sin \left( \frac{\pi (N-|\Delta \lambda_{\text{max}}|)}{2N} \right)}{N \sin \left( \frac{\pi}{N} \right)} \right) \approx -2.3 \text{ dB} \tag{29}
\]

which is smaller than the energy loss in the differential correlator and M-part correlator.

![Figure 4. Illustration of the main lobes of the correlator output around the maximum frequency offset \( \Delta \lambda_{\text{max}} \) (\( \Delta \lambda_{\text{max}} > 0 \) in this example). The dash line is the copy of the solid one with the same color.](image)

Therefore, the ZC sequence with root \( \mu = 1 \) is very suitable for our proposed conjugated-sequences-based PSS structure whose correlator output is insensitive to frequency offset.

5. Simulation Results

In this section, simulation results are given to verify the performance of our proposed PSS signal with a couple of ZC sequences which are conjugated. The system parameters are set as follows: the proposed PSS is generated in a time domain in the orthogonal frequency division multiplexing (OFDM) system, the subcarrier spacing is \( \Delta f_s = 1.25 \text{ kHz} \), the length of ZC sequence is \( N = 131 \), the root of ZC sequence is \( \mu = 1 \), and the synchronization signal period is 10 ms (the same as the NPSS in NB-IoT [18]). In the simulation, the frequency offset is randomly and uniformly selected and added. The additive white Gaussian noise (AWGN) channel is used in the simulations. When the timing error is larger than 1 us, we regard it as detection error.

Figure 5 shows a contrast of the timing detection error rate between the M-part correlator and the differential correlator. We can see that 3 kHz is a cut-off point between the M-part correlator and
the differential correlator. In low frequency offset scenarios, thanks to the reduction of phase change in each segment, the M-part correlator works better than the differential correlator. However, when the frequency offset is larger than 3 kHz or more, the M-part correlator has to enlarge M in order to ensure that each part’s phase change is small enough. A larger M means a lower correlation peak, so its performance largely depends on noise. In this scenario, differential correlator is evidently a better choice.

Figure 5. Timing detection error rate of the M-part correlator and the differential correlator with different frequency offsets.

Figure 6 shows the timing detection error rate of our proposed PSS at different SNRs. We also add the NB-IoT NPSS detection performance as a reference where the differential correlator is used [19]. For a fair comparison, we also simulated the timing performance of the combination of two adjacent NPSS that the estimated timing is the average of the timing estimated from the two adjacent NPSS. We can see that the deterioration of the timing performance caused by the frequency offset is small because the correlator output of the ZC sequence with root 1 is insensitive to the frequency offset. We can see that our proposed synchronization signal can work better than the NPSS with a differential correlator even under the maximum frequency offset 40 kHz. It is true that the detection performance of our proposed synchronization signal will be worse than the NB-IoT PSS with the increment of frequency offset due to the fact that the differentiation operation can remove the frequency offset effect on detection. However, our proposed PSS design is good enough to cover the potential frequency offset range in NB-IoT scenarios (less than 40 kHz [20]).
Figure 5. Timing detection error rate of the M-part correlator and the differential correlator with different frequency offsets.

Figure shows the timing detection error rate of our proposed PSS at different SNRs. We also add the NB-IoT NPSS detection performance as a reference where the differential correlator is used [19]. For a fair comparison, we also simulated the timing performance of the combination of two adjacent NPSS that the estimated timing is the average of the timing estimated from the two adjacent NPSS. We can see that the deterioration of the timing performance caused by the frequency offset is small because the correlator output of the ZC sequence with root 1 is insensitive to the frequency offset. We can see that our proposed synchronization signal can work better than the NPSS with a differential correlator even under the maximum frequency offset 40 kHz. It is true that the detection performance of our proposed synchronization signal will be worse than the NB-IoT PSS with the increment of frequency offset due to the fact that the differentiation operation can remove the frequency offset effect on detection. However, our proposed PSS design is good enough to cover the potential frequency offset range in NB-IoT scenarios.

Figure 6. Timing detection error rate of our proposed synchronization signal with different frequency offsets.

6. Conclusions

This paper investigates the downlink synchronization signal design in a large frequency offset scenario. We propose a general synchronization signal structure design with a couple of sequences which are conjugated to deal with the timing issue instead of the differentiation operation or partial correlator. Moreover, we discuss the general formulae of the suitable sequence for our proposed structure. ZC sequence with root 1 is analyzed as an example to verify the feasibility of the proposed conjugated structure. The loss of the detection energy is small due to the special ZC sequence’s insensitivity to frequency offset. The simulation results demonstrate that our proposed synchronization signal can work better than the NPSS under large frequency offset. From the simulation results, timing detection performance loss is approximately 2 dB when the frequency offset is in the range of −40 kHz through 40 kHz. Therefore, our proposed synchronization signal is very suit for the low cost NB-IoT scenarios.

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