Trade-offs in Decentralized Multi-Antenna Architectures: The WAX Decomposition

Juan Vidal Alegría, Fredrik Rusek, and Ove Edfors

Abstract—Current research on multi-antenna architectures is trending towards increasing the amount of antennas in the base stations (BSs) so as to increase the spectral efficiency. As a result, the interconnection bandwidth and computational complexity required to process the data using centralized architectures is becoming prohibitively high. Decentralized architectures can reduce these requirements by pre-processing the data before it arrives at a central processing unit (CPU). However, performing decentralized processing introduces also cost in complexity/interconnection bandwidth at the antenna end which is in general being ignored. This paper aims at studying the interplay between level of decentralization and the associated complexity/interconnection bandwidth requirement at the antenna end. To do so, we propose a general framework for centralized/decentralized architectures that can explore said interplay by adjusting some system parameters, namely the number of connections to the CPU (level of decentralization), and the number of multiplications/outputs per antenna (complexity/interconnection bandwidth). We define a novel matrix decomposition, the WAX decomposition, that allows information-lossless processing within our proposed framework, and we use it to obtain the operational limits of the interplay under study. We also look into some of the limitations of the WAX decomposition.

Index Terms—WAX decomposition, MIMO, Massive MIMO, LIS, WAX decomposition, decentralized processing, linear equalization, MF.

I. INTRODUCTION

MULTI-ANTENNA architectures have been widely employed since they were first introduced in the 1990s and they still remain a popular research topic. The main reason is the enormous improvements in data rate and reliability coming from exploiting space-division multiplexing and diversity. Current research and development on multi-antenna architectures is trending towards scaling up the number of antennas so as to increase the spatial resolution, thus increasing the spectral efficiency by serving several users in the same time-frequency resource. Furthermore, the exploitation of millimeter-wave spectrum in modern communications also justifies the increase in the number of antennas. The reason is that the huge path-loss associated to these frequencies when the electrical size of the antennas is kept constant needs to be overcome by focusing the transmitted energy more effectively. Massive multiple-input multiple-output (MIMO) and large intelligent surface (LIS) are some examples of the trend towards increasing the number of antennas, where massive MIMO considers base stations (BSs) with hundreds of antennas while LIS extends this concept even further by considering whole walls of electromagnetically active material.

Massive MIMO is already a reality and several prototypes have been developed and tested, such as [7]–[9]. In the prototypes presented in [7], [9], the use of centralized processing leads to huge data-rates between the antennas and the central processing unit (CPU), which limits the scalability of the system as the number of antennas grows. This fact is also noticed in [8], which sacrifices performance by relying on simple decentralized beam-forming to favor scalability. The scalability issue is likely to be exacerbated if we consider LIS, where we can think of practical deployments consisting of walls equipped with an even larger number of antennas than massive MIMO (the continuous surfaces are approximately equivalent to the discrete surfaces when the sampling is dense enough, as observed in [6], [10]). Other technologies that are gaining popularity and are likely to face scalability issues include cell-free massive MIMO [11]–[13], or intelligent reflecting surfaces (IRS) [13]–[15]. We will base our study in a general multi-antenna architecture so that it can be easily extended to more specific applications, such as the ones previously mentioned.

There is a current trend towards more decentralized architectures [16]–[24] so as to reduce the information transmitted to the CPU. The idea is to carry out pre-processing of the data at the antenna end (or close to it), so that the CPU does not need to have access to all the information required to decode raw data. Available literature on decentralized massive MIMO proposes a wide range of solutions from fully-decentralized architectures [16], [20]–[22], where channel state information (CSI) does not have to be available at the CPU, to partially decentralized architectures, where some of the processing tasks are distributed, but either full [17], [24] or partial CSI [19] is available at the CPU.

In this paper we do not address the problem of decentralized CSI estimation; we assume that perfect CSI is available at the CPU. We rely on the fact that CSI estimation does not limit the overall level of decentralization within our framework since it needs to be carried out only once per coherence block. Thus, CSI estimation takes a minor fraction of the coherence time, and the estimated CSI can be then used for the data phase throughout the rest of the coherence block without affecting the level of decentralization. However, the problem of estimating and sharing CSI in an efficient and scalable way within our framework remains as future work.

In [19] it is argued that an architecture is decentralized enough if it does not need extra hardware apart from the minimum required during the payload data phase. It also states that the volume of data transferred during the data phase has to be independent of the number of antennas at the base station.
However, as happens in [19]–[22], in order to reduce this volume of data (related to the number of connections to the CPU) and make it independent of the number of antennas, each antenna has to provide a number of outputs that scales with the number of users. Note that in a centralized architecture we would have only one output per antenna corresponding to one input to the CPU per antenna. We notice the existence of an interesting trade-off between the number of connections to the CPU and the number of outputs from each antenna.

The main goal of this paper is to study the interplay between level of decentralization and the corresponding increase in decentralized processing complexity for multi-antenna architectures. We measure the level of decentralization as the number of connections to a CPU required during the data phase, and we measure the decentralized processing complexity as the number of multiplications/outputs per antenna (or antenna panel) required to achieve a given level of decentralization. So as to study this trade-off, we present a general framework for a multi-antenna architecture which allows us to change the level of decentralization and complexity by adjusting some system parameters. In this framework, the antennas are grouped into panels of a given number of antennas (this number can be 1 as in [1]). Distributed processing is applied by applying a linear transformation to the inputs of each antenna panel, which generates a given number of outputs (complexity). The outputs are then combined using a combining module that is connected to the CPU using a fixed number of CPU inputs (level of decentralization).

To the best of our knowledge, the presented trade-off has not been explored in the available literature and the results we present are completely new. In [18] trade-offs between different decentralized architectures, algorithms, and data precision levels are studied. However, these trade-offs are mainly systematic while we are interested in fundamental trade-offs where information rates are maintained with respect to typical centralized systems. Thus, our framework focuses on complex baseband processing, and we assume that the detection is always performed in a CPU.

This paper extends the work presented in [1]. The list of contributions are summarized next:

- We present a novel general framework for multi-antenna systems that allows us to consider different levels of decentralization and complexity by adjusting the system parameters. This general framework accounts for typical centralized architectures, decentralized architectures such as the one presented in [19], and hybrid architectures in between those two. In [1], a similar framework to the one studied in this paper is considered. However, the architecture presented in the current work is more general since it adds the possibility of arranging the antennas into panels.
- We define the WAX decomposition, a novel matrix decomposition that allows us to define and exploit the trade-offs within our general framework while achieving information-lossless processing. In [1] the WAX decomposition is already introduced, but in the current work we adapt it to use it in a more general framework. Furthermore, we present novel results on the validity and application of said decomposition, e.g., Theorem 2.
- We present the trade-off in terms of number of multiplications/outputs per antenna panel and number of connections to the CPU for achieving information-lossless processing within our general framework. This trade-off is first studied in [1], but considering only the less general version of the current framework.
- We study through simulations the cost of obtaining simple combining modules that accept WAX decomposition within our general framework.
- We study complexity limitations for the combining modules within our general framework that accept WAX decomposition.
- We study the information-loss associated to operating within our framework when WAX decomposition is not available.
- We present a simple non-optimal solution for determining the distributed processing to be applied whenever WAX decomposition is not available. A deeper research on more effective solutions remains as future work.

The rest of the paper is organized as follows. Section II presents the general framework under study, as well as the system model and problem formulation. In Section III we present the WAX decomposition, which allows the application of information-lossless processing within our general framework. In Section IV we study the problem of defining a simple, but valid, combining network for our general framework. Section V presents some discussion on the resulting trade-offs, and some examples of the usage of WAX decomposition. Section VI explores broadly the case where WAX decomposition is not available and other information-lossy processing has to be applied to maximize the data-rates within our framework. We conclude the paper in Section VII with a summary of the contributions and future work.

Notation: In this paper, lowercase, bold lowercase and bold uppercase letters stand for scalars, column vectors and matrices, respectively. When using the mutual information operator, $I(\cdot;\cdot)$, bold uppercase in the sub-scripts refers to random vectors instead of their realizations. The operations $(\cdot)^T$, $(\cdot)^*$ and $(\cdot)^H$ denote transpose, conjugate, and conjugate transpose, respectively. The operation $\text{diag}(\cdot)$ outputs a block diagonal matrix with the input matrices/vectors as the diagonal blocks. The operator $\text{vec}(\cdot)$ transforms a matrix into a vector by concatenating its columns. $I_i$ corresponds to the identity matrix of size $i$, $I_{i\times j}$ denotes the $i \times j$ all-ones matrix, and $0_{i\times j}$ denotes the $i \times j$ all-zeros matrix. In this paper, a randomly chosen matrix corresponds to a realization of a random matrix whose elements are driven from a continuous probability distribution function.

II. SYSTEM MODEL

Let us consider $K$ single-antenna users transmitting to an $M$-antenna BS through a narrow-band channel. The $M \times 1$ received complex vector, $y$, can be expressed as

$$ y = Hs + n, \quad (1) $$

1Antenna panel refers to a group of co-located antennas.
where $H$ is the channel matrix of dimension $M \times K$, $s$ is the $K \times 1$ vector of symbols transmitted by the users, and $n$ is a zero-mean complex white Gaussian noise vector with sample variance $N_0$. The $M$ antennas are divided into panels of $N$ antennas; $M/N = P$ is thus restricted to integer values. Each panel, $p \in \{1, \ldots, P\}$, multiplies the received vector, $y_p = [y_{p-1}N + 1 \ldots y_pN]$, by an $L \times N$ matrix, $W_p^H$. The aggregated outputs are combined through a fixed $T \times LP$ matrix, $A^H$. The resulting vector is forwarded to a CPU, which can apply further processing. For our analysis, we will assume that the CPU can multiply the incoming matrix by a $K \times T$ matrix $X^H$ to be able to express the equivalent matrix in the form of already known linear equalizers. However, our main interest is to maximize the information rate at which the users transmit to the CPU, so the last step is not required for the analysis since it will not increase this information rate (recall the data-processing inequality [25]). Also, we define the previous matrices using conjugate transpose so as to ease upcoming notation. Fig. 1 shows a block diagram of the general framework under study.

**Fig. 1:** General framework considered in this paper.

The post-processed vector can be expressed as

\[ z = X^H A^H W^H y, \]

where $W$ is an $M \times LP$ block diagonal matrix of the form

\[ W = \text{diag}(W_1, W_2, \ldots, W_P). \]

We assume that the matrices $W_p$, and $X$ can be tuned for every channel realization, while the matrix $A$ is fixed. We can interpret $A$ as the matrix associated to a hardware combining network that can be redesigned, but it cannot be changed once deployed. Table I shows a classification of the different system parameters considered within our framework.

For tractability we assume that the channel matrix, $H$, is perfectly available at the BS. Thus, we can obtain $W_p$, and $X$ as a function of said matrix. However, the consequences of having an error in the estimation of $H$ due to imperfect CSI would not be enhanced by the framework under study. In fact, the information-loss associated to having imperfect CSI within our framework would not differ from the case of having imperfect CSI in a typical centralized architecture. The reason is that we could still apply an approximation of the spatially-matched filter (MF) within our framework using the imperfectly estimated channel matrix, as we will be able to understand from the upcoming analysis.

For a fixed $A$, we are interested in maximizing the information rate at which the users can transmit, i.e., we would like to solve the maximization problem

\[
\text{maximize } \sum_{p=1}^{P} I_{Z,p}(s; z; s).
\]

More specifically, we will explore the cases where the maximization results in $I_{Z,p}(s; z; s) = I_{Y,p}(s; y, s)$. From the data-processing inequality [25] we have

\[
I_{Z,p}(s; z; s) \leq I_{Y,p}(s; A^H W^H y; s) \leq I_{Y,p}(s; y; s).
\]

This means that the application of $X^H$ at the CPU cannot possibly increase the information rate and, as we mentioned before, it is just a manipulation to adapt the dimensions. Furthermore, assuming $s \sim CN(0_{K \times 1}, P_s I_K)$ so that the mutual information is maximized (and thus coincides with the capacity), we have [26]

\[
I_{Y,p}(s; y; s) = \log \det \left( I_M + \frac{P_s}{N_0} HH^H \right).
\]

Therefore, we can state that, if we are able to achieve

\[
I_{Z,p}(s; z; s) = I_{Y,p}(s; y; s),
\]

the information rate is maximized and the equivalent processing is information-lossless.

One of the main scopes of this paper is to study the conditions, in terms of constraints on the system parameters, for our framework to be able to perform information-lossless processing. The following lemma will be helpful.

**Lemma 1:** Considering the presented framework, the equality

\[
I_{Z,p}(s; z; s) = \log \det \left( I_M + \frac{P_s}{N_0} HH^H \right),
\]

is fulfilled if and only if we can find a $W$ and $X$ such that

\[
WAX = H.
\]

**Proof:** Let $R(y)$ be any sufficient statistic for $s$ (which means that it is information-lossless [25]). Then, for any $S \geq K$ there exists a full-rank $S \times K$ matrix $X$ such that

\[
R(y) = XH^H y.
\]

Therefore, for $S = K$, we have that $XH^HW^Hy$ is a sufficient statistic if and only if

\[
XH^HW^Hy = XH^Hy,
\]

which leads to

\[
X^{-1}XH^HW^Hy = H^H.
\]

**TABLE I: System parameters**

| Given parameters | Trade-off parameters |
|------------------|----------------------|
| $M, K$           | $L, T, N, P$         |
| Tunable parameters | $W, X$ | Parameters fixed by design | $A$ |

TABLE I: System parameters
but since $\mathbf{X}^{-1}$ can be absorbed by $\mathbf{X}$, we have
\[ \mathbf{X}^H \mathbf{A}^H \mathbf{W}^H = \mathbf{H}^H, \]
which proves the lemma by taking the conjugate transpose. □

Lemma 3 gives an important result to understand when the maximization in (4) achieves information-lossless processing. This lemma will be the basis in Section III for solving the maximization problem (4) whenever information-lossless processing is available within our framework. Note that (13) would correspond to applying MF within our framework, which is well known to be an information-lossless transformation if optimum processing is applied thereafter.

A. Notes on the downlink scenario

Throughout this paper we focus on the uplink scenario for improved clarity. However, this work can straightforwardly be extended to a downlink scenario. We next provide a few details.

Let us assume channel reciprocity, which eases notation and remarks the equivalence with the uplink scenario.\footnote{In case of non-reciprocity, the presented framework would still be valid as long as the base station has access to the downlink channel matrix.} This means that, given the uplink equation (1), the corresponding downlink equation for the vector received by the users is as follows
\[ y_d = \mathbf{H}^T z_d + \mathbf{n}_d, \]
where $y_d$ is now a $K \times 1$ vector with entries associated to the complex baseband signal seen by each user, $z_d$ is the $M \times 1$ precoded vector sent by the antennas, and $\mathbf{n}_d$ is the corresponding noise vector. We assume that the same framework as in Fig. 1 applies for the downlink, but changing the arrows from left to right. This implies that the linear operators (including the combining module) are assumed to be able to use inputs as outputs, and vice versa. The precoded vector would then be defined as
\[ z_d = \mathbf{W}^* \mathbf{A}^* \mathbf{X}^* s_d, \]
where $s_d$ is now a $K \times 1$ vector with complex entries associated to the signals intended for each user. In this case, Lemma 1 does not apply unless we assume that the users can collaborate with each other. However, we can achieve lossless precoding with respect to standard centralized linear precoding schemes, since we can still apply typical precoding schemes within our framework such as MF, zero-forcing (ZF) or minimum mean squared error (MMSE). Furthermore, with the assumption of channel reciprocity in place, the $\mathbf{W}$ matrices could be kept fixed from the uplink processing ($\mathbf{A}$ is still fixed by design) and $\mathbf{X}$ can be adapted to apply the desired precoding scheme. In the case of non-reciprocity of the channel, both $\mathbf{X}$ and $\mathbf{W}$ have to be recomputed for applying the desired precoding based on the downlink channel.

B. Previously studied architectures within our framework

There are several multi-antenna architectures that could be represented within our framework, which further motivates our study. In this case, what we mean with “represent an architecture within our framework” is that there is a combination of design variables within our framework that gives the same processing. The most obvious architecture that fulfills this is a typical centralized $M$-antenna architecture, e.g., centralized massive MIMO systems. In this case all the antennas are directly connected to a CPU, which corresponds to having one antenna per panel, and all antennas connected directly to the CPU, i.e., $N = 1$, $L = 1$, $\mathbf{W}_p = 1$ (scalar), $T = M$, $\mathbf{A} = \mathbf{I}_M$. This architecture is depicted in Fig. 2 (left).

Another architecture that can be represented within our framework is the decentralized massive MIMO architecture from [19]. In this case, there is also one antenna per panel, but antenna $m$ multiplies its input by the $K \times 1$ local channel vector, $\mathbf{h}_m$, and the result is summed over all the antennas so that the size of the vector transmitted to the CPU coincides with the number of users, $K$. Thus, this architecture can be represented within our framework by setting $N = 1$, $L = K$, $\mathbf{W}_p = \mathbf{h}_p$ (vector), $T = K$, $\mathbf{A} = [\mathbf{I}_K \mathbf{I}_K \ldots \mathbf{I}_K]^T$, which corresponds to MF if no $\mathbf{X}$ is applied at the CPU. This architecture is depicted in Fig. 2 (right). In [19] it is claimed that, for a system to be decentralized, the volume of data transmitted to the CPU during the data phase should not scale with $M$. However, the proposed solution reduces this scaling to $K$ by increasing the number of multiplications/outputs per antenna to $K$, which increases the decentralized processing complexity. Our framework allows us to freely adjust these parameters.

Comparing the two architectures from Fig. 2, where in both cases information-lossless processing can be applied, we immediately identify a trade-off between the number of connections to the CPU and the number of outputs per antenna panel as depicted in Fig. 3 (left). Note that, in this case, we have a single antenna per panel, i.e., $N = 1$. In our framework, these two quantities, which are traded-off against each other, correspond to $T$ and $L$, respectively. We can also see the trade-off in terms of multiplications per antenna as in Fig. 3 (right), which can lead to a fairer comparison if we consider panels with more than 1 antenna. The main reason is that, for $N > 1$, the number of outputs can be maintained [27], but the complexity still increases due to a higher number of multiplications.

If we look into other decentralized architectures, such as the ones presented in [18], [20]–[22], [27], we can also represent them within our framework and identify the trade-off between inputs to the CPU and multiplications/outputs per panel (in these cases the trade-off may not lead to information-lossless processing). For example, the architectures from [20]–[22] would all lead to the decentralized point in the plots from Fig. 3 since they all apply $K \times 1$ filters in each antenna during the data phase and generate $K$ outputs per panel during the data phase ($N = 1$ in these architectures). The same is true in [27] if we consider outputs per panel, although if we look at multiplications per panel we should scale the number of outputs by the number of antennas per panel, $N > 1$. Note that, even though [27] presents two different architectures with different level of decentralization, within our framework they lead to the same value of the trade-off; the only difference
is that in the fully-decentralized architecture $X$ would not be applied. In [18], however, we would not be able to represent the fully-decentralized architecture within our framework since detection is performed before reaching the CPU, while our framework only considers complex baseband processing.

### III. WAX Decomposition

Given an arbitrary $M \times K$ matrix, $H$, and a fixed $LP \times T$ matrix, $A$, we define the WAX decomposition of $H$, whenever it exists, as

$$ H = WAX. $$

This decomposition relates directly to Lemma 1, i.e., $W$ has the structure defined in (5) and $X$ is a $T \times K$ matrix. Furthermore, the existence of this matrix decomposition for any channel realization would assure that our framework can apply information-lossless processing.

The next lemma will allow us to restrict our study to the case where $N = L$ since this case can span a major part of the general problem through some manipulation.

**Lemma 2:** Assume $M/L$ and $L/N$ are integer valued. Consider some fixed $T \times K$ matrix $X$, an $M \times K$ matrix $H$, an $M \times T$ matrix $A$, and an $LP \times T$ matrix $\hat{A} = T_{L,N}A$, with

$$ T_{L,N} = I_{M/L} \otimes \left( I_{L \times T} \otimes I_L \right). $$

Then, the statement

$$ \exists W : H = WAX, $$

where $W$ is defined as in (5), is true if and only if

$$ \exists \tilde{W} : H = \tilde{W}AX, $$

where $\tilde{W} = \text{diag}(\tilde{W}_1, \ldots, \tilde{W}_{M/L})$, and $\tilde{W}_p$ are $L \times L$ matrices $\forall p \in \{1, \ldots, M/L\}$, i.e., $\tilde{W}$ has the same structure as (5) for $N = L$.

**Proof:** The right implication can be immediately seen by choosing $\tilde{W} = \hat{W}T_{L,N}$ and noticing that this matrix already has the required structure.

For the left implication it is enough to check that, for any $\tilde{W}$, there exists some $\tilde{W}$ that fulfills $\hat{W}T_{L,N} = \tilde{W}$, both $\tilde{W}$ and $\hat{W}$ with their corresponding structure. In this case, there is no linear transformation that gets $\hat{W}$ from $\hat{W}$, but we can construct it as follows. Let us take $\tilde{W} = \hat{W}T_{L,M}$, with $T_{L,M} = [I_L, \ldots, I_L]^T$. We can then select the diagonal blocks of $\tilde{W}$ to be $W_p = [\tilde{w}^{T}_{(p-1)N+1}, \ldots, \tilde{w}^{T}_{pN}]$, where $\tilde{w}_m$, $m = \{1, \ldots, M\}$, are the rows of $\tilde{W}$. Thus, there is a one-to-one mapping between all possible $\tilde{W}$ and $W$ that fulfills $\hat{W}T_{L,N} = \tilde{W}$. \hfill $\square$

Lemma 2 also implies that, whenever the assumptions apply, the trade-off between the number of outputs per panel and the number of inputs to the CPU required ($L$ and $T$, respectively) does not depend on the number of antennas per panel ($N$). However, the number of multiplications per panel would scale with $N$. An important constraint of Lemma 2 is that we need to have $L \geq N$, which can be easily checked to be a requirement for having information-lossless processing within our framework unless $L = K$ (leading to the trivial solution of setting $W_p$ as the local MFs).

The rest of this paper assumes $N = L$, unless otherwise stated, due to the intrinsic generality of this case. This means that $W$ in (16) is a square $M \times M$ block diagonal matrix given by (3), with $P = M/L$, and containing square $L \times L$ blocks. The physical implication of this restriction is that all the panels in Fig. 1 have the same number of antennas and outputs. However, as seen in the proof of Lemma 2, we can construct almost any other case from the square case by applying some transformations, with the only limitation that $M/L$ and $L/N$ must evaluate to integer values, as well as

---

**Fig. 2:** Architecture of centralized massive MIMO (left), and decentralized massive MIMO from [19] (right).

**Fig. 3:** Number of inputs to the CPU (related to $T$) vs. number of outputs (left)/number of multiplications (right) per antenna (related to $L$) for the architectures depicted in Fig. 2. Available literature does not address the behavior in between 1 and $K$ outputs per antenna.
that $A$ must be constructed using a specific structure. For simplicity, we will use the same notation as in the general case \cite{16}, but considering the new restriction on the dimensions.

### A. Solving WAX

Let us assume for now that there exists the WAX decomposition of $H$, we later investigate when this is the case. We provide next a step-by-step solution for practical computation of (16), i.e., for obtaining the matrices $W$ and $X$ in (16) based on the current channel realization, $H$, and the fixed combining network, $A$. This step-by-step solution will also be useful to set the ground for our main result on the applicability of WAX decomposition.

Let us express $H = \begin{bmatrix} H_1^T H_2^T \cdots H_P^T \end{bmatrix}^T$ and $A = \begin{bmatrix} A_1^T & A_2^T & \cdots & A_P^T \end{bmatrix}^T$; where $H_p$ and $A_p \forall p \in \{1, \ldots, P\}$ are $L \times K$ and $L \times T$ blocks, respectively. The following lemma will be useful.

**Lemma 3:** For all matrices $H$ satisfying $\text{rank}(H_n) = L$ there exists a block diagonal matrix $W$ and a matrix $X$ such that $WAX = H$ if and only if there exists a block diagonal invertible matrix $\hat{W}$ with the same form as $W$ that fulfills $AX - \hat{W}H = 0_{M \times K}$.

**Proof:** Assume existence of a block diagonal matrix $W$ and a matrix $X$ such that $WAX = H$. This is equivalent to

$$ W_p A_p X = H_p, \quad 1 \leq p \leq P. $$

Since, by assumption, $\text{rank}(H_p) = L$, it follows that $\text{rank}(W_p) = L \forall p$, making the matrix $W$ invertible.

The reverse statement is trivial; if an invertible $W$ exists, then we can set $W = W^{-1}$. \hfill \square

For a randomly chosen $H$, the condition $\text{rank}(H_p) = L$ holds with probability 1. We can then compute the WAX decomposition by invoking Lemma \text{3}, which yields the linear system

$$ AX - W^{-1}H = 0_{M \times K}. \quad (20) $$

Using the vectorization operator we get an equivalent linear system of equations

$$ Bu = 0_{MK \times 1}. \quad (21) $$

where $u$ corresponds to the $(TK + ML) \times 1$ vector of unknowns,

$$ u = \begin{bmatrix} \text{vec}(X) \\text{vec}(W_1) \vdots \text{vec}(W_P) \end{bmatrix}, \quad (22) $$

and $B$ is an $MK \times (TK + ML)$ block matrix of the form $B = [B_1 B_2]$, resulted from the vectorization operation, with

$$ B_1 = I_K \otimes A, \quad B_2 = -(H^T \otimes I_M)P. \quad (23) $$

$P$ is an $M^2 \times ML$ block matrix composed of identity matrices, $I_L$, separated by rows of zeros so as to disregard the zeros in $\text{vec}(W)$. The solution to (21) can be found by setting $u$ to be any vector in the null-space of $B$, which will always be non-zero if condition (24) is met (as will be seen shortly). Then, we can obtain the corresponding $W^{-1}$ and $X$ from $u$ through inverse vectorization, and we should check that the resulting $W^{-1}$ is full rank so that we can obtain $W$ by taking the matrix inverse. Thus, the complexity of performing the WAX decomposition using the provided method is equivalent to that of finding the null-space of an $MK \times (TK + ML)$ matrix, and inverting $P$ matrices of dimension $L \times L$.

We have now set the ground for presenting our main result on the applicability of WAX decomposition, which is established in the following theorem.

**Theorem 1:** Assuming $N = L$, which implies that $P = M/L$ must evaluate to an integer value, fulfilling the inequality

$$ T > \max \left( \frac{MK - L}{K}, K - 1 \right) \quad (24) $$

assures that, given a fixed randomly chosen $A \in \mathbb{C}^{M \times T}$, a randomly chosen $H \in \mathbb{C}^{M \times K}$ will admit a decomposition of the form (16) with probability 1.

**Proof:** See Appendix A. \hfill \square

An immediate result of Theorem 1 is that, since we are only interested in having $L \leq K$ due to its practical implications (otherwise there is no dimensionality reduction compared to local MF from Fig. 2 right side), then we have $T \geq L$ since $T \geq K$. The case where Theorem 1 is not fulfilled is considered in Section VI.

For a randomly chosen $A$, $W^{-1}$ is full rank with probability 1, but note that some specific $A$ matrices may lead to rank deficient $W^{-1}$ even if (24) is met. That is, for a poorly chosen matrix $A$, the WAX decomposition of a matrix $H$ cannot be performed. In what follows next we study conditions on $A$ in order for the WAX decomposition to be feasible.

### B. Studying validity of matrix $A$

While Theorem 1 states that any randomly chosen $A$ works for WAX decomposition, we are, from a practical perspective, interested in $A$ matrices having simple forms (providing low computational complexity); for example, sparse matrices with elements in the set \{0, 1\}. This would significantly simplify the combining network. However, for such a matrix, Theorem 1 no longer applies. Therefore, it is of importance to investigate the exceptions to Theorem 1.

**Definition 1:** We consider $A$ to be valid for WAX decomposition (16) if the set of matrices $H$ that does not admit such a decomposition has measure 0. This is equivalent as saying that the probability of a randomly chosen $H$ admitting WAX decomposition for a valid $A$ is 1.

The following theorem will be useful to check if an $A$ matrix is valid or not.

**Theorem 2:** Consider a fixed $M \times T$ matrix $A$, where $T$ fulfills (24), and a randomly chosen $H \in \mathbb{C}^{M \times K}$, such that $B = [B_1 B_2]$ from (23) is full-rank. If the specific $H$ admits WAX decomposition (16) with $N = L$ for the given $A$, then $A$ will be valid for WAX decomposition with probability 1, i.e., any other randomly chosen $H$ will admit WAX decomposition for the same $A$ with probability 1.

\footnote{Note that, in the general case where $N \neq L$, selecting $A$ as in Lemma 2 maintains the overall sparsity properties of $A$ since the transformation $T_{L,N}$ just replicates the matrix $A$ in different positions, filling the rest with 0s.}
Proof: The proof is a side-result from the proof of Theorem 1 in Appendix A. We just have to notice that the determinant of \( W_p \) still fulfills (27) if \( A \) is fixed, so it is enough if we can find an \( H \) that gives \( \det(W_p) \neq 0 \) so that the determinant is 0 only for a countable set of \( H \) matrices.

The main contribution of Theorem 2 is that we can know if an \( A \) matrix is valid or not simply by trying to perform WAX decomposition of a single randomly chosen \( H \) using that \( A \). Theorem 2 will be widely used for our simulation results since it is our main result on the validity of \( A \). An essentially equivalent statement to that of Theorem 2 is to say that, for a given matrix \( A \), WAX decomposition of a randomly chosen matrix \( H \) is possible with probability either 1 or 0.

The following lemma states a necessary condition for \( A \) to be valid.

**Lemma 4:** Let \( \hat{A} \) be an \( FL \times T \) submatrix of \( A \) formed by selecting \( F \) out of the \( P \) blocks of size \( L \times T \) that conform \( A \), \( \{A_1, \ldots, A_P\} \). If \( A \) is valid, then

\[
\text{rank}(\hat{A}) = \min(FL, K). \tag{25}
\]

**Proof:** Let \( \hat{H} \) be an \( FL \times K \) submatrix of \( H \) formed by the \( F \) respective \( L \times K \) blocks of \( H \), and \( \tilde{W} \) an \( FL \times FL \) diagonal matrix with the \( F \) respective \( L \times L \) blocks from \( W \) as diagonal elements. If \( A \) is valid we can obtain \( \tilde{W} \) and \( X \) such that \( WAX = \hat{H} \) holds for any \( \hat{H} \) except those in a set of measure 0. For a randomly chosen \( H \), rank(\( \hat{H} \)) = \( \min(FL, K) \) with probability 1. Since

\[
\text{rank}(\tilde{W}AX) \leq \min\left(\text{rank}(\tilde{W}), \text{rank}(\hat{A}), \text{rank}(X)\right),
\]

condition (25) must be fulfilled.

A further necessary condition for \( A \) to be valid is given in Lemma 5.

**Lemma 5:** Let \( A_0 \) be a submatrix of \( A \) formed by selecting \( R \) rows from \( A \), where all rows are in different blocks \( A_p \). If \( A \) is valid, then

\[
\text{rank}(A_0) > R \frac{K - L}{K}.
\]

**Proof:** See Appendix B.

We point out that, since \( A \) is an \( M \times T \) matrix, \( \text{rank}(A_0) \) cannot exceed \( T \). However, with \( T > M(K - L)/K \), it is guaranteed that \( R \frac{K - L}{K} < T \).

An immediate consequence of Lemma 5 is that a block \( A_p \) cannot be repeated arbitrarily often in \( A \). In addition, any block \( A_p \) must have rank \( L \) (see Lemma 4). Repeating the block \( A_p \) \( r \) times in \( A \), and selecting \( A_0 \) as the same row within each of these \( r \) blocks yields,

\[
1 > r \frac{K - L}{K},
\]

which implies \( r < \frac{K}{K - L} \). Whenever \( L \leq K/2, r = 1 \) so that each block \( A_p \) can only occur once in \( A \).

Appendix C includes another, less intuitive, necessary condition for \( A \) to be valid. Despite extensive efforts, we have not been able to establish sufficient conditions for having a valid \( A \). The provided necessary conditions might not be helpful in generating valid \( A \) matrices, but they constitute initial progress on the matter. Thus, the problem of establishing sufficient conditions for valid \( A \) matrices remains open.

**IV. FINDING SPARSE A MATRICES**

Let us keep restricting ourselves to the case where \( N = L \) due to the generality of this case. Recall, however, that the transformation (17), which allows considering other \( N \) values, maintains the overall sparsity properties of \( A \) (although the percentage of 1s can decrease since 0s are being padded). As we have mentioned previously, from an implementation point of view, it is desirable to have \( A \) as a sparse matrix of 1s and 0s with as few 1s as possible. The main reason is the direct relation between the number of 1s in \( A \) and the number of sum operations required to implement such a combining matrix. Recall that we can view \( A \) as the matrix associated to a predesigned hardware combining module, which could be intuitively implemented through a network of sum modules.

In the previous section, we provided some necessary conditions on matrix \( A \) for the WAX decomposition to be applicable. However, after extensive research on the matter, we have not been able to find sufficient conditions for having a valid \( A \). This motivates the need of simulation results to further understand the limits on the sparsity of \( A \). Nevertheless, we will support these simulations with a simple theoretical bound that allows us to gain better understanding of how sparse \( A \) may be.

From Lemma 4 we can say that each \( L \times T \) block \( A_p \) \( p \in \{1, \ldots, P\} \), has to be of rank \( L \). If we put it together with Lemma 5 we can say that a valid \( A \) has a maximum of

\[
\frac{K}{K - L} - 1
\]

equal \( A_p \) blocks. Therefore, if we aim at \( A \) matrices of 1s and 0s with the minimum number of 1s, we can lower bound the number of 1s through the following lemma.

**Lemma 6:** Given a valid \( A \in \{0, 1\}^{M\times T} \), the number of 1s in \( A \), which coincidentally coincides with its squared Frobenius norm, can be lower bounded by

\[
\|A\|_F^2 \geq \sum_{k=1}^{Q-1} r_{\max}(k - Q) \left(\begin{array}{c} T \\ k \end{array}\right) + QM, \tag{27}
\]

where \( Q \) is obtained by

\[
Q = \arg\min_{q} \sum_{k=1}^{q} r_{\max} \left(\begin{array}{c} T \\ k \end{array}\right) \geq M. \tag{28}
\]

**Proof:** Let us impose the restriction that every row in \( A \) can be repeated a maximum of \( r_{\max} \) times, as suggested by Lemma 5 where the restriction of selecting the rows from different blocks can be relaxed since we are interested in a lower bound. Each row of \( A \) must have at least a 1 at some location, otherwise, considering \( L \leq T \), a row of only 0s would result in an \( A_p \) block with rank lower than \( L \) (contradicting Lemma 4). Therefore, we can bound the number of 1s in \( A \) by considering all possible combinations of rows having a single 1, each of which could be repeated a maximum of \( r_{\max} \) times. Then, we can do the same for rows having 2 1s, and go on until we have enough rows to fill the \( M \) rows.
of \( A \). Straightforward combinatorics result in the statements in the lemma.

Even though the lower bound presented in Lemma 6 might look a bit hard to compute at first sight, for reasonable values of the design variables \((M \leq 3r_{\text{max}}T)\), it is enough to consider only rows with up to two 1s to have enough rows for filling \( A \), i.e., \( Q \leq 2 \).

Figs. 4 and 5 show the minimum percentage of 1s required to have a valid \( A \) through numerical optimization, as well as the theoretical lower bound from Lemma 6 with respect to \( 100/T_{\text{opt}} \). The optimum \( T, T_{\text{opt}} \), is computed as

\[
T_{\text{opt}} = \max \left( \left[ \frac{MK - L}{K} + 1 \right], K \right), \quad (29)
\]

which corresponds to the minimum integer \( T \) that fulfills Theorem 1. The reason for having \( 100/T_{\text{opt}} \) is that it corresponds to a trivial lower bound in the percentage of 1s, i.e., if we have a single 1 per row. We can see that the relation between the minimum percentage of 1s and \( 100/T_{\text{opt}} \) is close to linear. In Fig. 4 \( K \) is fixed and \( L \) is changed, while in Fig. 5 it is the other way around. Although our initial intention was not to provide a tight bound to the numerical simulation, we can note that the obtained percentage of 1s for valid \( A \) matrices is in general close to the lower bound computed given by Lemma 6 (within \( \pm 3\% \) error in the plots).

The algorithm for computing the minimum percentage of 1s consists of selecting \( A \) by adding 1s at random positions, with some constraints related to Lemma 4 until a valid \( A \) has been found. Then, we reduce through exhaustive search the number of 1s in \( A \) as much as possible while maintaining its validity. The validity of \( A \) is evaluated by considering Theorem 2.

Table II shows the minimum number of 1s for having a valid \( A \) considering different values of \( K \) and \( L \) (recall \( L \leq K \) for information-lossless processing). The same algorithm as for Figs. 4 and 5 is employed, and \( T \) is also selected to be \( T_{\text{opt}} \) from (29). We can see that, even though from Figs. 4 and 5 the percentage of 1s increases with \( L \) and decreases with \( K \), for the total number of 1s it is the other way around, i.e., it increases with \( K \) and decreases with \( L \). This is because the size of \( A \) increases at a higher rate when \( T \) is increased. However, if we implement \( A \) using sum modules with only two inputs, we would need to subtract \( T \) to the number of 1s in \( A \), since a single 1 in a column of \( A \) would not require any sum module. Table III shows the minimum number of 2-input sum modules required to have a valid \( A \), i.e., the values of this table correspond to the values of Table II subtracting the respective \( T \) value to each entry. As we can see, the number of sum modules required does not vary considerably with \( K \) and \( L \), and it remains in the order of \( M \).

Fig. 6 shows the percentage of valid \( A \) matrices for different percentage of 1s, where the 1s are placed at random positions with the only restriction that they have at least a 1 per row and per column. The algorithm employed consists of generating a big number of random \( A \) matrices with the corresponding percentage of 1s (while fulfilling a simple restriction related to Lemma 4 with \( F = 1 \)), and checking what percentage of them are valid (Theorem 2 comes in handy here too). The different curves correspond to the same parameter combinations as the optimum \( T = T_{\text{opt}} \) as much as possible while maintaining its validity.

![Fig. 4: Minimum percentage of 1s required for a valid \( A \in \{0,1\}^{M\times T} \) with respect to \( 100/T_{\text{opt}} \) for \( M = 60, K = 7, L = \{2, 3, 4, 5, 6\} (N = L) \).](image1)

![Fig. 5: Minimum percentage of 1s required for a valid \( A \in \{0,1\}^{M\times T} \) with respect to \( 100/T_{\text{opt}} \) for \( M = 24, K = \{6, 5, 4\}, L = 3 (N = L) \).](image2)

![Fig. 6: Percentage of valid \( A \) matrices for different percentage of 1s, where the 1s are placed at random positions with the only restriction that they have at least a 1 per row and per column. The algorithm employed consists of generating a big number of random \( A \) matrices with the corresponding percentage of 1s (while fulfilling a simple restriction related to Lemma 4 with \( F = 1 \)), and checking what percentage of them are valid (Theorem 2 comes in handy here too). The different curves correspond to the same parameter combinations as the optimum \( T = T_{\text{opt}} \).](image3)

### Table II: Minimum number of 1s required to have a valid \( A \) for \( M = 60, T = T_{\text{opt}} \)

| \( L/K \) | 1 | 2 | 3 | 4 | 5 | 6 | 7 |
|------|---|---|---|---|---|---|---|
| 1    | 60 | 89 | 98 | 102 | 104 | 105 | 108 |
| 2    | -  | 60 | 78 | 89 | 94 | 98 | 102 |
| 3    | -  | -  | 60 | 72 | 80 | 89 | 91 |
| 4    | -  | -  | -  | 60 | 68 | 78 | 84 |
| 5    | -  | -  | -  | -  | 60 | 65 | 72 |
| 6    | -  | -  | -  | -  | -  | 60 | 66 |

The validity of \( A \) is evaluated by considering Theorem 2.
| L/K | 1   | 2   | 3   | 4   | 5   | 6   | 7   |
|-----|-----|-----|-----|-----|-----|-----|-----|
| 1   | 59  | 58  | 57  | 56  | 55  | 54  | 56  |
| 2   | -   | 58  | 57  | 56  | 55  | 54  | 56  |
| 3   | -   | -   | 57  | 56  | 55  | 54  | 56  |
| 4   | -   | -   | -   | 56  | 55  | 54  | 56  |
| 5   | -   | -   | -   | -   | 55  | 54  | 54  |
| 6   | -   | -   | -   | -   | -   | 54  | 57  |

TABLE III: Minimum number of 2-input sum modules required to implement a valid \( A \) for \( M = 60, T = T_{opt} \)

![Fig. 6: Percentage of valid \( A \in \{0,1\}^{M \times T} \) with 1s at random positions with respect to the percentage of 1s for \( M = 24 \), \( L = 3 \) (N=L).](image)

![Fig. 7: Number of inputs to the CPU (related to \( T \)) v.s. number of outputs (left)/number of multiplications (right) per antenna (related to \( L \)).](image)

V. DISCUSSION AND EXAMPLES

As mentioned previously, one of the goals of this paper is to find the trade-off between the different system parameters required for the equivalent processing to be information-lossless. An interesting case is when \( N = 1 \), since it relates directly to the trade-off between the centralized and decentralized architectures shown in Fig. 2. Assuming that our framework is equipped with a valid matrix \( A \), the trade-off between \( L \) and \( T \) comes directly from condition (24). We can select \( T \) as \( T_{opt} \) from (29), as can be seen in Fig. 7.

It is interesting to observe that we reach a reduction compared with the centralized architecture also for \( L = 1 \). To elaborate a bit further, we observe that with \( L = 1 \), the number of CPU inputs becomes \( T = T_{max} \triangleq \lceil M - \frac{M}{K} \rceil + 1 \). This reduction comes about since we have allowed the antennas to perform multiplications, which leads to a reduction in the number of CPU inputs from \( M \) to, at most, \( T_{max} \). The centralized architecture, illustrated in the left part of Fig. 2, has the same number of outputs per antenna, namely 1, but does not perform any multiplications. Therefore, the CPU must operate with \( T = M \). If we let \( L_{mult} \) denote the number of multiplications per antenna, the centralized architecture corresponds to \( L_{mult} = 0 \), and we can select \( T \) as

\[
T = \begin{cases} 
M \\
\max \left( \lceil M \frac{K-L_{mult}}{K} \rceil + 1, K \right) \\
L_{mult} = 0 \\
L_{mult} > 0.
\end{cases}
\]

This is conceptually illustrated in the right part of Fig. 7.

We conclude this section with a few examples.

**Example 1:** Assume a design with a CPU limited to \( T \leq 50 \) inputs, and antenna panels with \( L = 2 \) outputs. We now consider how many antennas and users (\( M \) and \( K \), respectively) can be handled by the system. From (24), we have \( 50 > M \frac{K-2}{K} \) implying that

\[
50 \frac{K}{K-2} > M.
\]

To maximize the left hand side, excluding the special case \( K = L = 2 \) (which allows for an unlimited number of antennas), we set \( K = 3 \) and obtain \( M < 150 \) so that we can at most use 149 antennas. Differently put, if we choose to equip the base station with 149 antennas, we can at most serve \( K = 3 \) users. With 150+ antennas, only 2 users can be served. Setting \( K = 4 \), yields that at most 99 antennas can be used.

We next provide two numerical examples of the WAX decomposition. The first one is meant to illustrate that it is indeed possible to obtain valid sparse matrices \( A \) comprising only elements in the set \( \{0,1\} \).

**Example 2:** Let \( M = 100, N = 4, P = 25, K = 10, \) and \( L = 4 \). From Theorem 1 we have that \( T > 100 \times 0.6 = 60 \), so we take \( T = 61 \). It can be numerically verified that the matrix

\[
A = \begin{bmatrix}
I_{61} \\
I_{22} \\
I_{39} \\
I_{17} \\
I_5 \\
I_5 \\
I_5 \\
I_2 \\
0_{2 \times 1}
\end{bmatrix}
\]

is valid. We designed this \( A \) by aiming at a minimum number of non-zero elements, while satisfying both Lemma 4 and Lemma 5. It can be verified that \( A \) has 158 ones and 5942 zeros. Thus, merely 2.6% of \( A \) is non-zero.

Our next example is providing the reader with a graphical illustration of the WAX decomposition.

**Example 3:** Let \( M = 8, N = 2, P = 4, K = 5, \) and \( L = 2 \). Thus, \( T > 4.8 \), so we select \( T = 5 \). In this case, the number of variables and the number of equations associated to the linear system (20) is \( TK + ML = 41 \) and \( MK = 40 \).
respectively; thus, we have precisely one more variable than equations. A particular example of the WAX decomposition for the given parameters is shown in (31). The strength of the WAX decomposition is that, for any $H$, except for those in a set of measure 0, the matrix $A$ can be kept as it is while only $W$ and $X$ need to change.

VI. INFORMATION-LOSS WITHOUT WAX DECOMPOSITION

Throughout the previous sections of the paper we have focused on performing information-lossless processing within our framework, i.e., we have focused on the cases where the maximization (4) leads to the WAX decomposition when Theorem 1 applies. This means that we could solve both problems using the same approach, and thus, without altering the overall complexity. In case (24) is fulfilled, (32) would give 0, and the solution would also solve the maximization (4), which is not true in general.

Solving (32) can be seen as applying approximate MF within our framework. This minimization can be found in closed-form when $L \leq \min(T, K)$ as we will now prove. Let us rewrite the norm as

$$\|AX - \bar{W}H\|_F^2 = \sum_{p=1}^{P} \|A_pX - \bar{W}_pH_p\|_F^2. \quad (33)$$

Assuming the optimum $X$ has been fixed, we would have $\bar{W}_p = A_pXH_p^\dagger (H_p^\dagger being the right pseudo-inverse of $H_p$), which is of rank $\min(T, K, L)$ when $A$ and $H$ are randomly chosen. We can restrict ourselves to $L \leq \min(T, K)$ due to its previously mentioned practical interest. In this case, (32) is solved by considering the equivalent linear system from (21)

$$\|AX - \bar{W}H\|_F^2 = u^H B^H Bu. \quad (34)$$

The vector $u$ corresponds to the entries of matrices $X$ and $W$, so the constraint on the norms of $\bar{W}$ and $X$ corresponds to an arbitrary norm constraint on $u$. With this in mind, the solution to the minimization problem is obtained by setting $u$ to be the eigenvector associated to the lowest eigenvalue of $B^H B$. If the conditions for WAX were fulfilled, the lowest eigenvalue of $B^H B$ would be 0, which would lead to the information-lossless solution.

A. Approximate MF

A simple first approach, intuitively related to how we compute WAX decomposition, is to work on the minimization problem

$$\min_{X, \{W_p\}_{p=1}^{P}} \quad \|AX - \bar{W}H\|_F^2, \quad \text{s.t.} \quad \rank(\bar{W}_p) = L, \forall p \quad (32)$$

where $\bar{W}$ has the same structure as (3) for $N = L$ and $P = M/L$, and the actual matrix $\bar{W}$ to be used in our framework would be obtained as $W = \bar{W}^{-1}$. The last constraint in (32) ensures a non-zero solution, where the scalar $c$ can be any non-zero real value. The minimization (32) leads to the WAX decomposition when Theorem 1 applies.

B. Antenna selection

Another practical approach that would give a lower bound to the maximum $I_{Z,S}(z,s)$ from (4) is to consider antenna selection so as to reduce $M$ until Theorem 1 is satisfied. Note that this approach is valid only when $T \geq K$, which was not the case with the previous approach. Furthermore, for $N > 1$, the antenna selection would correspond to panel selection since we need an integer number of panels, which means we can only reduce $N$ antennas at a time.

Note that, when applying $W = \bar{W}^{-1}$, the multiplication associated to (2) will cancel out any common scaling of $W$ and $X$.

$$\begin{bmatrix}
1 & -1 & 0 & 0 & 0 & 0 & 0 & 0 \\
2 & 1 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 2 & 1 & 0 & 0 & 0 \\
0 & 0 & 0 & 2 & 1 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 2 & -1 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 2 & -1 \\
\end{bmatrix} = \begin{bmatrix}
1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & 1 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 1 & 0 & 1 \\
0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 2 & -1 \\
0 & 0 & 0 & 0 & 0 & 0 & 2 & -1 \\
\end{bmatrix} \begin{bmatrix}
-2 & -1 & 1 & 2 \\
1 & -2 & -1 & 1 \\
1 & -1 & -2 & -1 \\
0 & 1 & 0 & 0 \\
0 & 1 & 0 & 0 \\
0 & 0 & 1 & 1 \\
0 & -1 & 2 & 1 \\
-2 & -2 & 0 & 0 \\
\end{bmatrix} \begin{bmatrix}
-3 & 1 & 0 & 1 & 0 \\
-2 & -4 & -3 & 5 & 6 \\
1 & 1 & -2 & -2 & -2 \\
0 & -2 & 0 & 1 & 0 \\
-2 & -2 & 5 & 4 & -4 \\
2 & -2 & 3 & 0 & -4 \\
1 & -2 & 2 & 3 & 4 \\
4 & -2 & 2 & 0 & -8 \\
\end{bmatrix}$$

\(W\) \(A\) \(X\) \(H\)
How to optimally make the panel selection is also a research problem in itself, but we limit our results to simple selection where the panels having the highest local channel matrix norms are the ones being used.

C. Numerical results

Definition 2: Let us define the relative rate for our framework as

$$E_{A,H} \left( \frac{I_{Z,S}(z,s)}{I_{Y,S}(y,s)} \right),$$

(35)

where A and H are standard IID Gaussian random matrices.

Figs. 8 and 9 show the average relative rates, which have been optimized using norm minimization and panel selection. We have also included as a comparison the relative rate obtained by standard brute force numerical optimization using the result from (32) as starting point. This numerical optimization can give us a hint of what average relative rates could be achieved if more clever optimization approaches were considered. In the plots, we have considered the range of L and T values not accepting a WAX decomposition according to Theorem 1. Fig. 8 shows that when we reduce T below $T_{opt}$ the performance gets slowly degraded, so the system would still be able to work at acceptable rates even if we cannot perform WAX decomposition. In the case of Fig. 9 reducing L below $L_{opt}$ attains a steeper loss, but the degradation is still reasonable. We can also see that the norm minimization associated to (32) presents a considerable loss with respect to the numerical optimization, but it can still serve as a simple auxiliary method that allows our framework to keep serving users under conditions where WAX is not possible, e.g., if the number of users increase. In fact, (32) corresponds to the WAX decomposition when the parameters allow it, so that no further processing would be needed in such a system.

Panel selection, can perform better than the norm minimization in some cases, but the limitation of having to disregard full panels makes it perform slightly worse for most values of T in Fig. 8. Furthermore, this method is slightly less versatile than norm minimization since it is not available for all possible values of T, as can be seen in Fig. 9. However, we can note that $T < K$ intuitively translates into having to discard the data from certain users, which could potentially be considered in more complex antenna selection schemes.

VII. CONCLUSIONS

We have introduced a general framework that allows the exploitation of the trade-off between complexity (number of multiplications/outputs per panel) and level of decentralization (connections to CPU) in multi-antenna architectures. We have presented the WAX decomposition, a matrix decomposition that achieves information-lossless processing within our framework under some restrictions. Said restrictions also describe the trade-off between number of multiplications/outputs per panel and number of connections to the CPU if we consider only information-lossless processing. Furthermore, we have studied the problem of finding simple combining networks (A matrices) that admit WAX decomposition within our framework. Finally, we have broadly studied the information-loss produced when the system parameters lead to non-availability of the WAX decomposition.

Future work could include a deeper study on the cases where WAX decomposition is not available and information-lossy processing has to be applied. This same line would include the design of efficient algorithms and analytical solutions to optimize the achievable information rates. Furthermore, finding efficient conditions for the combining network (i.e., A) to be valid for WAX decomposition still remains unresolved. Other lines of work could include narrowing the study to some of the exceptions that we have not covered such as when the parameters are not divisible, etc.

APPENDIX A: PROOF OF THEOREM 1

We first make the observation that the rank of A cannot be lower than the rank of H. The rank of a randomly chosen...
A is \( \min(M, T) \) with probability 1. Assuming that \( M \geq K \), this implies \( T \geq K \), expressed as \( T > \max(\cdot, K - 1) \) in the statement.

We next provide a lemma that will be useful.

**Lemma 7:** Let \( W \) and \( \hat{W} \) be two matrices of the same form as \( W \) in (3) with \( N = L \) (i.e., they are block diagonal matrices where the blocks are \( L \times L \) matrices). If \( AX = WH \) is solvable such that \( \det(W_p) > 0 \), \( 1 \leq p \leq P \), then \( AX = WH \) is solvable.

**Proof:** Suppose \( AX = WH \) is solvable such that \( \det(W_p) > 0 \), \( 1 \leq p \leq P \). This implies that \( \hat{W}^{-1} \) exists. Thus,

\[
\hat{W}^{-1} AX = H.
\]

The lemma follows by observing that \( \hat{W}^{-1} \) is of the same form as \( W \), so we can take \( W = \hat{W}^{-1} \). \( \square \)

Let us now study \( AX = WH \). Said matrix equation specifies \( MT \) linear equations in \( TK + ML \) variables, and hence, it is solvable when \( T > M(K - L)/K \), as can be seen from the vectorized version in (21). It remains to show that for randomly chosen \( A \) and \( H \), the solution satisfies \( \det(W_p) > 0 \), \( 1 \leq p \leq P \). Let us define \( \mathcal{V} \) as the set of admissible solutions, i.e.,

\[
\mathcal{V} = \{ A, H \mid \exists W, X: AX = WH, \det(W_p) \neq 0, \forall p, \det(BB^H) \neq 0 \}, \tag{36}
\]

where \( B = [B_1, B_2] \) is again the matrix associated to the equivalent linear system (21), which is given by (23). Assuming that \( B \) is full-rank, the solution to \( AX = WH \) depends on \( F = TK + ML - MK \) free variables, here denoted by \( \{z_f\} \). The solution \( \hat{W}_{i,j} \) is a linear combination of the free variables \( \{z_f\} \) where the weights depend on \( A \) and \( H \), i.e.,

\[
\hat{W}_{i,j} = \sum_{f=1}^{F} c_{p, i, j, f}(A, H)z_f.
\]

Note that the number of free variables, \( F \), can be increased if \( B \) is not full-rank (leading to a different polynomial expression), that is why we are only interested in the solutions giving a full-rank \( B \), i.e., \( \det(BB^H) \neq 0 \). Thus, the following lemma will come in handy.

**Lemma 8:** Given randomly chosen matrices \( H \) and \( A \), the matrix \( B = [B_1, B_2] \) (\( B_1 \) and \( B_2 \) are defined as in (23)) fulfills \( \det(BB^H) \neq 0 \) with probability 1.

**Proof:** We can define the determinant \( \det(BB^H) \) as a polynomial expression of the form

\[
\det(BB^H) = \sum_g b_g(A, H) \prod_{i,j,k,l} \{H \}^{h_{i,j,g}}_{l,k} \{A \}^{a_{i,k,g}}_{l,k}.
\]

This polynomial expression will evaluate to 0 only for a countable set (which thus attains probability 0) of \( A \) and \( H \) matrices if we can find at least an \( A \) and an \( H \) such that \( \det(BB^H) \neq 0 \). One example of this is when \( A \) is chosen randomly and we have \( \{H\}_{i,j} = \{A\}_{i,j}, i \in \{1, \ldots, M\}, j \in \{1, \ldots, K\} \).

A similar reasoning to the one used in the proof of Lemma 8 can be applied to show that for randomly chosen \( A \) and \( H \) we have \( \hat{W} \neq 0 \) with probability 1. The determinant \( \det(W_p) \) can be written as a polynomial combination of the previous \( (W_p)_{l,i,j} \). Thus we can express it as

\[
\det(W_p) = \sum_{g=1}^{G} c_g(A, H) \prod_{f=1}^{F} q_{g,f}, \tag{37}
\]

for some \( G \), with \( \sum_{f=1}^{F} q_{g,f} = P, \forall g \). Thus, the only possibility for having \( \det(W_p) = 0 \) is if the coefficients are zero, i.e., \( \hat{c}_g(A, H) = 0, 1 \leq q \leq G \). However, the coefficients \( \hat{c}_g(A, H) \) are rational expressions of the entries in \( A \) and \( H \). This means that, in order to have \( \hat{c}_g(A, H) = 0 \), a polynomial multi-variate expression of the entries in \( A \) and \( H \) must be 0. Again, this can only happen at most in a countable set of \( A \) and \( H \) as long as we find an \( A \) and an \( H \) such that \( \det(W_p), \forall p \), while still assuring that \( B \) is full-rank so that \( F \) remains fixed. The same example as in the proof of Lemma 8 i.e., randomly chosen \( A \) and \( \{H\}_{i,j} = \{A\}_{i,j}, \ i \in \{1, \ldots, M\}, j \in \{1, \ldots, K\} \), gives the trivial solution \( W = I_M \), and thus fulfills both conditions. Therefore, we have proved that a randomly chosen \( A \) and \( H \) will be in the set \( \mathcal{V} \) with probability 1.

**APPENDIX B: PROOF OF LEMMA 8**

From the structure of \( B_1 \) in (23), we observe that a particular row of \( A \) appears exactly in \( K \) rows of \( B \). Let us denote \( B_0 \) as the submatrix of \( B \) formed by all rows in \( B \) where the rows of \( A_0 \) appear. Clearly, to satisfy (21), we must in particular satisfy \( B_0u = 0_{RK \times 1} \). Now, \( B_0 \) reads

\[
B_0 = [I_K \otimes A_0 \ H_0],
\]

where \( H_0 \) is formed from \( H \) as follows: Let \( \iota(\ell) \) denote the block \( H_{t(\ell)} \) where the \( t \)th row in \( A_0 \) is taken from. Let

\[
H_0[t_0^{(1)} H_0[t_2^{(2)} \ldots H_0[t_R^{(T)}]^T, \tag{38}
\]

and let \( \Pi(\ell) \) be an \( R \times L \) matrix with a single entry equal to 1 at row \( \ell \) and column \( \iota(\ell) \mod L \), and all other equal to 0. Then,

\[
\hat{H}_0 = \begin{bmatrix}
0_{D_0} & H_{t(1)} \Pi(1) & 0_{D_1} & H_{t(2)} \Pi(2) & \ldots & 0_{D_{R-1}} & H_{t(R)} \Pi(R) & 0_{D_{R}}
\end{bmatrix},
\]

where we have used the shorthand notation

\[
D_k = RK \times (\iota(k) + 1 - \iota(k))L^2, \quad \iota(0) \equiv 1, \quad \iota(R + 1) \equiv M/L.
\]

To study the null space of \( B_0 \) we may just as well study the null space of \((I_K \otimes Q_0^H)B_0 \), where \( Q_0^H R_0 = A_0 \) is the QR decomposition of \( A_0 \). We have,

\[
(I_K \otimes Q_0^H)B_0 = [I_K \otimes R_0 (I_K \otimes Q_0^H) \ H_0]. \tag{39}
\]

Let \( K = \text{rank}(A_0) \). The matrix \( I_K \otimes R_0 \) consequently has \( K(R - k) \) all-zero rows.

If we extract all-zero rows, we obtain,

\[
\begin{bmatrix}
\text{vec}(X) \\
\text{vec}(W_1) \\
\vdots \\
\text{vec}(W_P)
\end{bmatrix} = 0_{K(R-K) \times 1}.
\]
where $P$ is a $K(R - \kappa) \times KR$ matrix that extracts the rows where $I_K \otimes R_0$ is all-zero. This implies that we can discard $X$ so that we equivalently obtain

$$P(I_K \otimes Q^H_0) \hat{H}_0 \begin{bmatrix} \text{vec}(W_1) \\ \vdots \\ \text{vec}(W_p) \end{bmatrix} = 0_{K(R - \kappa) \times 1}. \quad (40)$$

We next note that, due to the many all-zero columns in $\hat{H}_0$ (represented by the terms $0_{P_s}$ in (38), not all the $W_p$ matrices matter. In fact, it can be straightforwardly verified that (40) is equivalent to

$$P(I_K \otimes Q^H_0)H_0 \begin{bmatrix} w_{s(1)} \\ \vdots \\ w_{s(R)} \end{bmatrix} = 0_{K(R - \kappa) \times ML}, \quad (41)$$

where $w_{sm}$ is the $1 \times L$ vector formed from extracting the entries at the $m$th row of $W$ that are allowed to take non-zero values, and

$$H_0 = \begin{bmatrix} H^H_{s(1)} \otimes I(1) \\ H^H_{s(2)} \otimes I(2) \\ \vdots \\ H^H_{s(R)} \otimes I(R) \end{bmatrix}, \quad (42)$$

where $I(\ell)$ is the non-zero column of $I(\ell)$.

For randomly chosen $H$, the matrix $P(I_K \otimes Q^H_0)H_0$ is full rank with probability 1. Therefore, (42) only has a non-trivial solution whenever the number of unknowns is larger than the number of equations, i.e., whenever, $RL > K(R - \kappa)$. Consequently,

$$\kappa > \frac{R - L}{K}. \quad (43)$$

**APPENDIX C: NECESSARY CONDITION FOR $A$**

As per previous results, a matrix $H$ can, with probability 1, be decomposed as $H = WAX$ if and only if there exists a full rank matrix $W$ such that $AX = WH$. We are now interested in establishing necessary conditions for the matrix $A$ so that this is possible. Let $A_S$ denote a submatrix of $A$ comprising an arbitrary selection of rows in $A$ with rank ($A_S$) = $r$. An immediate consequence is that rank ($A_SX$) $\leq r$. Assuming that $H = WAX$ holds, rank ($\hat{W}SH$) = rank ($A_SX$), where $\hat{W}S$ is a submatrix of $W$ comprising rows corresponding to those in $A_S$. On the other hand, if no $\hat{W}S$ exists such that rank ($\hat{W}SH$) $\leq r$, we can infer that $A$ does not allow for a WAX decomposition, i.e., there are no matrices $W$ and $X$ such that $H = WAX$. Therefore, a necessary condition on $A$ for the existence of a WAX decomposition is

$$\text{rank}(A_S) \geq \min_{\hat{W}S \in W} \text{rank}(\hat{W}SH), \quad \forall S, \quad (43)$$

where we will define the set $W$ after having introduced further notation. The matrix $A$ contains $M$ rows, and since $L$ divides $M$, $A$ contains $P = M/L$ blocks of $L$ rows. Let the $1 \times P$ vector $a$ denote the number of rows in $A_S$ taken from the $p$th block in $A$, and let $A_{Sp}$ and $\hat{W}_{Sp}$ be the $a_p \times T$ and $a_p \times \hat{W}$ submatrices of $A_S$ and $\hat{W}_S$, respectively, formed from these $a_p$ rows. With that, $W$ is the set containing all block-diagonal matrices where the $p$th block is $a_p \times K$ and has rank $a_p$ (the latter is needed to ensure that the overall matrix $W$ is invertible).

We are now ready to study (43). Suppose that the minimum of (43) is $r$. Since $\hat{W}SH$ has dimensions $(\sum_p a_p) \times K$ this implies that the null-space of $\hat{W}SH$ has dimension $K - r$. Thus, if rank ($\hat{W}SH$) = $r$, there must exist a $K \times (K - r)$ matrix $N$ such that $\hat{W}SHN = 0$. Recalling that $\hat{W}S$ is block-diagonal with each block being of dimension $a_p \times L$ and having rank $a_p$, it follows that

$$\text{rank}(H_pN) \leq L - a_p \quad (44)$$

where $H_p$ is of dimension $L \times K$ and $H = [H_1^H \cdots H_p^H]^H$. Thus, if we want to solve the minimization in (43), we have the equivalent problem of finding the maximum possible number of columns in the matrix $N$ so that it is full rank and rank $(H_pN)$ $\leq L - a_p$, $p = 1 \ldots P$.

We next define the rank-profile $J$ of a matrix $N$ as

$$J_{bp} = \text{rank}(H_pN_{1:b}) - \text{rank}(H_pN_{1:b-1}), \quad \text{where } N_{1:b} \text{ denotes columns } 1, 2, \ldots, b \text{ of } N. \quad \text{We remark that } J_{bp} \in \{0,1\}, \quad \text{and that } \Phi \text{ depends on } H, \text{ although our notation does not indicate this dependency. In the following lemma, we characterize the admissible rank-profiles.}$$

**Lemma 9:** There exists a full rank matrix $N$ with $B$ columns such that rank $(H_pN) \leq L - a_p$ if and only if the rank-profile matrix $J$ satisfies

$$K - Ld_{<L}(\tilde{a}) + \sum_{b'=1}^{b-1} \sum_{p=1}^{P} J_{b',p} + \sum_{p=1}^{P} J_{bp} \left( L - \sum_{b'=1}^{b-1} J_{b',p} \right) \geq b, \quad 1 \leq b \leq B,$$

$$\sum_{b=1}^{B} J_{bp} \leq \tilde{a}_p, \quad 1 \leq p \leq P,$$

where $d_{<L}(\cdot)$ denotes the number of elements of its argument that are less than $L$, and $\tilde{a}_p = L - a_p$.

**Proof:** We first note that if $a_p = 0$ for any $p$, then the condition in (43) is trivially satisfied for any $N$ so we can without loss of generality assume that $a_p > 0$, $\forall p$. From the definition of $J_{bp}$ if follows that rank $(H_pN) = \sum_{b=1}^{B} J_{bp}$, thus, the second set of conditions in the lemma is trivial.

Consider now a specific value $b$. If $J_{bp} = 0$, it follows that column $b$ of $N$, $n_b$, is restricted to

$$n_b \in I_{bp} = N(H_p) \cup \text{span}(H_p^+H_pN_{1:b-1}),$$

while $J_{bp} = 1$ does not restrict $n_b$ so that $n_b \in I_{bp} = \mathbb{C}^K$. By inspection, it can be seen that the dimensionality of $I_{bp}$ can be written as

$$\text{dim}(I_{bp}) = K(1 - J_{bp}) + J_{bp} \left( K - L + \sum_{b'=1}^{b-1} J_{b',p} \right).$$

Altogether, we have that

$$n_b \in \bigcap_{p=1}^{P} I_{bp}. $$
The dimensionality of the intersection satisfies
\[
\dim \left( \bigcap_{p=1}^{P} \mathcal{I}_{b,p} \right) = \sum_{p=1}^{P} \dim(\mathcal{I}_{b,p}) - (P - 1)K.
\]

This dimensionality must be at least \( b \), since there are already \( b - 1 \) vectors in \( \mathcal{N}_{1:b-1} \) in the same space, and the rank of \( \mathcal{N}_{1:b} \) must be full for all \( b \). By manipulation, the statement of the lemma can be obtained.

Altogether, a necessary condition on \( A \) is that the rank of any submatrix \( A_S \) satisfies rank \( (A_S) \geq K - B_S \), where \( B_S \) is the largest integer \( B \) that satisfies the conditions specified in Lemma\(^9\). Note that the vector \( a \) in the conditions depends on \( S \). It can be seen from Lemma\(^9\) that finding the largest integer \( B \) that satisfies the conditions is a non-linear (quadratic) integer problem and we have not been able to solve it in closed form.

**REFERENCES**

[1] J. V. Alegría, F. Rusek, J. R. Sánchez, and O. Edfors, “Trade-offs in quasi-decentralized massive mimo,” in 2020 IEEE International Conference on Communications Workshops (ICC Workshops), 2020, pp. 1–6.

[2] J. G. Andrews, S. Buzzi, W. Choi, S. V. Hanly, A. Lozano, A. C. K. Soong, and J. C. Zhang, “What will 5G be?” IEEE Journal on Selected Areas in Communications, vol. 32, no. 6, pp. 1065–1082, 2014.

[3] Z. Pi and F. Khan, “An introduction to millimeter-wave mobile broadband systems,” IEEE Communications Magazine, vol. 49, no. 6, pp. 101–107, 2011.

[4] T. L. Marzetta, “Noncooperative cellular wireless with unlimited numbers of base station antennas,” IEEE Transactions on Wireless Communications, vol. 9, no. 11, pp. 3590–3600, 2010.

[5] F. Rusek, D. Persson, B. K. Lau, E. G. Larsson, T. L. Marzetta, O. Edfors, and F. Tufvesson, “Scaling up MIMO: Opportunities and challenges with very large arrays,” IEEE Signal Processing Magazine, vol. 30, no. 1, pp. 40–60, 2013.

[6] S. Hu, F. Rusek, and O. Edfors, “Beyond massive MIMO: The potential of data transmission with large intelligent surfaces,” IEEE Transactions on Signal Processing, vol. 66, no. 10, pp. 2746–2758, 2018.

[7] S. Malkowsky et al., “The world's first real-time testbed for massive MIMO: Design, implementation, and validation,” IEEE Access, vol. 5, pp. 9073–9088, 2017.

[8] C. Shepard et al., “Argos: Practical many-antenna base stations,” in Proceedings of the 18th Annual International Conference on Mobile Computing and Networking (Mobicom), New York, NY, USA, 2012, pp. 53–64.

[9] Q. Yang, X. Li, H. Yao, J. Fang, K. Tan, W. Hu, J. Zhang, and Y. Zhang, “Bigstation: Enabling scalable real-time signal processing in large MU-MIMO systems,” in ACM SIGCOMM ’13. ACM, 2013, pp. 399–410. [Online]. Available: [https://www.microsoft.com/en-us/research/publication/bigstation-enabling-scalable-real-time-signal-processing-in-large-mu-mimo-systems/](https://www.microsoft.com/en-us/research/publication/bigstation-enabling-scalable-real-time-signal-processing-in-large-mu-mimo-systems/)

[10] J. V. Alegría and F. Rusek, “Achievable rate with correlated hardware impairments in large intelligent surfaces,” in 2019 IEEE 8th International Workshop on Computational Advances in Multi-Sensor Adaptive Processing (CAMSAP), 2019, pp. 559–563.

[11] H. Q. Ngo, A. Ashikhmin, H. Yang, E. G. Larsson, and T. L. Marzetta, “Cell-free massive mimo versus small cells,” IEEE Transactions on Wireless Communications, vol. 16, no. 3, pp. 1834–1850, 2017.

[12] E. Björnson and L. Sanguinetti, “Scalable cell-free massive mimo systems,” IEEE Transactions on Communications, vol. 68, no. 7, pp. 4247–4261, 2020.

[13] J. Zhang, E. Björnson, M. Matthaiou, D. W. K. Ng, H. Yang, and J. D. Love, “Prospective multiple antenna technologies for beyond 5G,” IEEE Journal on Selected Areas in Communications, vol. 38, no. 8, pp. 1637–1660, 2020.

[14] Q. Wu and R. Zhang, “Intelligent reflecting surface enhanced wireless network via joint active and passive beamforming,” IEEE Transactions on Wireless Communications, vol. 18, no. 11, pp. 5394–5409, 2019.

[15] J. Chen, Y. C. Liang, Y. Pei, and H. Guo, “Intelligent reflecting surface: A programmable wireless environment for physical layer security,” IEEE Access, vol. 7, pp. 82 599–82 612, 2019.

[16] K. Li, R. R. Sharan, Y. Chen, T. Goldstein, J. R. Cavallaro, and C. Studer, “Decentralized baseband processing for massive MU-MIMO systems,” IEEE Journal on Emerging and Selected Topics in Circuits and Systems, vol. 7, no. 4, pp. 491–507, 2017.

[17] E. Bertilsson, O. Gustafsson, and E. G. Larsson, “A scalable architecture for massive MIMO base stations using distributed processing,” in 2016 50th Asilomar Conference on Signals, Systems and Computers, 2016, pp. 864–868.

[18] K. Li, J. McNaney, C. Tarver, O. Castañeda, C. Jeon, J. R. Cavallaro, and C. Studer, “Design trade-offs for decentralized baseband processing in massive mu-mimo systems,” in 2019 53rd Asilomar Conference on Signals, Systems, and Computers, 2019, pp. 906–912.

[19] J. R. Sánchez, J. Vidal Alegría, and F. Rusek, “Decentralized massive MIMO systems: Is there anything to be discussed?” in 2019 IEEE International Symposium on Information Theory (ISIT), 2019, pp. 787–791.

[20] M. Sarajlic, F. Rusek, J. R. Sanchez, L. Liu, and O. Edfors, “Fully decentralized approximate zero-forcing precoding for massive MIMO systems,” IEEE Wireless Communications Letters, pp. 1–1, 2019.

[21] J. R. Sanchez, F. Rusek, M. Sarajlic, O. Edfors, and L. Liu, “Fully decentralized massive MIMO detection based on recursive methods,” in 2018 IEEE International Workshop on Signal Processing Systems (SiPS), 2018, pp. 53–58.

[22] J. Vidal Alegría, J. R. Sánchez, F. Rusek, L. Liu, and O. Edfors, “Decentralized equalizer construction for large intelligent surfaces,” in 2019 IEEE 90th Vehicular Technology Conference (VTC2019-Fall), 2019, pp. 1–6.

[23] Z. Zhang, H. Li, Y. Dong, X. Wang, and X. Dai, “Decentralized signal detection via expectation propagation algorithm for uplink massive mimo systems,” IEEE Transactions on Vehicular Technology, pp. 1–1, 2020.

[24] A. Amiri, S. Rezaie, C. N. Manchon, and E. de Carvalho, “Distributed receivers for extra-large scale mimo arrays: A message passing approach,” 2020.

[25] T. M. Cover and J. A. Thomas, Elements of Information Theory (Wiley Series in Telecommunications and Signal Processing). USA: Wiley-Interscience, 2006.

[26] A. Paulraj, R. Nabar, and D. Gore, Introduction to Space-Time Wireless Communications, 1st ed. USA: Cambridge University Press, 2008.

[27] C. Jeon, K. Li, J. R. Cavallaro, and C. Studer, “Decentralized equalization with feedforward architectures for massive mu-mimo,” IEEE Transactions on Signal Processing, vol. 67, no. 17, pp. 4418–4422, 2019.