Primary tunnel junction thermometry

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We describe the concept and experimental demonstration of primary thermometry based on a four probe measurement of a single tunnel junction embedded within four arrays of junctions. We show that in this configuration random sample specific and environment-related errors can be avoided. This method relates temperature directly to Boltzmann constant, which will form the basis of the definition of temperature and realization of official temperature scales in the future.

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Temperature is a relatively poorly known quantity in modern metrology. It is well recognized that the way the international temperature scale is currently realized, in particular towards low temperatures, needs to be seriously reconsidered. It is currently based largely on artefacts which should be replaced by methods relating to thermodynamic temperature via Boltzmann constant $k_B$ [1, 2]. Methods based on solid state tunnel junctions, Coulomb blockade thermometry (CBT) [3, 4] and shot noise thermometry (SNT) [5, 6], have both shown great promise as $k_B$-based thermometers for metrology. However, both of them fall short up to now, when it comes to sufficient absolute accuracy. In case of SNT, the limitations are mainly of practical nature, and can possibly be overcome by a careful design of the sensor and the measurement set-up. For CBT, an uncontrolled error source is of more fundamental concern: CBT involves a measurement of a series connection of nominally identical junctions. The inevitable spread in junction parameters leads, however, to an error, which can usually be made small, but which limits the accuracy in particular when the average junction size is small [3, 8].

In Coulomb blockade thermometry an array of tunnel junctions shows a drop in its differential conductance around zero bias voltage, because of the influence of single-electron charging effects. The ideal operation regime of a CB thermometer is determined by the ratio of the single-electron charging energy, $E_C = e^2/2C$, where $C$ is the (average) junction capacitance, and the thermal energy $k_B T$ at temperature $T$ such that $E_C \ll k_B T$. The measured conductance peak [see Fig. 1(a)] has two important characteristics, its full voltage width at half minimum, $V_{1/2}$, and its normalized (by asymptotic conductance at large voltages, $G_T$) depth $\Delta G / G_T$. Here the first one is given by $5.44 k_B T / \epsilon$ per junction, and serves as the primary thermometer, provided the junctions in the sensor are mutually identical. The latter one is inversely proportional to $T$. SJT thermometry is based on the same principle as CBT but there the objective is to measure the conductance of a single tunnel junction, embedded in a four probe configuration through lines consisting of arrays of tunnel junctions, see Fig. 1(b). In this topology, the advantageous protection from the influence of electromagnetic environment is achieved. At the same time, this configuration abolishes any requirement of a uniform structure, because only one junction is probed, and the rest of the junctions, indeed not necessarily identical, act as an environment for this one.

We separate the theoretical analysis into two parts. First we consider the case where the influence of the environment beyond the junction array can be neglected. We show that the measurement is perfect in this case even for a non-uniform structure. Then we use this result as the seed for analyzing the influence of the dissipative environment on the performance of the thermometer, and show that with sufficiently long junction arrays the accuracy can be maintained at the desired level.

The tunnelling rate through a junction $i$ in forward $(+)$ or backward $(-)$ direction with normal conductors in thermal equilibrium is given by [8]

$$\Gamma_{0,i}(\delta F_i^\pm) = \frac{1}{e^2 R_{T,i}} \gamma(x)(\delta F_i^\pm),$$

(1)

where $R_{T,i}$ is the junction resistance, $\gamma(x) = x/(1 - e^{-x/k_B T})$, and $\delta F_i^\pm$ is the change in electrostatic energy in tunnelling. We may separate this energy change as
\[ \delta F_{\pm} = \pm eV_i + \delta E_{\text{ch},i}^\pm, \] where \( V_i \) is the (mean) voltage drop across junction \( i \) and \( \delta E_{\text{ch},i}^\pm \) is the internal energy change associated with charging the capacitors of the array. In the present analysis we limit ourselves to the lowest order result in \( Ec/k_BT \), which is what yields the basic results in thermometry. In this spirit, we then expand \( \Gamma_{\text{ref}}(\delta F_{\pm}) \approx \Gamma_{\text{ref}}(\pm eV_i) + 2\Gamma_{\text{ref}}(\pm eV_i)\delta E_{\text{ch},i}^\pm \). Analytic corrections for lower temperatures can be obtained readily by expanding up to higher orders, but they will not be considered here. The current \( I_i \) through junction \( i \) can be obtained as \( I_i = e \sum_{\{n\}} \sigma(\{n\})[\Gamma_{\text{ref}}(\delta F_{\pm}^+) - \Gamma_{\text{ref}}(\delta F_{\pm}^-)] \). Here, \( \sigma(\{n\}) \) is the occupation probability of the charge configuration \( \{n\} \) on the islands within the array. With these premises, and by using identities \( \gamma(x) - \gamma(-x) = x \) and \( \gamma'(x) + \gamma'(-x) = 1 \), we obtain

\[ I_i = \frac{1}{eR_{T,i}} \sum_{\{n\}} \sigma(\{n\})[eV_i + (\delta E_{\text{ch},i}^+ + \delta E_{\text{ch},i}^-)\gamma'(eV_i) - \delta E_{\text{ch},i}^\pm], \] (2)

The internal charging energy for each charge configuration \( \{n\} \) is given by \( E_{\text{ch}} = \frac{1}{2} \sum_{\{n\}} (C^{-1})_{i,j}n_in_j \), where \( C^{-1} \) is the inverse capacitance matrix of the junction array. We have neglected the offset charges on the islands since in the high temperature regime the charge distribution is quasi-continuous, \( \langle \delta n_i^2 \rangle \gg 1 \). Let us denote the islands surrounding the junction \( i \) by L and R. Then for the relevant processes only \( n_L \) changes into \( n_L \pm 1 \) and \( n_R \) to \( n_R \pm 1 \), whereas all the other charge numbers remain constant. There are two junction connections to islands L and R in SJT. Evaluating \( \delta E_{\text{ch},i}^\pm \) as the difference of \( E_{\text{ch}} \) for the charge configurations before and after the tunnelling event, and making use of the properties \( \sum_{\{n\}} \sigma(\{n\}) = 1 \) and \( \sum_{\{n\}} n_i\sigma(\{n\}) = 0 \) for all \( k \) because of the symmetry of \( E_{\text{ch}} \), we obtain the normalized conductance of junction \( i \), \( (G/G_T)_i \equiv \frac{R_{T,i}}{\delta E_{\text{ch},i}} \), as

\[ (G/G_T)_i = 1 - \frac{\delta_i}{k_BT}g(v_i), \] (3)

where \( \delta_i = e^2[(C^{-1})_{LL} + (C^{-1})_{RR} - 2(C^{-1})_{LR}], \) \( g(x) = e^x(e^x - x - 1) \), and \( v_i = eV_i/k_BT \). The result of Eq. (3) is in fact the basis of the standard CBT formula in linear arrays of junctions. Equation (3) tells that conductance of a single junction in SJT is accurate as a thermometer in any array of junctions: the magnitude of conductance suppression depends on the distribution of junction sizes via the capacitance matrix \( \langle \delta_i \rangle \), but the temperature can be determined unambiguously from, e.g., the half width of \( g(v_i) \), if \( V_i \) can be measured. Error-free measurement of \( V_i \) is indeed possible in the configuration of Fig. 1(b). This happens since the voltage measurement via two arrays is typically performed using an amplifier with very large input impedance (in any case much larger than the resistance of the junction arrays). Then, essentially no current flows through these two arrays, and there is no voltage drop across them. Thus \( V_i \) is indeed the voltage seen by the amplifier.

The analysis above applies to the case where the connection to the bias sources and signal amplifiers has zero impedance. In practice this is not the case, and the environment impedance introduces errors to Coulomb blockade thermometry which can be suppressed by using long arrays of junctions [8]. Similarly, one can realize the single junction measurement which avoids the errors by the embedding arrays. To analyze the remaining errors in this case quantitatively, we may write the tunnelling rates \( \Gamma^\pm_0 \) instead of \( \Gamma^\pm_0 \) of Eq. (1) as

\[ \Gamma^\pm_i = \frac{1}{e^2R_{T,i}} \int \gamma(E')P(\delta F_{\pm}^i - E')dE'. \] (4)

Here, \( P(E) \) originates from the environment theory of single-electron tunnelling [12], and it yields the probability (density) of electron to exchange energy \( E \) when it tunnels: positive (negative) \( E \) refers to energy emission (absorption) by electron. In the first analysis above we thus assumed \( P(E) = \delta(E) \), i.e., the Dirac delta function. The same steps as in the ideal dissipationless environment above lead now to

\[ \frac{G}{G_T}_i = 1 - \int \left[h(v_i + E/k_BT) - h(v_i - E/k_BT)\right]P(E)dE = \frac{\delta_i}{2k_BT} \int \left[g(v_i + E/k_BT) + g(v_i - E/k_BT)\right]P(E)dE. \] (5)

Here, \( h(x) = e^x(e^x - x - 1)/(e^x - 1)^2 \). For \( P(E) = \delta(E) \), Eq. (5) reduces naturally to [3]. Equation (5) yields an easy way to evaluate the influence of environment even in complex circuits.

In general \( P(E) \) is obtained from the phase-phase correlation function \( J(t) \) with \( P(E) = \frac{1}{2\pi\hbar} \int_{-\infty}^{\infty} dt \exp[J(t) + \frac{\pi}{2}Et] \) [12]. Here we assume that the array is uniform and embedded in a resistive environment with resistance \( R \). We are interested in conductance of one junction within an array. \( J(t) \) can be written as

\[ J(t) = \pi \frac{R_{\text{eq}}}{R_K} \{ \cot(B)(1 - e^{-|\tau|}) - \frac{|\tau|}{B} \} - 2e^{-k\pi|\tau|/B} \sum_{k=1}^{N-1} \frac{1}{k\pi(1 - (k\pi/B)^2)} - i[\text{sign}(\tau)(1 - e^{-|\tau|})] \]. (6)

Here \( \tau = t/(R_{\text{eq}}C_{\text{eq}}), \) \( B = \hbar/(2kB_TR_{\text{eq}}C_{\text{eq}}), \) \( R_{\text{eq}} = R/N^2 \) and \( C_{\text{eq}} = NC \). For a linear array of \( N \) junctions we have \( \delta_i = \frac{\hbar}{2N^2 e^2}. \) The results for the corresponding SJT are identical to those upon replacing \( N \) by \( N' + 1 \) in the expressions above. Here \( N' \) is the number of junctions in each of the four surrounding arrays. Above we have assumed that the stray capacitances are small.

Figure 2 shows numerical results based on the analysis above. Figure 2(a) shows the width as normalized to the ideal width in delta-function environment for a
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≈ \( 0.1 \) in the range \( 100 \Omega \) of a junction in arrays with varying length and as a function of \( R \). It is clear that one needs to protect the junction by a long array, if accurate measurement of temperature using \( V_{1/2} \) is to be obtained. For \( N = 2 \), an error of about 10% in the range \( 100 \Omega \leq R \leq 500 \Omega \) is expected. The impedance of the environment at high frequencies is approximately \( Z_{\text{env}} = \sqrt{\epsilon/\mu} \), determined by the permittivity \( \epsilon \) and permeability \( \mu \) of the medium. For vacuum its value is \( \approx 377 \Omega \), and for a circuit on silicon it is a few times smaller. Such increase of \( V_{1/2} \) due to environment in short arrays is supported quantitatively by experiments, see, e.g., Ref. [2]. The depth of the conductance dip is shown in Fig. 2(b): it depends strongly on \( R \) only in short arrays. We note that: (i) Since the environment is never known precisely in the experiment, there is almost no way to correct theoretically for such errors: the only working strategy then is to suppress these errors precisely by embedding the measured junction in a long array. (ii) The effect of error suppression is essentially proportional to \( N^{-2} \) (if \( k_B T R C / h < N \)). Therefore, an array with \( N \sim 50 \) is in principle sufficient for measurements with \( 10^{-4} \) absolute accuracy. (iii) Embedding a junction in a very resistive environment instead of a junction array is not the best strategy in thermometry either, which is indicated by the very slowly decaying tails of the error at large values of \( R \). To fully appreciate this point, we show in Fig. 2(c) and (d) the width and depth, respectively, of a single junction peak in a purely resistive environment. Although the width at large values of \( R \) slowly approaches unity, experimentally it is hard to fabricate resistive environments with \( R \ll 10 \, \text{k}\Omega \).

This concludes our proof that, theoretically, the influence of uncontrolled error sources, the inhomogeneity of the junction array and the noise of the environment, can be efficiently suppressed in a SJT. We discuss next the proof-of-the-concept experiments. Samples [see Fig. 3(a)] were fabricated by electron beam patterning and shadow angle evaporation with an oxidation step between the two electrode layers. Both the bottom and the top electrodes are of aluminium, they are 40 nm and 45 nm thick, respectively. The bottom electrode was thermally oxidized at 100 mbar for 10 min before deposition of the top electrode at an oblique angle. Two samples (A and B) with two types of structures have been measured in this work. The single tunnel junction was connected either directly to the external leads or it was embedded within four arrays of \( N' = 20 \) junctions, respectively. The two types of structures were fabricated on the same chip in the same vacuum cycle. Nominally, the central junctions are identical in the two cases, and all the junctions are 0.6 \( \mu m^2 \) of area, yielding a junction resistance of \( \approx 6 \, k\Omega \) (Sample A) and \( \approx 4 \, k\Omega \) (Sample B).

The samples were measured in a dilution refrigerator with a 40 mK base temperature. However, these structures were not suitable for very low temperature measurements: we observe strong self-heating due to weak electron-phonon coupling in the present geometry near base temperature \( 13 \). Therefore we present here data at temperatures at and above 150 mK. Conductance measurements in the SJT configuration yield a deeper and narrower peak than for the unprotected single junction structure in agreement with the calculated results of Fig. 2. This is shown in Fig. 3(b) for Sample B. Both the
SJT \((N' = 20)\) and the bare junction \((N' = 0)\) data follow the environment calculation assuming \(R = 80 \Omega\), a value which is consistent with the discussion above. The arrays can thus indeed be employed to efficiently protect the junctions against environment fluctuations.

Figure 4 shows four-probe conductance measurements of the SJT sample at a few temperatures together with fits according to the model presented. We included in the fits the influence of self-heating in the manner presented in Ref. [13], and this yielded perfect match to the peaks, see the lines on top of the data. The self-heating has, however, only a small influence on the curves at the temperatures shown. The extracted temperatures from such fits are shown in the inset of Fig. 4 against the reading of the CBT "reference thermometer", which was one of the \(N' = 20\) junction arrays in the same sample. The dominating discrepancies between the two thermometers are due to finite errors in the fitting procedure in each case. The agreement between the two is good over the whole temperature range in Fig. 4.

In the theoretical analysis we focused on the high temperature and high junction resistance limit, and did not discuss errors due to, e.g., enhanced Coulomb effects at lower temperatures [8] and strong tunnelling in low resistance junctions [14, 15]. Yet these errors can be treated similarly to what has been done in standard Coulomb blockade thermometry, and their influence can be estimated and kept at a tolerable level by proper choice of junction sizes for each temperature and with suitable tunnel barrier parameters. One more (controllable) error to judge is the influence of the size of the islands between the junctions, \(\ell\); as long as it is smaller than the distance to "horizon", \(\ell \ll \hbar c/k_B T\), the above lump element analysis is valid [16, 17]. Here, \(c = (\mu e)^{-1}\) is the signal propagation speed. The condition gets critical at particularly high temperatures, and extra care to place the array close to the junction has to be taken then. We want to add that the presented thermometry is not necessarily limited to the standard planar tunnel junction design, but may be applicable, e.g., in scanning probe or break junction geometries, since the junction parameters of the surrounding arrays need not be the same as those of the central junction. This might lead to the possibility of using tunable tunnel junctions in thermometry.

Summarizing, we propose an absolute single tunnel junction thermometer. We have demonstrated the concept in preliminary experiments. Our method may turn out to be valuable in future realization of the international temperature scale based on Boltzmann constant.

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FIG. 4: Measurements on a SJT (Sample A). Conductance dips at four temperatures are shown together with fits according to the model presented, including the self-heating correction [13]. The inset shows a comparison of the temperature deduced from SJT (vertical axis) against that obtained by an ordinary CBT measurement across one of the embedding arrays (horizontal axis). The solid line has unit slope, and the error bars indicate the confidence interval of the fits.