InfiniWolf: Energy Efficient Smart Bracelet for Edge Computing with Dual Source Energy Harvesting

Michele Magno†‡, Xiaying Wang†, Manuel Eggimann†, Lukas Cavigelli†, Luca Benini†‡
†Dept. of Information Technology and Electrical Engineering, ETH Zürich, Switzerland
‡Dept. of Electrical, Electronic and Information Engineering, University of Bologna, Italy

Abstract—This work presents InfiniWolf, a novel multi-sensor smartwatch that can achieve self-sustainability exploiting thermal and solar energy harvesting, performing computationally high demanding tasks. The smartwatch embeds both a System-on-Chip (SoC) with an ARM Cortex-M processor and Bluetooth Low Energy (BLE) and Mr. Wolf, an open-hardware RISC-V based parallel ultra-low-power processor that boosts the processing capabilities on board by more than one order of magnitude, while also increasing energy efficiency. We demonstrate its functionality based on a sample application scenario performing stress detection with multi-layer artificial neural networks on a wearable multi-sensor bracelet. Experimental results show the benefits in terms of energy efficiency and latency of Mr. Wolf over an ARM Cortex-M4F micro-controllers and the possibility, under specific assumptions, to be self-sustainable using thermal and solar energy harvesting while performing up to 24 stress classifications per minute in indoor conditions.

Index Terms—Energy Harvesting, Biomedical Applications, Wearable devices.

I. INTRODUCTION

Technology advancements in low-power integrated circuits (IC), sensors and wireless transmission have enabled the design and implementation of light-weighted and unobtrusive devices [1]. A successful class of such devices is wearable devices, where electronics are worn on the human body. Wearable devices a commercial reality are today with many products for fitness and entertainment applications already being available on the market [2]. However, one of the most critical issues of those devices is the limited energy supply due to the small size of the battery.

Machine learning (ML) is playing a crucial role in many fields, including wearable devices used for biomedical applications [3], [4]. ML is used for data analysis of the sensor data, and they are the core perception units in our integrated intelligence era [5]. The new trend is achieving “smart wearables” that can extract useful information from several novel sensors attached to the body with in situ processing. Thus, embedded ML algorithms are expected to enable machine intelligence in resource-constrained wearable devices [6]. To achieve this goal, researchers working on embedded ML are designing specialized hardware architectures to deal with the demand for large computational and storage capability [7]–[9]. Due to the computational effort and large datasets, embracing typical ML methods for machine intelligence is still a challenging task in battery-powered wearable devices with limited hardware capabilities [3], [4]. Typically, processors for wearable devices are microcontrollers, often from the ARM Cortex-M family, achieving few million operations per second (MOPS) with a power consumption of a few mW, compatible with the battery of the wearable device. On the other hand, the computational resources of microcontrollers tend to be insufficient to run ML algorithms on the sensor data. Recently, researchers have proposed novel processing units to overcome those limits, enabling on-board data processing with state-of-the-art ML algorithms [10]. The most promising one exploits parallelism as much as possible, achieving extremely high energy efficiency with near-threshold transistor operation [11]. However, low-power and energy-efficient processors are not enough to achieve the ultimate goal of wear-and-forget. This ambitious goal is achievable only if the device harvests enough energy from its environment. Harvesting energy from environmental sources has been explored in a wide range of application scenarios. However, in a small-sized wearable context, it is still a challenging task due to the restrictive form-factor constraints [12]. Steady progress is being made in this area: traditional “timekeeping only” watches are fully harvesting powered, and several mature products are available on the market. On the other hand, smartwatches need much more power for their much more sophisticated sensing capabilities and functions. Moreover, harvesting energy to supply always-on wearable devices represents an exciting challenge, which needs particular design attention. Environmental energy sources vary significantly over time, and power management needs to implement methods that can opportunistically take advantage of periods of overabundant energy and survive intervals when the system is starving for energy.

This paper proposes InfiniWolf, a smartwatch that exploits the most advanced hardware and software low-power design in combination with power management and energy harvesting technologies to achieve a perpetual and endlessly wearable smart sensor-rich device. In particular, we integrate advanced RISC-V based parallel ultra-low power (PULP) open.hardware processor coupled with aggressive power management and highly efficient environmental energy conversion into a single wearable system. The main goal is to shape an entirely new class of smartwatches that outpaces the state-of-the-art in energy autonomy ready to run sophisticated artificial intelligence algorithms. We show the superior energy efficiency achieved by the developed device and the RISC-V based processor over an ARM Cortex-M4F micro-controller. To evaluate the performance of the implemented smartwatch, we present a challenging application case of stress detection using a multi-layer perceptron (MLP), a very well-known class of artificial neural networks (ANNs).
II. SYSTEM ARCHITECTURE

Fig. 1 shows the block diagram and the architecture of the designed smartwatch. The smartwatch features two processors, a Nordic nRF52832 Bluetooth low energy SoC with an ARM Cortex-M4 processor and a programmable Parallel Ultra-Low-Power processor called Mr. Wolf. The Nordic SoC handles communication with a remote host and auxiliary support for necessary data processing if needed. It provides Bluetooth Low Energy (BLE) 5 communication capabilities, it performs power management various modes of operation (sleep, raw data streaming, data acquisition, and processing) and keeps track of the battery charging status. Mr. Wolf offers high versatility and high compute efficiency at an ultra-low-power budget [11]. It features two power domains: SoC and Cluster. The former contains a tiny (12 kGate) RISC-V processor [13] called Ibex and the latter is equipped with 8 RISC-V cores with custom DSP extensions called RISCY [13]. The dual-processor architecture allows local end-to-end processing (i.e., on-board classification using ML) with lower power and higher energy efficiency than streaming the data out for remote analysis. Moreover, this architecture allows lower latency and more robustness with respect to wireless connectivity.

A dual-source energy harvester for solar and thermoelectric generator (TEG) modules has been embedded. The main goal is to achieve perpetual work when the energy transducers are deployed on a wrist band harvesting energy from light and body heat. The choice of two energy harvesting sources is motivated by increased flexibility and robustness of the energy intake, while form-factor is not compromised as the two harvesters exploit different sides of the watch (top for solar and bottom side for thermal). The smart power supply unit (PSU) allows the system to operate with low losses while harvesting energy, monitoring sensors and managing the power according to the policies implemented. The smartwatch includes a 9-axis motion sensor, a pressure sensor, a microphone, and an ultra-low-power ECG/EMG and bioimpedance analog front-end (Maxim MAX30001) to acquire biomedical signals as well as a low power galvanic skin response (GSR) front-end. The wearable device can be worn on the user’s wrist and periodically and opportunistically acquires information from the sensors according to the available energy. The device is holistically designed to be ultra-low power and without any need to replace or manually recharge the on-board 120 mAh LiPo battery. Thus, the design allows extreme power management to keep the quiescent and operative energy consumption low. Figure 2 shows the smartwatch prototype used to measure data on power consumption and energy efficiency.

III. MULTI-LAYER PERCEPTION AND STRESS DETECTION

In the last few decades, ANNs have been widely used to provide machines with the human ability to make decisions, detecting patterns, predicting actions. An MLP is a class of feed-forward ANNs which comprises at least three layers of neurons: an input layer, a hidden layer, and an output layer. Despite the developments of other types of ANNs such as convolutional and recurrent neural networks, MLPs are still very commonly used due to their simplicity and good performance in many tasks such as emotion classification [6] and disease detection [14].

The main goal of implementing a real-world application is to provide an insightful performance comparison between the two processors present in the developed smartwatch, namely an ARM Cortex-M4 and the PULP-based Mr. Wolf. We used the dataset in [13] that includes labeled data of stressed and unstressed people. Previous work [15]–[18] evaluated the most relevant features for the task. However, when it comes to battery-operated wearable devices one has to take the resource constraints of the microcontroller into consideration, especially its limited memory. The work in [19] extracts features and designs an MLP taking into account these memory constraints. Based on [19], we obtained 5 relevant features from ECG and GSR data and trained an MLP to perform stress detection. The dataset is split into subsets with equal stress levels (i.e., transitions between different stress levels are omitted) and extract features by using overlapping windows. From the ECG data, we derived the root mean square (RMSSD) and the standard deviation (SDSD) of the differences of successive neighboring RR intervals, and the number of adjacent RR interval pairs that differ by more than 50 ms (NN50). For the GSR signals, we detect the rising edges and calculate...
the height (GSRH) and the length (GSRL) of the slope following [18].

We trained an MLP using the FANN library [20], feeding these 5 features as input. The network consists of 2 hidden layers with 50 units each and 3 output nodes in the output layer as shown in Fig. 3 Here we name it Network A. The used activation function is the tanh function. The network has in total 108 neurons and 3003 weights, yielding an estimated memory footprint of 14 kB. Each neuron requires 4 integers, i.e. 16 bytes, to store the information such as the activation function used, the neuron indexes, etc. Each weight uses 4 bytes of storage and two additional integers are stored per layer, i.e. 8 additional bytes, to save the number of inputs and outputs for each layer. To further demonstrate the energy efficiency of our proposed platform, we designed a bigger network, named Network B, which still fits into the memory and measured the performance. This bigger network has 100 input and 8 output nodes and contains 24 hidden layers with an increasing number of nodes, i.e. the first two hidden layers have 8 neurons each, the subsequent couple of hidden layers have 8 more neurons each, and so on, with a total of 1356 neurons, 81032 weights and an estimated memory size of 353 kB. To get consistent measurements, we repeat all experiments 1000 times with a warm cache and then average the results.

IV. PRELIMINARY EXPERIMENTAL RESULTS

This section shows the experimental results on the execution time and energy of InfiniWolf to perform stress detection. We measured the energy harvested by the two transducers to evaluate the possibility to achieve self-sustainability. Moreover, we present a comparison between two processors: Mr. Wolf and ARM Cortex-M4F on the Nordic nRF52832, running at 64 MHz. The Nordic nRF52832 is considered one of the most energy-efficient ARM Cortex-M4F on the market due to its low current consumption of only 46 µW per MHz. Mr. Wolf has 64 kB L1 memory closer to the cluster cores and 512 kB L2 memory in the SoC domain. It can run up to 450 MHz, with the most energy-efficient point being at 100 MHz [11], which has been used in this evaluation. Using the FPU, Network A executes in 38478 cycles, while in fixed point it needs 30210 cycles. The fixed-point implementation is 1.3× faster than the floating-point version and it is also more energy-efficient. Thus we will focus on the fixed-point implementation for the following analyses. The measured run-time in number of cycles for both Network A and Network B on the ARM Cortex-M4 and on Mr. Wolf is shown in Table III and the estimated energy consumption is listed in Table IV. The tables demonstrate three possible ways to use Mr. Wolf: 1) using only the SoC domain, i.e. the computation is done in the fabric controller (FC) using the IBEX core with the basic RV32IM instruction set and with the cluster powered off; or using the cluster to perform the computations on either either 2) a single R15CY core, or 3) multiple R15CY cores to do parallel computation. The R15CY cores feature custom instruction set extensions to efficiently perform digital signal processing besides the basic RV32IM instruction set. These extensions offer 1.3× and 1.7× speed-up with respect to the ARM Cortex-M4 using CMSIS-DSP for Network A and B, respectively. However, the activation of the cluster domain costs more energy as shown in Table IV The advantage of cluster computing in Mr. Wolf is much more evident when using multiple cores. The parallel execution using up to 8 cores provides respectively 4.9× and 8.3× speedup for Network A and B with respect to ARM Cortex-M4.

Besides the energy for the detection, it is essential to include the energy to acquire the data. Due to the low power analog front-end, the data acquisition of the ECG consumes only 171 µW, while the GSR front-end consumes 30 µW when active. To perform stress detection InfiniWolf, it acquires data for 3 s (needing 600 µJ) and performs the feature extraction in 50 µs (1 µJ assuming Mr. Wolf consuming 20 mW in parallel execution [11]). The best overall energy cost for a single detection is 602.2 µJ thank to the extremely fast parallel execution of the classification.

A. Power Generation and Self-sustainability

In this section, we provide information about the energy-harvesting capabilities of InfiniWolf, which hosts multiple energy harvesters (thermal and solar). The power generation of each source has been evaluated separately under laboratory conditions. Both harvesting sources have been attached to InfiniWolf into the Matrix Powerwatch case. InfiniWolf uses the built-in Matrix Industries TEG-module, while we adopted two small thin-film flexible photovoltaic panels SP3-12 by Flexsolarcells as shown in Fig. 4. We measured the power intake in our lab using the precise Keysight B2900A Series Precision Source/Measure Unit (SMU), which constitutes the heart of the measurement. InfiniWolf was sent to sleep mode, so the measurements take into account all the losses and the quiescent current of InfiniWolf.

Table I shows the measurements under different lighting conditions typical for indoor (700 lx) and outdoor (30 klx with
sun) scenarios.

In the same way, we measured the intake power into the battery. Table II shows the measured data with different temperature gradients between the two TEG plates. Note that the TEG continuously generates energy in every condition.

Assuming InfiniWolf staying in challenging indoor condition for 6 hours, and the worst-case scenario for the TEG energy harvester, it will acquire 21.44 J per day. As the intake energy is calculated including the losses and Infiniwolf quiescent current, the stress detection rate to achieve self-sustainability can be approximated as the total energy divided by the energy per detection calculated previously. Then Infiniwolf is able to perform up to 24 detection per minute, which is more than enough to many application scenarios.

V. CONCLUSION

We presented InfiniWolf, an ultra-low-power, energy-efficient wearable device with multiple sensors able to run complex ML algorithms. The Mr. Wolf SoC allows having a long-term and even self-sustaining wearable device by combining dual source energy harvesting with extremely energy-efficient processing using the novel open-hardware parallel ultra-low-power processor based on RISC-V. Experimental results show the incredible energy-efficiency of the processor for a stress detection application using a multi-layer perception.

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| TABLE I | SOLAR POWER GENERATION UNDER DIFFERENT LIGHTING CONDITIONS |
|--------------|---------------------|---------------------|
| Light Intensity: | Outdoor | Indoor |
| Skin: | 30 Klx | 70 Klx |
| Cortex-M4: | 24.7 mW | 0.9 mW |

|TABLE II | HUMAN WRIST TEG POWER HARVESTING MEASUREMENTS WITH AND WITHOUT ACTIVE COOLING |
|-----------------|-----------------|-----------------|
| Conditions: | Room: 22 °C | Room: 15 °C | Room: 15 °C |
| Skin: 34 °C | Skin: 30 °C | Skin: 30 °C |
| no wind | no wind | 42 km/h wind |
| Power: | 24.0 μJ/W | 55.5 μJ/W | 155.4 μJ/W |

|TABLE III | RUTIME IN CYCLES, PULP v. ARM CORTEX-M4F |
|-----------------|---------------------|---------------------|
| Runtime in #cycles | ARM Cortex-M4 | PULP Mr. Wolf |
| IBEX | Single RISCY | Multi RISCY |
| Network A | 30210 | 40861 | 22772 | 0266 |
| Network B | 902763 | 955388 | 519354 | 108336 |

|TABLE IV | ENERGY CONSUMPTION PER CLASSIFICATION [μJ] |
|-----------------|---------------------|---------------------|
| Energy in μJ | Nordic w/ ARM Cortex-M4 | PULP Mr. Wolf |
| IBEX | Single RISCY | Multi RISCY |
| Net. A | 5.1 | 2.9 | 2.9 |
| Net. B | 153.8 | 31.5 | 65.6 | 21.6 |

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