Convergence Acceleration in Wireless Federated Learning: A Stackelberg Game Approach

Kaidi Wang ‡, Member, IEEE, Yi Ma ‡, Senior Member, IEEE, Mahdi Boloursaz Mashhadi ‡, Senior Member, IEEE, Chuan Heng Foh ‡, Senior Member, IEEE, Rahim Tafazolli ‡, Senior Member, IEEE, and Zhi Ding ‡, Fellow, IEEE

Abstract—This paper studies issues that arise with respect to the joint optimization for convergence time in federated learning over wireless networks (FLOWN). We consider the criterion and protocol for selection of participating devices in FLOWN under the energy constraint and derive its impact on device selection. In order to improve the training efficiency, age-of-information (AoI) enables FLOWN to assess the freshness of gradient updates among participants. Aiming to speed up convergence, we jointly investigate global loss minimization and latency minimization in a Stackelberg game based framework. Specifically, we formulate global loss minimization as a leader-level problem for reducing the number of required rounds, and latency minimization as a follower-level problem to reduce time consumption of each round. By decoupling the follower-level problem into two sub-problems, including resource allocation and sub-channel assignment, we achieve an approximate solution of the global loss minimization and matching theory. At the leader-level, we derive an upper bound of convergence rate and subsequently reformulate the global loss minimization problem and propose a new age-of-update (AoU) based device selection algorithm. Simulation results indicate the superior performance of the proposed AoU based device selection scheme in terms of the convergence rate, as well as efficient utilization of available sub-channels.

Index Terms—Wireless federated learning, Stackelberg game, age-of-information, device selection, resource allocation, sub-channel assignment.

I. INTRODUCTION

The rapid development of mobile devices and applications has ushered us into the fifth-generation (5G) era. Much of the network services in 5G and beyond is expected to address explosive growth and need of machine learning (ML) and data science [1]. In conventional centralized ML, a central server is equipped at the access point (AP) to collect all raw data for model training. However, due to the limited wireless resources and potential privacy issues, centralized ML is impractical for some scenarios [2]. In this context, federated learning (FL) is a framework for distributed ML algorithms to collaboratively train a central learning model while keeping the data locally [3]. Specifically, in FL, a global model is shared among multiple devices, and each device trains the received global model based on the local data and produces a local model [4]. Thereafter all local models are transmitted to the server via wireless communication networks to generate the updated global model [5]. Since raw data do not leave the device, and the model size is much smaller to share, such that there is less concern about data privacy and lower consumption of data network resources [6].

A. Related Works

Owing to its growing popularity, FL-related design and optimization in existing wireless communication architectures have attracted widespread attention, in which convergence time is regarded as an important performance metric. As indicated in [7], the convergence time is jointly determined by the number of communication rounds and the time consumption per round. The former is closely related to convergence rate, while the latter is normally defined as latency.

In view of the relationship between global loss and convergence rate, some works focused on the global loss minimization problem in order to reduce the number of required communication rounds [8], [9], [10], [11]. In [8], a FL algorithm with multiple local training was designed. It considered the impact of local and global update rounds on the convergence bound, and developed an approximate solution of the global loss minimization problem. In [9], packet error rate was introduced to indicate whether the FL parameter transmission was successful or not. Specifically, the joint problem of user selection, resource block allocation, and power allocation was studied under delay and energy constraints [9]. Incorporating FL in a massive multiple-input-multiple-output (MIMO) scenario with energy harvesting, the authors of [10] included the consideration of user scheduling and power allocation in a global loss minimization problem. Adopting a model pruning scheme, in [11], the authors jointly optimized device selection, time slot allocation and pruning ratio in order to maximize the convergence rate with a latency constraint.

0018-9545 © 2024 IEEE. Personal use is permitted, but republication/redistribution requires IEEE permission. See https://www.ieee.org/publications/rights/index.html for more information.
As another factor in determining the convergence time of FL, latency, including computation time and communication time, was extensively researched in previous works [12], [13], [14], [15]. The authors of [12] designed a realistic wireless network for FL, where a limited number of users can be selected at each round for aggregation. By obtaining user selection and resource block allocation schemes, the convergence time of FL was minimized. Setting a local accuracy level at each device, the FL algorithm with multiple local update rounds was proposed in a cell-free massive MIMO scenario [13], in which time consumption for downlink transmission, uplink transmission, and computation was considered in the formulated training time minimization problem. A multi-task FL framework was studied in a multi-access edge computing (MEC) scenario, where edge nodes were included to accomplish different learning tasks [14]. In order to minimize the latency of each communication round, the optimal matching between edge nodes and end devices was obtained. In [15], the authors proposed a hybrid learning scheme, where part of data can be offloaded from devices to the server, while the remaining data was utilized for local training. It was demonstrated that the proposed scheme has the ability to reduce the total time consumption.

B. Motivation and Contribution

Although global loss minimization and latency minimization have been separately studied in existing works [8], [9], [10], [11], [12], [13], [14], [15], the interaction between these two objectives remains unclear. Specifically, devices that have a significant constructive impact on training convergence may have poor channel conditions, thereby increasing the latency of the corresponding aggregation round. On the other hand, focusing on minimizing latency may cause devices with high channel gains to be repeatedly selected, leading to an increase in the global loss [16], [17]. Therefore, it is necessary to investigate this interaction and construct a dynamic trade-off. To this end, this work adopts Stackelberg game and presents a novel framework to jointly consider learning and communication in wireless FL systems, where the server and devices tend to minimize the global loss and latency, respectively. Unlike the conventional papers on global loss minimization that treat latency as a definite threshold [8], [9], [10], [11], latency in this work can be flexibly adjusted to ensure the convergence rate. Compared to [18], an energy budget is included and its impact on device selection is analyzed, constructing a more practical and challenging scenario. On the other hand, inspired by the concept of age-of-information (AoI) [19], age-of-update (AoU) [20] is defined in this work as a metric to evaluate the staleness of model updates. In this context, a novel device selection method is designed to estimate the contribution of devices in each communication round without analyzing the model/gradient or transmitting additional information to the server. Different from [20] and [21] which target overall AoU/AoI minimization, AoU in this work is regarded as a weight to prioritize selecting devices with larger AoU. The main contributions of this paper are summarized as follows:

- A latency-sensitive FL scenario is considered, where multiple devices transmit parameters to the server over a limited number of sub-channels. In order to jointly minimize global loss and latency, a Stackelberg game based problem is formulated, where global loss minimization and latency minimization are considered as leader-level and follower-level problems, respectively. It is proved that the with the given sub-channels, some devices cannot transmit local models to the server due to the energy consumption constraint. Based on the analysis, the Stackelberg equilibrium of the formulated problems is established.

- The follower-level problem is divided into two sub-problems, including resource allocation and sub-channel assignment. Due to non-convexity and monotonicity, a monotonic optimization based solution is proposed for the resource allocation problem. Moreover, a matching based algorithm is developed to address the sub-channel assignment problem with the incomplete preference list, where the properties of the proposed algorithm are analyzed.

- In order to solve the leader-level problem, the upper bound of the convergence rate is derived, which indicates that the convergence rate can be improved by selecting devices with large data size. Therefore, the global loss minimization problem is reformulated as a weighted device selection problem. By ordering devices based on AoU and data size, a priority list is created, and an algorithm is designed to select devices by predicting sub-channel assignment and resource allocation.

- The simulation results on Modified National Institute of Standards and Technology (MNIST), Canadian Institute for Advanced Research (CIFAR-10) and Stanford Sentiment Treebank Version 2 (SST-2) databases are presented. It is indicated that the designed AoU based device selection scheme can improve the convergence rate and achieve the lowest global loss. Moreover, the proposed solutions for resource allocation and sub-channel assignment can efficiently utilize available sub-channel and dynamically adjust energy utilization in order to reduce latency.

C. Organization

The remainder of this paper is organized as follows. In Section II and Section III, the system model and problem formulation are described, respectively. The solution of latency minimization problem is presented in Section IV, and the solution of global loss minimization problem is obtained in Section V. Section VI demonstrates the simulation results. The conclusions are summarized in Section VII.

II. System Model

Consider an FL scenario where $N$ wireless mobile devices collaboratively train a joint learning model. Each device is equipped with a single antenna and the FL process is orchestrated by a wireless server. In each communication round, the devices intend to train neural networks based on local data and then transmit parameters to the server for aggregation. Moreover, the limited communication resources are considered. Specifically, there are $K$ available sub-channels, $K \leq N$, and each sub-channel is occupied by at most one device. Therefore, only a subset of devices can be selected for the global model
aggregation in each communication round. The collections of all devices and sub-channels are denoted by \( \mathcal{N} = \{1, 2, \ldots, N\} \) and \( \mathcal{K} = \{1, 2, \ldots, K\} \), respectively.

### A. Computation Model

In each communication round, after receiving the global model, the selected devices need to train their respective local learning models with the equipped central processing units (CPUs). Based on the dynamic voltage and frequency scaling (DVFS) technique, the CPU core can be operated at different frequency levels, and hence, the consumed time and energy change accordingly [22]. For any device \( n \) assigned to sub-channel \( k \), the computational time consumption is given by

\[
T_{k,n}^{\text{cp}}(\tau_{k,n}) = \frac{\mu \beta_n}{\tau_{k,n} C_n},
\]

where \( \mu \) is a coefficient to denote the required CPU cycles for training one sample, \( \beta_n \) is the number of dataset samples at device \( n \), \( \tau_{k,n} \) is a designed proportion of computational capacity, and \( C_n \) is the CPU frequency of device \( n \). Note that since the size of local data utilized in training does not change over the computing time, the test accuracy or loss reduction is not affected. The energy consumption for computation can be expressed as follows:

\[
E_{k,n}^{\text{cp}}(\tau_{k,n}) = \kappa_0 \mu \beta_n (\tau_{k,n} C_n)^2,
\]

where \( \kappa_0 \) is the power consumption coefficient per CPU cycle.

### B. Communication Model

After training, local models are transmitted from selected devices to the server. For any device \( n \), the achievable data rate at sub-channel \( k \) is given by

\[
R_{k,n}(p_{k,n}) = B \log_2 \left( 1 + p_{k,n} |h_{k,n}|^2 \right),
\]

where \( B \) is the bandwidth, \( p_{k,n} \) is the power allocation coefficient of device \( n \), and \( |h_{k,n}|^2 \) is the normalized channel gain. Particularly, \( |h_{k,n}|^2 = P_k |g_{k,n}|^2 d_n^{-\alpha} \sigma^2 \), where \( P_k \) is the maximum transmit power in each sub-channel, \( g_{k,n} \sim CN(0, 1) \) the small-scale fading coefficient, \( \eta \) is the frequency dependent factor, \( d_n \) is the distance between device \( n \) and the server, \( \alpha \) is the path loss exponent, and \( \sigma^2 \) is the variance of additive white Gaussian noise (AWGN). In the considered scenario, the small-scale fading coefficients vary with the communication rounds.\(^1\)

For simplicity, the index related to communication rounds is omitted from the notations. Based on the achievable data rate, the time consumption for communication can be presented by

\[
T_{k,n}^{\text{cm}}(p_{k,n}) = \frac{D \left( w_{n}^{(t)} \right)}{R_{k,n}(p_{k,n})},
\]

where \( D(w_{n}^{(t)}) \) is the size of the local model \( w_{n}^{(t)} \) at device \( n \) in round \( t \). It is assumed that the data size of local models is the same for all devices and rounds, i.e., \( D(w) = D(w_{1}^{(t)}), \forall n, t \). The energy consumption for communication is given by

\[
E_{k,n}^{\text{cm}}(p_{k,n}) = p_{k,n} P_k T_{k,n}^{\text{cm}}(p_{k,n}).
\]

### C. AoU Based Device Selection

In any communication round \( t \), a subset of devices, denoted by \( \mathcal{N}_t \), is selected, i.e., \( \mathcal{N}_t \subseteq \mathcal{N} \) and \( |\mathcal{N}_t| \leq K \). The status of any device in round \( t \) can be represented by a binary variable \( S_{n}^{(t)} \in \{0, 1\} \), where \( S_{n}^{(t)} = 1 \) indicates device \( n \) is selected to participate in the aggregation in round \( t \); \( S_{n}^{(t)} = 0 \) otherwise. The set of all device selection indicators is denoted by \( S^{(t)} \). Therefore, in any round, the set of selected devices \( \mathcal{N}_t \) and the set of device selection indicators \( S^{(t)} \) can be mutually inferred.

In order to improve the performance of FL, device selection should be determined by data quality. It is revealed in [19], [26] that staleness of gradient update may negatively impact learning outcomes. Therefore, the server tends to select the devices with fresher gradient update for aggregation. In particular, if a device has been skipped for several rounds, its gradient update is relatively informative, and the probability of the server selecting the device should increase. Conversely, if a device was selected in the previous round, the probability for its reselection should decrease. To this end, the concept of AoI is adopted to define AoU [20], [27]. In round \( t \), any device \( n \)’s AoU, denoted by \( A_{n}^{(t)} \), is presented as follows:

\[
A_{n}^{(t)} = \begin{cases} A_{n}^{(t-1)} + 1, & \text{if } S_{n}^{(t-1)} \sum_{k=1}^{K} \psi_{k,n}^{(t-1)} = 0, \\ 1, & \text{if } S_{n}^{(t-1)} \sum_{k=1}^{K} \psi_{k,n}^{(t-1)} = 1, \end{cases}
\]

where \( \psi_{k,n}^{(t)} \in \{0, 1\} \) is the sub-channel assignment indicator.

Particularly, \( \psi_{k,n}^{(t)} = 1 \) indicates that device \( n \) is assigned to sub-channel \( k \) in round \( t \); \( \psi_{k,n}^{(t)} = 0 \) otherwise. According to the above definition, AoU represents the number of communication rounds since the last transmission, which is jointly decided by device selection and sub-channel assignment. As shown in Fig. 1, if device \( n \) was not selected in round \( t - 1 \), i.e., \( S_{n}^{(t-1)} = 0 \), or it is not assigned to any sub-channel, i.e., \( \sum_{k=1}^{K} \psi_{k,n}^{(t-1)} = 0 \), its AoU is incremented by 1; otherwise, its AoU is reset to 1. With this consideration, a large AoU implies more informative update, and therefore device selection should partially depend on AoU. In any round \( t \), \( a_{n}^{(t)} \) defined below is

\[a_{n}^{(t)}\]
used as a weight\(^2\) to prioritize selecting devices with larger AoU for aggregation:

\[
\alpha_n^{(t)} = \frac{A_n^{(t)}}{\sum_{i=1}^{N} A_i^{(t)}}. \tag{7}
\]

D. Sub-Channel Assignment

The time consumption of each communication round, i.e., latency, also plays an important role in FL [7], [12], [28]. By including computation and communication phases, the time consumption of any device \(n\) assigned to sub-channel \(k\) can be expressed as follows:

\[
T_{k,n}(\tau_{k,n}, p_{k,n}) = T_{k,n}^{\text{sp}}(\tau_{k,n}) + T_{k,n}^{\text{cm}}(p_{k,n}), \tag{8}
\]

where the time consumption for global model transmission from the server to devices is ignored as in [14], [29]. With the given set of selected devices \(\mathcal{N}_f\), the latency in round \(t\) can be presented as follows:

\[
T^{(t)} = \max_{n \in \mathcal{N}_f} \left\{ \sum_{k=1}^{K} \psi_{k,n}^{(t)} T_{k,n}(\tau_{k,n}, p_{k,n}) \right\}. \tag{9}
\]

It indicates that the latency is affected by sub-channel assignment. Specifically, if any device is assigned to a sub-channel with an enhanced channel gain, the time consumption of this device decreases, and hence, the latency of this round may be reduced. Accordingly, the energy consumption of device \(n\) assigned to sub-channel \(k\) is given by

\[
E_{k,n}(\tau_{k,n}, p_{k,n}) = E_{k,n}^{\text{sp}}(\tau_{k,n}) + E_{k,n}^{\text{cm}}(p_{k,n}). \tag{10}
\]

III. PROBLEM FORMULATION

This work focuses on minimizing the convergence time of FL, which is defined as the sum of latency across all communication rounds [7]. Therefore, the convergence time is determined by i) the number of communication rounds, and ii) the time consumption of each round. That is, the server can select devices with more data and larger AoU to reduce the number of required rounds, or devices with better channel conditions to reduce the latency in each round. However, devices with more data and larger AoU are often not preferred due to larger time and energy consumption, while devices with good channel conditions can be frequently selected, which usually causes smaller AoU. Therefore, there exists a trade-off between these two options. In order to comprehensively minimize convergence time, the number of required communication rounds and latency should be jointly minimized. This situation can be described by the Stackelberg game, where global loss minimization and latency minimization are considered as leader-level and follower-level problems, respectively.

1) Leader-Level Problem (Computation): In the considered FL algorithm, the local loss at device \(n\) is given by

\[
f_n(w^{(t)}) = \frac{1}{\beta_n} \sum_{i=1}^{\beta_n} \ell(w^{(t)}; x_{n,i}, y_{n,i}), \tag{11}
\]

where \(w^{(t)}\) is the global model in round \(t\), \(\ell(w^{(t)}; x_{n,i}, y_{n,i})\) is a loss function, \(x_{n,i}\) is the \(i\)-th input data of device \(n\), and \(y_{n,i}\) is the corresponding label. In round \(t\), the global loss can be presented below

\[
F(w^{(t)}) = \frac{1}{\sum_{n=1}^{N} \beta_n} \sum_{n=1}^{N} \beta_n \sum_{i=1}^{\beta_n} \ell(w^{(t)}; x_{n,i}, y_{n,i}). \tag{12}
\]

By including device selection and sub-channel assignment, an AoU based global loss minimization problem is formulated as follows:

\[
\min_{S^{(t)}} \frac{\sum_{n=1}^{N} \alpha_n^{(t)} S_n^{(t)}}{\sum_{n=1}^{N} \sum_{k=1}^{K} \psi_{k,n}^{(t)} \sum_{j=1}^{\beta_n} \ell(w^{(t)}; x_{n,i}, y_{n,i})} \tag{13}
\]

s.t. \(S_n^{(t)} \in \{0, 1\}, \forall n \in \mathcal{N}\),

\[
\sum_{n=1}^{N} S_n^{(t)} \leq K. \tag{13b}
\]

Constraint (13b) indicates that the number of selected devices in each round is not greater than the number of available sub-channels. Note that only device selection is considered in the computation phase, and the sub-channel assignment indicator is decided in the communication phase, even though it has an impact on the global loss.

2) Follower-Level Problem (Communication): Based on the \(S^{(t)}\) given by the leader-level problem, the set of selected devices \(\mathcal{N}_f\) can be obtained. Then, sub-channel assignment and resource allocation can be implemented according to the set \(\mathcal{N}_f\). In any round \(t\), the latency minimization problem can be presented as follows:

\[
\min_{\psi^{(t)}, \tau, p} \max_{n \in \mathcal{N}_f} \left\{ \sum_{k=1}^{K} \psi_{k,n}^{(t)} T_{k,n}(\tau_{k,n}, p_{k,n}) \right\}, \tag{14}
\]

s.t. \(E_{k,n}(\tau_{k,n}, p_{k,n}) \leq E_{k,n}^{\text{max}}, \tag{14a}\)

\(\tau_{k,n} \in [0, 1], p_{k,n} \in [0, 1], \tag{14b}\)

\(\psi_{k,n}^{(t)} \in \{0, 1\}, \tag{14c}\)

\(\sum_{n \in \mathcal{N}_f} \psi_{k,n}^{(t)} = 1, \forall k \in K, \tag{14d}\)

\(\sum_{k=1}^{K} \psi_{k,n}^{(t)} = 1, \forall n \in \mathcal{N}_f, \tag{14e}\)

where \(\psi_{k,n}^{(t)}, \tau, \) and \(p\) are the sets of all sub-channel assignment indicators, computational resource allocation coefficients, and power allocation coefficients, respectively. In constraint (14a), the maximum energy consumption \(E_{k,n}^{\text{max}}\) is included. In constraints (14b) and (14c), the value ranges of all optimization variables are defined. Constraints (14d) and (14e) indicate that each sub-channel can be occupied by one device, and each device can be assigned to one sub-channel, respectively.

3) Stackelberg Equilibrium: With any given solution of the leader-level problem, the formulated follower-level problem can be infeasible, as shown in follows:
Proposition 1: With any device selection $S_k^{(i)} = 1$, problem (14) is infeasible if the following condition holds:

$$\ln(2) P_t D \left( w_n^{(i)} \right) \geq E_n^{\max} B |h_{k,n}|^2, \forall k \in K.$$  \hspace{1cm} (15)

Proof: Refer to Appendix A.

Proposition 1 indicates that the energy consumption constraint can affect device participation, thereby reducing learning performance [30]. The following remark can be obtained.

Remark 1: In wireless FL scenarios, energy consumption constraints can restrict the transmission of local models individually, resulting in a decrease in the global loss.

The above proposition and remark show that there exists an interaction between these two problems. That is, the selected devices may not be able to transmit local models to the server due to the poor channel conditions. This interaction is consistent with the Stackelberg competition model, in which the leader and the follower tend to maximize their own utilities [31]. For solving the formulated Stackelberg game based problem, Stackelberg equilibrium [32] is introduced as follows:

Definition 1: In the formulated Stackelberg game based problem, by respectively defining $G_L$ and $G_F$ as the objective functions of leader-level and follower-level problems, solution $(S^{(i)}, \psi^{(i)}, \tau^*, p^*)$ is the Stackelberg equilibrium if the following conditions hold:

$$G_L(S^{(i)}, \psi^{(i)}, \tau^*, p^*) \leq G_L(S^{(i)}, \psi^{(i)}, \tau^*, p^*),$$

$$G_F(S^{(i)}, \psi^{(i)}, \tau, p^*) \leq G_F(S^{(i)}, \psi^{(i)}, \tau, p).$$ \hspace{1cm} (16)

In order to achieve the Stackelberg equilibrium, the leader should predict the possible solution of the follower-level problem, i.e., the feasibility of the selected devices, and then propose a strategy which can minimize the global loss after transmission. As a result, the solution of the follower-level problem should be obtained before the leader-level problem. Note that both problems are solved at the server, and the solutions of each communication round can be transmitted to all devices together with the global model. Since the server has powerful computing capabilities, the impact of this process on latency can be ignored.

IV. SOLUTION OF FOLLOWEr-LEVEL PROBLEM

The formulated follower-level problem in (14) is a non-convex problem with binary constraints. In this section, the follower-level problem is decoupled into two sub-problems and solved iteratively. With the fixed sub-channel assignment, the sub-problem related to resource allocation is given by

$$\Gamma = \min_{\tau, p} \left\{ T_{k,n}(\tau_{k,n}, p_{k,n}) \right\} \forall n \in \mathcal{N}_k, \forall k \in K \},$$

s.t. (14a), (14b), \hspace{1cm} (17)

where $\Gamma$ is a $K \times |\mathcal{N}_k|$ matrix containing the minimum time consumptions for all possible device and sub-channel combinations. With the given matrix $\Gamma$, the sub-problem related to sub-channel assignment can be presented as follows:

$$\min_{\psi^{(i)}} \max_{n \in \mathcal{N}_k} \left\{ \sum_{k=1}^{K} \psi^{(i)}_{k,n} \Gamma_{k,n} \right\},$$

s.t. (14c), (14d), (14e), \hspace{1cm} (18)

where $\Gamma_{k,n}$ is one element of matrix $\Gamma$. Note that there exists an infeasible combination if the condition in Proposition 1 holds. In this case, this combination will be marked as infeasible and avoided in problem (18).

A. Joint Optimization of Computational Resource Allocation and Power Allocation

In problem (17), the time consumption of any combination only depends on its corresponding resource allocation. Therefore, this problem can be divided into multiple sub-problems to obtain all elements of matrix $\Gamma$. The feasibility of combinations is verified by utilizing Proposition 1. For any feasible combination $\Gamma_{k,n}$, the sub-problem is given by

$$\min_{\tau_{k,n}, p_{k,n}} T_{k,n}(\tau_{k,n} + T_{k,n}(p_{k,n})$$

s.t. $E_{k,n}(\tau_{k,n}) + E_{k,n}(p_{k,n}) \leq E_{n}^{\max}$, (19a)

$$\tau_{k,n} \in [0, 1], p_{k,n} \in [0, 1].$$ \hspace{1cm} (19b)

Since problem (19) is non-convex, traditional optimization methods, such as convex optimization, cannot be directly employed. In this case, monotonic optimization is introduced. In order to utilize monotonic optimization, the monotonicity of problem (19) should be analyzed. According to [33], the following proposition can be obtained.

Proposition 2: For any device $n$ assigned to sub-channel $k$, with computational resource allocation coefficient $T_{k,n}$ and power allocation coefficient $p_{k,n}$, the time consumption $T_{k,n}(\tau_{k,n}, p_{k,n})$ is a decreasing function, while the energy consumption $E_{k,n}(\tau_{k,n}, p_{k,n})$ is an increasing function.

Proof: Refer to Appendix B.

Proposition 2 indicates the following remark.

Remark 2: In the considered FL framework, minimizing the latency leads to the maximized energy consumption.

According to Proposition 2, it is indicated that the objective function and all constraints in problem (19) are monotonic. Next, this problem is transformed to the canonical formulation, as shown in follows:

$$\max_{z_{k,n}} f(z_{k,n}),$$

s.t. $z_{k,n} \in \mathcal{G}$, \hspace{1cm} (20a)

where $z_{k,n} = \{\tau_{k,n}, p_{k,n}\}$, $\mathcal{G} = \{z_{k,n} \in [0, 1], g(z_{k,n}) \leq 0\}$,

$$f(z_{k,n}) = -\frac{\mu_{n}}{\tau_{k,n} C_n} - \frac{D(w^{(i)})}{B \log_2(1 + p_{k,n} |h_{k,n}|^2),}$$ \hspace{1cm} (21)

and

$$g(z_{k,n}) = \kappa_0 \mu_{n} \left( \frac{1}{\tau_{k,n} C_n} \right)^2 + \frac{p_{k,n} \mu_{n} D(w^{(i)})}{B \log_2(1 + p_{k,n} |h_{k,n}|^2)} - E_{n}^{\max}. \hspace{1cm} (22)$$
Algorithm 1: Polyblock Outer Approximation Algorithm.

1: Initialize vertex set $\mathcal{V}(1) = \{v(1)\}$, where $v(1) = \{1, 1\}$.
2: Initialize polyblock $\mathcal{P}(1)$ with vertex set $\mathcal{V}(1)$.
3: Set $\epsilon$ and $\theta = 1$.
4: if $|f(\phi(v(1))) - f(\phi(v(1-1)))| > \epsilon$ then
5:   Obtain $\phi(v(1))$ from Eq. (29).
6:   Calculate vertices $\tilde{v}_1(\theta)$ and $\tilde{v}_2(\theta)$ as follows:
   $$\tilde{v}_i(\theta) = v(\theta) - (v_i(1) - \phi_i(v(1)))e_i, \forall i \in \{1, 2\}.$$ 
7:   Update vertex set $\mathcal{V}(\theta+1)$ as follows:
   $$\mathcal{V}(\theta+1) = \mathcal{V}(\theta) \setminus \tilde{v}_i(\theta) \cup \{\tilde{v}_1(\theta), \tilde{v}_2(\theta)\}.$$ 
8: Construct polyblock $\mathcal{P}(\theta+1)$ with vertex set $\mathcal{V}(\theta+1)$.
9: Find vertex $v(\theta+1)$ from $\mathcal{V}(\theta+1)$, where
   $$v(\theta+1) = \arg\max\{f(\phi(v))|v \in \mathcal{V}(\theta+1)\}.$$ 
10: Set $\theta = \theta + 1$.
11: end if
12: Set $z_{k,n} = \phi(v(\theta))$.

In problem (20), the objective function $f(z_{k,n})$ is monotonically increasing with $z_{k,n}$, and then the optimal solution is located on the boundary of the feasible set $\mathcal{G}$. However, due to the non-convexity, the expression of the boundary cannot be directly derived. In this case, a polyblock outer approximation algorithm is proposed to approach the feasible set $\mathcal{G}$ by constructing polyblock $\mathcal{P}$ [34], as shown in Algorithm 1.

As shown in Fig. 2(a), by setting the first vertex $v(1) \in \mathcal{V}(1)$, the initial polyblock $\mathcal{P}(1)$ can be constructed as the box $[0, 1]$, which contains the feasible set. The projection of $v(1)$ on the upper boundary of feasible set $\mathcal{G}$ is calculated, denoted by $\phi(v(1))$. Based on point $\phi(v(1))$, two new vertices $\tilde{v}_1(\theta)$ and $\tilde{v}_2(\theta)$ can be obtained to replace $v(1)$, as shown in Fig. 2(b). The new vertices are calculated as follows:
   $$\tilde{v}_i(\theta) = v(\theta) - (v_i(1) - \phi_i(v(1)))e_i, \forall i \in \{1, 2\},$$
where $v_i(1)$ is the $i$-th element of $v(1)$, $\phi_i(v(1))$ is the $i$-th element of $\phi(v(1))$, and $e_i$ is the $i$-th unit vector. As shown in Fig. 2(c), a new polyblock $\mathcal{P}(2)$ is obtained, where $\mathcal{G} \subset \mathcal{P}(2) \subset \mathcal{P}(1)$. The vertex set of $\mathcal{P}(2)$ is updated as follows:
   $$\mathcal{V}(2) = \{v(1), v(1)\} \cup \{\tilde{v}_1(\theta), \tilde{v}_2(\theta)\}. $$
After that, the optimal vertex is selected from vertex set $\mathcal{V}(2)$ and denoted by $v(2)$, which satisfies the following condition:
   $$v(2) = \arg\max\{f(\phi(v))|v \in \mathcal{V}(2)\}. $$

The projection of the optimal vertex can achieve the maximum value of (21). For example, Fig. 2(c) shows the case that $\tilde{v}_1(\theta)$ is selected as the optimal vertex $v(2)$. This process is repeated, and a smaller polyblock $\mathcal{P}(3) \subset \mathcal{P}(2)$ is constructed based on vertex $v(2)$, as shown in Fig. 2(d). This algorithm is completed if the following condition is satisfied:
   $$|f(\phi(v(\theta))) - f(\phi(v(\theta-1)))| \leq \epsilon,$$ 
where $\epsilon$ is the error tolerance. The output $z_{k,n} = \phi(v(\theta))$ is the optimal solution of problem (20). Moreover, the projection of any vertex $v(\theta)$ satisfies the following condition:
   $$\phi(v(\theta)) = \zeta v(\theta),$$
where $\zeta \in (0, 1)$ is a ratio coefficient obtained as follows:
   $$\zeta = \max\{\zeta \in (0, 1)|\tilde{v}_1(\theta) \in \mathcal{G}\} = \max\{\zeta \in (0, 1)|g(\tilde{v}_1(\theta)) \leq 0, \tilde{v}_1(\theta) \in [0, 1]\}. $$
Since $g(\tilde{v}_1(\theta))$ is a monotonic increasing function, $\zeta$ satisfies $g(\tilde{v}_1(\theta)) = 0$, which can be written as follows:
   $$\kappa_0 \mu_0 \beta_n (\zeta \epsilon_1(1) C_n)^2 + \frac{\zeta \epsilon_2(\theta) P_t D(w_n^{(1)})}{B \log_2(1 + \zeta \epsilon_2(\theta) |h_{k,n}|^2)} = E_n^{\max}. $$
The above nonlinear equation can be solved by multiple numerical solvers, such as fsolve in MATLAB or Python. It is worth pointing out that during Algorithm 1, with any given vertex $v(\theta) = \{v_1(\theta), v_2(\theta)\}$, where $\theta \geq 2$, condition $\zeta \in (0, 1)$ is always satisfied. When $\theta = 1$, $\zeta = 1$ may be obtained with initial vertex $v(1)$, which means that the feasible set $\mathcal{G}$ includes the first vertex. In other words, the energy consumption constraint $g(z_{k,n}) < 0$ can be satisfied with any $\tau_{k,n}$ and $p_{k,n}$ in $[0, 1]$. In this case, the optimal solution is obtained by $z_{k,n} = v(1)$.

According to [35], the complexity of Algorithm 1 is dominated by step 9 and the number of iterations. Specifically, step 9 selects an optimal vertex from the vertex candidate set, which contains $\theta + 1$ vertices at the $\theta$-th iteration. Since the complexity
in solving (20) to obtain each vertex is $O(2)$, the complexity of step 9 is $O(2(\theta + 1))$. With the given number of iterations $T_c$, the total complexity of Algorithm 1 can be expressed as $O(2T_c(T_c + 1))$. 

B. Matching Based Sub-Channel Assignment

Based on Algorithm 1, the minimum time consumption of all selected devices in all sub-channels is obtained in matrix $\Gamma$. In this subsection, a matching based algorithm is proposed to solve the binary integer programming problem in (18), where matrix $\Gamma$ is utilized to construct the preference list. In problem (18), the set of selected devices, i.e., $\mathcal{N}_i$, is provided by the leader-level problem. As revealed in Section V, to minimize global loss, the number of selected devices is maximized in each round, i.e., $|\mathcal{N}_i| = K$. Therefore, $\mathcal{N}_i$ and $\mathcal{K}$ are two disjoint sets with the same size, and this scenario can be considered as a one-to-one matching $\Psi$ from $\mathcal{N}_i$ to $\mathcal{K}$. Furthermore, since some combinations in matrix $\Gamma$ are marked as infeasible, the player in this matching may have an incomplete preference list [36], [37]. In this case, the elements in $\Gamma$ cannot be directly employed, and the utility of device $n$ assigned to sub-channel $k$ in matching $\Psi$ is defined below

$$U_n(\Psi) = \begin{cases} U_{\text{max}}, & \text{if } \Gamma_{k,n} \text{ is infeasible,} \\ \Gamma_{k,n}, & \text{otherwise,} \end{cases}$$

(30)

where $U_{\text{max}}$ is a large constant indicating that the assignment of device $n$ and sub-channel $k$ is infeasible. In the one-to-one matching, the utility of any sub-channel is equal to the utility of the occupied device, i.e., $U_k(\Psi) = U_{\Psi(k)}(\Psi), \forall k \in \mathcal{K}$. By calculating the utility, the preference list of any player can be established. Since that problem (18) is a minimization problem, the preference of device $n$ can be defined below

$$(k, \Psi) \prec_n (k', \Psi') \Rightarrow U_n(\Psi) > U_n(\Psi').$$

(31)

The above function indicates that device $n$ is willing to be assigned to sub-channel $k'$ in matching $\Psi'$, rather than $k$ in matching $\Psi$, since its utility can be strictly decreased by switching from $k$ to $k'$. Since all players are matched, the considered case follows the concept of two-sided exchange matchings [38]. In this case, if any device intends to be assigned to a sub-channel, it needs to exchange with the device occupying this sub-channel, instead of directly joining this sub-channel. A notation $\Psi_n^{n'}$ is introduced to represent the case where two devices $n$ and $n'$ are swapped in matching $\Psi$, which is defined as follows:

$$\Psi_n^{n'}(m) = \begin{cases} \Psi(n') = k', & m = n, \\ \Psi(n) = k, & m = n', \end{cases}$$

(32)

where $\Psi_n^{n'}$ only replaces two pairs defined by $\Psi(n) = k$ and $\Psi(n') = k'$ into $\Psi(n) = k'$ and $\Psi(n') = k$, respectively. It is indicated that any swap operation involves four players, and hence, it should be approved by the involved players. However, in a matching with the incomplete preference list, there may be sub-channels that are unacceptable for some devices. In order to prioritize feasible combinations, the approval of sub-channels is removed, and the swap operation is approved by the swap-blocking pair $(n, n')$, which is defined below [39]:

**Algorithm 2: Sub-Channel Assignment Algorithm.**

1: **Initialization:**
2: **Initialization:**
3: **Initialization:**
4: **Initialization:**
5: **Initialization:**
6: **Initialization:**
7: **Initialization:**
8: **Initialization:**
9: **Initialization:**
10: **Initialization:**
11: **Initialization:**
12: **Initialization:**

**Definition 2:** A swap-blocking pair $(n, n')$ is confirmed if and only if the following conditions hold
1) $U_n(\Psi_n^{n'}(n)) \leq U_n(\Psi)$ and $U_n(\Psi_n^{n'}(n')) \leq U_n(\Psi)$;
2) At least one inequality above is strict.

By searching swap-blocking pairs, a matching based sub-channel assignment algorithm is presented in Algorithm 2. The proposed algorithm can be started from any initial matching. During the execution of the algorithm, an active device $n$ is selected and sequential swap is attempted with all other devices. If a swap-blocking pair $(n, n')$ is formed, the new matching $\Psi_n^{n'}$ is recorded, and the algorithm continues. The main loop of Algorithm 2 executes repeatedly. When the last device has searched for all other devices, the first device becomes the active device again. The algorithm ends if no swap blocking pair can be found in a full round of the main loop. At this stage, a stable matching is established, which satisfies the following definition [38], [39]:

**Definition 3:** A matching $\Psi$ is two-sided exchange-stable (2ES) if and only if there is no further swap-blocking pair.

Note that some devices may not be assigned to feasible sub-channels in the stable matching, i.e., their utilities are equal to $U_{\text{max}}$. In this case, these devices cannot transmit local models to the server, and the corresponding sub-channel assignment indicators should be set to zero in the leader-level problem. In the following, the complexity, convergence and stability of the proposed matching based sub-channel assignment algorithm are analyzed.

1) **Complexity:** By considering the worst case, the computational complexity of the proposed matching based algorithm can be expressed as $O(CK^2)$, where $C$ is the number of main loops. In the worst case, $K$ devices can play the role of active devices to search $K - 1$ devices, and thus $K(K - 1)$ times of calculations should be performed in one loop. With the given number of main loops $C$, the computational complexity of the proposed algorithm is obtained.

2) **Convergence:** Assuming $\Psi^{(a)}$ and $\Psi^{(b)}$ are two adjacent matching in Algorithm 2, i.e., $\Psi^{(a)} \rightarrow \Psi^{(b)}$, where $a \neq b$, one
swap-blocking pair is found among this transformation. According to Definition 2, at least one device can achieve less utility, and the utilities of other devices cannot be increased. Hence, the matching transformation cannot be reversed. With the finite number of devices and sub-channels, the number of possible matchings is finite and equal to the Bell number [40]. Therefore, from any initial matching, the proposed algorithm is guaranteed to converge to a stable matching.

3) Stability: Based on Definition 3, any final matching obtained from the matching based sub-channel assignment algorithm is 2ES. Specifically, if the final matching obtained from Algorithm 2 is not 2ES, there exists at least one swap-blocking pair, which can further reduce the sum utility of all devices. However, it contradicts the conditions for completing the proposed algorithm.

V. SOLUTION OF LEADER-LEVEL PROBLEM

To solve the formulated global loss minimization problem in (13), the loss function and local data must be available at the server, which is impractical and contradicts the motivation to utilize FL. In this section, by analyzing the impact of device selection on the expected convergence rate, such information can be detached from the leader-level problem. By employing the gradient descent method, with global model \( w^{(t)} \), device \( n \) can update the local model as follows:

\[
\begin{align*}
\psi_{n}^{(t)} &= \psi_{n}^{(1)} - \frac{\lambda}{\beta_{n}} \sum_{i=1}^{S_{n}} \nabla \ell(w^{(1)}; x_{n,i}, y_{n,i}),
\end{align*}
\]

where \( \lambda \) is the learning rate. After that, the selected devices transmit the updated local models to the server for aggregation. By including device selection and sub-channel assignment, the aggregated global model in round \( t \) is given by

\[
\begin{align*}
w^{(t+1)} &= \sum_{n=1}^{N} S_{n}^{(t)} \sum_{k=1}^{K} \psi_{k,n}^{(t)} \beta_{n} w_{n}^{(t)} \sum_{n=1}^{N} S_{n}^{(t)} \sum_{k=1}^{K} \psi_{k,n}^{(t)} \beta_{n} w_{n}^{(t)}
= \lambda \sum_{n=1}^{N} S_{n}^{(t)} \sum_{k=1}^{K} \psi_{k,n}^{(t)} \beta_{n} w_{n}^{(t)}
= \lambda \nabla F(w^{(1)}) - \nabla F(w^{(t)}),
\end{align*}
\]

where \( \nabla F(w^{(1)}) \) is the learning rate. After that, the selected devices transmit the updated local models to the server for aggregation. By including device selection and sub-channel assignment, the aggregated global model in round \( t \) is given by

\[
\begin{align*}
w^{(t+1)} &= \sum_{n=1}^{N} S_{n}^{(t)} \sum_{k=1}^{K} \psi_{k,n}^{(t)} \beta_{n} w_{n}^{(t)} \sum_{n=1}^{N} S_{n}^{(t)} \sum_{k=1}^{K} \psi_{k,n}^{(t)} \beta_{n} w_{n}^{(t)}
= \lambda \sum_{n=1}^{N} S_{n}^{(t)} \sum_{k=1}^{K} \psi_{k,n}^{(t)} \beta_{n} w_{n}^{(t)}
= \lambda \nabla F(w^{(1)}) - \nabla F(w^{(t)}),
\end{align*}
\]

In order to derive the expected convergence rate, the following assumptions are considered [8], [9], [23], [45]:

1) With respect to the global model \( w^{(1)} \), \( \nabla F(w^{(1)}) \) is uniformly Lipschitz continuous, i.e.,
\[
\| \nabla F(w^{(1)}) - \nabla F(w^{(t)}) \| \leq L \| w^{(t+1)} - w^{(t)} \|,
\]
where \( L \) is a positive parameter.

2) \( F(w^{(1)}) \) is strongly convex with a positive constant \( \mu \), i.e.,
\[
\begin{align*}
F(w^{(t+1)}) &\geq F(w^{(t)}) + \left( w^{(t+1)} - w^{(t)} \right)^{T} \nabla F(w^{(t)}) + \frac{\mu}{2} \| w^{(t+1)} - w^{(t)} \|^{2}.
\end{align*}
\]

3) \( F(w^{(1)}) \) is twice-continuously differentiable, as below:
\[
\mu \beta \leq \nabla^{2} F(w^{(1)}) \leq \mu \beta.
\]

4) The following inequality is satisfied with any device \( n \) and sample \( i \):
\[
\| \nabla \ell((w^{(1)}); x_{n,i}, y_{n,i}) \|^{2} \leq \rho \| \nabla F(w^{(1)}) \|^{2},
\]
where \( \rho \) is a non-negative constant.

It is worth pointing out that the above assumptions are commonly considered in the FL related optimization, and can be satisfied by widely adopted loss functions. Based on these assumptions, the following proposition yields the effect of device selection on convergence rate.

Proposition 3: In the case that device selection and sub-channel assignment satisfy \( S_{n}^{(i)} = \sum_{k=1}^{K} \psi_{k,n}^{(i)} \forall n \in N \), with the learning rate \( \lambda = 1/L \) and the optimal global model \( w^{*} \), the upper bound of convergence rate in round \( t \) is
\[
\begin{align*}
\mathbb{E}[F(w^{(1)}) - F(w^{*})] &\leq \left( 1 - \frac{\mu}{L} \right)^{t} \mathbb{E}[F(w^{(1)}) - F(w^{*})]
+ 2\rho \sum_{i=1}^{t} \left( 1 - \frac{\mu}{L} \right)^{t-i} \| \nabla F(w^{(1)}) \|^{2} \sum_{n=1}^{N} \beta_{n} \left( 1 - S_{n}^{(i)} \sum_{k=1}^{K} \psi_{k,n}^{(i)} \right).
\end{align*}
\]

Proof: Refer to Appendix C.

It is indicated that the convergence rate is bounded by two terms. The first term, i.e., \( (1 - \frac{\mu}{L})^{t} \mathbb{E}[F(w^{(1)}) - F(w^{*})] \), is the expected gap between the first global loss and the optimal global loss in round \( t \). The second term is related to device selection, where the status of devices in round \( i \), i.e., \( \alpha_{n}^{(i)} \), is included. Proposition 3 indicates the expected gap between the global loss in round \( t \) and the optimal loss. In this case, the global loss minimization problem in (13) can be achieved by minimizing the expected gap. Since that the first term does not include device selection, it can be treated as a constant. Meanwhile, terms \( 2\rho \sum_{i=1}^{t} \left( 1 - \frac{\mu}{L} \right)^{t-i} \| \nabla F(w^{(1)}) \|^{2} \sum_{n=1}^{N} \beta_{n} \) and \( \sum_{n=1}^{N} \beta_{n} \| \nabla F(w^{(1)}) \|^{2} \) are positive and not effected by device selection, and hence, they can be removed. By incorporating the AoU based weight \( \alpha_{n}^{(i)} \) into the device selection indicator \( S_{n}^{(i)} \), the objective function of the leader-level problem (13) can be reformulated as follows:
\[
\min_{S_{n}^{(i)}} \sum_{n=1}^{N} \beta_{n} \left( 1 - \alpha_{n}^{(i)} \right) S_{n}^{(i)} \sum_{k=1}^{K} \psi_{k,n}^{(i)}
\]
Algorithm 3: Device Selection Algorithm.

1: Initialization:
2: Generate list \( Q^{(t)} \) based on (43).
3: Initialize set \( N_t \) by selecting the first \( K \) devices from \( Q^{(t)} \).
4: Main Loop:
5: Obtain sub-channel assignment from Algorithm 2.
6: if \( \sum_{n \in N_t} \sum_{k=1}^{K} \psi_{k,n}^{(t)} < K \) and \( (N) \notin N_t \) then
7: for \( n \in N \) do
8: if \( \sum_{k=1}^{K} \psi_{k,n}^{(t)} = 0 \) then
9: Remove device \( n \) from set \( N_t \).
10: Add the next unselected device from list \( Q^{(t)} \).
11: end if
12: end for
13: end if

By removing the constant term \( \sum_{n=1}^{N} \beta_n \), the leader-level problem (13) can be equivalently transformed as follows:

\[
\max_{\mathbf{s}^{(t)}} \sum_{n=1}^{N} \alpha_n^{(t)} \beta_n \mathbf{s}_n^{(t)} \sum_{k=1}^{K} \psi_{k,n}^{(t)}
\]

s.t. (33a), (33b).

Problem (42) can be treated as a weighted device selection problem, where AoU and data size play the role of weight factors. That is, the server tends to select the devices with large AoU and/or data size. In this case, the server can order and select devices based on this weight. In any round \( t \), all devices are sorted in a list \( Q^{(t)} \), which satisfies the following condition:

\[
\alpha_{(1)}^{(t)} \beta_{(1)} \geq \alpha_{(2)}^{(t)} \beta_{(2)} \geq \cdots \geq \alpha_{(N)}^{(t)} \beta_{(N)},
\]

where \( (1) \) and \( (N) \) denote the devices with the highest and lowest priority, respectively. Moreover, it is indicated by problem (42) that the server tends to select more devices, and hence, constraint (13b) can be rewritten as \( \sum_{n=1}^{N} \beta_n = K \). According to the list \( Q^{(t)} \), problem (42) can be solved by Algorithm 3.

In the proposed device selection algorithm, \( K \) devices with the highest priority are selected in the initialization phase. During Algorithm 3, any device not assigned to a sub-channel will be replaced, until all selected devices have been assigned to sub-channels, or all devices in list \( Q^{(t)} \) have been adopted. In the worst case, all devices in list \( Q^{(t)} \) are traversed, and therefore the complexity of Algorithm 3 can be expressed as \( O(N) \).

VI. SIMULATION Results

In this section, the performance of the proposed solution is simulated and demonstrated. In this simulation, multiple devices are randomly distributed in a disc with radius \( R \), and the server is located in the center of the disc. The experiments includes three datasets (MNIST, CIFAR-10, and SST-2) with different models.\(^6\) The imbalanced independent identically distributed (IID) data distribution is adopted, where a factor \( c_n \in [1, 10] \) is randomly generated for all devices, and training samples (500 for MNIST, 50000 for CIFAR-10, and 67349 for SST-2) are shuffled and partitioned across devices based on fraction \( c_n / \sum_{n \in N} c_n \).

Unlike [8], [9], [10], [11], [12], [13], [14], [15] which focus on minimizing either global loss or latency, this paper studies the interaction between global loss minimization and latency minimization. Therefore, a direct comparison with the existing schemes in these works is unfair. Alternatively, the proposed scheme is benchmarked against the following conventional device selection schemes:

- **AoU based DS**: The server selects the top \( K \) devices in (43).
- **Random DS**: The server selects \( K \) devices randomly.
- **Cluster based DS**: All devices are randomly allocated to \( N/K \) clusters such that the clusters are selected in rotation.
- **Fixed DS**: The same \( K \) devices are selected in all rounds.

In the above schemes, the proposed solutions are adopted, including monotonic optimization based resource allocation (MO-RA) and matching based sub-channel assignment (M-SA). Moreover, fixed resource allocation (FIX-RA) and random sub-channel assignment (R-SA) are incorporated as benchmarks, where \( \tau_{k,n} = p_{k,n} = 0.5, \forall k, n \) is set in FIX-RA. The parameters of the simulation are shown in Table I.

In Fig. 3, the global loss achieved by different schemes is presented. Given the same number of communication rounds, the proposed scheme can achieve the lowest global loss on all datasets, reflecting the derived convergence rate in Proposition 3. This improvement benefits from two approaches,
including AoU based weights and efficient utilization of sub-channels. The former is able to select devices that can provide more contributions in each communication round, while the latter ensures the maximization of the number of selected devices. Moreover, it can be observed that without Algorithm 3, AoU based DS can still outperform other schemes, which confirms that AoU based weights have the capability to improve learning efficiency. In terms of the random DS and cluster based DS, the devices have the same probability to be selected, and hence, the similar global loss is achieved by these two schemes. For the fixed DS, due to the fact that the size of training data in this case is less than that in other schemes, its performance is the worst. Compared to the MNIST dataset, the CIFAR-10 image classification task is more complex, and hence, the differences between the schemes are not that obvious, as shown in Fig. 3(b). However, the performance of these schemes is still consistent with Fig. 3(a). It is worth noting that by employing the CIFAR-10 dataset, the data size and model size is increased, and therefore, the number of devices that can satisfy the energy constraint is reduced. This issue is severe when utilizing AoU based DS, as the server tends to select devices that are generally more difficult to meet this condition, resulting in poor performance. This effect is mitigated when a simpler task is adopted, as shown in Fig. 3(c). Since there are only two labels on the SST-2 dataset, the differences between schemes become significant, and the advantages of the proposed scheme in global loss are clearly demonstrated.

Fig. 4 shows the performance of the proposed optimization solutions, where different resource allocation and sub-channel assignment approaches are incorporated into the proposed device selection scheme. It is clear that the proposed scheme can achieve the best performance with the proposed solutions, i.e., MO-RA and M-SA. This is because in this case, the server can select devices who can provide more contributions in the aggregation without replacing them with lower priority devices. When other solutions are utilized, some devices may no longer meet the maximum energy consumption constraint and thus be replaced by devices with lower priority, resulting in poor learning performance. This result also corroborates our conclusion that the contribution of a device in aggregation is proportional to its AoU and data size. In Fig. 4(a), an obvious feature is that the proposed scheme using FIX-RA and R-SA performs well in the early stage of training (from round 1 to round 100), but poorly in the later stage. This is due to the fact that in this case, the number of devices that can satisfy the maximum energy consumption requirement is very small, and thus device selection is performed on a smaller subset. For simple tasks, such as MNIST with IID data distribution, repeatedly using smaller training datasets can achieve faster convergence in the early stages, however, this can lead to overfitting problems and poor final results. Moreover, it is worth pointing out that compared to sub-channel assignment, resource allocation plays a more important role in minimizing the global loss, and this trend is more significant when the model size is larger, as shown in Fig. 4(b) and Fig. 4(c).

In Fig. 5 and Fig. 6, the impact of the number of devices and radius on global loss is presented, respectively. In Fig. 5, since the size of the total training data is fixed, an increase in
the number of devices means less training data per device. That is, when the number of selected devices, i.e., $K$, is fixed, as the number of devices rises, the amount of training data utilized in each communication round reduces, resulting in an increase in the global loss. With the proposed optimization solutions, devices that can provide more contributions can be selected, and therefore the learning performance is improved. In Fig. 6, the increase in radius can be understood as a deterioration in channel conditions. According to Proposition 1, in this case, more devices become unavailable and cannot participate in the aggregation. As a result, the achievable global loss increases with the radius. By employing the proposed optimization solutions, the negative impact caused by channel degradation can be alleviated to a certain extent, thereby narrowing the gap in global loss.

In the considered Stackelberg game based framework, the number of selected devices varies between schemes, and therefore, the latency cannot be compared independently. It can be seen from Fig. 7 that the proposed scheme can efficiently utilize all available sub-channels, while the latency of each communication round is also increased accordingly. That is because the proposed scheme needs to guarantee the performance of training through the leader level problem. This explains why the achievable global loss of the proposed scheme is lower than others. It is also indicated that the proposed RA and SA algorithms can increase the number of selected devices with random DS.

The effect of energy limitation is demonstrated in Fig. 8, and the results confirm Proposition 1. According to Proposition 1, as the maximum energy consumption increases, the device participation increases. As a result, by employing random DS, the number of selected devices in each communication round is increased, and meanwhile the latency of each round also increases. On the other hand, it can be observed that the proposed algorithm, MO-RA, has the ability to dynamically adjust the computational resource allocation coefficients and power allocation coefficients. Therefore, the latency can be reduced with this solution.

As shown in Fig. 9, the latency decreases with the increasing transmit power, since the achievable data rate can be increased. With FIX-RA, the number of selected devices is reduced when the transmit power is greater than 6 dBm, because the fixed power allocation coefficient can no longer satisfy the energy consumption constraint. For MO-RA, the power allocation coefficient is optimized, and thus the number of selected devices is
not affected. However, the decrease in latency has been slowed down accordingly.

VII. CONCLUSION

This paper investigates FL in a practical wireless communication scenario, where limited sub-channels and energy consumption are considered. With the goal of minimizing convergence time, we jointly formulate global loss minimization and latency minimization based on the Stackelberg game. We present a monotonic optimization based algorithm to find the optimal solution of computational resource allocation and power allocation. Based on the obtained solution of resource allocation, we further develop a matching based algorithm with incomplete preference list to solve the sub-channel assignment problem. For the global loss minimization problem, we incorporate AoU based weights and derive an upper bound on convergence rate, which presents a priority for selecting devices. Simulation results demonstrate significant reduction of the global loss, establishing that our developed solutions can efficiently utilize available sub-channels and reduce latency. It is worth highlighting that the AoU based device selection can be extended to mobile environments as a practical and promising research direction.

APPENDIX A PROOF OF PROPOSITION 1

Suppose that device \( n \) is selected and assigned to sub-channel \( k \) in round \( t \), i.e., \( S_n(t) = 1 \) and \( w_n(t) = 1 \), based on constraint (14a), the following condition will be satisfied by the optimal solutions \( \tau^*_{k,n} \) and \( p^*_{k,n} \):

\[
E_{k,n}^{cm}(\tau^*_{k,n}) + E_{k,n}^{cm}(p^*_{k,n}) \leq E_n^{\max}.
\]

Due to the fact that the energy consumption for computation is strictly greater than zero, i.e., \( E_{k,n}^{cm}(\tau^*_{k,n}) > 0 \), the following inequality can be obtained:

\[
E_{k,n}^{cm}(p^*_{k,n}) < E_n^{\max},
\]

which can be rewritten as follows:

\[
\frac{p^*_{k,n} P_i D(w_{n(t)})}{B \log_2(1 + p^*_{k,n} |h_{k,n}|^2)} < E_n^{\max}.
\]

Since \( p^*_{k,n} |h_{k,n}|^2 > 0, \) \( p^*_{k,n} |h_{k,n}|^2 > \ln(1 + p^*_{k,n} |h_{k,n}|^2) \) holds, the following inequality can be obtained:

\[
\frac{\ln(2) P_t D(w_{n(t)})}{B |h_{k,n}|^2} < \frac{p^*_{k,n} P_i D(w_{n(t)})}{B \log_2(1 + p^*_{k,n} |h_{k,n}|^2)}.
\]

Therefore, the following condition must be satisfied:

\[
\frac{\ln(2) P_t D(w_{n(t)})}{B |h_{k,n}|^2} < E_n^{max},
\]

and this proposition is proved.

APPENDIX B PROOF OF PROPOSITION 2

It is obvious that the time consumption of any device \( n \) in sub-channel \( k \) is monotonically decreasing with \( \tau_{k,n} \) and \( p_{k,n} \), and hence, the proof of this part is omitted. In terms of the energy consumption, as shown in constraint (19a), the monotonicity can be proved by two parts. The first term, \( E_{k,n}^{cm}(\tau_{k,n}) \), is the energy consumption for computation, which is monotonically increasing with the computational resource allocation coefficient. The second term is the energy consumption for communication, which can be presented by

\[
E_{k,n}^{cm}(p_{k,n}) = \frac{p_{k,n} P_i D(w_{n(t)})}{B \log_2(1 + p_{k,n} |h_{k,n}|^2)}.
\]

The derivative of the above function is

\[
\frac{\partial E_{k,n}^{cm}(p_{k,n})}{\partial p_{k,n}} = \frac{\ln(2) P_t D(w_{n(t)})}{B(1 + p_{k,n} |h_{k,n}|^2) \log_2(1 + p_{k,n} |h_{k,n}|^2)}
\times [(1 + p_{k,n} |h_{k,n}|^2) \ln(1 + p_{k,n} |h_{k,n}|^2) - p_{k,n} |h_{k,n}|^2].
\]

Since the first term of the above function is always greater than zero, the monotonicity of function \( E_{k,n}^{cm}(p_{k,n}) \) depends on the term in the square bracket. Particularly, \( E_{k,n}^{cm}(p_{k,n}) \) is an increasing function if the following inequality holds:

\[
(1 + p_{k,n} |h_{k,n}|^2) \ln(1 + p_{k,n} |h_{k,n}|^2) - p_{k,n} |h_{k,n}|^2 \geq 0.
\]

Suppose \( \theta_{k,n} = \frac{1}{p_{k,n} |h_{k,n}|^2 + 1} \), the above inequality is equivalent to the following inequality:

\[
\frac{1}{\theta_{k,n}} (\theta_{k,n} - 1 - \ln \theta_{k,n}) \geq 0.
\]

Since \( \theta_{k,n} > 0, \ln \theta_{k,n} \leq \theta_{k,n} - 1 \) holds, and then the above inequality is always satisfied, which indicates that (49) is an increasing function. As a result, the total energy consumption is an increasing function, and this proposition is proved.

APPENDIX C PROOF OF PROPOSITION 3

In order to prove this proposition, an auxiliary function is defined as follows:

\[
G(w) = \frac{L}{2} w^\top w - F(w).
\]
With respect to \(w\), the second-order partial derivative of the above function is given by
\[
\frac{\partial^2 G(w)}{\partial w^2} = L - \frac{\partial^2 F(w)}{\partial w^2}.
\] (54)

Since \(F(w)\) satisfies the uniformly Lipschitz condition, the above function is always greater than or equal to zero, and hence, \(G(w)\) is a convex function. By utilizing the second-order Taylor series expansion, the following inequality can be obtained:
\[
G\left(w^{(t+1)}\right) \geq G\left(w^{(t)}\right) + \left(w^{(t+1)} - w^{(t)}\right)^\top \nabla F\left(w^{(t)}\right) + \frac{\lambda}{2} \|w^{(t+1)} - w^{(t)}\|^2.
\] (55)

From (53), the above inequality can be equivalently transformed as follows:
\[
F\left(w^{(t+1)}\right) \leq F\left(w^{(t)}\right) + \left(w^{(t+1)} - w^{(t)}\right)^\top \nabla F\left(w^{(t)}\right) + \frac{L}{2} \|w^{(t+1)} - w^{(t)}\|^2.
\] (56)

Based on (34), the following inequality can be obtained:
\[
F\left(w^{(t+1)}\right) \leq F\left(w^{(t)}\right) - \lambda \left[\nabla F\left(w^{(t)}\right) - \hat{w}\left(t\right)\right]^\top \nabla F\left(w^{(t)}\right) + \frac{\lambda^2 L}{2} \|\nabla F\left(w^{(t)}\right) - \hat{w}\left(t\right)\|^2.
\] (57)

When \(\lambda = 1/L\), the above inequality can be transformed as follows:
\[
\mathbb{E}\left[F\left(w^{(t+1)}\right)\right] \leq \mathbb{E}\left[F\left(w^{(t)}\right) - \lambda \left[\nabla F\left(w^{(t)}\right) - \hat{w}\left(t\right)\right]^\top \nabla F\left(w^{(t)}\right) + \frac{\lambda^2 L}{2} \|\nabla F\left(w^{(t)}\right) - \hat{w}\left(t\right)\|^2\right]
= \mathbb{E}\left[F\left(w^{(t)}\right) - \frac{1}{L} \|\nabla F\left(w^{(t)}\right)\|^2 + \frac{1}{L} \left(\hat{w}\left(t\right)\right)^\top \nabla F\left(w^{(t)}\right) + \frac{1}{2L} \|\hat{w}\left(t\right)\|^2\right]
= \mathbb{E}\left[F\left(w^{(t)}\right)\right] - \frac{1}{2L} \|\nabla F\left(w^{(t)}\right)\|^2 + \frac{1}{2L} \mathbb{E}\left(\|\hat{w}\left(t\right)\|^2\right).
\] (58)

By defining, (59) shown at the bottom of this page, based on condition \(S^{(t)}_n = \sum_{k=1}^{K} \psi_{k,n}^{(t)} = 1, \forall n \in \mathcal{N}_t\), the following equation can be derived:
\[
\mathbb{E}\left(\|\hat{w}\left(t\right)\|^2\right) \leq \mathbb{E}\left[\left\|\nabla F\left(w^{(t)}\right) - \frac{f\left(\mathcal{N}_t, \psi^{(t)}_n, w^{(t)}\right)}{\sum_{n=1}^{N} S^{(t)}_n \sum_{k=1}^{K} \psi_{k,n}^{(t)} \beta_n} \right\|^2\right].
\] (59)

Due to the fact that the number of all devices’ samples is not less than the number of unselected devices’ samples, i.e.,
\[
\sum_{n=1}^{N} \beta_n \geq \sum_{n=1}^{N} \beta_n \left(1 - S^{(t)}_n \sum_{k=1}^{K} \psi_{k,n}^{(t)}\right) \geq 0,
\] (65)
the following inequality can be obtained from (64):

\[ E[\|\bar{w}^{(t)}\|^2] \leq \frac{4\rho}{L} \frac{\|\nabla F(w^{(t)})\|^2}{\sum_{n=1}^{N} \beta_n} \sum_{n=1}^{N} \beta_n \left(1 - S(t) \sum_{k=1}^{K} \psi(t)_n \right). \]  

(66)

As a result, (58) can be rewritten as follows:

\[ E[F(w^{(t+1)})] \leq E[F(w^{(t)})] - \frac{1}{2L} \|\nabla F(w^{(t)})\|^2 + 2\rho \frac{\|\nabla F(w^{(t)})\|^2}{L} \sum_{n=1}^{N} \beta_n \left(1 - S(t) \sum_{k=1}^{K} \psi(t)_n \right). \]  

(67)

By subtracting \( E[F(w^*)] \) in both sides of the above function, the following inequality can be obtained from (37) and (38):

\[ \|\nabla F(w^{(t)})\|^2 \geq 2\mu \|F(w^{(t)}) - F(w^*)\|. \]  

(69)

Hence, (68) can be rewritten as follows:

\[ E[F(w^{(t+1)}) - F(w^*)] \leq \left(1 - \frac{\mu}{L}\right) E[F(w^{(t)}) - F(w^*)] + 2\rho \frac{\|\nabla F(w^{(t)})\|^2}{L} \sum_{n=1}^{N} \beta_n \left(1 - S(t) \sum_{k=1}^{K} \psi(t)_n \right). \]  

(70)

Similarly, the convergence of successive rounds is given by

\[ E[F(w^{(t+1)}) - F(w^*)] \leq \left(1 - \frac{\mu}{L}\right)^2 E[F(w^{(t-1)}) - F(w^*)] + \left(1 - \frac{\mu}{L}\right) 2\rho \frac{\|\nabla F(w^{(t-1)})\|^2}{L} \sum_{n=1}^{N} \beta_n \left(1 - S(t-1) \sum_{k=1}^{K} \psi(t-1)_n \right) + 2\rho \frac{\|\nabla F(w^{(t)})\|^2}{L} \sum_{n=1}^{N} \beta_n \left(1 - S(t) \sum_{k=1}^{K} \psi(t)_n \right). \]  

(71)

As a result, we derive the upper bound of the convergence rate as

\[ E[F(w^{(t+1)}) - F(w^*)] \leq \left(1 - \frac{\mu}{L}\right) \frac{t}{L} E[F(w^{(1)}) - F(w^*)] + \frac{2\rho}{L} \sum_{i=1}^{t} \left(1 - \frac{\mu}{L}\right)^{t-i} \|\nabla F(w^{(i)})\|^2 \sum_{n=1}^{N} \beta_n \left(1 - S(i) \sum_{k=1}^{K} \psi(i)_n \right), \]  

(72)

which completes the proof.

REFERENCES

[1] S. Savazzi, M. Nicoli, M. Bennis, S. Kianoush, and L. Barbieri, “Opportunities of federated learning in connected, cooperative, and automated industrial systems,” IEEE Commun. Mag., vol. 59, no. 2, pp. 16–21, Feb. 2021.

[2] M. Chen et al., “Distributed learning in wireless networks: Recent progress and future challenges,” IEEE J. Sel. Areas Commun., vol. 39, no. 12, pp. 3579–3605, Dec. 2021.

[3] Z. Qin, G. Y. Li, and H. Ye, “Federated learning and wireless communications,” IEEE Wireless Commun., vol. 28, no. 5, pp. 134–140, Oct. 2021.

[4] W. Xia, W. Wen, K.-K. Wong, T. Q. Quek, J. Zhang, and H. Zhu, “Federated-learning-based client scheduling for low-latency wireless communications,” IEEE Wireless Commun., vol. 28, no. 2, pp. 32–38, Apr. 2021.

[5] M. Chen, H. V. Poor, W. Saad, and S. Cui, “Wireless communications for collaborative federated learning,” IEEE Commun. Mag., vol. 58, no. 12, pp. 48–54, Dec. 2020.

[6] L. Qiao, Z. Gao, M. B. Mashhadi, and D. Gündüz, “Massive digital over-the-air computation for communication-efficient federated edge learning,” 2024, doi: 10.1109/JSAC.2024.3431572.

[7] J. Konečný, H. B. McMahan, D. Ramage, and P. Richtárik, “Federated optimization: Distributed machine learning on off-device intelligence,” 2016, arXiv:1610.02527.

[8] S. Wang et al., “Adaptive federated learning in resource constrained edge computing systems,” IEEE J. Sel. Areas Commun., vol. 37, no. 6, pp. 1205–1221, Jun. 2019.

[9] M. Chen, Z. Yang, W. Saad, C. Yin, H. V. Poor, and S. Cui, “A joint learning and communications framework for federated learning over wireless networks,” IEEE Trans. Wireless Commun., vol. 20, no. 1, pp. 269–283, Jan. 2021.

[10] R. Hamdi, M. Chen, A. B. Saidi, M. Qaraqe, and H. V. Poor, “Federated learning over energy harvesting wireless networks,” IEEE Internet Things J., vol. 9, no. 1, pp. 92–103, Jan. 2022.

[11] S. Liu, G. Yu, R. Yin, J. Yuan, L. Shen, and C. Liu, “Joint model pruning and device selection for communication-efficient federated edge learning,” IEEE Trans. Commun., vol. 70, no. 1, pp. 231–244, Jan. 2022.

[12] M. Chen, H. V. Poor, W. Saad, and S. Cui, “Convergence time optimization for federated learning over wireless networks,” IEEE Trans. Wireless Commun., vol. 20, no. 4, pp. 2457–2471, Apr. 2021.

[13] T. T. Vu, D. T. Ngo, N. H. Tran, H. Q. Ngo, M. N. Dao, and R. H. Middleton, “Cell-free massive MIMO for wireless federated learning,” IEEE Trans. Wireless Commun., vol. 19, no. 10, pp. 6377–6392, Oct. 2020.

[14] D. Chen et al., “Matching-theory-based low-latency scheme for multitask federated learning in MEC networks,” IEEE Internet Things J., vol. 8, no. 14, pp. 11415–11426, Jul. 2021.

[15] Z. Li, L. Chen, N. Zhao, Y. Chen, G. Wei, and F. R. Yu, “Computation offloading for edge-assisted federated learning,” IEEE Trans. Veh. Technol., vol. 70, no. 9, pp. 9330–9344, Sep. 2021.

[16] H. T. Nguyen, H. V. Poor, and M. Chiang, “Contextual model aggregation for fast and robust federated learning in edge computing,” 2022, arXiv:2203.12738.

[17] H. Imani, J. Anderson, and T. El-Ghzawi, “iSample: Intelligent client coupling in federated learning,” in 2022 IEEE 6th Int. Conf. Fog Edge Compute., 2022, pp. 58–65.

[18] W. Shi, S. Zhou, Z. Niu, M. Jiang, and L. Geng, “Joint device scheduling and resource allocation for latency constrained wireless federated learning,” IEEE Trans. Wireless Commun., vol. 20, no. 1, pp. 453–467, Jan. 2021.

[19] R. D. Yates, Y. Sun, D. R. Brown, S. K. Kaul, E. Modiano, and S. Ulukus, “Age of information: A new dimension in survey,” IEEE J. Sel. Areas Commun., vol. 39, no. 5, pp. 1183–1210, May 2021.

[20] H. H. Yang, A. Arafa, T. Q. S. Quek, and H. Vincent Poor, “Age-based scheduling policy for federated learning in mobile edge networks,” in 2020 IEEE Int. Conf. Acoust. Speech Signal Process., 2020, pp. 8743–8747.

[21] K. Wang, Z. Ding, D. K. C. So, and Z. Ding, “Age-of-information minimization in federated learning based networks with Non-IID dataset,” IEEE Trans. Wireless Commun., vol. 23, no. 8, pp. 8939–8953, Aug. 2024.

[22] K. Wang, F. Fang, D. B. d. Costa, and Z. Ding, “Sub-channel scheduling, task assignment, and power allocation for OMA-based and NOMA-based MEC systems,” IEEE Trans. Commun., vol. 69, no. 4, pp. 2692–2708, Apr. 2021.

[23] M. B. Mashhadi, M. Mahdavimoghadam, R. Tafazoli, and W. Saad, “Collaborative learning with a drone orchestrator,” IEEE Trans. Veh. Technol., vol. 73, no. 1, pp. 637–650, Jan. 2024.

[24] S. Zhang et al., “Federated learning in intelligent transportation systems: Recent applications and open problems,” IEEE Trans. Intell. Transp. Syst., vol. 25, no. 5, pp. 3259–3285, May 2024.

[25] R. Zhang, J. Hao, H. Wang, B. Li, X. Cheng, and L. Yang, “A survey on federated learning in intelligent transportation systems,” IEEE Trans. Intell. Veh., early access, Aug. 20, 2024, doi: 10.1109/TIV.2024.3446319.
W. Dai, Y. Zhou, N. Dong, H. Zhang, and E. Xing, “Toward understanding the impact of staleness in distributed machine learning,” in *Proc. Int. Conf. Learn. Representations*, 2019, pp. 1–20.

R. Talak, S. Karaman, and E. Modiano, “Improving age of information in wireless networks with perfect channel state information,” *IEEE/ACM Trans. Netw.*, vol. 28, no. 4, pp. 1765–1778, Aug. 2020.

J. Ren, Y. He, D. Wen, G. Yu, K. Huang, and D. Guo, “Scheduling for cellular federated edge learning with importance and channel awareness,” *IEEE Trans. Wireless Commun.*, vol. 19, no. 11, pp. 7690–7703, Nov. 2020.

Z. Yang, M. Chen, W. Saad, C. S. Hong, and M. Shikh-Bahaei, “Energy efficient federated learning over wireless communication systems,” *IEEE Trans. Wireless Commun.*, vol. 20, no. 3, pp. 1935–1949, Mar. 2021.

K. Wang, Z. Ding, D. K. So, and Z. Ding, “Exploring age-of-information weighting in federated learning under data heterogeneity,” 2024, arXiv:2405.15978.

Z. Han, *Game Theory in Wireless and Communication Networks: Theory, Models, and Applications*. Cambridge, U.K.: Cambridge Univ. Press, 2012.

T. Basar and G. J. Olsder, *Dynamic Noncooperative Game Theory*, vol. 23. Philadelphia, PA, USA: Siam, 1999.

H. Tuy, “Monotonic optimization: Problems and solution approaches,” *SIAM J. Optim.*, vol. 11, no. 2, pp. 464–494, 2000.

J. Wang, Q. Liu, H. Liang, G. Joshi, and H. V. Poor, “Tackling the objective inconsistency problem in heterogeneous federated optimization,” in *Proc. Automata Lang. Program. 26th Int. Colloq.*, Prague, Czech Republic, Springer, 2002, pp. 443–452.

A. Roth and M. Sotomayor, “Two-sided matching,” in *Handbook Game Theory Econ. Appl.*, vol. 1, pp. 485–541, 1992.

E. Bodine-Baron, C. Lee, A. Chong, B. Hassibi, and A. Wierman, “Peer effects and stability in matching markets,” in *Proc. Int. Symp. Algorithmic Game Theory*, Springer, 2011, pp. 117–129.

D. Ray, *A Game-Theoretic Perspective on Coalition Formation*. Oxford, U.K.: Oxford Univ. Press, 2007.

J. Wang, Q. Liu, H. Liang, G. Joshi, and H. V. Poor, “Tackling the objective inconsistency problem in heterogeneous federated optimization,” in *Proc. Adv. Neural Inf. Process. Syst.*, 2020, vol. 33, pp. 7611–7623.

T. Li, A. K. Sahu, M. Zaheer, M. Sanjabi, A. Talwalkar, and V. Smith, “Federated optimization in heterogeneous networks,” in *Proc. Mach. Learn. Syst.*, 2020, vol. 2, pp. 429–450.

J. Hamer, M. Mohri, and A. T. Suresh, “Fedboost: A communication-efficient algorithm for federated learning,” in *Proc. Int. Conf. Mach. Learn.*, 2020, pp. 3973–3983.

D. Wen, K.-J. Jeon, and K. Huang, “Federated dropout—A simple approach for enabling federated learning on resource constrained devices,” *IEEE Wireless Commun. Lett.*, vol. 11, no. 5, pp. 923–927, May 2022.

S. Samarakoon, M. Bennis, W. Saad, and M. Debbah, “Distributed federated learning for ultra-reliable low-latency vehicular communications,” *IEEE Trans. Commun.*, vol. 68, no. 2, pp. 1146–1159, Feb. 2020.

S. Boyd and L. Vandenberghe, *Convex Optimization*. Cambridge, U.K.: Cambridge Univ. Press, 2004.

Yi Ma (Senior Member, IEEE) is currently a Chair Professor with the Institute for Communication Systems (ICS), University of Surrey, Guildford, U.K. He has authored or coauthored more than 200 peer-reviewed IEEE journals and conference papers in the areas of deep learning, cooperative communications, cognitive radios, interference utilization, cooperative localization, radio resource allocation, multiple-input multiple-output, estimation, synchronization, and modulation and detection techniques. He holds ten international patents in the areas of spectrum sensing and signal modulation and detection. He was the Tutorial Chair for EuroWireless2013, PIMRC2014, and CAMAD2015. He was the Founder of the Crowd-Net Workshop in conjunction with ICC’15, ICC’16, and ICC’17. He is the Co-Chair of the Signal Processing for Communications Symposium in ICC’19.

Mahdi Boloursaz Mashhadi (Senior Member, IEEE) received the B.S., M.S., and Ph.D. degrees in mobile telecommunications from the Sharif University of Technology (SUT), Tehran, Iran, in 2011, 2013, and 2018, respectively. He was a Visiting Research Associate with the University of Central Florida, Orlando, FL, USA, in 2018, and Queen’s University, Kingston, ON, Canada, in 2017. He was a Panel Judge for the International Telecommunication Union (ITU) to evaluate innovative submissions on applications of AI/ML in 5G and beyond wireless networks since 2021. He is currently a Lecturer with the 5G/6G Innovation Centre (5G/6GIC), Institute for Communication Systems (ICS), University of Surrey (UoS), Guildford, U.K. Prior to joining ICS, he was a Postdoctoral Research Associate with the Intelligent Systems and Networks (ISN) Research Group, Imperial College London, London, U.K., from 2019 to 2021. He has more than 40 peer reviewed publications and patents in the areas of wireless communications, machine learning, and signal processing. He was the recipient of the Best Paper Award from the IEEE EWDTS 2012 conference, and the Exemplary Reviewer Award from the IEEE ComSoc in 2021 and 2022. He is an Associate Editor for the Springer Nature Wireless Personal Communications Journal.

Chuan Heng Foh (Senior Member, IEEE) received the M.Sc. degree from Monash University, Melbourne, VIC, Australia, in 1999, and the Ph.D. degree from the University of Melbourne, Melbourne, in 2002. After the Ph.D. degree, he spent six months as a Lecturer with Monash University. In 2002, he joined Nanyang Technological University, Singapore, as an Assistant Professor till 2012. He is currently a Senior Lecturer with the University of Surrey, Guildford, U.K. He has authored or coauthored more than 180 refereed papers in international journals and conferences. His research interests include protocol design, machine learning application, and performance analysis of various computer networks, including wireless local area networks, mobile ad-hoc and sensor networks, vehicular networks, the Internet of Things, 5G/6G networks, and open RAN. He was the Vice Chair (Europe/Africa) for IEEE TCGCC in 2015 and 2017. He is currently the Vice-Chair of the IEEE VTS Ad Hoc Committee on Mission Critical Communications.
Rahim Tafazolli (Senior Member, IEEE) is currently a Regius Professor of electronic engineering, a Professor of mobile and satellite communications, the Founder and Director of 5GIC, 6GIC, and ICS (Institute for Communication System) with the University of Surrey. He has more than 40 years of experience in digital communications research and teaching. He has authored or coauthored more than 1000 research publications and is regularly invited to deliver keynote talks and distinguished lectures to international conferences and workshops. He was the Leader of study on “grand challenges in IoT” (Internet of Things) in the U.K., during 2011–2012, for RCUK (Research Council U.K.) and the U.K. TSB (Technology Strategy Board). He was also awarded the 28th KIA Laureate Award- 2015 for his contribution to communications technology. He holds Fellowship of Royal Academy of Engineering (FREng), Institute of Engineering and Technology (FIET), as well as that of Wireless World Research Forum. He is the Editor of two books on Technologies for Wireless Future (Wiley) vol. 1, in 2004 and vol. 2, in 2006.

Zhi Ding (Fellow, IEEE) received the Ph.D. degree in electrical engineering from Cornell University, Ithaca, NY, USA, in 1990. He is currently with the Department of Electrical and Computer Engineering, University of California (UC), Davis, CA, USA, where he holds the position of a Distinguished Professor. From 1990 to 2000, he was a Faculty Member of Auburn University, Auburn, AL, USA and later, University of Iowa, Iowa City, IA, USA. Prof. Ding joined the College of Engineering, UC Davis in 2000. His research interests and expertise include wireless networking, communications, signal processing, multimedia, and learning. Prof. Ding supervised more than 30 Ph.D. dissertations since joining UC Davis. His research team of enthusiastic researchers works very closely with industry to solve practical problems and contributes to technological advances. His team has collaborated with researchers around the world and welcomes self-motivated young talents as new members. Prof. Ding was the Chief Information Officer and Chief Marketing Officer of the IEEE Communications Society. He was an Associate Editor for IEEE TRANSACTIONS ON SIGNAL PROCESSING from 1994 to 1997 and from 2001 to 2004, and Associate Editor of IEEE SIGNAL PROCESSING LETTERS from 2002 to 2005. He was a Member of technical committee on Statistical Signal and Array Processing and a member of technical committee on Signal Processing for Communications during 1994–2003. Dr. Ding was the General Chair of the 2016 IEEE International Conference on Acoustics, Speech, and Signal Processing and the Technical Program Chair of the 2006 IEEE Globecom. He was also an IEEE Distinguished Lecturer (Circuits and Systems Society, 2004–2006, Communications Society, 2008–2009). He served on as IEEE Transactions on Wireless Communications Steering Committee Member during 2007–2009 and its Chair during 2009–2010. Dr. Ding is the co-author of the textbook: Modern Digital and Analog Communication Systems, 5th edition, Oxford University Press, 2019. Prof. Ding was the recipient of the IEEE Communication Society’s WTC Award in 2012 and the IEEE Communication Society’s Education Award in 2020.