Modular Information Flow through Ownership

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Abstract

Statically analyzing information flow, or how data influences other data within a program, is a challenging task in imperative languages. Analyzing pointers and mutations requires access to a program’s complete source. However, programs often use pre-compiled dependencies where only type signatures are available. We demonstrate that ownership types can be used to soundly and precisely analyze information flow through function calls given only their type signature. From this insight, we built Flowistry, a system for analyzing information flow in Rust, an ownership-based language. We prove the system’s soundness as a form of noninterference using the Oxide formal model of Rust. Then we empirically evaluate the precision of Flowistry, showing that modular flows are identical to whole-program flows in 94% of cases drawn from large Rust codebases. We illustrate the applicability of Flowistry by using it to implement prototypes of a program slicer and an information flow control system.

CCS Concepts: • Software and its engineering → Automated static analysis.

Keywords: information flow, ownership types, rust

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1 Introduction

Information flow describes how data influences other data within a program. Information flow has applications to security, such as information flow control [31], and to developer tools, such as program slicing [37]. Our goal is to build a practical system for analyzing information flow, meaning:

1. Applicable to common language features: the language being analyzed should support widely used features like pointers and in-place mutation.
2. Zero configuration to run on existing code: the analyzer must integrate with an existing language and existing unannotated programs. It must not require users to adopt a new language designed for information flow.
3. No dynamic analysis: to reduce integration challenges and costs, the analyzer must be purely static — no modifications to runtimes or binaries are needed.
4. Modular over dependencies: programs may not have source available for dependencies. The analyzer must have reasonable precision without whole-program analysis.

As a case study on the challenges imposed by these requirements, consider analyzing the information that flows to the return value in this C++ function:

```c++
1 // Copy elements 0 to max into a new vector
2 vector<int> copy_to(vector<int>& v, size_t max) {
3 vector<int> v2; size_t i = 0;
4 for (auto x(v.begin()); x != v.end(); ++x) {
5 if (i == max) { break; }
6 v2.push_back(*x); ++i;
7 }
8 return v2;
9 }
```

Here, a key flow is that v2 is influenced by v: (1) push_back mutates v2 with *x as input, and (2) x points to data within v. But how could an analyzer statically deduce these facts? For C++, the answer is by looking at function implementations. The implementation of push_back mutates v2, and the implementation of begin returns a pointer to data in v.

However, analyzing such implementations violates our fourth requirement, since these functions may only have their type signature available. In C++, given only a function’s type signature, not much can be inferred about its behavior, since the type system does not contain information relevant to pointer analysis.

Our key insight is that ownership types can be leveraged to modularly analyze pointers and mutation using only a function’s type signature. Ownership has emerged from several intersecting lines of research on linear logic [16], class-based alias management [8], and region-based memory management [18]. The fundamental law of ownership is that data
cannot be simultaneously aliased and mutated. Ownership-based type systems enforce this law by tracking which entities own which data, allowing ownership to be transferred between entities, and flagging ownership violations like mutating immutably-borrowed data.

Today, the most popular ownership-based language is Rust. Consider the information flows in this Rust implementation of copy_to:

```rust
fn copy_to(v: &Vec<i32>, max: usize) -> Vec<i32> {
    let mut v2 = Vec::new();
    for (i, x) in v.iter().enumerate() {
        if i == max {
            break;
        }
        v2.push(*x);
    }
    return v2;
}
```

To determine that push mutates v2, we leverage mutability modifiers. All references in Rust are either immutable (i.e. the type is `&T`) or mutable (the type is `&mut T`). Therefore `v.iter()` as input (excepting interior mutability, discussed in Section 4.3), while push may mutate v2 because it takes `&mut self` as input.

To determine that `i` points to `v`, we leverage lifetimes. All references in Rust are annotated with a lifetime, either explicitly (such as `&a`) or implicitly. Shared lifetimes indicate aliasing: because `&mut self` in `iter` has lifetime `&a`, and because the returned `Iter` structure shares that lifetime, then we can determine that `Iter` may contain pointers to `self`.

Inspired by this insight, we built Flowistry, a system for analyzing information flow in the safe subset of Rust programs. Flowistry satisfies our four design criteria: (1) Rust supports pointers and mutation, (2) Flowistry does not require any change to the Rust language or to Rust programs, (3) Flowistry is a purely static analysis, and (4) Flowistry uses ownership types to analyze function calls without needing their definition. This paper presents a theoretical and empirical investigation into Flowistry in five parts:

1. We provide a precise description of how Flowistry computes information flow by embedding its definition within Oxide [38], a formal model of Rust (Section 2).
2. We prove the soundness of our information flow analysis as a form of noninterference (Section 3).
3. We describe the implementation of Flowistry that bridges the theory of Oxide to the practicalities of Rust (Section 4).
4. We evaluate the precision of the modular analysis on a dataset of large Rust codebases, finding that modular flows are identical to whole-program flows in 94% of cases, and are on average 7% larger in the remaining cases (Section 5).
5. We demonstrate the utility of Flowistry by using it to prototype a program slicer and an IFC checker (Section 6).

We conclude by presenting related work (Section 7) and discussing future directions for Flowistry (Section 8). Due to space constraints, we omit many formal details, all auxiliary lemmas, and all proofs. The interested reader can find them in Appendix A. Flowistry and our applications of it are publicly available, open-source, MIT-licensed projects at https://github.com/willcrichton/flowistry.

## 2 Analysis

Inspired by the dependency calculus of Abadi et al. [1], our analysis represents information flow as a set of dependencies for each variable in a given function. The analysis is flow-sensitive, computing a different dependency set at each program location, and field-sensitive, distinguishing between dependencies for fields of a data structure.

While the analysis is implemented in and for Rust, our goal here is to provide a description of it that is both concise (for clarity of communication) and precise (for amenability to proof). We therefore base our description on Oxide [38], a formal model of Rust. At a high level, Oxide provides three ingredients:

1. A syntax of Rust-like programs with expressions $e$ and types $\tau$.
2. A type-checker, expressed with the judgment $\Sigma; \Delta; \Gamma + e : \tau \Rightarrow \Gamma'$ using the contexts $\Gamma$ for types and lifetimes, $\Delta$ for type variables, and $\Sigma$ for global functions.
3. An interpreter, expressed by a small-step operational semantics with the judgment $\Sigma + (\sigma; e) \rightarrow (\sigma'; e')$ using $\sigma$ for a runtime stack.

We extend this model by assuming that each expression in a program is automatically labeled with a unique location $\ell$. Then for a given expression $e$, our analysis computes the set of dependencies $\kappa := \{\ell\}$. Because expressions have effects on persistent memory, we further compute a dependency context $\Theta := \{\vec{p} \mapsto \kappa\}$ from memory locations $p$ to dependencies $\kappa$. The computation of information flow is intertwined with type-checking, represented as a modified type-checking judgment (additions highlighted in red):

$$\Sigma; \Delta; \Gamma; \Theta + e_{\ell} : \tau \Rightarrow \Gamma'; \Theta'$$

This judgment is read as, “with type contexts $\Sigma, \Delta, \Gamma$ and dependency context $\Theta, e$ at location $\ell$ has type $\tau$ and dependencies $\kappa$, producing a new dependency context $\Theta'$.”

Oxide is a large language — describing every feature, judgment, and inference rule would exceed our space constraints. Instead, in this section we focus on a few key rules that demonstrate the novel aspects of our system. We first lay the foundations for dealing with variables and mutation (Section 2.1), and then describe how we modularly analyze references (Section 2.2) and function calls (Section 2.3). The remaining rules can be found in Appendix A.1.
2.1 Variables and mutation

The core of Oxide is an imperative calculus with constants and variables. The abstract syntax for these features is below:

Variable $x$  
Path $q := e | n.q$  
Place $π := x.q$  
Constant $c := () | n | \text{true} | \text{false}$  
Base Type $r^b := \text{unit} | u32 | \text{bool}$  
Sized Type $r^s := r^b | (r^s_1, \ldots, r^s_n) | \ldots$  
Expression $e := c | π | \text{let} x : r^s_a = e_1; e_2 | π := e | e_1; e_2 | \ldots$

Constants are Oxide’s atomic values and also the base-case for information flow. A constant’s dependency is simply itself, expressed through the T-let rule:

T-let $\Sigma; \Delta; Γ; Θ ⊢ n \cdot t : u32 \Rightarrow Γ; Θ$

Variables and mutation are introduced through let-bindings and assignment expressions, respectively. For example, this (location-annotated) program mutates a field of a tuple:

\[
\text{let } t : (u32, u32) = (1_{t_1}, 2_{t_2}); t.1 := 3_{t_1}
\]

Here, $t$ is a variable and $t.1$ is a place, or a description of a specific region in memory. For information flow, the key idea is that let-bindings introduce a set of places into $Θ$, and then assignment expressions change a place’s dependencies within $Θ$. In the above example, after binding $t$, then $Θ$ is:

\[
Θ = \{ t.1, 0 \cdot t.1 \Rightarrow \{ t_1, t_2 \} \}
\]

After checking “$t.1 := 3$”, then $t_1$ is added to $Θ(t)$ and $Θ(t.1)$, but not $Θ(t.0)$. This is because the values of $t.1$ and $t.0$ have changed, but the value of $t.0$ has not. Formally, the let-binding rule is:

T-Let $\Sigma; \Delta; Γ; Θ ⊢ e_1 : r^s_1 \Rightarrow Γ_1; Θ$  
$Γ_1; Γ_1 ⊢ r^s_a \Rightarrow Γ'_1; Θ'_1  
Σ; Δ; gc-loans(Γ'_1, x : r^s_a); Θ'_1 ⊢ e_2 : r^s_2 \Rightarrow Γ_2; x : r^s_0; Θ_2$

Again, this rule (and many others) contain aspects of Oxide that are not essential for understanding information flow such as the subtyping judgment $r_1 ≤ r_2$ or the metafunction gc-loans. For brevity we will not cover these aspects here, and instead refer the interested reader to Weiss et al. [38]. We have demphasized (in grey) the judgments which are not important to understanding our information flow additions.

The key concept is the formula $Θ_1[∀π = π^D[x] . π → \kappa_1]$. This introduces two shorthands: first, $π^D[x]$ means “a place $π$ with root variable $x$ in a context $π^∗$”, used to decompose a place. In T-Let, the update to $Θ_1$ happens for all places with a root variable $x$. Second, $Θ_1[π → \kappa_1]$ means “set $π$ to $κ_1$ in $Θ_1$”. So this rule specifies that when checking $e_2$, all places within $x$ are initialized to the dependencies $κ_1$ of $e_1$.

Next, the assignment expression rule is defined as updating all the conflicts of a place $π$:

T-assign $\Sigma; Δ; Γ; Θ ⊢ e : r^s \Rightarrow Γ_1; Θ$

\[
Γ_1(π) = \text{letprov}(π) \Rightarrow Γ_1 ; Γ_1 ⊢ \text{uniq} \Rightarrow \{ \text{uniq} \} \]

\[
Δ; Γ ⊢ π ⇒ t^s \Rightarrow Γ_2 ; Θ_2
\]

If you conceptualize a type as a tree and a path as a node in that tree, then a node’s conflicts are its ancestors and descendants (but not siblings). Semantically, conflicts are the set of places whose value change if a given place is mutated. Recall from the previous example that $t.1$ conflicts with $t$ and $t.1$, but not $t.0$. Formally, we say two places are disjoint (\#) or conflict (\&) when:

\[
x_1.q_1 ≠ x_2.q_2 \iff (∀q_1 \cdot q_1 \text{ is not a prefix of } q_2) ∧ (∀q_2 \cdot q_2 \text{ is not a prefix of } q_1)
\]

Then to update a place’s conflicts in $Θ$, we define the metafunction update-conflicts to add $κ$ to all conflicting places $p'$. (Note that this rule is actually defined over place expressions $p$, which are explained in the next subsection.)

update-conflicts ($Θ, p, κ$) $\equiv \forall p' \cdot κ_{p'} \in Θ_{cfl} . p' \mapsto κ_{p'} \cup κ$

where $Θ_{cfl} = \{ p' \mapsto κ_{p'} \in Θ | p \cap p' \}$

Finally, the rule for reading places is simply to look up the place’s dependencies in $Θ$:

T-Move $\Delta; Γ ⊢ π ⇒ t^s \Rightarrow Γ[π ↦ t^s]; Θ$

Σ; Δ; Γ; Θ ⊢ π : r^s \Rightarrow Γ(π) = t^s \text{ noncopyable}_{cfl} r^s$

2.2 References

Beyond concrete places in memory, Oxide also contains references that point to places. As in Rust, these references have both a lifetime (called a “provenance”) and a mutability qualifier (called an “ownership qualifier”). Their syntax is:

Concrete Provenance $r$  
Abstract Provenance $q$

Place Expression $p := x \mid *p \mid p.n$

Provenance $p := q \mid r$

Ownership Qualifier $ω := \text{shrd} \mid \text{uniq}$

Sized Type $r^s := \ldots \mid &r \omega r^s$

Expression $e := \ldots \mid &r \omega p \mid p := e \mid \text{letprov}(r) e$
Provenances are created via a `letprov` expression, and references are created via a borrow expression `& r . p` that has an initial concrete provenance `r` (abstract provenances are just used for types of function parameters). References are used in conjunction with place expressions `p` that are places whose paths contain dereferences. For example, this program creates, reborrows, and mutates a reference:

```rust
letprov(r_1, r_2, r_3, r_4)
let x : (u32, u32) = (0, 0);
let y : &r_2 uniq (u32, u32) = &r_1 uniq x;
let z : &r_4 uniq u32 = &r_3 uniq (*y).1;
```

Consider the information flow induced by `*z := 1_t`. We need to compute all places that `z` could point-to, in this case `x.1`, so `t` can be added to the conflicts of `x.1`. Essentially, we must perform a pointer analysis [33].

The key idea is that Oxide already does a pointer analysis! Performing one is an essential task in ensuring ownership-safety. All we have to do is extract the relevant information with Oxide’s existing judgments. This is represented by the information flow extension to the reference-mutation rule:

```
T-AssignDeref
Σ; Δ; Γ; Θ ⊢ e : r^SI_0 ⊕ k ⇒ Γ_1; Θ_1
Δ; Γ_1 ⊢ uniq p : r^SI_0
Δ; Γ; Θ ⊢ p ⇒ l \{ l \}
\[ Θ_2 = Θ_1[∀∗p′ ∈ l \{ l \}. update-conflicts (Θ_1, p′, k)] \]
Σ; Δ; Γ; Θ ⊢ p := e : unit ⊕ \emptyset ⇒ Γ′ ⊢ p; Θ_2
```

5. Then `Γ(r_4) = Γ(r_3)` because `r_3 := r_4`.

6. Finally, the loan set for `*z` is:

```
Γ(r_4) ∪ \{uniq(*z)\} = \{uniq.x.1, uniq(*y).1, uniq(*z)\}
```

Applying this concept to the `T-AssignDeref` rule, we compute information flow for reference-mutation as: when mutating `p` with loans `l`, add `k_e` to all the conflicts for every loan `uniq p′ ∈ l`.

### 2.3 Function calls

Finally, we examine how to modularly compute information flow through function calls, starting with syntax:

```
Type Var α   Frame Var ϕ
Expression e ::= \ldots | f<Φ(p), τ(π)> (π)
```

Global Entry `ε ::= fn f<Φ(p), τ(π), ϕ, q_1 : (q_2)>(x : t^SI_0) → t^SI_1 \{ e \}
```

Global Env. `Σ ::= \ldots | Σ, ε`

Oxide functions are parameterized by frame variables `ϕ` (for closures), abstract provenances `q` (for provenance polymorphism), and type variables `α` (for type polymorphism). Unlike Oxide, we restrict to functions with one argument for simplicity in the formalism. Calling a function `f` requires an argument `π` and any type-level parameters `Φ, ρ, r`.

The key question is: without inspecting its definition, what is the *most precise* assumption we can make about a function’s information flow while still being sound? By "precise" we mean "if the analysis says there is a flow, then the flow actually exists", and by "sound" we mean "if a flow actually exists, then the analysis says that flow exists." For example consider this program:

```
fn f(q_1, q_2)(x : (&q_1 uniq u32, &q_2 shr u32)){ ??? }
let x : u32 = 1_t; let y : u32 = 2_t;
letprov(r_1, r_2)
```

First, what can `f(t)` mutate? Any data behind a shared reference is immutable, so only `*t.0` could possibly be mutated, not `*t.1`. More generally, the argument’s transitive mutable references must be assumed to be mutated.

Second, what are the inputs to the mutation of `*t.0`? This could theoretically be any possible value in the input, so both `*t.0` and `*t.1`. More generally, every transitive readable place from the argument must be assumed to be an input to the mutation. So in this example, a modular analysis of the
information flow from calling cp would add \{t_1, t_2\} to Θ(x) but not Θ(y).

To formalize these concepts, we first need to describe the transitive references of a place. The \(ω\)-refs\((p, r)\) meta-function computes a place expression for every reference accessible from \(p\). If \(ω = \text{uniq}\) then this just includes unique references, otherwise it includes unique and shared ones.

\[
\omega\text{-refs}(p, r^\|) = \emptyset
\]

\[
\omega\text{-refs}(p, (r_1^\|, \ldots, r_i^\|)) = \bigcup_i \omega\text{-refs}(p, r_i^\|)
\]

\[
\omega\text{-refs}(p, \& \omega, r^\|_x) = \begin{cases} \{p\} \cup \omega\text{-refs}(p, r^\|_x) & \text{if } ω \leq ω' \\ \emptyset & \text{otherwise} \end{cases}
\]

Here, \(ω \leq ω'\) means “a loan at \(ω\) can be used as a loan at \(ω'\),” defined as uniq \(≤\) shr and \(ω \leq ω'\) otherwise. Then \(ω\)-loans\((p, r, Δ, Γ)\) can be defined as the set of concrete places accessible from those transitive references:

\[
\omega\text{-loans}(p, r, Δ, Γ) \overset{\text{def}}{=} \bigcup_{p_1, \omega\text{-refs}(p, r)} \{p_2 | ωp_2 \in \{I\}\} \quad \text{where } Δ; Γ r_ω p_1 ⇒ \{I\}
\]

Finally, the function application rule can be revised to include information flow as follows:

\[
\text{T-App} \quad \frac{Σ; Δ; Γ \vdash \Phi \quad Δ; Γ \vdash ρ \quad Σ; Δ; Γ + τ^\|}{Σ; Δ; Γ + τ^\|}
\]

\[
Σ(f) = f n(\overset{\text{def}}{=} f n(Φ, Γ, ρ, p_1, q_2)]](x: r^\|_q) \rightarrow r^\|_q)
\]

\[
Σ; Δ; Γ \vdash π: r^\|_q → r^\|; \bullet \kappa ⇒ Γ; Θ
\]

\[
Δ; Γ + q_2[[p\|_q] → q_1[[p\|_q] ⇒ Γ_2,
\]

\[
κ_\text{arg} = \kappa \cup \bigcup_{p \in \text{shrd-loans}(π, r^\|, Δ; Γ)} Θ(p)
\]

\[
Θ' = Θ[∀ p \in \text{uniq-loans}(π, r^\|, Δ; Γ, Γ_2)]
\]

\[
\text{update-conflicts } (Θ, p, κ_\text{arg})\]

\[
Σ; Δ; Γ; Θ \vdash f(Φ, Γ, ρ, r^\|) = Σ([f(Φ, Γ, ρ, r^\|)](x: r^\|_q) \rightarrow r^\|_q) \bullet κ_\text{arg} ⇒ Γ_2; Θ'
\]

The collective dependencies of the input π are collected into κarg. Then every unique reference is updated with κarg. Additionally, the function’s return value is assumed to be influenced by any input, and so has dependencies κarg.

Note that this rule does not depend on the body \(e\) of the function \(f\), only its type signature in \(Σ\). This is the key to the modular approximation. Additionally, it means that this analysis can trivially handle higher-order functions. If \(f\) were a parameter to the function being analyzed, then no control-flow analysis is needed to guess its definition.

3 Soundness

To characterize the correctness of our analysis, we seek to prove its soundness: if a true information flow exists in a program, then the analysis computes that flow. The standard soundness theorem for information flow systems is noninterference [17]. At a high level, noninterference states that for a given program and its dependencies, and for any two execution contexts, if the dependencies are equal between contexts, then the program will execute with the same observable behavior in both cases. For our analysis, we focus just on values produced by the program, instead of other behaviors like termination or timing.

To formally define noninterference within Oxide, we first need to explore Oxide’s operational semantics. Oxide programs are executed in the context of a stack of frames that map variables to values:

\[
\text{Stack } σ ::= \bullet | σ \circ \varsigma
\]

\[
\text{Stack Frame } \varsigma ::= \bullet | σ, x \mapsto v
\]

For example, in the empty stack \(\bullet\), the expression "let \(x : u32 = 1; \ x := 2\)" would first add \(x \mapsto 1\) to the stack. Then executing \(x := 2\) would update \(σ(x) = 2\). More generally, we use the shorthand \(σ(p)\) to mean “reduce \(p\) to a concrete location \(τ\), then look up the value of \(π\) in \(σ\).”

The key ingredient for noninterference is the equivalence of dependencies between stacks. That is, for two stacks \(σ_1\) and \(σ_2\) and a set of dependencies \(κ\) in a context \(Θ\), we say those stacks are the same up to \(κ\) if all \(p\) with \(Θ(p) \subseteq κ\) are the same between stacks. Formally, the dependencies of \(κ\) and equivalence of heaps are defined as:

\[
\text{deps}(Θ, κ) \overset{\text{def}}{=} \{p \mid p \mapsto κ_p \in Θ \land κ_p \subseteq κ\}
\]

\[
σ_1 \sim_κ σ_2 \overset{\text{def}}{=} ∀ p \in P . \ σ_1(p) = σ_2(p)
\]

\[
σ_1 \sim_κ σ_2 \overset{\text{def}}{=} σ_1 \sim_{\text{deps}(Θ, κ)} σ_2
\]

Then we define noninterference as follows:

**Theorem 3.1 (Noninterference).** Let \(e\) such that:

\[
Σ; \bullet; Γ; Θ \vdash e : r \bullet κ ⇒ Γ'; Θ'
\]

For \(i \in \{1, 2\}\), let \(σ_i\) such that:

\[
Σ \vdash σ_i : Γ \quad \text{and} \quad Σ \vdash (σ_i; e) \mapsto (σ_i'; ν_i)
\]

Then:

\[
(σ_1 \sim_κ σ_2 \implies ν_1 = ν_2)
\]

\[
(∀ p \mapsto κ_p \in Θ'. \ σ_1 \sim_κ σ_2 \implies σ_1'(p) = σ_2'(p))
\]

This theorem states that given a well-typed expression \(e\) and corresponding stacks \(σ_i\), then its output \(ν_i\) should be equal if the expression’s dependencies \(κ\) are initially equal. Moreover, for any place expression \(p\), if its dependencies in the output context \(Θ'\) are initially equal then the stack value will be the same after execution.

Note that the context \(Δ\) is required to be empty because an expression \(e\) can only evaluate if it does not contain abstract type or provenance variables. The judgment \(Σ \vdash σ_i : Γ\) means “the stack \(σ_i\) is well-typed under \(Σ\) and \(Γ\)”. That is, for all places \(π\) in \(Γ\), then \(π \in σ\) and \(σ(π)\) has type \(Γ(π)\).

The proof of Theorem 3.1, found in Appendix A.2, guarantees that we can soundly compute information flow for Oxide programs.
fn get_count(
  h: &mut HashMap<String, u32>,
  k: String
) -> u32 {
  if !h.contains_key(&k) {
    h.insert(k, 0); 0
  } else {
    *h.get(&k).unwrap()
  }
}

Figure 1. Example of how Flowistry computes information flow. On the left is a Rust function get_count that finds a value in a hash map for a given key, and inserts 0 if none exists. On the right get_count is lowered into Rust’s MIR control-flow graph, annotated with information flow. Each rectangle is a basic block, named at the top. Arrows indicate control flow (panics omitted). Beside each instruction is the result of the information flow analysis, which maps place expressions to locations in the CFG (akin to $\Theta$ in Section 2). For example, the insert function call adds dependencies to *h because it is assumed to be mutated, since it is a mutable reference. Additionally, the switch instructions and _4 variable are added as dependencies to h because the call to insert is control-dependent on the switch.

4 Implementation

Our formal model provides a sound theoretical basis for analyzing information flow in Oxide. However, Rust is a more complex language than Oxide, and the Rust compiler uses many intermediate representations beyond its surface syntax. Therefore in this section, we describe the key details of how our system, Flowistry, bridges theory to practice. Specifically:

1. Rust computes lifetime-related information on a control-flow graph (CFG) program representation, not the high-level AST. So we translate our analysis to work for CFGs (Section 4.1).
2. Rust does not compute the loan set for lifetimes directly like in Oxide. So we must reconstruct the loan sets given the information exported by Rust (Section 4.2).
3. Rust contains escape hatches for ownership-unsafe code that cannot be analyzed using our analysis. So we describe the situations in which our analysis is unsound for Rust programs (Section 4.3).

4.1 Analyzing control-flow graphs

The Rust compiler lowers programs into a “mid-level representation”, or MIR, that represents programs as a control-flow graph. Essentially, expressions are flattened into sequences of instructions (basic blocks) which terminate in instructions that can jump to other blocks, like a branch or function call. Figure 1 shows an example CFG and its information flow.

To implement the modular information flow analysis for Mirror, we reused standard static analysis techniques for CFGs, i.e., a flow-sensitive, forward dataflow analysis pass where:

- At each instruction, we maintain a mapping from place expressions to a set of locations in the CFG on which the place is dependent, comparable to $\Theta$ in Section 2.
- A transfer function updates $\Theta$ for each instruction, e.g.
  
  $$ p := e $$

  follows the same rules as in $\text{T-ASSIGNDEREF}$ by adding the dependencies of $e$ to all conflicts of aliases of $p$.
- The input $\Theta_{in}$ to a basic block is the join of each of the output $\Theta_{out}^i$ for each incoming edge, i.e. $\Theta_{in} = \bigvee_i \Theta_{out}^i$. The join operation is key-wise set union, or more precisely:
  
  $$ \Theta_1 \vee \Theta_2 \overset{\text{def}}{=} \{ x \mapsto \Theta_1(x) \cup \Theta_2(x) \mid x \in \Theta_1 \vee x \in \Theta_2 \} $$

- We iterate this analysis to a fixpoint, which we are guaranteed to reach because ($\Theta, \vee$) forms a join-semilattice.

To handle indirect information flows via control flow, such as the dependence of $h$ on contains_key in Figure 1, we compute the control-dependence between instructions. We define control-dependence following Ferrante et al. [15]: an instruction $X$ is control-dependent on $Y$ if there exists a
directed path \( P \) from \( X \) to \( Y \) such that any \( Z \) in \( P \) is post-dominated by \( Y \), and \( X \) is not post-dominated by \( Y \). An instruction \( X \) is post-dominated by \( Y \) if \( Y \) is on every path from \( X \) to a return node. We compute control-dependencies by generating the post-dominator tree and frontier of the CFG using the algorithms of Cooper et al. [9] and Cytron et al. [11], respectively.

Besides a return, the only other control-flow path out of a function in Rust is a panic. For example, each function call in Figure 1 actually has an implicit edge to a panic node (not depicted). Unlike exceptions in other languages, panics are designed to indicate unrecoverable failure. Therefore we exclude panics from our control-dependence analysis.

4.2 Computing loan sets from lifetimes
To verify ownership-safety (perform “borrow-checking”), the Rust compiler does not explicitly build the loan sets of lifetimes (or provenances in Oxide terminology). The borrow checking algorithm performs a sort of flow-sensitive dataflow analysis that determines the range of code during which a lifetime is valid, and then checks for conflicts e.g. in overlapping lifetimes (see the non-lexical lifetimes RFC [24]).

However, Rust’s borrow checker relies on the same fundamental language feature as Oxide to verify ownership-safety: outlives-constraints. For a given Rust function, Rust can output the set of outlives-constraints between all lifetimes in the function. These lifetimes are generated in the same manner as in Oxide, such as from inferred subtyping requirements or user-provided outlives-constraints. Then given these constraints, we compute loan sets via a process similar to the ownership-safety judgment described in Section 2.2. In short, for all instances of borrow expressions \( \&r \omega p \) in the MIR program, we initialize \( \Gamma(r) = \{ p \} \). Then we propagate loans via \( \Gamma(r) = \bigcup_{r'>r} \Gamma(r') \) until \( \Gamma \) reaches a fixpoint.

4.3 Handling ownership-unsafe code
Rust has a concept of raw pointers whose behavior is comparable to pointers in C. For a type \( T \), an immutable reference has type \( \&T \), while an immutable raw pointer has type \( \text{&} \\text{const} \ T \). Raw pointers are not subject to ownership restrictions, and they can only be used in blocks of code demarcated as unsafe. They are primarily used to interoperate with other languages like C, and to implement primitives that cannot be proved as ownership-safe via Rust’s rules.

Our pointer and mutation analysis fundamentally relies on ownership-safety for soundness. We do not try to analyze information flowing directly through unsafe code, as it would be subject to the same difficulties of C++ in Section 1. While this limits the applicability of our analysis, empirical studies have shown that most Rust code does not (directly) use unsafe blocks [2, 14]. We further discuss the impact and potential mitigations of this limitation in Section 8.

5 Evaluation
Section 3 established that our analysis is sound. The next question is whether it is precise: how many spurious flows are included by our analysis? We evaluate two directions:

1. What if the analysis had more information? If we could analyze the definitions of called functions, how much more precise are whole-program flows vs. modular flows?
2. What if the analysis had less information? If Rust’s type system was more like C++, i.e. lacking ownership, then how much less precise do the modular flows become?

To answer these questions, we created three modifications to Flowistry:

- **Whole-program**: the analysis recursively analyzes information flow within the definitions of called functions. For example, if calling a function \( f(\&\text{mut} \ x, \ y) \) where \( f \) does not actually modify \( x \), then the **Whole-program** analysis will not register a flow from \( y \) to \( x \).
- **Mut-blind**: the analysis does not distinguish between mutable and immutable references. For example, if calling a function \( f(\&x) \), then the analysis assumes that \( x \) can be modified.
- **Ref-blind**: the analysis does not use lifetimes to reason about references, and rather assumes all references of the same type can alias. For example, if a function takes as input \( f(x: \text{&}\text{mut} \ i32, \ y: \text{&}\text{mut} \ i32) \) then \( x \) and \( y \) are assumed to be aliases.

The **Whole-program** modification represents the most precise information flow analysis we can feasibly implement. The **Mut-blind** and **Ref-blind** modifications represent an ablation of the precision provided by ownership types. Each modification can be combined with the others, representing \( 2^3 = 8 \) possible conditions for evaluation.

To better understand **Whole-program**, say we are analyzing the information flow for an expression \( f(\text{&}\text{mut} \ x, \ y) \) where \( f \) is defined as \( f(a, b) \ ( \ (*a).1 = b; \ ) \). After analyzing the definition of \( f \), we translate flows to parameters of \( f \) into flows on arguments of the call to \( f \). So the flow \( b \rightarrow (*a) \).1 is translated into \( y \rightarrow x \).1. Additionally, if the definition of \( f \) is not available, then we fall back to the modular analysis. Importantly, due to the architecture of the Rust compiler, the only available definitions are those *within the package being analyzed*. Therefore even with **Whole-program**, we cannot recurse into e.g. the standard library.

With these three modifications, we compare the number of flows computed from a dataset of Rust projects (Section 5.1) to quantitatively (Section 5.2) and qualitatively (Section 5.3) evaluate the precision of our analysis.

5.1 Dataset
To empirically compare these modifications, we curated a dataset of Rust packages (or “crates”) to analyze. We had two selection criteria:
Table 1. Dataset of crates used to evaluate information flow precision, ordered in increasing number of variables analyzed. Each project often contains many crates, so a sub-crate is specified where applicable, and the root crate is analyzed otherwise. Metrics displayed are LOC (lines of code), number of variables, number of functions, and the average number of MIR instructions per function (size of CFG).

| Project | Crate    | Purpose                           | LOC   | # Vars | # Funcs | Avg. Instrs/Func |
|---------|----------|-----------------------------------|-------|--------|---------|------------------|
| rayon   | core/lib | Data parallelism library          | 15,524| 10,607 | 1,079   | 16.6             |
| Rocket  | core/lib | Web backend framework             | 10,688| 12,040 | 741     | 25.5             |
| rustls  | rustls   | TLS implementation                | 16,866| 23,407 | 868     | 42.4             |
| sccache |          | Distributed build cache           | 23,202| 23,987 | 643     | 62.1             |
| nalgebra|          | Numerics library                  | 31,951| 35,886 | 1,785   | 26.7             |
| image   |          | Image processing library          | 20,722| 39,077 | 1,096   | 56.8             |
| hyper   |          | HTTP server                       | 15,082| 44,900 | 799     | 82.9             |
| rg3d    |          | 3D game engine                    | 54,426| 59,590 | 3,448   | 25.7             |
| rav1e   |          | Video encoder                     | 50,294| 76,749 | 931     | 115.4            |
| RustPython | vm       | Python interpreter                | 47,927| 97,637 | 3,315   | 51.0             |

Total: 286,682 435,979 14,696

1. To mitigate the single-crate limitation of WHOLE-PROGRAM, we preferred large crates so as to see a greater impact from the WHOLE-PROGRAM modification. We only considered crates with over 10,000 lines of code as measured by the cloc utility [12].

2. To control for code styles specific to individual applications, we wanted crates from a wide range of domains.

After a manual review of large crates in the Rust ecosystem, we selected 10 crates, shown in Table 1. We built each crate with as many feature flags enabled as would work on our Ubuntu 16.04 machine. Details like the specific flags and commit hashes can be found in Appendix A.3.

For each crate, we ran the information flow analysis on every function in the crate, repeated under each of the 8 conditions. Within a function, for each local variable \( x \), we compute the size of \( \Theta(x) \) at the exit of the CFG — in terms of program slicing, we compute the size of the variable’s backward slice at the function’s return instructions. The resulting dataset then has four independent variables (crate, function, condition, variable name) and one dependent variable (size of dependency set) for a total of 3,487,832 data points.

Our main goal in this evaluation is to analyze precision, not performance. Our baseline implementation is reasonably optimized — the median per-function execution time was 370.24\( \mu \)s. But WHOLE-PROGRAM is designed to be as precise as possible, so its naïve recursion is sometimes extremely slow. For example, when analyzing the GameEngine::render function of the rg3d crate (with thousands of functions in its call graph), the modular analysis takes 0.13s while the recursive analysis takes 23.18s, a 178\( \times \) slowdown. Future work could compare our modular analysis to whole-program analyses across the precision/performance spectrum, such as in the extensive literature on context-sensitivity [33].

Figure 2. Distribution in differences of dependency set size between WHOLE-PROGRAM and MODULAR analyses. The x-axis is a log-scale with 0 added for comparison. Most sets are the same, so 0 dominates (left). A log-scale (right) shows the tail more clearly.

5.2 Quantitative results
We observed no meaningful patterns from the interaction of modifications — for example, in a linear regression of the interaction of MUT-BLIND and REF-BLIND against the size of the dependency set, each condition is individually statistically significant (\( p < 0.001 \)) while their interaction is not (\( p = 0.337 \)). So to simplify our presentation, we focus only on four conditions: three for each modification individually active with the rest disabled, and one for all modifications disabled, referred to as MODULAR.

5.2.1 WHOLE-PROGRAM. For WHOLE-PROGRAM, we compare against MODULAR to answer our first evaluation question: how much more precise is a whole-program analysis than a modular one? To quantify precision, we compare the percentage increase in size of dependency sets for a given variable between two conditions. For instance, if WHOLE-PROGRAM computes \( |\Theta(x)| = 2 \) and MODULAR computes...
The statistics convey a sense of how often each condition influences precision. But it is equally valuable to understand the kind of code that leads to such differences. For each condition, we manually inspected a sample of cases with non-zero differences vs. Modular.

5.3.1 Modularity. One common source of imprecision in modular flows is when functions take a mutable reference as input for the purposes of passing the mutable permission off to an element of the input.

For example, the function `crop` from the image crate returns a mutable view on an image. No data is mutated, only the mutable permission is passed from whole image to sub-image. However, a modular analysis on the image input would assume that image is mutated by `crop`.

Another common case is when a value only depends on a subset of a function’s inputs. The modular approximation assumes all inputs are relevant to all possible mutations, but this is not always naturally the case.

5.3.2 Mutability. The reason Mut-blind is less precise than Modular is quite simple — many functions take immutable references as inputs, and so many more mutations have to be assumed.

```rust
fn crop<I: GenericImageView>(
    image: &mut I, x: u32, y: u32,
    width: u32, height: u32
) -> SubImage<&I> {
    let (x, y, width, height) =
        crop_dimms(image, x, y, width, height);
    SubImage::new(image, x, y, width, height)
}
```

For example, this function from nalgebra returns a boolean whose value solely depends on the argument `diag`. However, a modular analysis of a call to this function would assume that `self` and `b` is relevant to the return value as well.

```rust
fn solve_lower_triangular_with_diag_mut<R2, C2, S2>(
    &self, b: &mut Matrix<N, R2, C2, S2>, diag: N,
) -> bool {
    if diag.is_zero() { return false; }
    true
}
```

Therefore, the information from ownership types is valuable in increasing the precision of our information flow analysis. Dependency sets are often larger without access to information about mutability or lifetimes.

5.3 Qualitative results

The statistics convey a sense of how often each condition influences precision. But it is equally valuable to understand...
5.4 Threats to validity

Finally, we address the issue: how meaningful are the results above? How likely would they generalize to arbitrary code rather than just our selected dataset? We discuss a few threats to validity below.

5.4.1 Are the results due to only a few crates? If differences between techniques only arose in a small number of situations that happen to be in our dataset, then our technique would not be as generally applicable. To determine the variation between crates, we generated a histogram of non-zero differences for the Modular vs. Mut-blind comparison, broken down by crate in Figure 4.

As expected, the larger code bases (e.g. rav1e and RustPython) have more non-zero differences than smaller codebases — in general the correlation between non-zero differences and total number of variables analyzed is strong, $R^2 = 0.79$. However variation also exists for crates with roughly the same number of variables like image and hyper. Mut-blind reduces precision for variables in hyper more often than image. A qualitative inspection of the respective codebases suggests this may be because hyper simply makes greater use of immutable references in its API.

These findings suggest that the impact of ownership types and the modular approximation likely do vary with code style, but a broader trend is still observable across all code.

5.4.2 Would whole-program be more precise with access to dependencies? A limitation of our whole-program analysis is our inability to access function definitions outside the current crate. Without this limitation, it may be that the Modular analysis would be significantly worse than Whole-program. So for each variable analyzed by Whole-program, we additionally computed whether the information flow for that variable involved a function call across a crate boundary.

Overall 96% of cases reached at least one crate boundary, suggesting that this limitation does occur quite often in practice. However, the impact of the limitation is less clear. Of the 96% of cases that hit a crate boundary, 6.6% had a non-zero
A program slicer integrated into VSCode. Above, the user selects a slicing criterion like the variable \( f \). Then the slicer highlights the criterion in green, and fades out lines that are not part of the backward slice on \( f \). For example, \texttt{write_all} mutates the file so it is in the slice, while \texttt{metadata} reads the file so it is not in the slice. Below, the user can manipulate aspects of a program such as commenting out code related to timing. The user computes a forward slice on \( \text{start} \), adds this slice to their selection (in blue), then tells the IDE to comments out all lines in the selection.

An IFC checker. Above, the \texttt{ifc_traits} library exports a \texttt{Secure} for users to mark sensitive data, like \texttt{Password}, and insecure operations like \texttt{insecure_print}. Below, a compiler plugin invoked on the program checks for information flows from data with a type implementing \texttt{Secure} to insecure operations. Here \texttt{insecure_print} is conditionally executed based on a read from \texttt{PASSWORD}, so this flow is flagged.

The program slicer in Figure 5a is a VSCode extension that fades out all lines of code that are not relevant to the user’s selection, i.e. not part of the modular slice. Rather than present a slice of the entire program like in prior slicing tools, we can use Flowistry’s modular analysis to present lightweight slices of just within a given function. Users can apply the slicer for comprehension tasks such as reducing the scope of a bug, or for refactoring tasks such as removing an aspect of a program like timing or logging.

The IFC checker in Figure 5b is a Rust library and compiler plugin. It provides the user a library with the traits \texttt{Secure} and \texttt{Insecure} to indicate the relative security of data types and operations. Users can apply the IFC checker to catch sensitive data leaks in an application. This prototype is purely intraprocedural, but future work could build an interprocedural analysis by using Flowistry’s output as procedure summaries in a larger information flow graph.

**Figure 5.** Two applications of information flow built using Flowistry.

---

**6 Applications**

We have demonstrated that ownership can be leveraged to build an information flow analysis that is static, modular, sound, and precise. Our hope is that this analysis can serve as a building block for future static analyses. To bootstrap this future, we have used Flowistry to implement prototypes of a program slicer and an IFC checker, shown in Figure 5.

The program slicer in Figure 5a is a VSCode extension that fades out all lines of code that are not relevant to the user’s selection, i.e. not part of the modular slice. Rather than present a slice of the entire program like in prior slicing tools, we can use Flowistry’s modular analysis to present lightweight slices of just within a given function. Users can apply the slicer for comprehension tasks such as reducing the scope of a bug, or for refactoring tasks such as removing an aspect of a program like timing or logging.

The IFC checker in Figure 5b is a Rust library and compiler plugin. It provides the user a library with the traits \texttt{Secure} and \texttt{Insecure} to indicate the relative security of data types and operations. Then the compiler plugin uses Flowistry to determine whether information flows from \texttt{Insecure} variables to \texttt{Secure} variables. Users can apply the IFC checker to catch sensitive data leaks in an application. This prototype is purely intraprocedural, but future work could build an interprocedural analysis by using Flowistry’s output as procedure summaries in a larger information flow graph.
7 Related Work

Our work draws on three core concepts: information flow, modular static analysis, and ownership types.

Information flow. Information flow has been historically studied in the context of security, such as ensuring low-security users of a program cannot infer anything about its high-security internals. Security-focused information flow analyses have been developed for Java [25], Javascript [4], OCaml [27], Haskell [34], and many other languages.

Each analysis satisfies some, but not all, of our requirements from Section 1. For instance, the JFlow [25] and FlowCaml [27] languages required adding features to the base language, violating our second requirement. Some methods like that of Austin and Flanagan [4] for Javascript rely on dynamic analysis, violating our third requirement. And Haskell only supports effects like mutation through monads, violating our first requirement.

Nonetheless, we draw significant inspiration from mechanisms in prior work. Our analysis resembles the slicing calculus of Abadi et al. [1]. The use of lifetimes for modular analysis of functions is comparable to security annotations in FlowCaml [27]. The CFG analysis draws on techniques used in program slicers, such as the LLVM dg slicer [7].

Modular static analysis. The key technique to making static analysis modular (or “compositional” or “separate”) is symbolically summarizing a function’s behavior, so that the summary can be used without the function’s implementation. Starting from Rountev et al. [29] and Cousot and Cousot [10], one approach has been to design a system of “procedure summaries” understood by the static analyzer and distinct from the language being analyzed. This approach has been widely applied for static analysis of null pointer dereferences [40], pointer aliases [13], data dependencies [35], and other properties.

Another approach, like ours, is to leverage the language’s type system to summarize behavior. Tang and Jouvelot [36] showed that an effect system could be used for a modular control-flow analysis. Later work in Haskell used its powerful type system and monadic effects to embed many forms of information flow control into the language [6, 23, 30, 34].

Ownership types. Rust and Oxide’s conceptions of ownership derive from Clarke et al. [8] and Grossman et al. [18]. For instance, the Cyclone language of Grossman et al. uses regions to restrict where a pointer can point-to, and uses region variables to express relationships between regions in a function’s input and output types. A lifetime is similar in that it annotates the types of pointers, but differs in how it is analyzed.

Recent works have demonstrated innovative applications of Rust’s type system for modular program analysis. Astrauskas et al. [3] embed Rust programs into a separation logic to verify pre/post conditions about functions. Jung et al. [20] use Rust’s ownership-based guarantees to implement more aggressive program optimizations.

Closer to our domain, Balasubramanian et al. [5] implemented a prototype IFC system for Rust by lowering programs to LLVM and verifying them with SMACK [28], although their system is hard to contrast with ours given the high-level description in their paper. Njor and Gustafsson [26] implemented a static taint analysis for Rust, although it is not field-sensitive, alias-sensitive, or modular.

8 Discussion

Looking forward, two interesting avenues for future work on Flowistry are improving its precision and addressing soundness in unsafe code. For instance, the lifetime-based pointer analysis is sound but imprecise in some respects. Lifetimes often lose information about part-whole relationships. Consider the function that returns a mutable pointer to a specific index in a vector:

```plaintext
fn get_mut<&'a>(&'a mut self, i: usize) -> Option<&'a mut T>;
```

These lifetimes indicate only that the return value points to something in the input vector. The expressions `v.get_mut(i)` and `v.get_mut(i + 1)` are considered aliases even though they are not. Future work could integrate Flowistry with verification tools like Prusti [3] to use abstract interpretation for a more precise pointer analysis in such cases.

Additionally, Rust has many libraries built on unsafe code that can lose annotations essential to information flow, such as interior mutability. For example, shared-memory concurrency in Rust looks like this:

```plaintext
let n: Arc<Mutex<i32>> = Arc::new(Mutex::new(0));
let n2: Arc<Mutex<i32>> = Arc::clone(&n);
*n2.lock().unwrap() = 1;
```

Arc::clone does not share a lifetime between its input and output, so a lifetime-based pointer analysis therefore cannot deduce that `n2` is an alias of `n`, and Flowistry would not recognize that the mutation on line 3 affects `n`. Future work can explore how unsafe libraries could be annotated with the necessary metadata needed to analyze information flow, similar to how RustBelt [21] identifies the pre/post-conditions needed to ensure type safety within unsafe code.

Overall, we are excited by the possibilities created by having a practical information flow analysis that can run today on any Rust program. Many exciting systems for tasks like debugging [22], example generation [19], and program repair [39] rely on information flow in some form, and we hope that Flowistry can support the development of these tools.

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A Appendix

This appendix contains three major sections. First, in Appendix A.1 we include information flow inference rules beyond those presented in Section 2 for the remaining features of Oxide. Second, in Appendix A.2 we provide a complete proof of the noninterference theorem in Section 3, along with associated lemmas. Finally, in Appendix A.3 we provide additional details necessary to replicate the evaluation.

A.1 Additional rules

In Figure 6, we provide the remaining information flow rules for the expression forms not covered in Section 2. T-TUPLE, T-SEQ, T-LETPROV are relatively straightforward, so we focus on the remaining three rules.

First, T-BORROW: a borrow is unique in that its runtime value is a place, i.e. ptr π. A borrow &π will always have value ptr π, so it has no dependencies. But the borrow is a reborrow, i.e. of the form &p, then we need to include the dependencies of all places that p could point-to. Therefore we include Θ(p′) for each loan p′ in the loan set of p.

Next, T-COPY shows a read from an arbitrary place expression p. Unlike T-MOVE, we have to account for p referring to many possible memory locations. Again this is captured by the ownership-safety judgment r_shd p ⇣ {I}. Therefore the dependencies of p are the dependencies of any possibly read place, i.e. \( \bigcup \Theta(p′) \).

Finally, T-BRANCH shows how to handle conditional execution. The \( k \) is simple, as the value of an if-expression could depend on either branch, \( k_2 \cup k_3 \), along with the condition, \( k_1 \). The more complex aspect is handling effects in \( \Theta \). The core idea is that if a place \( p \) could be mutated in either e2 or e3, then that place has a control dependency on e1, and \( k_1 \) should be part of the dependencies of \( p \).

To encode this idea, we introduce two new metafunctions. First, \( \Theta_2 \cup \Theta_3 \) represents the union distributed over like entries:

\[
\theta_2 \cup \theta_3 \overset{def}{=} \{ p \mapsto \theta_2(p) \cup \theta_3(p) \mid p \in \Theta_2 \cup p \in \Theta_3 \}
\]

Next, we represent "could be mutated in e2 or e3" via \( \Theta'(\theta_1) \), similarly distributed over like entries:

\[
\theta' \setminus \theta_1 \overset{def}{=} \{ p \mapsto \theta'(p) \setminus \theta_1(p) \mid (p \in \Theta' \cup p \in \Theta_1) \wedge \theta'(p) \setminus \theta_1(p) \neq \phi \}
\]

Then the rule says: after independently computing the contexts \( \theta_2 \) and \( \theta_3 \), compute a unioned context \( \theta' \). Then for all places \( p \) that had new dependencies generated in e2 or e3, i.e. \( p \in \Theta' \setminus \Theta_1 \), add \( k_1 \) to the dependencies of \( p \).

A.2 Proofs

The proof of non-interference relies on a few key lemmas about the semantics of Oxide. We start by defining and proving these lemmas (Appendix A.2.1), and then proceed to prove noninterference (Appendix A.2.2).

A.2.1 Lemmas.

Lemma A.1 (Mutating a place also mutates its conflicts). Let:

- \( \pi_{\text{mut}} = \pi_{\text{mut}}[x] \), \( \sigma \) where \( \sigma \vdash \pi_{\text{mut}} \)
- \( \sigma' \) be a value and \( \sigma' = \sigma[x \mapsto V[v]] \)
- \( \pi_{\text{any}} \in \sigma \)

Then \( \sigma(\pi_{\text{any}}) \neq \sigma'(\pi_{\text{any}}) \implies \pi_{\text{mut}} \cap \pi_{\text{any}} = \emptyset \).

Proof.

1. Assume \( \sigma(\pi_{\text{any}}) \neq \sigma'(\pi_{\text{any}}) \). Want to show \( \pi_{\text{mut}} \cap \pi_{\text{any}} = \emptyset \).
2. Because only x is assigned, then \( \pi_{\text{any}} = \pi_{\text{any}}[x] \).
3. Assume for sake of contradiction that \( \pi_{\text{mut}} \cap \pi_{\text{any}} \neq \emptyset \). Let q be the shared path and \( n_{\text{mut}}, n_{\text{any}} \) be the split, i.e. \( \pi_{\text{mut}} = x.q.n_{\text{mut}}, q_{\text{mut}} \) and \( \pi_{\text{any}} = x.q.n_{\text{any}}, q_{\text{any}} \).
4. By ER-Projection, then

\[
\sigma \vdash x.q.n_{\text{mut}} \downarrow V[(u_0, \ldots, u_{n_{\text{any}}}, \ldots, q_{\text{mut}}, \ldots, u_n)]
\]

5. \( n_{\text{any}} = \sigma(x.q.n_{\text{any}}) \) by induction on the derivation of \( V \).
6. Therefore \( \sigma'(x.q.n_{\text{mut}}) = n_{\text{any}} = \sigma(x.q.n_{\text{mut}}) \).
7. This is a contradiction with \( \sigma(\pi_{\text{any}}) \neq \sigma'(\pi_{\text{any}}) \), therefore \( \pi_{\text{mut}} \cap \pi_{\text{any}} = \emptyset \).

Lemma A.2 (A place expression’s loan set contains the place it points-to at runtime.). Let:

- \( \sigma, \Sigma, \Gamma \) where \( \Sigma \vdash \sigma \vdash \Gamma \)
- \( \pi_{\text{mut}} \) where \( \bullet \Gamma \vdash \pi_{\text{mut}} \)
- \( \pi_{\text{any}} \) where \( \sigma \vdash \pi_{\text{any}} \)

Then \( \pi_{\text{any}} \cap \pi_{\text{mut}} \implies \exists \pi_{\text{loan}} \in \{I\} \cdot \pi_{\text{any}} \cap \pi_{\text{loan}} \)

Proof. Proof by induction on the derivation of \( \bullet \Gamma \vdash \pi_{\text{mut}} \)

(a) O-SAFEPLACE:

1. If \( \pi_{\text{any}} \neq \pi_{\text{any}} \) then \( \exists r . \pi_{\text{any}} \in \Gamma(r) \). But the first premise of O-SAFEPLACE, it must be the case that \( \pi_{\text{any}} \neq \pi_{\text{any}} \), a contradiction. Therefore \( \pi_{\text{any}} = \pi_{\text{any}} \).
2. Then by the conclusion of O-SAFEPLACE,

\( \{I\} = \{ \text{uniq} \pi_{\text{mut}} \} \). Then the theorem holds for \( \pi_{\text{loan}} = \pi_{\text{mut}} \).

(b) O-DEREF:

1. Let \( \pi_{\text{mut}} = \pi_{\text{mut}}[r_{\text{ptr}}] \) and \( \Gamma(r_{\text{ptr}}) = \& r \text{ uniq } r \).
2. By extension of Lemma E.6 [2019, p. 47]

\[
\exists \text{uniq } p_{\text{mut}} \in \Gamma(r) . \sigma \vdash \pi_{\text{mut}}[p_{\text{mut}}] \downarrow \pi_{\text{mut}}
\]

3. By the inductive hypothesis on

\[\bullet \Gamma \vdash \pi_{\text{mut}}[r_{\text{ptr}}] \]

then:

\[\exists \text{uniq } p_{\text{loan}} \in \{ \text{uniq } p_{\text{mut}} \} \cdot \pi_{\text{any}} \cap \pi_{\text{loan}} \]

4. Because \( \{ \text{uniq } p_{\text{mut}} \} \subseteq \{I\} \), then the theorem holds.

(c) O-DEREFABS: does not apply since \( \Lambda = \bullet \).
5. Because its argument

\[ \text{Lemma A.3} \]

(A function only mutates unique references in its argument). Let:

1. \( \Gamma, \pi, \sigma \) where \( \Gamma(\pi) = \tau^{si} \) and \( \Sigma \vdash \sigma : \Gamma \)
2. \( f \) where \( \Sigma \vdash (\sigma ; f(\pi)) \rightarrow (\sigma'; \_ ) \)
3. \( \sigma'' = \sigma' [\forall p_{\text{loan}} \in \text{uniq-loans}(\pi_{\text{arg}}, \tau^{si}, \bullet, \Gamma) \cdot p_{\text{loan}} \mapsto \sigma(p_{\text{loan}})] \)

Then \( \sigma = \sigma'' \).

**Proof:**

1. Let:

\[ \Sigma(f) = \text{fn} f \left( \tilde{\varphi}, \tilde{\varphi}, \tilde{\varphi}, \tilde{\varphi}_0 : \tilde{\varphi}_0 ; \tilde{\varphi} \right) (x : \tau^{si}_x) \rightarrow \tau^{si}_x \{ e \} \]

2. By E-App, E-EvalCtx, and E-Framed:

\[ \Sigma \vdash (\sigma ; f(\pi)) \rightarrow (\sigma^* ; x \Rightarrow \sigma(\pi)) ; \text{framed} e \rightarrow (\sigma' ; \xi ; \text{framed} v) \rightarrow (\sigma' ; v) \]

3. By inspection of the operational semantics, the only rule that could modify \( \sigma \) (as apart from \( \xi \)) is E-Assign. Assume that \( \rho_{\text{mut}} \vdash e \) is executed under stack \( \rho_{\text{mut}} \Rightarrow \rho_{\xi} \) during \( f(\pi) \) where \( \rho_{\text{mut}} = \rho_{\text{mut}} \). \( \rho_{\text{mut}} \) are in \( \text{shrd-loans}(\pi_{\text{arg}}, \tau^{si}, \bullet, \Gamma) \), so it must be that \( \rho_{\text{mut}} = \rho^{\text{si}}[\rho_{\text{loan}}] \) and \( \rho_{\text{loan}} \in \text{shrd-loans}(\pi_{\text{arg}}, \tau^{si}, \bullet, \Gamma) \).

4. By inspection of the operational semantics, the only way to create a pointer is E-Borrow on a place \( p \). The only places in \( \rho_{\text{mut}} \) that are accessible from \( e \) are \( \text{shrd-loans}(\pi_{\text{arg}}, \tau^{si}, \bullet, \Gamma) \), so \( \rho_{\text{mut}} : \rho^{\text{si}} \). Therefore \( \rho_{\text{loan}} \in \text{uniq-loans}(\pi_{\text{arg}}, \tau^{si}, \bullet, \Gamma) \).

5. Because \( e \) is well-typed under \( x : \tau^{si}_x \), then any shared references in \( x \) cannot be mutated: T-AssignDeref requires \( \tau_{\text{uniq}} \rho_{\text{mut}} \Rightarrow \{ \tilde{\ell} \} \), which by O-Deref requires that the reference under \( \rho_{\text{mut}} \) has type \( \&r \) uniq \( \tau \). Therefore \( \rho_{\text{loan}} \in \text{uniq-loans}(\pi_{\text{arg}}, \tau^{si}, \bullet, \Gamma) \).

6. Hence, all mutated places in \( \sigma' \) are projections of a \( \rho_{\text{loan}} \).

Therefore \( \sigma' [\forall p_{\text{loan}} \in \text{uniq-loans}(\pi_{\text{arg}}, \tau^{si}, \bullet, \Gamma) \cdot p_{\text{loan}} \mapsto \sigma(p_{\text{loan}})] \) reverts all possible mutations, and \( \sigma = \sigma'' \).

\[ \Box \]

**Lemma A.4** (A function’s effects are only influenced by its argument.) Let:

1. \( \Gamma, \pi, \sigma_i \) where \( \Gamma(\pi) = \tau^{si} \) and \( i \in \{1, 2\} \) and \( \Sigma \vdash \sigma_i : \Gamma \)
2. \( f \) where \( \Sigma \vdash (\sigma_i ; f(\pi)) \rightarrow (\sigma_i' ; v_i) \)
3. \( P = (\pi_{\text{arg}}) \cup \text{shrd-loans}(\pi_{\text{arg}}, \tau^{si}, \bullet, \Gamma) \)
4. \( \sigma_1 \sim P \sigma_2 \quad \Rightarrow \quad \sigma_1' \sim P \sigma_2' \wedge v_1 = v_2 \)

**Proof:**

1. As with the proof for Lemma A.3, have \( (\sigma_i ; f(\pi)) \rightarrow (\sigma_i' ; v_i) \).

2. By inspection of the operational semantics, the only rule that could read \( \sigma \) is E-Copy via the premise \( (\sigma_{\text{read}} ; \text{read} : \varphi) \rightarrow (\sigma_{\text{read}} ; \varphi) \) where \( \sigma_{\text{read}} \rightarrow \text{read} \).

3. As with the proof for Lemma A.3, the only possible value of \( \text{read} = \rho^{\text{si}}[\rho_{\text{loan}}] \) where \( \rho_{\text{loan}} \in \text{shrd-loans}(\pi_{\text{arg}}, \tau^{si}, \bullet, \Gamma) \).

4. Because \( \rho_{\text{loan}} \in P \) and \( \sigma_1 \sim P \sigma_2 \) then \( \sigma_1(\rho_{\text{loan}}) = \sigma_2(\rho_{\text{loan}}) \).

5. Similarly because \( \pi_{\text{arg}} \in P \) then \( \sigma_1(\pi_{\text{arg}}) = \sigma_2(\pi_{\text{arg}}) \).

6. Therefore every readable input for \( f \) is equal, and for the same function body \( e \), the evaluation should result in the same effects on \( \sigma \) and same output \( \varphi \). Hence, \( \sigma_1' \sim P \sigma_2' \).

\[ \Box \]

A.2.2 Noninterference.

**Theorem A.5** (Noninterference). Let \( e \) such that

\[ \Sigma; \bullet ; \Gamma \vdash e : \tau \bullet \Rightarrow \Gamma' ; \Theta' \]
For $i \in \{1, 2\}$, let $\sigma_i$ such that
\[
\Sigma \vdash \sigma_i : \Gamma \quad \text{and} \quad \Sigma \vdash (\sigma_i; e) \rightarrow (\sigma_i'; v_i).
\]

Then:
\begin{enumerate}[(a)]
\item $\sigma_1 \sim_\Theta \sigma_2 \implies v_1 = v_2$
\item $\forall p \mapsto_\kappa p \in \Theta' \cdot \sigma_1 \sim_\Theta \sigma_2 \implies \sigma_1'(p) = \sigma_2'(p)$
\end{enumerate}

Proof. Proof by induction over the derivation of $e : \tau$.
- **T-UCUS**: $e = n_i$ where $\kappa = \{\ell\}$. (a) is trivial because $n$ always evaluates to $n$, and (b) is trivial because $n$ has no effects.
- **T-MOVE**: $e = \pi$ where $\kappa = \Theta(\pi)$. (b) is trivial because $\pi$ has no effects, so we focus on (a): $\sigma_1 \sim_\Theta(\pi) \sigma_2 \implies v_1 = v_2$
  1. By the definition of equivalence of stacks, $\Theta(\pi) \subseteq \Theta(\pi) \implies \sigma_1'(\pi) = \sigma_2'(\pi)$.
  2. By E-MOVE, $v_1 = \sigma_1(\pi)$.
  3. Therefore $v_1 = v_2$.
- **T-COPY**: the proof is the same as for T-MOVE.
- **T-SEQ**: $e = e_1; e_2$ where $\kappa = \kappa_2$.
  1. $\sigma_1 \sim_\Theta \sigma_2 \implies v_1 = v_2$
  2. By E-EVALCTX, $\Sigma \vdash (\sigma_1; e_1) \rightarrow (\sigma_1'; v_1)$.
  3. By E-SEQ, $\Sigma \vdash (\sigma_1'; v_1; e_2) \rightarrow (\sigma_1''; v_2)$.
  4. WTS $\sigma_1'' = \sigma_2''$.
  5. By the IH for $e_2$, $v_2 = v_2 = \sigma_2''(\pi)$ if $\sigma_1 \sim_\kappa \sigma_2$.
  6. Let $\pi \mapsto \kappa_\pi \in \Theta_2$ such that $\kappa_\pi \subseteq \kappa_\pi$. WTS $\sigma_1(\pi') = \sigma_2(\pi')$.
  7. By assumption, because $\kappa_\pi \subseteq \kappa_\pi$, then $\kappa_\pi \subseteq \kappa_\pi$.
  8. By assumption, $\sigma_1(\pi') = \sigma_2(\pi')$.
- **T-ASSIGN**: $e = p \mu := e'$. Let $\Sigma \vdash (\sigma_1; e) \rightarrow (\sigma_1'; v)$.
  1. By E-EVALCTX and E-ASSIGN, $\Sigma \vdash (\sigma_1; p \mu := e) \rightarrow (\sigma_1'; x \mapsto \forall_i; v_1)\[\forall_j; v_2]\[\forall_k; v_3] \rightarrow (\sigma_1'; x \mapsto \forall_i; v_1)$.
  2. By the IH for $e_2$, $\sigma_1'(\pi) = \sigma_2'(\pi)$ if $\sigma_1 \sim_\kappa \sigma_2$.
  3. The proof that $\sigma_1'' \sim_\kappa \sigma_2''$ follows similarly as above.
- **T-LET**: $e = \\text{llet } x : e_1; e_2\] = e_1; e_2\]$. The proof of (b) follows from the proof for T-SEQ. We focus on (a): $\sigma_1 \sim_\Theta \sigma_2 \implies v_1 = v_2$
  1. By E-EVALCTX, E-LET, E-EVALCTX, and E-SHIFT:
  \[
  \Sigma \vdash (\sigma_1; let \ x : e_1; e_2 \] \rightarrow (\sigma_1''; let \ x : e_1; e_2 \] \rightarrow (\sigma_1''; let \ x \mapsto \sigma_1''; v_1) \rightarrow (\sigma_1''; x \mapsto v_1) \rightarrow (\sigma_1''; x \mapsto \sigma_1''; v_1).
  \]
  2. Let $\sigma_1'' = \sigma_2''$, $x \mapsto v_1$. By IH (a) for $e_2$, $\sigma_1'' \sim_\epsilon \sigma_2''$ then $v_1 = v_2$.
  3. Let $\pi$ such that $\Theta'_\pi(\pi) \subseteq \kappa_\pi$. WTS $\sigma_1''(\pi) = \sigma_2''(\pi)$.
  4. If $\Theta'_\pi(\pi) \subseteq \Theta(\pi)$, then $\sigma_1''(\pi) = \sigma_2''(\pi)$ is true by assumption.
  5. Otherwise must be $\pi = [x] \mapsto \kappa_\pi$ and $\kappa_\pi \subseteq \Theta'_\pi(\pi)$.

6. By IH (a) for $e_1$, if $\sigma_1 \sim_\Theta \sigma_2$ then $\sigma_1'' = \sigma_2''$.
  7. Because $\sigma_1 \sim_\Theta \sigma_2$ and $\kappa_1 \subseteq \kappa_2$ by (3), then $\sigma_1 \sim_\Theta \sigma_2$ and $v_1 = v_2$.
  8. Therefore $\sigma_1''(\pi) = \sigma_2''(\pi)$.
- **T-ASSIGN**: $e = p \mu := e'$. Because $v_i = ()$ then (a) is trivial, so we focus on (b):
  \[
  \forall \pi \mu \mapsto \kappa_\pi \in \Theta'_\pi \cdot \sigma_1 \sim_\Theta \sigma_2 \implies \sigma_1'(\pi \mu) = \sigma_2'(\pi \mu).
  \]
  1. By E-EVALCTX, $\Sigma \vdash (\sigma_1; e) \rightarrow (\sigma_1'; v_1)$. By IH (b) on $e$, then $\sigma_1'(\pi \mu) = \sigma_2'(\pi \mu)$.
  2. By E-ASSIGN, $\Sigma \vdash (\sigma_1'; \pi \mu := v_1) \rightarrow (\sigma_1''[x \mapsto \forall_i; v_1])$ where $\pi \mu = \pi \mu[x]$ and $\sigma_1''[x \mapsto \forall_i; v_1]$.
  3. By Lemma A.1, if $\sigma_1''(\pi \mu) \neq \sigma_1'(\pi \mu)$ then $\pi \mu \cap \pi \mu$.
  4. By T-ASSIGN, because $\pi \mu \cap \pi \mu$ then $\Theta'(\pi \mu) = \Theta(\pi \mu) \cup \kappa_\pi$.
  5. Because $\kappa_\pi \subseteq \Theta'(\pi \mu)$, then $\sigma_1 \sim_\Theta \sigma_2$.
  6. By IH (a) on $e$, then $v_1 = v_2$.
  7. Therefore $\sigma_1''(\pi \mu) = \sigma_2''(\pi \mu)$.
- **T-ASSIGN**:
  \[
  e = p \mu := e'. \quad \text{Like T-ASSIGN, we focus on (b):} \quad \forall \pi \mu \mapsto \kappa_\pi \in \Theta'_\pi \cdot \sigma_1 \sim_\Theta \sigma_2 \implies \sigma_1'(\pi \mu) = \sigma_2'(\pi \mu).
  \]
  1. By E-EVALCTX and E-ASSIGN, $\Sigma \vdash (\sigma_1; p \mu := e) \rightarrow (\sigma_1''[x \mapsto \forall_i; v_1])$ where $\sigma_1''[x \mapsto \forall_i; v_1]$.
  2. By IH (b) on $e$, $\sigma_1''(\pi \mu) = \sigma_2''(\pi \mu)$.
  3. By Lemma A.1.2, only consider the case where $\pi \mu \cap \pi \mu$.
  4. By Lemma A.2, because $\pi \mu \cap \pi \mu$ then $\exists \mu \in \pi \mu \cap \pi \mu$.
  5. By T-ASSIGN, then $\kappa \subseteq \Theta'(\pi \mu)$, and $\sigma_1'' = \sigma_2''$.
  6. Therefore $\sigma_1''(\pi \mu) = \sigma_2''(\pi \mu)$ as in the proof for T-ASSIGN.
- **T-APP**: $e = f(\pi \arg)$ where $\kappa = \kappa_\arg$.
  1. $\sigma_1 \sim_\Theta \sigma_2 \implies v_1 = v_2$
  2. By T-APP, $\Theta(p) \subseteq \kappa_\arg$. Therefore $\sigma_1(p) = \sigma_2(p)$ and hence $v_1 = v_2$.
  3. By T-APP, $\Theta(p) \subseteq \kappa_\arg$.
  4. By Lemma A.3 and Lemma A.1, if $\sigma_1'(\pi \mu) \neq \sigma_1'(\pi \mu)$ then $\exists \mu \in \pi \mu \cap \pi \mu$.
  5. By T-APP, $\kappa \subseteq \kappa_\arg$.
  6. By Lemma A.4, then $\sigma_1''(\pi \mu) = \sigma_2''(\pi \mu)$.
A.3 Evaluation details

Table 2 provides the build configurations needed to precisely reproduce the codebases used in Section 5. We also provide a Zenodo artifact containing a Docker image that can exactly reproduce the results in Section 5: https://zenodo.org/record/6327882

Table 2. Build configuration for each package used in the evaluation. Crate is the sub-directory of the crate used for evaluation. Commit is the point in the project’s Git history used. Features is the flags passed to cargo, where none means “no feature flags” and all means --all-features.

| Project      | Crate   | Commit                  | Features                      |
|--------------|---------|-------------------------|-------------------------------|
| rayon        | core    | c571f8ffbf4f748c09b4e1e6d9797b1b4414d07 | all                           |
| Rocket       | core/lib| 8d4d0116e6e2100b8010805d40bfa19a7357e900 | none                          |
| rustls       | rustls  | cdf1dada21a537e141d96c6d6e9c5685bb43fbcb8e | all                           |
| sccache      |         | 3f318a8675e4c3d4f5e8a8b2d086189f2ae5f5cf | none                          |
| nalgebra     |         | 984ba1a6934a686b1f26ff4a6ac8f68b33b70 | rand, arbitrary, sparse, debug, io, llvm |
| image        |         | e916e9dda5f4253f6cc4557b0fe5fa3876ac18e5 | none                          |
| hyper        |         | ed2f2db7b6a2963cea7577df05dda4c156fe7e7246 | full                          |
| rg3d         |         | ca7b85f2b30e45b82caee0591ee1abf65bb3eb00 | all                           |
| rave1        |         | 1b663324727857c6d6ad0b19257f3ca3a9b26a | none                          |
| RustPython   | vm      | 9143e51b7524a5884d5ed23b0f2f5b0610ac5b | compiler                      |

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