Research Article

An Improved Model Predictive Control Method for Three-Port Soft Open Point

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Received 26 March 2021; Revised 9 June 2021; Accepted 15 June 2021; Published 29 June 2021

1. Introduction

With the increasing application of new energy such as wind energy and solar energy, the penetration rate of renewable distributed energy in the distribution network is getting higher and higher [1, 2]. Additionally, with the large-scale access of new loads represented by electric vehicles, the power flow of the power grid is becoming more and more complicated, and power quality problems, such as voltage crossing the line, are becoming more and more prominent. These problems have seriously affected the stability and safety of the distribution network [3, 4]. The original adjustment methods of the distribution network have limited control accuracy and adjustment speed, which has restricted the improvement of the control ability of the distribution network. Therefore, to further improve the flexibility and reliability of the operation of the distribution network, the development of power electronic equipment for the distribution network has attracted the attention of scholars from all over the world. Soft open point (SOP) is a new type of power electronic device that can replace the traditional contact switch, which is becoming a research hotspot [5, 6]. SOP cannot only solve the shortcomings of the traditional contact switch that it only has two states and a limited number of switches but also it has the function of regulating the flow and the characteristics of various control methods. Many scholars have done in-depth research on SOP, including optimization planning [7–9], operation scheduling [10, 11], and fault recovery [12, 13]. However, there is little research on the control strategy of SOP itself. Ouyang et al. proposed a three-port SOP based on a power electronic transformer and studied its DC voltage control strategy [14]. Hou et al. adopted a double closed-loop control strategy [15], but for a three-port SOP, a double closed-loop control
strategy requires a total of 12 proportional-integral (PI) regulators, resulting in a complex control structure and difficult parameter adjustment, which can lead to low current tracking accuracy and slow dynamic response. Li et al. used the compound control of feedback linearization control and sliding mode control to replace the current inner loop in order to reduce the PI parameters, but the sliding mode controller parameters were difficult to design [16].

With the development of the microprocessor, finite control set model predictive control (FCS-MPC) has been applied more and more often to power electronic systems. Its advantages include intuitive principle, easy modeling, and no complex parameter design. It also has a very good effect on overcoming the nonlinearity and uncertainty in the industrial control process. FCS-MPC is widely used in inverters, rectifiers, permanent magnet synchronous machine, and modular multilevel converters [17–23], but rarely in the three-port SOP. Peng et al. proposed a three-port SOP coordinated control strategy based on FCS-MPC [24]. Although FCS-MPC has a faster dynamic response speed and better output power quality, it also has some problems such as a large amount of calculation and delay in current prediction. To solve the above problems, an improved model predictive control strategy based on three-port SOP is proposed, which can replace the inner loop current control of double closed-loop control. By reducing the amount of calculation and increasing the delay compensation and future reference compensation, the proposed strategy further reduces the problem of the current delay, improves the current tracking accuracy, and ensures faster dynamic response speed. The main contributions of this paper are as follows:

(1) In order to realize the decoupling control of the system, the mathematical model of three-port SOP is established by the vector method and the dq coordinate transformation method.

(2) The two-step prediction method based on the voltage vector and the vector angle compensation method are combined to improve FCS-MPC, which can further improve the control performance. Usually, the two-step prediction method takes the current as the target, but this article adopts the two-step prediction method that takes voltage as the target, which can reduce the amount of calculation. Meanwhile, the vector angle compensation method is used to compensate the future reference value lag, including the compensation of reference current and reference voltage.

(3) Improved model predictive control is applied to three-port SOP for the first time. By using the improved FCS-MPC to replace the inner loop current control with double closed-loop control, the complex parameter design process of the proportional-integral (PI) regulator can be omitted, and the dynamic response speed and steady-state tracking accuracy of the current can be effectively improved.

The rest of the article is arranged as follows: Section 2 establishes the mathematical model of the three-port SOP. Section 3 analyzes the control principle of the improved FCS-MPC and designs the overall control scheme of the system. Section 4 simulates and analyzes the proposed control scheme through MATLAB/Simulink. Section 5 concludes the paper with a discussion of the results.

2. Three-Port SOP Mathematical Model

The two-port SOP composed of two voltage source converters (VSCs) has limitations in the form of access in the distribution network, as it cannot realize the flexible interconnection of multiple ports. Hence, this paper takes the three-port SOP as the research object to establish the mathematical model of the three-port SOP. As shown in Figure 1, the three-port SOP is connected by three VSCs through DC side capacitors, and it is usually connected at the end of the distribution network to connect the three different feeders.

The main circuit structure of the three-port SOP is shown in Figure 2. The subscript \( j = 1, 2, \) and \( 3 \), representing port 1, port 2, and port 3. The subscripts \( a, b, \) and \( c \) denote phase \( a, \) phase \( b, \) and phase \( c. E_{ja}, E_{jb}, \) and \( E_{jc} \) indicate the \( a, \) \( b, \) and \( c \) phase grid voltages of port \( j. U_{jaN}, U_{jbN}, \) and \( U_{jcN} \) represent the voltages of port \( j \) of VSC relative to point \( N \) in the figure; hence, they are the output voltages of the inverter. \( R_j \) represents the equivalent resistance of the \( j \) port line, \( L_j \) represents the \( j \) port filter reactor, \( C \) represents DC side capacitance, and \( idc_j \) represents the current on the DC side of the \( j \) port VSC.

The three-port SOP switch states are defined as follows:

\[
S_k = \begin{cases} 
1, & \text{if } k - \text{phase upper bridge arm is turned on and lower bridge arm is turned off.} \\
0, & \text{if } k - \text{phase lower bridge arm is turned on and upper bridge arm is turned off.} 
\end{cases} \quad (k = a, b, \text{and } c). \quad (1)
\]

The output voltage of each port is represented by the switch states:

\[
U_{jkN} = S_k \cdot U_{dc}, \quad k = a, b, \text{and } c, \quad (2)
\]

where \( S_k \) is \( j \) port switch states and \( U_{dc} \) is AC side voltage.

Since the unit vector \( \alpha = e^{j2\pi/3} = (-1 + j\sqrt{3})/2 \) represents the phase difference of 120°, the output voltage vector of each port can be defined as
where $U_{jaN}$, $U_{jbN}$, and $U_{jcN}$ represent the voltages of port $j$ of VSC relative to point $N$.

Assuming that the direction of the reference current is shown in Figure 2, the dynamic equation on the AC side of the three-port SOP is as follows:

$$U_j = \frac{2}{3}(U_{jaN} + \alpha U_{jbN} + \alpha^2 U_{jcN}), \tag{3}$$

where $U_{jaN}$, $U_{jbN}$, and $U_{jcN}$ represent the voltages of port $j$ of VSC relative to point $N$.

Assuming that the direction of the reference current is shown in Figure 2, the dynamic equation on the AC side of the three-port SOP is as follows:

$$U_j = L \frac{d}{dt} \left( \frac{2}{3}(i_{ja} + ai_{jb} + \alpha^2 i_{jc}) \right) + R \left( \frac{2}{3}(i_{ja} + ai_{jb} + \alpha^2 i_{jc}) \right)$$

$$+ \frac{2}{3}(E_{ja} + \alpha E_{jb} + \alpha^2 E_{jc}) + \frac{2}{3}(U_{nN} + \alpha U_{nN} + \alpha^2 U_{nN}). \tag{5}$$

According to the definition mode of the output voltage vector of (4), the load current vector and the grid voltage vector are defined as

$$i_j = \frac{2}{3}(i_{ja} + ai_{jb} + \alpha^2 i_{jc}),$$

$$E_j = \frac{2}{3}(E_{ja} + \alpha E_{jb} + \alpha^2 E_{jc}). \tag{6}$$

Because $(2/3)(U_{nN} + \alpha U_{nN} + \alpha^2 U_{nN}) = (2/3)(1 + \alpha + \alpha^2)U_{nN} = 0$, the vector form of the AC-side dynamic equation of the three-port SOP is as follows:

$$U_j = L \frac{d}{dt} i_j + R j_j - E_j. \tag{7}$$

The dynamic equation of DC side voltage is as follows:
where $U_{jc}$ is the DC side bus voltage, $i_{dc1}$, $i_{dc2}$, and $i_{dc3}$ represent the current on the DC side of port 1, port 2, and port 3, respectively, and $m_{ja}$, $m_{jb}$, and $m_{jc}$ are the switching functions, and the switching states can be defined as

\[
\begin{align*}
    m_{ja} &= s_a - \frac{s_a + s_b + s_c}{3}, \\
    m_{jb} &= s_b - \frac{s_a + s_b + s_c}{3}, \\
    m_{jc} &= s_c - \frac{s_a + s_b + s_c}{3},
\end{align*}
\]

where $s_a$, $s_b$, and $s_c$ are the switch states of $a$, $b$, and $c$ phases, respectively.

If all the devices in the three-port SOP are ideal components,

\[
P_1 + P_2 + P_3 = U_{dc} \sum_{j=1}^{3} m_{ja} i_{ja} + m_{jb} i_{jb} + m_{jc} i_{jc}.
\]  

(10)

Substituting (8) into (10),

\[
\sum_{j=1}^{3} P_j = U_{dc} \sum_{j=1}^{3} i_{dcj} = U_{dc} C \frac{du_{dc}}{dt}
\]  

(11)

When the system is in a steady state, the DC side voltage remains stable and the rate of DC voltage change is zero. At this time, $\sum_{j=1}^{3} P_j = 0$. That is to say, when the loss of SOP is ignored, the input and output active power of each port are conserved in the steady-state operation.

According to the instantaneous power theory, the instantaneous power expression of each port is as follows:

\[
\begin{align*}
P_j &= E_{jd} i_{jd} + E_{jq} i_{jq}, \\
Q_j &= E_{jq} i_{jd} - E_{jd} i_{jq},
\end{align*}
\]

(12)

where $P_j$ and $Q_j$ are the active and reactive power output of port $j$, $i_{jd}$ and $i_{jq}$ are the components of three-phase load output current on the $d$-axis and $q$-axis, respectively, and $E_{jd}$ and $E_{jq}$ are the components of grid voltage on the $d$-axis and $q$-axis of port $j$, respectively.

Under the condition of three-phase grid voltage balance, the $d$-axis of the dq coordinate system is oriented to the direction of the grid voltage vector $E_j$ by using the grid voltage vector oriented control. We can obtain $E_{jq} = 0$, and $E_{jd}$ is constant. Thus, (12) can be changed into

\[
\begin{align*}
P_j &= \frac{3}{2} E_{jd} i_{jd}, \\
Q_j &= \frac{3}{2} E_{jq} i_{jq},
\end{align*}
\]

(13)

It can be seen from (13) that there is a linear relationship between the power of each port and the component of the current on the $d$-axis and $q$-axis, respectively. By controlling $i_{jd}$ and $i_{jq}$, the active power and reactive power of each port of the three-port SOP can be independently controlled, so the power decoupling control is realized.

3. Control of Three-Port SOP

3.1. Traditional FCS-MPC and Its Delay Compensation Method. The traditional FCS-MPC takes the current as the target to predict. The predicted current corresponding to each voltage vector is predicted by the prediction model. The predicted current is compared with the reference current, and then, the predicted current value closest to the reference current is selected by the cost function. The switching state corresponding to the predicted current is applied to the SOP. Assuming that the sampling interval is $T_s$, after discretizing (7), the expression of the predicted current vector for each port can be obtained as follows:

\[
i_{jd}^P(k+1) = \left(1 - \frac{B_j T_s}{L_j}\right) i_{jd}(k) + \frac{T_s}{L_j} \left(U_j(k) - E_j(k)\right),
\]

(14)

where $i_{jd}^P(k+1)$ is the predicted current vector value for port $j$ at time $k+1$, $i_{jd}(k)$ represents the output current vector of port $j$ at time $k$, $E_j(k)$ represents the power network voltage vector of port $j$ at time $k$, and $U_j(k)$ represents the output voltage vector of the port $j$ at time $k$. Each port VSC can generate seven different voltage vectors according to eight different switch states. According to (3), the voltage vectors are shown in Table 1.

The traditional FCS-MPC does not consider the system delay, but in the actual system, there is a periodic delay in the control system because of the sample delay, the long execution time of the algorithm, the switch action delay, and so on. As shown in Figure 3, without considering the delay compensation, the system uses the voltage vector $U_j(k)$ of the current moment to predict the predicted current $i_{jd}^P(k+1)$ of the next moment. However, because it takes a certain time to calculate the optimal voltage vector at the current time, which is longer than the sampling interval, the selected voltage vector cannot act on time $k$ immediately. It can only act on time $k+1$, which leads to the one-beat delay. Due to the existence of calculation delay, the system can only predict $i_{jd}^P(k+1)$ (rather than $i_{jd}^P(k+2)$) and act at time $k+2$. This causes a big deviation between the predicted current and the actual current, deteriorates system performance, and increases the current ripple. Therefore, it is necessary to compensate for the time delay of the control system.

As shown in Figure 4, although it is impossible to predict $i_{jd}^P(k+1)$ at time $k$ immediately after adding delay compensation, the predicted current value at time $k$ ($i_{jd}^P(k+1)$) can be calculated first during the calculation time. The predicted current $i_{jd}^P(k+2)$ can be calculated by using $i_{jd}^P(k+1)$ at time $k+1$, and the optimal voltage vector can also be selected. After the completion of the calculation, it is possible to apply the voltage vector $U_j(k+1)$ to the control system at
(1) Calculating time

Know $U_j(k)$ is unknown

Calculate $U_j(k)$

Use $U_j(k)$

Predict $\dot{q}(k+1)$

Use $\dot{q}(k+1)$

Calculating time

$U_j(k + 1)$

$U_j(k)$

$\dot{q}(k)$

$\dot{q}(k+1)$

Figure 3: FCS-MPC without delay compensation.

(2) Calculating time

Know $U_j(k)$

Calculate $\dot{q}(k+1)$

Use $U_j(k)$

$U_j(k+1)$

$\dot{q}(k)$

$\dot{q}(k+2)$

Predict $\dot{q}(k+2)$

Use $\dot{q}(k+2)$

Calculating time

$U_j(k + 1)$

$U_j(k+1)$

$\dot{q}(k)$

$\dot{q}(k+2)$

Figure 4: FCS-MPC considering delay compensation.

time $k+1$, and $i_j^p(k+2)$ can be predicted at time $k+1$ and applied to time $k+2$.

According to (14), after adding delay compensation, the expression of the predicted current vector is

$$i_j^p(k+2) = \left(1 - \frac{R_j T_{s}}{L_j}\right) i_j^p(k+1) + \frac{T_{s}}{L_j} (U_j(k+1) - E_j(k+1)),$$

where $i_j^p(k+1)$ can be calculated by (14), $E_j(k+1)$ represents the voltage vector of port $j$ at time $k+1$, and $U_j(k+1)$ is the VSC output voltage vector of the port $j$ at time $k+1$.

The vector expression of the cost function is

$$g_j = (i_j^p(k+2) - i_{jref}(k+2))^2,$$

where $i_{jref}(k+2)$ is the reference current vector of $k+2$. Because the sampling frequency is higher than the reference signal frequency, it can be considered that $i_{jref}(k+2) = i_{jref}(k)$.

### 3.2. Improved FCS-MPC and Its Delay Compensation Method

Although FCS-MPC’s delay compensation method solves the problem of delay in the process of operation, it greatly increases the amount of computation, which makes the running speed of the overall control system slower and increases the computational burden of the processor in practical applications. To reduce the amount of computation of the control system and improve the running speed of the system, the delay compensation method needs to be optimized.

It can be seen from (15) that, in order to obtain the current prediction value $i_j^p(k+2)$, optimal voltage vector $U_j(k+1)$ must be selected by calculating (16), and then, the predicted value is calculated. If the predicted value $i_j^p(k+2)$ of $k+2$ is equal to the reference value $i_{jref}(k+2)$, the reference value of the voltage vector $U_{jref}(k+1)$ can be obtained. The reference value of this voltage is compared with corresponding switching state.
Equation (15) is transformed to obtain the vector expression of the voltage reference value:

\[
U_{jref}(k + 1) = \frac{L_j}{R_j}(i_{jref}(k + 2) - i_{jd}(k + 1)) + E_{jd}(k + 1) + Ri_{jd}(k + 1).
\]  

(17)

Since the vector expression cannot be calculated directly in the actual control system, this expression is converted to the \(dq\) coordinate system:

\[
\begin{aligned}
U_{jdref}(k + 1) &= \frac{L_j}{R_j}(i_{jdref}(k + 2) - i_{jd}(k + 1)) + E_{jd}(k + 1) + Ri_{jd}(k + 1), \\
U_{jqref}(k + 1) &= \frac{L_j}{R_j}(i_{jqref}(k + 2) - i_{jq}(k + 1)) + E_{jq}(k + 1) + Ri_{jq}(k + 1),
\end{aligned}
\]

(18)

where \(U_{jdref}(k + 1)\) and \(U_{jqref}(k + 1)\) are the components of the reference voltage in the \(dq\) coordinate system, \(i_{jdref}(k + 2)\) and \(i_{jqref}(k + 2)\) are the components of the reference current at time \(k + 2\) in the \(dq\) coordinate system, \(E_{jd}(k + 1)\) and \(E_{jq}(k + 1)\) are the components of the grid voltage in the \(dq\) coordinate system at time \(k + 1\), and \(i_{jd}(k + 1)\) and \(i_{jq}(k + 1)\) are the components of the predicted values of \(k + 1\) in the \(dq\) coordinate system.

Accordingly, a new cost function is constructed:

\[
G_j = \left(\frac{U_{jdref}(k + 1) - U_{jd}(k + 1)}{\Delta U_1}\right)^2 + e_j^2
\]

where \(U_{jd}(k + 1)\) and \(U_{jq}(k + 1)\) are the components of the output voltage vector of the port \(j\) in the \(dq\) coordinate system. Since there are eight voltage vectors, their components in the \(dq\) coordinate system \((U_{jd}(k + 1)\) and \(U_{jq}(k + 1))\) should also have eight each.

Figure 5 depicts the principle of selecting the optimal voltage vector. Assume that the reference voltage vector \(U_{jref}\) calculated by (17) is at the position shown in the figure, the errors between the reference voltage vector \(U_{jref}\) and all nonzero voltage vectors are expressed by \(\Delta U_1\) through \(\Delta U_8\), and the error with the zero vector is \(U_{jref}\). Then, we can see intuitively that the length of \(\Delta U_1\) is the smallest, which shows that the error between the reference voltage vectors \(U_{jref}\) and \(U_{jd}\) is the smallest. \(U_{jd}\) is the optimal voltage vector, and the switching state \((100)\) corresponding to \(U_{jd}\) is the optimal switching state.

It is worth noting that switching states \((000)\) and \((111)\) can both generate zero voltage vectors when the zero voltage vector is selected as the optional voltage vector. One of the switching states in \((000)\) and \((111)\) should be selected according to the principle of the minimum switching frequency. If the previous switch state is \((100)\), the current switch state should be selected as \((000)\). If the previous switch state is \((110)\), the current switch state should be selected as \((111)\).

By comparing the traditional delay compensation method with the improved delay compensation method, it can be seen that the traditional delay compensation method needs to substitute 8 output voltage vectors into (15), and then, 8 current prediction values are obtained through 8 operations. The eight predicted current values are substituted into (16), and after 8 operations, the optimal voltage vector is obtained. Therefore, a total of 16 operations are performed in one cycle. The improved delay compensation method only needs to perform one operation on (17) to obtain the reference voltage vector, and then, by comparing the reference voltage vector with 8 output voltage vectors, the optimal voltage vector can be obtained. Therefore, only 9 operations are required in one cycle, and the amount of calculation is significantly reduced.

### 3.3. Future Reference Value Compensation

The above methods assume that the future reference value is the same as the current reference value. For example, in (16)–(18), the current reference value \(i_{jref}(k + 2)\) is approximated to the time \(k\) reference value \(i_{jref}(k)\). However, when the reference signal is sinusoidal, there will be a delay in the predictive current tracking reference current, which will affect the current tracking accuracy and make the control effect worse. Therefore, it is necessary to compensate for the future reference value. In this paper, the vector angle compensation method is used to compensate for the future reference value.

The future reference value can be estimated according to the change of the vector angle of a sampling time. Because the reference current \(i_{jref}(k)\) is expressed as a vector, it can be expressed in the form of amplitude \(I_{jref}(k)\) and phase angle \(\theta\):

\[
i_{jref}(k) = I_{jref}(k)e^{j\theta(k)}.
\]

In the steady state, if the current vector rotates at an angular velocity \(\omega\) and the amplitude remains constant, the reference current phase angle at time \(k + 1\) is
where $\theta(k)$ is the reference current phase angle at time $k$ and $\omega T_s$ represents the angle that the vector has rotated in a sampling interval.

The reference current at time $k+1$ can be obtained by replacing (20) with (21):

$$i_{ref}(k+1) = i_{ref}(k)e^{j\theta(k+1)} = i_{ref}(k)e^{j\theta(k)}e^{j\omega T_s}. \quad (22)$$

Substitute (20) into (22) to get

$$i_{ref}(k+1) = i_{ref}(k)e^{j\omega T_s}. \quad (23)$$

In the same way, the grid side voltage of $k+1$ is as follows:

$$E_j(k+1) = E_j(k)e^{j\omega T_s}. \quad (24)$$

The current reference value of $k+2$ is

$$i_{ref}(k+2) = i_{ref}(k)e^{j2\omega T_s}. \quad (25)$$

### 3.4. Stability Analysis

Take port 1 as an example, the stability of SOP is analyzed, and other port analysis methods are the same. Port 1 is equivalent to the Thevenin equivalent circuit in the synchronous rotating coordinate system ($dq$ axis), as shown in Figure 6.

In the figure, $V_p$, $i_p$, and $Z_l$ are the voltage, current, and impedance on the AC side, respectively. $V_p$, $i_p$, and $Z_l$ are the voltage, current, and impedance on the DC side, respectively. $V_p$, $i_p$, and $Z_l$ are $2 \times 2$ matrices under the $dq$ axis. $V_p$, $i_p$, and $i_l$ are $2 \times 1$ vectors [25]. Then, the output voltage is

$$V_p = \frac{Z_l}{Z_s + Z_l}V_s + \frac{Z_s}{Z_s + Z_l}V_l = \frac{V_s + Z_s Y_l V_l}{E + Z_s Y_l}, \quad (26)$$

and the return-ratio matrix is defined as follows:

$$T = Z_s Y_l, \quad (27)$$

where $E$ represents the identity matrix, and $Y_l = Z_l^{-1}$. So if and only if $E + Z_s Y_l$ has no right pole, the system is stable. In other words, the necessary and sufficient condition for system stability is that the Nyquist curve of the return-ratio matrix does not enclose the ($-1$, $j0$) point. According to this concept, the stability of the system is guaranteed when the eigenvalues ($\lambda_{T_l}$ and $i = 1$ and 2) of the return-ratio matrix are outside a certain forbidden region. As shown in Figure 7, a new type of the prohibited area is adopted [26]. The criteria are as follows:

$$\left|\arg(\lambda_{T_l})\right| \leq 180^\circ - \theta_{PM}, \quad \text{Re}(\lambda_{T_l}) \geq -\epsilon_{GM}. \quad (28)$$

A is maintained outside the prohibited area to achieve system stability by using the phase margin ($\theta_{PM}$) and gain margin ($-\epsilon_{GM}$).

### 4. Overall Control Scheme Design of the System

It can be seen from (11) that, in normal operation, in order to ensure the stability of power transmission, it is necessary to keep the DC side voltage stable in the control of one port and ensure that the power is controllable in the other two ports. Meanwhile, the reactive power of each port can be controlled without the participation of DC voltage.

Based on the above analysis, VSC1 is selected as the rectifier side, VSC2 and VSC3 are selected as the inverter side to design the overall control scheme. The outer loop control scheme of VSC1 is constant DC voltage and reactive power mode ($U_{dc}, Q$ control). The outer loop control of VSC2 and VSC3 is constant active power and reactive power control mode (PQ control). Their inner loop control modes are all improved FCS-MPC with delay compensation. The purpose of the outer loop control is to provide the current reference value for the inner loop control, and the purpose of the inner loop control is to provide the switch signal for the VSC of each port.

#### 4.1. $U_{dc}, Q$ Control Mode

Figure 8 shows the control scheme of port 1, whose inner loop control is improved FCS-MPC with delay compensation and whose outer loop control is
When the $U_{d}Q$ control mode is adopted, the control goal of VSC1 is to control the voltage on the DC side and provide reactive power on the AC side. If self-loss is not considered, when the three-port SOP works stably, the active power of the system maintains a balanced transmission. So the $d$-axis current reference value can be obtained by inputting the error between the actual DC voltage and the reference value into the PI controller:

$$i_{dref}(k) = \left( k_p + \frac{k_i}{S} \right) (U_{dref} - U_d),$$  \tag{29}

where $k_p$ and $k_i$ are the parameters of the PI regulator and $U_{dref}$ is the reference value of DC voltage. According to (12), the reactive power $Q_1$ can be calculated, and the error between $Q_1$ and the reactive power reference value $Q_{1ref}$ can be input into the PI controller to obtain the $q$-axis current reference value $i_{qref}$.

The reference current obtained by the outer loop control is the reference current at time $k$. In order to compensate for the delay of the reference value, the reference current at time $k$ is compensated by the vector angle compensation method (equation (25)), and the reference currents $i_{qref}(k + 2)$ and $i_{qref}(k + 2)$ can be obtained. After collecting the three-phase voltage $E_{abc}$ of the power grid at time $k$, the three-phase current $i_{abc}$, and the inverter output voltages $U_{1ab}$, $U_{1b}$, and $U_{1c}$, they are converted into vector forms $U_1(k)$, $E_1(k)$, and $i_1(k)$ with equations (3) and (6). Then, they are input into the prediction function (equation (14)), and the predicted current value of $i_{qref}(k + 1)$ can be obtained.

According to (24), when the vector angle compensation method is used to compensate the power network voltage vector $E_1(k)$ of time $k$, the voltage vector $E_1(k + 1)$ of time $k + 1$ can be obtained. The reference voltage vector can be obtained by inputting the reference current value, current prediction value, and grid voltage prediction value into the prediction function with delay compensation (equation (17)). According to the need of the cost function, the reference voltage vector is converted to the $dq$ coordinate system to get the $dq$ axis components $U_{dref}(k + 1)$ and $U_{qref}(k + 1)$, and the output voltage vectors are converted to the $dq$ coordinate system to get the $dq$ axis components of the output voltage vectors. Then, by comparing the $dq$ axis components of the output voltage vectors and the reference voltage vector, the voltage vector that minimizes the cost function is obtained. Finally, the voltage vector is converted to the switch state and input to the VSC1 of port 1.

### 4.2. PQ Control Mode

Figure 9 shows the control schemes of port 1 and port 3. Because their control schemes are similar, port 2 is taken as an example to analyze its control scheme. The inner loop control of port 2 is an improved FCS-MPC with delay compensation, but the outer loop control is PQ control. When using PQ control, VSC2 aims to control active power $P$ and reactive power $Q$. According to (12), the control mode of active power and reactive power can be designed. But there are some problems, such as large parameter error and slow control speed. Therefore, the PQ outer loop control in this paper adopts the steady-state inverse model. Compared with the traditional model, there is no feedback link, but it can improve the dynamic performance. By inverting (12), the formula of the steady-state inverse model can be obtained:

$$
\begin{align*}
    i_{2d} & = \frac{2}{3} E_{2d} P_{2ref} + E_{2q} Q_{2ref}, \\
    i_{2q} & = \frac{2}{3} E_{2q} P_{2ref} - E_{2d} Q_{2ref},
\end{align*}
$$

\tag{30}

where $P_{2ref}$ and $Q_{2ref}$ are the reference values of active and reactive power, respectively, $i'_{2d}$ and $i'_{2q}$ are the reference predictions, and $E_{2d}$ and $E_{2q}$ are the component of the grid voltage at port 2 on the $dq$ axis.

As shown in Figure 10, the steady-state inverse model structure can be constructed according to (30). Combined with Figures 9 and 10, the outer loop control process of VSC2 is analyzed. According to (12), the actual values of
power $P_2$ and $Q_2$ are calculated. Then, they are input into the steady-state inverse model. The reference predictions of the $dq$ axis components of the output current ($i'_{2d}$ and $i'_{2q}$) can be obtained from (30). Through the PI controller, the differences between the actual values of active and reactive power and the corresponding reference values are converted into modified values of the corresponding current components ($\Delta i_d$ and $\Delta i_q$), and then, the current reference values ($i_{2qref}(k)$ and $i_{2pref}(k)$) are obtained by adding them. The inner loop control process is the same as above.

5. Simulation Analysis

In order to verify the effectiveness of the proposed control strategy, the three-port SOP simulation model shown in Figure 2 is built in MATLAB/Simulink. It is assumed that when the three-port SOP is running normally, port 1 (VSC1) runs in $U_{dc,Q}$ mode, while port 2 (VSC2) and port 3 (VSC3) run in the PQ mode. The simulation parameters are shown in Table 2.

5.1. Steady-State Operation Control. The DC side reference voltage of port 1 is set to 200 V; the reference current of port 2 is 25 A and that of port 3 is 20 A. Figures 10(a) and 11(a) show the $A$-phase current waveforms of port 2 and port 3, respectively. It can be seen from the figure that the actual current can accurately track the reference current and there is no steady-state error. Figures 11(b) and 12(b) show the three-phase output current of port 2 and port 3, respectively. It can be seen that although the reference current of the two ports is different, the quality of their output current is still very high. Figures 11(c) and 12(c) show the output power of port 2 and port 3, respectively. It can be seen from the figure that the steady-state power output is also very stable and the waveform is smooth and does not fluctuate. Figure 13 shows the DC side voltage waveform; it can be seen that the DC voltage only needs less than 0.01 s to track the reference value, and the voltage can be kept stable without fluctuation after reaching the steady state. Figure 14 shows the power transmission diagram of the three ports; it can be seen that the stability of the DC side voltage can ensure the stable power transmission. The output power of one port is equal to the output power of the other two ports.

5.2. Dynamic Operation Control. Assuming that, at 0.25 s, there is a 5 A fluctuation in the reference current of port 3. The change of the waveform is shown in Figure 15. To facilitate the analysis, the output current is converted to the $dq$ coordinate system, as shown in Figures 13 and 14. As can be seen from Figure 15(a), the current can respond quickly and reach a steady state again when it encounters a step change, and the whole step process takes less than 0.4 ms. At the same time, Figure 15(b) shows the change of power corresponding to the change of the current on the $dq$ axis. The $d$-axis current component changes from 20 A to 25 A, and

![Figure 9: The control scheme of port 2 and port 3.](image)

![Figure 10: Steady-state inverse model.](image)

| Table 2: System simulation parameters. |
|---------------------------------------|
| System parameters | Numerical value |
| Grid voltage amplitude, $E$ (V)       | 311             |
| Filter inductance, $L$ (mH)           | 10              |
| AC side equivalent resistance, $R$ (Ω) | 0.3             |
| DC side capacitor, $C$ (μf)           | 800             |
| DC side voltage, $U_{dc}$ (V)         | 200             |
| Sampling interval, $t$ (μs)           | 10              |

![Table 2: System simulation parameters.](image)
Figure 11: Response waveform of port 2 under improved FCS-MPC. (a) A-phase current waveform. (b) Port output current waveform. (c) Port output power.
Figure 12: Response waveform of port 3 under improved FCS-MPC. (a) A-phase current waveform. (b) Port output current waveform. (c) Port output power.
the active power changes from 6.2 kW to 7.7 kW. The $q$-axis current component and the reactive power remain equal to zero, which is not affected by the current step. It shows that both current components of $dq$ axis and power can be decoupled. Figure 15(c) shows a three-phase output current, which indicates that the high-quality power output can be maintained after the current change.

Figures 16(a)–16(c) show the change of the current component on the $dq$ axis, power, and three-phase current of port 2, respectively. It can be seen that the control of port 2 can keep the output current and power stable. Figure 17 shows the DC side voltage waveform. It can be seen from the figure that the DC voltage does not change, which indicates that the control of port 1 can keep the DC side voltage stable. The current fluctuation of port 3 does not affect the control effect of other ports, which shows that the three ports can be controlled independently and realize the function of fault isolation. Figure 18 shows the power transmission waveform among the three ports. It can be seen from the figure that the power change of port 3 will cause the power change of port 1, which indicates that the power transmission between ports is in dynamic balance and the transmission is stable.

5.3. Comparison and Analysis

5.3.1. Comparative Analysis with Double Closed-Loop Control. In the actual operation process, the system often needs to adjust the size of the reference value according to the needs. The response speed of the two control schemes is compared by setting the reference current from 20 A to 25 A. Figures 19(a) and 19(b) show the change of the current component of the $dq$ axis under the double closed-loop control and the improved FCS-MPC, respectively. It can be seen from the figure that the current under the double closed-loop control needs to go through the step process of
Figure 15: Waveform change of port 3 when reference current fluctuates. (a) Load current component on the dq axis. (b) Diagram of power change. (c) Three-phase current variation diagram.
Figure 16: Waveform change of port 2 when reference current fluctuates. (a) Load current component on the \(dq\) axis. (b) Diagram of power change. (c) Three-phase current variation diagram.
Figure 17: DC side voltage change. Change of transmission power of three ports when reference current fluctuates.

Figure 18: Change of transmission power of three ports when reference current fluctuates.

Figure 19: Continued.
Figure 19: Comparison of current components of the $dq$ axis under two control schemes in terms of current changes. (a) Double closed-loop control. (b) Improved FCS-MPC.

Figure 20: Continued.
Figure 20: Comparison of three-phase current under two control schemes in terms of current changes. (a) Double closed-loop control. (b) Improved FCS-MPC.

Figure 21: Continued.
Figure 21: Comparison of DC side voltage under two control schemes in the steady state. (a) FCS-MPC. (b) Improved FCS-MPC.

Figure 22: Continued.
Figure 22: Comparison of A-phase current under two control schemes in the steady state. (a) FCS-MPC. (b) Improved FCS-MPC.

Figure 23: Continued.
1.8 ms to reach the steady state again, whereas the current of the improved FCS-MPC only needs 0.4 ms to reach the steady state again. It can be seen from Figures 20(a) and 20(b) that regardless of the initial time or step time, the steady-state output current of the improved FCS-MPC is faster than that of the double closed-loop control, which shows that the improved FCS-MPC control scheme has the characteristic of rapid response. Not only that, but the output current ripple of the improved FCS-MPC is also lesser, which is beneficial to the improvement of power quality.

5.3.2. Comparative Analysis with FCS-MPC. FCS-MPC itself also has a faster response speed, but due to its large amount of calculation, it will cause current delay and other problems, which will reduce the accuracy of the actual current tracking reference current. The reference current is set to 20 A, and the DC side reference voltage is set to 200 V to compare the steady-state current and voltage tracking accuracy of FCS-MPC and improved FCS-MPC. Figure 19 displays a comparison of the DC side voltage tracking effect. Comparing Figures 21(a) and 21(b), it can be seen that, at the same sampling frequency, the DC side voltage of the improved FCS-MPC is closer to the reference voltage and the tracking effect is better. The DC side voltage of FCS-MPC has more ripples. Excessive ripples will increase the loss of the power grid and reduce transmission efficiency. Figure 20 displays a comparison of the current tracking effect of A phase. The current delay will cause a large deviation when the actual current tracks the reference current. Comparing Figures 22(a) and 22(b), it can be seen that due to the obvious deviation between the reference current and the actual current, the current of FSC-MPC has a relatively serious delay problem, but due to the small deviation between the reference current and the actual current, the improved FCS-MPC can solve the current delay problem and improve the current tracking accuracy. Figure 23 displays a three-phase output current comparison. Comparing Figures 23(a) and 23(b), it can be seen that the three-phase output current waveform of the improved FCS-MPC is smoother, which can effectively improve the power quality of the distribution network.

6. Conclusion
To realize the decoupling control between the variables of the system, this paper uses the vector method and $dq$ coordinate transformation method to establish a three-port SOP mathematical model. The control scheme designed in this paper combines the advantages of FCS-MPC while improving its shortcomings. In view of a large amount of calculation and the delay problem of FCS-MPC, the two-step prediction method based on the voltage vector and the vector angle compensation method is used in combination to compensate for the current delay problem in this paper. The control target is converted from the grid-connected current to the output voltage vector of each port of SOP to reduce the amount of calculation, and then, an improved FCS-MPC is proposed. Using the improved FCS-MPC instead of the inner loop current control of the double closed-loop control can reduce PI parameters and reduce the difficulty of the control system design. The simulation results show that the proposed control scheme improves the accuracy of the current tracking reference value while ensuring fast dynamic response speed, effectively reduces the current and voltage ripple, improves the output power quality, and improves the power distribution network, which provide new ideas for improving the flexibility and stability of the distribution network.

When the system is subjected to external interference, changes in the operating environment, saturation of the
magnetic circuit of electromagnetic components, system overload, etc., the parameters of the resistance, inductance, capacitance, and other components in the SOP will change. The change of the parameter will also affect the predicted value and lead to a decrease in control performance. Therefore, the next step of the research will consider using discrete parameter observers to estimate the inductance and capacitance values of the system in real time. Since the change of parameters can be regarded as a kind of disturbance, the system model can be compensated in real time by establishing a disturbance observer to observe the change of disturbance.

Data Availability

The data used to support the findings of this study are included within the article.

Conflicts of Interest

The authors declare that there are no conflicts of interest regarding the publication of this paper.

Acknowledgments

The authors thank LetPub (https://www.letpub.com) for its linguistic assistance and scientific consultation during the preparation of this manuscript. This study was supported by the Natural Science Research Project of Jiangsu Higher Education Institution (18KJD470004), Strategic Priority Research Program of Chinese Academy of Sciences (XDA21050304), Open Research Fund of Jiangsu Collaborative Innovation Center for Smart Distribution Networks (XTCX201809), Fundamental Research Special Project of NJIT (JCY201816), University Student Science and Technology Innovation Fund Project of NJIT (TB20211638 and TB20211644), and Postgraduate Research & Practice Innovation Program of Jiangsu Province (JXCG2019036).

References

[1] R. Wang, Q. Sun, D. Ma, and Z. Liu, “The small-signal stability analysis of the droop-controlled converter in electromagnetic timescale,” IEEE Transactions on Sustainable Energy, vol. 10, no. 3, pp. 1459–1469, 2019.
[2] Q. Sun, R. Han, H. Zhang, J. Zhou, and J. M. Guerrero, “A multiagent-based consensus algorithm for distributed coordinated control of distributed generators in the energy internet,” IEEE Transactions on Smart Grid, vol. 6, no. 6, pp. 3006–3019, 2015.
[3] I. Konstantelos, S. Giannelos, and G. Strbac, “Strategic valuation of smart grid technology options in distribution networks,” IEEE Transactions on Power Systems, vol. 32, no. 2, pp. 1293–1307, 2017.
[4] C. Long, J. Wu, L. Thomas, and N. Jenkins, “Optimal operation of soft open points in medium voltage electrical distribution networks with a distributed generation,” Applied Energy, vol. 184, pp. 427–437, 2016.
[5] W. Cao, J. Wu, N. Jenkins, C. Wang, and T. Green, “Operating principle of soft open points for electrical distribution network operation,” Applied Energy, vol. 164, pp. 245–257, 2016.
[6] W. Cao, J. Wu, N. Jenkins, C. Wang, and T. Green, “Benefits analysis of soft open points for electrical distribution network operation,” Applied Energy, vol. 165, pp. 36–47, 2016.
[7] P. Li, H. Ji, C. Wang et al., “Coordinated control method of voltage and reactive power for active distribution networks based on soft open point,” IEEE Transactions on Sustainable Energy, vol. 8, no. 4, pp. 1430–1442, 2017.
[8] J. Wang, N. Zhou, C. Y. Chung, and Q. Wang, “Coordinated planning of converter-based DG units and soft open points incorporating active management in unbalanced distribution networks,” IEEE Transactions on Sustainable Energy, vol. 11, no. 3, pp. 2015–2027, 2020.
[9] P. Li, H. Ji, C. Wang et al., “Optimal operation of soft open points in active distribution networks under three-phase unbalanced conditions,” IEEE Transactions on Smart Grid, vol. 10, no. 1, pp. 380–391, 2019.
[10] F. Sun, J. Ma, M. Yu, and W. Wei, “Optimized two-time scale robust dispatching method for the multi-terminal soft open point in unbalanced active distribution networks,” IEEE Transactions on Smart Grid, vol. 12, no. 1, pp. 587–598, 2021.
[11] H. Ji, C. Wang, P. Li, F. Ding, and J. Wu, “Robust operation of soft open points in active distribution networks with high penetration of photovoltaic integration,” IEEE Transactions on Sustainable Energy, vol. 10, no. 1, pp. 280–289, 2019.
[12] T. Ding, Z. Wang, W. Jia, B. Chen, C. Chen, and M. Shahidehpour, “Multi-period distribution system restoration with routing repair crews, mobile electric vehicles, and soft-open-point networked microgrids,” IEEE Transactions on Smart Grid, vol. 11, no. 6, pp. 4795–4808, 2020.
[13] S. Sun, W. Cong, Y. Sheng, M. Chen, and Z. Wei, “Distributed power service restoration method of distribution network with soft open point,” in Proceedings of the 2020 IEEE/IAS Industrial and Commercial Power System Asia, pp. 1616–1621, Weihai, China, July 2020.
[14] S. Ouyang, J. Liu, Y. Yang, X. Chen, S. Song, and H. Wu, “DC voltage control strategy of three-terminal medium-voltage power electronic transformer-based soft normally open points,” IEEE Transactions on Industrial Electronics, vol. 67, no. 5, pp. 3684–3695, 2020.
[15] Y. Hou, Y. Xu, Z. Wang, X. Chen, and H. Cui, “Research on application of three-port SNOP based on dual closed-loop control in distribution network,” in Proceedings of the 2017 IEEE International Conference on Smart Technologies and Management for Computing, Communication, Controls, Energy and Materials, pp. 389–395, Chennai, India, August 2017.
[16] B. Li, Y. Liang, G. Wang, H. Li, and J. Ding, “A control strategy for soft open points based on adaptive voltage droop outer-loop control and sliding mode inner-loop control with feedback linearization,” International Journal of Electrical Power & Energy Systems, vol. 122, 2020.
[17] B. Yu, W. Song, J. Li, B. Li, and M. S. R. Saeed, “Improved finite control set model predictive current control for five-phase VSIs,” IEEE Transactions on Power Electronics, vol. 36, no. 6, pp. 7038–7048, 2021.
[18] C. Zheng, T. Dragičević, Z. Zhang, J. Rodriguez, and F. Blaabjerg, “Model predictive control of LC-filtered voltage source inverters with optimal switching sequence,” IEEE Transactions on Power Electronics, vol. 36, no. 3, pp. 3422–3436, 2021.
[19] A. Sarajian, C. Garcia, Q. Guan et al., “Overmodulation methods for modulated model predictive control and space vector modulation,” IEEE Transactions on Power Electronics, vol. 36, no. 4, pp. 4549–4559, 2021.
[20] H. Makhamreh, M. Trabelsi, O. Kükrer, and H. Abu-Rub, “A Lyapunov-based model predictive control design with reduced sensors for a PUC7 rectifier,” *IEEE Transactions on Industrial Electronics*, vol. 68, no. 2, pp. 1139–1147, 2021.

[21] X. Zhang, G. Tan, T. Xia, and X. Wu, “Optimized switching finite control set model predictive control of NPC single-phase three-level rectifiers,” *IEEE Transactions on Industrial Electronics*, vol. 35, no. 10, pp. 10097–10108, 2020.

[22] Y. Jin, X. Qian, J. Hou et al., “A novel sliding-discrete-control-set modulated model predictive control for modular multi-level converter,” *IEEE Access*, vol. 9, pp. 10316–10327, 2021.

[23] W. Hu, C. Ruan, H. Nian, and D. Sun, “Zero-sequence current suppression strategy with common-mode voltage control for open-end winding PMSM drives with common DC bus,” *IEEE Transactions on Industrial Electronics*, vol. 68, no. 6, pp. 4691–4702, 2021.

[24] B. Peng and G. Zhang, “Coordination control strategy for three-port SNOP based on FCS-MPC,” *The Journal of Engineering*, vol. 2019, no. 16, pp. 1005–1010, 2019.

[25] W. Rui, S. Qiuye, M. Dazhong, and H. Xuguang, “Line impedance cooperative stability region identification method for grid-tied inverters under weak grids,” *IEEE Transactions on Smart Grid*, vol. 11, no. 4, pp. 2856–2866, 2020.

[26] R. Wang, Q. Sun, W. Hu, Y. Li, D. Ma, and P. Wang, “SoC-based droop coefficients stability region analysis of the battery for stand-alone supply systems with constant power loads,” *IEEE Transactions on Power Electronics*, vol. 36, no. 7, pp. 7866–7879, 2021.