Hardware Impairments in Large-scale MISO Systems: Energy Efficiency, Estimation, and Capacity Limits

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Abstract—The use of large-scale antenna arrays has the potential to bring substantial improvements in energy efficiency and/or spectral efficiency to future wireless systems, due to the greatly improved spatial beamforming resolution. Recent asymptotic results show that by increasing the number of antennas one can achieve a large array gain and at the same time naturally decorrelate the user channels; thus, the available energy can be focused very accurately at the intended destinations without causing much inter-user interference. Since these results rely on asymptotics, it is important to investigate whether the conventional system models are still reasonable in the asymptotic regimes. This paper analyzes the fundamental limits of large-scale multiple-input single-output (MISO) communication systems using a generalized system model that accounts for transceiver hardware impairments. As opposed to the case of ideal hardware, we show that these practical impairments create finite ceilings on the estimation accuracy and capacity of large-scale MISO systems. Surprisingly, the performance is only limited by the hardware at the single-antenna user terminal, while the impact of impairments at the large-scale array vanishes asymptotically. Furthermore, we show that an arbitrarily high energy efficiency can be achieved by reducing the power while increasing the number of antennas.

I. INTRODUCTION

The spectral efficiency in wireless communications is not only limited by the information-theoretic capacity [1], but also by practical issues such as propagation environment, channel estimation accuracy [2], transceiver hardware impairments [3], and signal processing complexity [4]. It is of profound importance to increase the spectral efficiency in future networks, to keep up with the increasing demand for wireless services. However, this is a challenging task and usually comes at the price of having stricter hardware and overhead requirements.

A new network architecture has recently been proposed with the remarkable potential of both increasing the spectral efficiency and taking care of the aforementioned practical issues. It is known as large-scale multiple-input multiple-output (MIMO), or “massive MIMO”, and is based on having a large number of antennas at the base stations and exploiting channel reciprocity in time-division duplex (TDD) mode [4]–[7]. Some key features are: 1) propagation losses are mitigated by a large array gain due to coherent beamforming; 2) impact of channel estimation errors vanishes asymptotically in the large-dimensional space; 3) low-complexity signal processing algorithms are asymptotically optimal; and 4) inter-user interference is easily mitigated by the high beamforming resolution.

The impact of transceiver hardware impairments on large-scale MIMO has received little attention, although large arrays might only be economically feasible if inexpensive hardware can be used at each antenna unit. Cheap hardware components are particularly prone to the impairments (e.g., amplifier nonlinearities, I/Q-imbalance, and phase noise) that exist in any physical transceiver implementation [8]–[11]. Transceiver impairments fundamentally limit the capacity in the high-power regime [3], while their impact in the large-antenna regime is less known. To the best of our knowledge, the only previous work in this area is [12] which derives lower bounds on the achievable uplink sum rate for systems with phase noise.

This paper analyzes the aggregate impact of hardware impairments on systems with large antenna arrays, in contrast to the ideal hardware considered in [9]–[11] and the single type of impairments in [12]. Section II reviews the generalized system model with hardware impairments from [8]–[11]. Section III derives a new pilot-based channel estimator and shows that the estimation accuracy is limited by the level of impairments. Section IV derives lower and upper bounds on the downlink capacity and shows that there exists a finite capacity ceiling in large-scale MISO systems with impairments. Despite these discouraging results, Section V shows that a very high energy efficiency and resilience towards hardware impairments at the base station can be achieved. For brevity, we consider a single-user system, but extensions to multi-cell scenarios (e.g., with pilot contamination) are outlined throughout the manuscript.

II. SYSTEM MODEL

This paper analyzes the fundamental spectral and energy efficiency limits of a communication system with an N-antenna base station (BS) and a single-antenna user terminal (UT). A main characteristic in the analysis will be that the number of antennas $N$ can be very large. To avoid infeasible overhead signaling, we consider a TDD system where channel
state information (CSI) is obtained by pilot signaling in the uplink. The acquired CSI will be utilized for downlink data transmission, by exploiting channel reciprocity; see Fig. 1.

The flat-fading channel between the base station and the user terminal is denoted \( h \in \mathbb{C}^{N \times 1} \). This stochastic channel has a static realization for the duration of a data block and independent realizations between blocks. Each realization is circularly-symmetric complex Gaussian: \( h \sim \mathcal{CN}(0, R) \). This is known as Rayleigh fading and the positive semi-definite covariance matrix is \( \mathbb{E}\{hh^H\} = R \in \mathbb{C}^{N \times N} \), where \( \mathbb{E}\{\cdot\} \) denotes expectation. We assume that the spectral norm of \( R \) is uniformly bounded, irrespective of the number of antennas \( N \). This is a common technical assumption that enables asymptotic analysis (cf. [6]), but which is also a necessary physical property that originates from the law of energy conservation.

Physical transceivers suffer from hardware impairments that 1) create a mismatch between the intended data signal and what is actually generated and emitted; and 2) distort the signal 1) create a mismatch between the intended data signal and what is actually generated and emitted; and 2) distort the signal 1) create a mismatch between the intended data signal and what is actually generated and emitted; and 2) distort the signal. The independent additive noise \( \eta \sim \mathcal{CN}(0, \sigma^2) \) in (5), which is essentially the same relation. Although the distortion noises are Gaussian when conditioning on a channel realization; see Fig. 1. Similar to (2), we consider a generalized model with the received signal \( z \in \mathbb{C}^N \) being

\[
\mathbf{z} = \mathbf{h}(d + \nu^T_r) + \mathbf{\nu} + \eta_r^B S
\]

where \( d \in \mathbb{C} \) is a scalar deterministic pilot signal with power \( p^U_T = |d|^2 \). The independent additive noise \( \mathbf{\nu} \sim \mathcal{CN}(0, \mathbf{S}) \) has a positive definite covariance matrix \( \mathbf{S} \in \mathbb{C}^{N \times N} \). This term contains both receiver noise and potential interference from neighboring systems (e.g., pilot contamination [3]–[7]). We assume that \( \mathbf{S} \) has a uniformly bounded spectral norm as \( N \to \infty \), for the same physical reasons as for \( R \).

The uplink impairments in the hardware used for uplink transmission are modeled by the independent distortion noises \( \nu_r^U T \sim \mathcal{CN}(0, \nu_r^U T) \) and \( \eta_r^B S \sim \mathcal{CN}(0, \nu_r^B S) \) at the transmitter and receiver, respectively. Similar to [3], their covariance matrices are model as

\[
\mathbf{\gamma}^B_S = \kappa_r^B S p^U_T \text{diag}([|h_1|^2, \ldots, |h_N|^2])
\]

\[
\nu_r^U T = \kappa_r^U T p^U_T \mathbf{w}
\]

Note that we might have \( \kappa_r^B S \neq \kappa_r^U T \) at the base station and \( \kappa_r^U T \neq \kappa_r^B S \) at the user terminal since different pieces of hardware are generally used for transmission and reception.

### III. Uplink Channel Estimation

This section considers estimation of the current channel realization \( h \) by comparing the received uplink signal \( z \) in (4) with the predefined pilot signal \( d \). The classic results on pilot-based channel estimation consider Rayleigh fading channels that are observed in independent complex Gaussian noise with known statistics [13], [14]. However, this is not the case herein because the distortion noises \( \eta_r^U T \) and \( \eta_r^B S \) depend on the unknown random channel \( h \). The dependence is either through the multiplication \( h^H \eta_r^U T \) or the conditional variance of \( \eta_r^B S \) in (5), which is essentially the same relationship. Although the distortion noises are Gaussian when conditioning on a channel realization, the effective distortion is the product of Gaussian variables and, thus, has a complex double Gaussian distribution [15]. Consequently, an optimal channel estimator cannot be deduced from the standard results provided in [13], [14].

We will now derive the linear minimum mean square error (LMMSE) estimator. Note that (*) denotes conjugation.

**Theorem 1.** The LMMSE estimator of \( h \) from the observation of \( z \) in (4) is

\[
\hat{h} = d^* R^T (p^U_T (1 + \kappa_t^B T) R + p^U_T \kappa_r^B S R_{\text{diag}} + S)^{-1} z
\]

where \( R_{\text{diag}} = \text{diag}(r_{11}, \ldots, r_{NN}) \) consists of the diagonal elements of \( R \). The total MSE is \( \mathbb{E}[\|h - \hat{h}\|^2] \) is tr(\( C \)), where \( C = \mathbb{E}[(h - \hat{h})(h - \hat{h})^H] \) is the error covariance matrix:

\[
C = R - p^U_T R (p^U_T (1 + \kappa_t^B T) R + p^U_T \kappa_r^B S R_{\text{diag}} + S)^{-1} R
\]

*1The total transmit power is \( p^B S (1 + \kappa_t^B T) \) under hardware impairments and not the usual \( p^B S \), since distortions also contribute a small amount of power. For simplicity, we will still refer to the signal power \( p^B S \) as the transmit power since the total power is fully characterized by and is very close to \( p^B S \).

*2The square root of each \( \kappa \)-parameter equals the error vector magnitude (EVM), which is a common quality measure of transceivers.
**Proof:** The LMMSE estimator has the form \( \hat{h} = A z \) where \( A \) minimizes MSE = \( \mathbb{E}\{ \| h - \hat{h} \|^2 \} \). An expression is obtained by taking expectations over \( \eta_{\text{UT}}^{\text{BS}} \) for an arbitrary fixed \( h \) and then taking the expectation over \( h \). The LMMSE estimator in (6) follows from straightforward differentiation and \( C \) and the MSE then follow from the definitions.

We remark that there might exist non-linear estimators that outperform the LMMSE estimator in Theorem 1. This stands in contrast to conventional channel estimation with independent Gaussian noise, where the LMMSE estimator is also the MMSE estimator [14, Remark 1]. However, the difference in MSE performance should be small, since the dependent distortion noises are relatively weak.

**Corollary 1.** Consider the special case of \( R = \lambda I \) and \( S = \sigma_{\text{BS}}^2 I \). The error covariance matrix in (7) becomes

\[
C = \lambda \left( 1 - \frac{p_{\text{UT}}^\lambda}{p_{\text{UT}}^\lambda (1 + \kappa_{\text{UT}}^\lambda + \kappa_{\text{BS}}^\lambda) + \sigma_{\text{BS}}^2} \right) I. \quad (8)
\]

In the high uplink power regime, we have

\[
\lim_{p_{\text{UT}} \to \infty} C = \lambda \left( 1 - \frac{1}{1 + \kappa_{\text{UT}}^\lambda + \kappa_{\text{BS}}^\lambda} \right) I. \quad (9)
\]

This corollary brings important insights on the average estimation error per element in \( h \). The error variance is given by the factor in front of the identity matrix in (8). It is independent of the number of antennas \( N \), thus letting \( N \) grow large will neither increase nor decrease the estimation error per element. The estimation error is clearly a decreasing function of the pilot power \( p_{\text{UT}} \), however the error variance will not converge to zero as the pilot power is increased. As seen in (9), there is a strictly positive error floor of \( \lambda (1 - \frac{1}{1 + \kappa_{\text{UT}}^\lambda + \kappa_{\text{BS}}^\lambda}) \) due to the transceiver hardware impairments. Thus, perfect estimation accuracy cannot be achieved in practice, not even asymptotically. The error floor is characterized by the sum of the levels of impairments \( \kappa_{\text{UT}}^\lambda, \kappa_{\text{BS}}^\lambda \) in the transmitter and receiver hardware, respectively. In terms of estimation accuracy, it is therefore equally important to have high-quality hardware at both the base station and the user terminal.

There will be an error floor also for non-diagonal \( R \) and \( S \); the general high-power limit is easily computed from (7).

The channel can be decomposed as \( h = \hat{h} + \epsilon \), where \( \hat{h} \) is the LMMSE estimate in Theorem 1 and \( \epsilon \in \mathbb{C}^{N \times 1} \) denotes the unknown estimation error. Contrary to conventional estimation with independent noise (cf. [13] Chapter 15.8), \( \hat{h} \) and \( \epsilon \) are neither independent nor jointly complex Gaussian, but only uncorrelated and zero-mean. The covariance matrices are \( \mathbb{E}\{h \hat{h}^H\} = R - C \) and \( \mathbb{E}\{ \epsilon \epsilon^H\} = C \) with \( C \) given in (7).

### A. Numerical Illustration

The estimation accuracy of the LMMSE estimator under hardware impairments is illustrated in Fig. 2 with \( N = 10 \) and \( N = 100 \) antennas. The channel covariance matrices \( R \) are generated using the exponential model with correlation coefficient 0.7 [16], while the noise covariance is \( S = I \). Fig. 2 shows the (normalized) estimation error per channel element as a function of the average SNR, defined as \( p_{\text{UT}}^\lambda \text{tr}(R) / \text{tr}(S) \).

Four hardware setups with different levels of impairments are considered: \( \kappa_{\text{UT}}^\lambda = \kappa_{\text{BS}}^\lambda \in \{0, 0.05^2, 0.10^2, 0.15^2\} \). Fig. 2 confirms that there are non-zero error floors at high SNRs, as proved by Corollary 1. The error floor increases when increasing the level of impairments. The estimation error is very close to the floor when the uplink SNR reaches 20-30 dB, thus further increase in SNR only brings minor improvement. Moreover, the curves for \( N = 10 \) and \( N = 100 \) are virtually the same, showing that the error per antenna is almost independent of \( N \) even under high spatial channel correlation.

### IV. Downlink Data Transmission

This section analyzes the capacity of the downlink channel in (2), where a linear beamforming vector \( w \) is applied to transform the MISO channel \( h \) into an effective single-input single-output (SISO) channel \( h^T w \in \mathbb{C} \). The beamforming vector is a function of the estimate \( \hat{h} \) obtained at the base station and the statistical properties of other channel parameters.

The receiver has a statistical characterization of the effective channel \( h^T w \), obtained from the long-term statistics and possibly some additional downlink pilot signaling. We leave the extent of CSI at the user terminal undefined since the bounds derived in this section hold for anything between no downlink pilot signaling and perfect CSI estimation.

Under these conditions, the capacity of the generalized memoryless fading downlink channel in (2) is

\[
C = \mathbb{E}\left\{ \max_{w(h)} \left( \max_{s, y} \mathcal{I}(s; y) : f(s) \leq \rho_{\text{BS}} \right) \right\} \quad (10)
\]

where \( \mathcal{I}(s; y) \) denotes the mutual information between the received signal \( y \) and data signal \( s \). The beamforming vector \( w(h) \) can be any deterministic function of \( h \), while \( f(s) \) denotes the probability distribution of the data signal. The expression in (10) combines the well-known capacity formulas from [17] with a maximization over \( w(h) \). The expectation is taken over different data blocks with static values on \( h, \hat{h} \).

We will characterize the capacity by deriving lower and upper bounds on (10) and study their behavior in the limit of...
infinitely many transmit antennas $N$. This brings insights on how hardware impairments affect large-scale MISO systems.

A. Upper Bound on Channel Capacity

An upper bound on (10) can be achieved by adding side information at the transmitter and receiver. We assume that the channel realization $h$ is known perfectly at the transmitter and receiver in each fading block. Since the receiver and distortion noise in (2) are complex Gaussian and independent of the useful signal under perfect CSI, we deduce that Gaussian signaling, $s \sim \mathcal{CN}(0, p_{BS}^2)$, is optimal [1, 3] and obtain

$$C \leq \mathbb{E} \left\{ \max_{w,h: \|w\|=1} \log_2 (1 + \text{SINR}(w)) \right\}$$

where (11)

$$\text{SINR}(w) = \frac{|h^T w|^2}{\kappa_t^{BS} \sum_{i=1}^N |h_i w_i|^2 + \kappa_{UT} h^T w h^T + \sigma_{p,BS}^2}.$$  (12)

To find an upper bound in closed form, we first need to find the optimal beamforming vector $w$ on the channel capacity.

**Lemma 1.** The SINR in (12) is maximized as

$$\max_{w: \|w\|=1} \text{SINR}(w) = h^T \left( \kappa_t^{BS} D_h + \kappa_{UT} h^T h^T + \sigma_{p,BS}^2 \right)^{-1} h^*$$

for $D_h = \text{diag}(|h_1|^2, \ldots, |h_N|^2)$. The optimum is attained by

$$w = \frac{\left( \kappa_t^{BS} D_h + \sigma_{p,BS}^2 I \right)^{-1} h^*}{\| \left( \kappa_t^{BS} D_h + \sigma_{p,BS}^2 I \right)^{-1} h^* \|},$$

where

$$G = \sum_{i=1}^N 1 \left( 1 - \frac{\sigma_{p,UT}^2}{p_{BS} \kappa_t^{BS} r_{ii}} \right) E_1 \left( \frac{\sigma_{p,UT}^2}{p_{BS} \kappa_t^{UT} r_{ii}} \right) e^{-\frac{\sigma_{p,UT}^2}{p_{BS} \kappa_t^{UT} r_{ii}}},$$

and $E_1(x) = \int_1^\infty \frac{e^{-tx} tx}{t} dt$ denotes the exponential integral.

**Proof:** Using Lemma [1] and the Woodbury identity, the upper bound in (11) can be expressed as $\mathbb{E} \{ m(h) \} = \mathbb{E} \{ m(\psi) \} = \mathbb{E} \{ m(h^T \psi) \}$, where $\psi = h^T (\kappa_t^{BS} D_h + \sigma_{p,BS}^2 I)^{-1} h^*$. Since $m(.)$ is a concave function, we apply Jensen’s inequality to obtain a new upper bound $\mathbb{E} \{ m(h) \} = \mathbb{E} \{ m(\psi) \}$ Finally, (15) and (16) are obtained by evaluating $\mathbb{E} \{ m(\psi) \}$.

This closed-form upper bound provides important insights on the achievable performance under hardware impairments.

**Theorem 2.** The upper capacity bound in (10) is upper bounded as

$$C \leq C_{\text{upper}} = \log_2 \left( 1 + \frac{G}{1 + \kappa_{UT}^N} \right).$$

$$\text{where } r_{11}, \ldots, r_{NN} \text{ are the diagonal elements of } R.$$  (15)

$$G = \sum_{i=1}^N 1 \left( 1 - \frac{\sigma_{p,UT}^2}{p_{BS} \kappa_t^{BS} r_{ii}} \right) E_1 \left( \frac{\sigma_{p,UT}^2}{p_{BS} \kappa_t^{UT} r_{ii}} \right) e^{-\frac{\sigma_{p,UT}^2}{p_{BS} \kappa_t^{UT} r_{ii}}},$$

and $E_1(x) = \int_1^\infty \frac{e^{-tx} tx}{t} dt$ denotes the exponential integral.

Proof: Using Lemma [1] and the Woodbury identity, the upper bound in (11) can be expressed as $\mathbb{E} \{ m(h) \} = \mathbb{E} \{ m(\psi) \} = \mathbb{E} \{ m(h^T \psi) \}$, where $\psi = h^T (\kappa_t^{BS} D_h + \sigma_{p,BS}^2 I)^{-1} h^*$. Since $m(.)$ is a concave function, we apply Jensen’s inequality to obtain a new upper bound $\mathbb{E} \{ m(h) \} = \mathbb{E} \{ m(\psi) \}$ Finally, (15) and (16) are obtained by evaluating $\mathbb{E} \{ m(\psi) \}$.

This closed-form upper bound provides important insights on the achievable performance under hardware impairments.

**Corollary 2.** The upper capacity bound in (15) has the following asymptotic properties:

$$\lim_{p_{BS} \to \infty} C_{\text{upper}} = \log_2 \left( 1 + \frac{N}{\kappa_t^{BS} + \kappa_{UT}^N} \right).$$  (17)

$$\lim_{N \to \infty} C_{\text{upper}} = \log_2 \left( 1 + \frac{1}{\kappa_{UT}^N} \right).$$  (18)

**Proof:** Note that $r_1 > 0, \ldots, r_N > 0$ since $R$ is positive definite. This implies $G \to \sum_{i=1}^N \frac{1}{\kappa_t^{BS} + \kappa_{UT}^N \kappa_t^{BS}} \sigma_{p,BS}^2 \to \infty$ for fixed $N$, giving (17). It also implies $\frac{G}{1 + \kappa_{UT}^N} \to 0$ as $N \to \infty$, giving (18).

This corollary shows that the capacity has finite ceilings both when the downlink transmit power $p_{BS}$ grows large and when the number of antennas $N$ grows large. The ceilings depend on the impairment parameters $\kappa_{BS}^N, \kappa_{UT}^N$ and the UT impairments are clearly $N$ times more influential. Note that even very small hardware impairments will ultimately limit the capacity. In other words, the ever-increasing capacity observed in the high-SNR and large-$N$ regimes with perfect transceiver hardware (cf. [4]–[7]) is not easily achieved in practice.

Interestingly, (15) indicates that only the quality of the terminal’s receiver hardware limits the capacity as $N \to \infty$. This means that the detrimental effect of hardware impairments in the base station disappears when the number of base station antennas grow large. This is, simply speaking, since the distortion noise is spread isotropically in space while the increased spatial resolution of the array enables very exact transmit beamforming. This is a very promising result since large arrays are more prone to impairments, due to economic and implementation limitations. To verify this observation, we also need a lower bound on the channel capacity.

B. Lower Bound on Channel Capacity

We obtain a lower capacity bound by making the potentially limiting assumptions of Gaussian codebook, no downlink pilot signaling, and Gaussian CSI uncertainty at the terminal. The next theorem is obtained by an approach from [2], which has been applied to massive MIMO in [5]–[7] (among others).

**Theorem 3.** The capacity in (10) is lower bounded as

$$C \geq C_{\text{lower}} = \log_2 \left( 1 + \frac{\text{SINR}}{1 + \kappa_{UT}^N} \right)$$

where $v = [v_1 \ldots v_K]^T$ with $\|v\| = 1$ is a function of $\hat{h}$ and $\hat{h}^*$.  (19)

$$\text{SINR} = \frac{\mathbb{E} \{ h^T v \|^2 - \| h^T v \|^2 + \kappa_t^{BS} N_i \| h_i \|^2 v_i^2 + \sigma_{p,BS}^2 \| v \|^2}{\| v \|^2}.$$  (20)

**Proof:** Any heuristic beamforming $\hat{h}$ gives a lower bound. This transforms (10) into a SISO channel and we use the approach in [2] Section III to find a lower bound on capacity with only statistics of $h^T v$ at the receiver.

The lower bound in (19) can be computed numerically for any channel distribution and any way of selecting the beamforming vector based on the channel estimate. To bring some insight on the behavior when the number of antennas, $N$, grows large, we have the following result for the case of (approximate) maximum ratio transmission (MRT).

**Corollary 3.** Suppose $v = \hat{h}^*$. then the lower bound in (19) can be expressed as in (21) at the top of the next page. The big-O terms $O \left( \frac{1}{\sqrt{N}} \right)$ vanish as $N \to \infty$, while the other terms (except the noise term) remain strictly positive.

**Proof:** The equivalence between (19) and (21) is proved by straightforward but lengthy use of Hölder’s inequality and the trace lemma in [18] Lemma B.26.
Combining the upper bound in Corollary 3 with the lower bound in Corollary 2, we have a clear characterization of the capacity behavior when $N \to \infty$. Contrary to the upper bound, the hardware impairments at the base station are present in (21) through $\mathbf{A}$ and $\Psi$. However, these variables vanish if we set $\eta_t^{UT} = 0$, in which case the lower bound actually equals the upper bound. Consequently, the capacity limit is mainly determined by the level of impairments at the user terminal, both in the uplink channel estimation ($\kappa_t^{UT}$) and the downlink transmission ($\kappa_r^{UT}$)—although the former connection was not visible in the upper bound since it assumed perfect CSI.

**Remark 2.** It is implicitly assumed in Corollaries 2 and 3 that \( \frac{1}{N_p^2 p^{UT}} \to 0 \) as $N \to \infty$, meaning that the noise variance $\sigma_{UT}^2$ cannot scale as fast as $\mathcal{O}(N)$. This assumption generally holds, but not under pilot contamination of the type considered in [4]–[7]. In those cases there will be additional terms in the denominator of the SINRs that further limits the capacity.

C. Numerical Illustration

This section illustrates our lower and upper bounds on the capacity. The average SNR is defined as $p^{UT} \frac{\text{tr}(R)}{\text{tr}(S)}$ and $p^{BS} \frac{\text{tr}(R)}{\text{tr}(S)}$ for pilot and data transmission, respectively, and is fixed at 20 dB, while we vary the number of antennas $N$ and the levels of impairments. For simplicity, $\kappa_t^{BS} \triangleq \kappa_t^{UT}$ at the base station and $\kappa_t^{UT} \triangleq \kappa_t^{UT}$ at the user terminal.

Fig. 4 considers a spatially uncorrelated scenario where $\mathbf{R} = \mathbf{S} \triangleq \mathbf{I}$. While the capacity with ideal hardware grows without bound as $N \to \infty$, the lower and upper bounds converge to finite limits under hardware impairments. Recall that these bounds hold under any CSI conditions at the user terminal; for example, the dashed lines in Fig. 4 show a lower bound for the practical case when a downlink pilot signal is transmitted for estimation of the effective channel $\mathbf{h}_T^H \mathbf{h}$. While the capacity with ideal hardware grows without bound as $N \to \infty$, the lower and upper bounds converge to finite limits under hardware impairments. Recall that these bounds hold under any CSI conditions at the user terminal; for example, the dashed lines in Fig. 4 show a lower bound for the practical case when a downlink pilot signal is transmitted for estimation of the effective channel $\mathbf{h}_T^H \mathbf{h}$.

Observe that the asymptotic capacity limits in Fig. 4 are characterized by the level of impairments, thus the hardware quality has a fundamental impact on the practical spectral efficiency. The majority of the multi-antenna gain is achieved at relatively low $N$; in particular, only minor improvements can be achieved by having more than $N = 100$ antennas.

Next, Fig. 4 considers the same scenario but with a fixed level of impairments $\kappa_t^{UT} = 0.05$ at the user terminal and different values at the base station. As expected from the analysis, the lower and upper bounds at small $N$ decrease as $\kappa_t^{BS}$ is increased, but the curves converge to the same value—thus showing that the impact of impairments at the base station vanishes as $N$ grows large.

V. Energy Efficiency

This section analyzes the energy efficiency (in bits/Joule), which is defined as the ratio of capacity (in bits/channel use) to radiated power (in Joules/channel use) [7].

\[
\begin{align*}
C \geq C_{\text{lower}} &= \log_2 \left( 1 + \frac{1}{(1 + \kappa_r^{UT})} \mathbb{E} \left[ \frac{(1 + d^{-1} \eta_t^{UT}) \sqrt{\text{tr}(R - C)}}{\sqrt{\text{tr}(\mathbf{A}((d + \eta_t^{UT})^2 R + \Psi) \mathbf{A}^H)}} \right] \right)^2 + \mathcal{O} \left( \frac{1}{\sqrt{N}} \right) + O(1)
\end{align*}
\]
transmission. Large-scale MISO systems can thus obtain an immense energy efficiency even under hardware impairments.

**Remark 3.** Although the ratio of capacity to radiated power in (22) can grow unboundedly as shown in (24), the overall energy efficiency (capacity divided by total power consumption) will not. Specifically, each antenna requires dedicated circuits with a non-zero power consumption $\varrho > 0$. This contributes an additional term $N \varrho$ in the denominator of (22) and implies that the overall energy efficiency is maximized at some finite $N$. This refined efficiency notion was analyzed in [19] and it was observed that large-scale MIMO offers great energy savings also when the circuit power is taken into account.

### A. Numerical Illustration

The energy efficiency (using the lower bound in Corollary 3) is illustrated in Fig. 5 when the transmit and pilot powers are $p_{\text{BS}} = p_{\text{UT}} = 30$ dBm at $N = 1$ and then reduced by a factor $1/N^t$ for $t \in \{0, \frac{1}{2}, \frac{1}{3}, \frac{1}{4}\}$. The channel covariance matrices $R$ are generated using the exponential model with correlation coefficient 0.7 (16), while the noise covariance $\mathbf{S}$ is a scaled identity matrix such that $\text{tr}(\mathbf{R})/\text{tr}(\mathbf{S}) = -10$ dBm at a bandwidth of 15 kHz. The impairment parameters are set to $\kappa_0 = \kappa_{10} = \kappa_{15} = \kappa_{20} = \kappa_{30} = \kappa_{t} = 0.05^2$.

Fig. 5 verifies that the energy efficiency grows unboundedly with $N$ for $t = \frac{1}{4}$ and $t = \frac{1}{2}$, while it converges to a finite value for $t = 0$ (i.e., when the transmit/pilot powers are fixed). Note that $\alpha_1 = \alpha_2 = 0$ in the figure, but changing these parameters only result in a common vertical shift of all the curves. Fig. 5 also shows the case of ideal hardware. Despite the fundamental differences when $N \rightarrow \infty$ or $p_{\text{BS}}, p_{\text{UT}} \rightarrow \infty$ (i.e., there is no capacity ceiling or estimation error floor under ideal hardware), the difference in terms of energy efficiency is remarkably small between ideal and practical hardware.

### VI. Conclusion

This paper analyzed how transceiver hardware impairments impact the capacity and estimation accuracy of large-scale MISO systems. We proved analytically that the impairments of physical hardware create a finite capacity ceiling and non-zero estimation error floor—irrespective of the SNR and number of base station antennas $N$. This stands in contrast to the very optimistic asymptotic results previously reported for ideal hardware. Interestingly, only the hardware impairments at the user terminal limits the performance as $N$ grows large. Despite these discouraging results, we showed that large-scale MISO systems can achieve an arbitrarily high energy efficiency.

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