When Do WOM Codes Improve the Erasure Factor in Flash Memories?

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Abstract—Flash memory is a write-once medium in which re-programming cells requires first erasing the block that contains them. The lifetime of the flash is a function of the number of block erasures and can be as small as several thousands. To reduce the number of block erasures, pages, which are the smallest write unit, are rewritten out-of-place in the memory. A Write-once memory (WOM) code is a coding scheme which enables to write multiple times to the block before an erasure. However, these codes come with significant rate loss. For example, the rate for writing twice (with the same rate) is at most 0.77.

In this paper, we study WOM codes and their tradeoff between rate loss and reduction in the number of block erasures, when pages are written uniformly at random. First, we introduce a new measure, called erasure factor, that reflects both the number of block erasures and the amount of data that can be written on each block. A key point in our analysis is that this tradeoff depends upon the specific implementation of WOM codes in the memory. We consider two systems that use WOM codes; a conventional scheme that was commonly used, and a new recent design that preserves the overall storage capacity. While the first system can improve the erasure factor only when the storage rate is at most 0.6442, we show that the second scheme always improves this figure of merit.

I. INTRODUCTION

Flash memories are, by far, the most important type of non-volatile memory (NVM) in use today. Flash devices are employed widely in mobile, embedded, and mass-storage applications, and the growth in this sector continues at a staggering pace. The most conspicuous property of flash-storage technology is its inherent asymmetry between cell programming and cell erasing. While it is fast and simple to increase a cell programming pace. The most conspicuous property of flash-storage technology is its inherent asymmetry between cell programming and cell erasing. While it is fast and simple to increase a cell, reducing its level requires a long and cumbersome operation of first erasing the entire block contains it and only then programming the cell. Such block erasures are not only time-consuming, but also degrade the lifetime of the memory, which can typically tolerate $10^3 - 10^5$ block erasures. Therefore, finding algorithms for increasing its lifetime despite this asymmetric programming behavior has become an important challenge.

A flash memory chip is built from floating-gate cells. A group of cells constitute a page, which is the smallest write unit, and the pages are organized in blocks, which are the smallest erase unit. Since pages can be updated only if their accommodating block is first erased, write update requests are performed out-of-place. Thus, when a page is updated, its previous location is marked as invalid. In order to accommodate this write procedure, the amount of physical storage has to be larger than the available logical storage. The ratio between the size of additional storage and logical storage is called over-provisioning. Furthermore, whenever there are no available blocks to accommodate page write requests, garbage collection (GC) is invoked to clean, i.e. erase, blocks for additional-page writes. However, when a block is chosen to be cleaned by GC, its valid pages must be read and rewritten to a clean block, thereby increasing the total number of pages written to the memory. The write amplification is the ratio between the number of physical page writes and the number of logical page writes.

Reducing the write amplification is crucial as it directly affects the memory performance and its lifetime. In general, there is a direct relation between over-provisioning and write amplification. Increasing over-provisioning reduces the write amplification [5]. However, high over-provisioning means that a large area of the memory is not exploited to store information. Thus, understanding the connection between the two measures is very important for optimizing the design of flash memories.

Write-once memory (WOM) codes were first introduced in 1982 by Rivest and Shamir [14]. In the binary version, write-once memory cells can only be irreversibly programmed from a value of zero to a value of one. The motivation to study WOM came from storage media like punch cards and optical disks. A renewed interest in WOM codes came along in the past years as a result of the tremendous research work on coding for flash memories. Flash memories impose similar constraints in which the level of each cell can only increase, and can be decreased only if its entire block is first erased. Thus, a WOM-code can be applied in flash memories to enable additional writes without first having to erase the block. WOM codes in flash memories were investigated both theoretically with respect to the number of block erasures, and practically by simulations; see e.g. [1], [8], [11]–[13], [19].

The reduction in the number of block erasures via WOM codes is beneficial for extending device lifetime. However, this benefit comes with a significant price of non-negligible increase in the redundancy, thereby decreasing the over-provisioning in the memory. Note that when using WOM codes, it is possible to write more pages with less erasures since every block can be written more than once. Therefore, write amplification is not the right figure of merit for this method. Thus, we introduce a new measure, called erasure factor, which is the ratio between the number of block erasures and number of logical block write requests. When WOM codes are not used, the erasure factor is equivalent to the write amplification.

The main goal of this paper is to analyze the erasure factor when using WOM codes, while pages are written uniformly at random. In order to have a fair comparison with systems that do not use WOM codes, we fix the over-provisioning and then compare the erasure factor. We analyze two different implementations of WOM codes. Conventional implementations of WOM codes used the codes in the page level such that each page is written multiple times, and hence incorporated capacity loss. We compare these with a new approach, recently proposed in [18], that avoids the capacity loss by encoding information into more than a single page after the first write.

The rest of the paper is organized as follows. In Section II, we introduce the necessary background on flash memories and WOM codes, and formally define the problem we study in
the paper. In Section [13] we review and state the results on the connection between over-provisioning and the erasure factor without using WOM codes. In Section [IV] we study the conventional implementation of WOM codes and similarly analyze this connection. We then continue in Section [V] to study a more efficient implementation of WOM codes in which no rate loss is incurred and similarly study its erasure factor.

II. DEFINITIONS AND PROBLEM STATEMENT

A. Flash Memory Structure

Flash memories consist of floating-gate cells that can typically store a single bit, two bits, or three bits. The cells are organized into blocks which usually contain 64-384 pages, where the size of a page ranges between 2KB and 16KB [8]. Due to the inherent asymmetry between programming and erasing, flash memories perform page writes out-of-place. This write procedure introduces the following concepts:

- **Flash Translation Layer (FTL):** The FTL is responsible for mapping logical locations to physical ones.
- **Over-provisioning (OP):** The ratio between the amounts of additional storage and logical storage. This overhead is necessary to accommodate out-of-place writes.
- **Garbage Collection (GC):** The process in charge of cleaning blocks in order to free more space for writing.
- **Write Amplification (WA):** The ratio between the number of physical page writes and the number of logical page write requests.

The following summarizes the setup, notations, and assumptions we use throughout the paper. These notations hold for a flash memory device, e.g. a solid state drive.

1. Every block has $Z$ pages, each of size $s$KB. There are $T$ physical pages and $U$ logical pages, where both $T$ and $U$ are a multiple of $Z$.
2. The over-provisioning is $\rho = (T-U)/U$ and $\alpha = U/T = 1/(\rho+1)$ is the storage rate, which is the ratio between logical data and physical storage.
3. $WA = P/L$, where $L$ is the number of write requests of logical pages and $P$ is the number of resulting physical page writes. We also define $L/Z$ to be the number of logical block writes.

B. WOM Codes

WOM codes were first introduced by Rivest and Shamir in 1982 [14], and were found to be very relevant in the context of rewriting algorithms for flash memories. In this setup, the memory consists of $n$ cells and the goal is to maximize the number of bits which can be written to the memory in $t$ writes, while guaranteeing that each cell is changed only from 0 to 1. The most famous example of a WOM code is the one given by Rivest and Shamir for writing two bits twice using only three cells [14]. In their work, they also analyzed the bounds on the amount of information that can be stored in a WOM. Since then, more constructions were given in the 1980’s and 1990’s, e.g., [4] as well as capacity analysis, e.g., [7], [9]. Several more constructions of WOM codes were recently given; see e.g. [2], [15], [17].

Assume $t$ messages are written to the memory, consisting of $n$ cells. On the $i$-th write, $1 \leq i \leq t$, the message size is $M_i$. The rate on the $i$-th write is defined to be $R_i = \log_b M_i$, and the sum-rate is $R_{\text{sum}} = \sum_{i=1}^t R_i$. We consider two types of WOM codes [17]. In a fixed-rate WOM code, the rate on all writes is the same, while in a variable-rate WOM code the rate may vary on each write. The capacity region of a $t$-write WOM is the set of all achievable rate tuples. For the binary case, the capacity region was found in [7], [9], [13]. It was also proved that the maximum achievable sum-rate for a WOM code with $t$ writes is $\log_2(t+1)$. Similar results were given for fixed-rate WOM codes [9]. For example the maximum sum-rate of a two-write fixed-rate WOM code is 1.54.

C. Problem Setup

The main goal of this work is to study the connection between the over-provisioning $\rho$ (or storage rate $\alpha$) and the number of block erasures. This connection depends upon the over-provisioning value, GC algorithm, and the probability distribution of the page write requests. We assume in this work that requests are uniformly distributed over the $U$ logical pages. We follow the observation from [10] claiming that greedy GC is optimal for uniform distribution, where greedy GC always chooses the block with the minimum number of valid pages for cleaning. We also assume that greedy garbage collection is invoked whenever there are no more clean blocks. That is, we don’t require a minimum fraction of available blocks since the analysis is very similar to the one without this requirement [5].

WOM codes allow to write the blocks multiple times before an erasure. Thus, WA is not the right figure of merit since it is possible to write more pages and yet erase less. Hence, we introduce a new measure that better characterizes this behavior.

Definition 1. The *erasure factor* $EF$ in a flash memory system is the ratio between the number of block erasures $E$ and the number of logical block writes $L/Z$. That is,

$$EF = \frac{E}{L/Z}.$$

Note that if no rewriting code is used then $EF = WA$. In the rest of the paper, we study the erasure factor of several systems with and without WOM codes, demonstrating how the specific usage of WOM codes directly affects this figure of merit.

III. THE RELATION BETWEEN OVER-PROVISIONING AND ERASURE FACTOR

The relation between the write amplification and over-provisioning has received a significant attention in recent years due to its importance to the lifetime of flash memories, see e.g. [5], [10], [16]. Of the numerous works in this area, we consider two recent studies which we believe give an accurate model of this analysis [5], [10]. The proof given here is based upon the analysis in these two studies and we give it in completeness since its understanding is crucial to the results in the paper. For the purpose of our discussion, we call the system in these studies, that does not use WOM codes, the baseline system.

Theorem 2. The number of block erasures $E$ and the erasure factor $EF_1(\alpha)$ of the baseline system are given by

$$E = \frac{P}{L} = \frac{L}{Z(1-\alpha')}, \quad EF_1(\alpha) = \frac{1}{1-\alpha'},$$

where $\alpha = \frac{\alpha'-1}{\ln(\alpha')}$, or $\alpha' = -\alpha \cdot W \left( -\frac{1}{\alpha} e^{-1/\alpha} \right)$, and $W(x)$ is the Lambert W function.

Proof: For $0 \leq i \leq Z$, let $N(i)$ be a random variable corresponding to the number of blocks with $i$ valid pages, so

$^1$and also on Z but we assume in the paper that Z is large enough to avoid this dependency.
\[ \sum_{i=0}^{Z} N(i) = T/Z. \] If we denote by \( Y \) the expected number of valid pages when a block is erased, then for \( 0 \leq i \leq Y - 1 \), \( N(i) = 0 \), and \( N(Y) \) is steadily small enough. We assume that the system is in steady state and thus the expected value of \( N(i) \) doesn’t change over time. According to this assumption, we also get that for \( Y + 1 \leq i \leq Z \),

\[ iN(i) = C, \]

for some constant \( C \), or \( N(i) = (Y + 1)N(Y + 1)/i \). Therefore, we get

\[ T/Z = \sum_{i=0}^{Z} N(i) = \sum_{i=Y+1}^{Z} (Y + 1)N(Y + 1)/i = \frac{(Y + 1)N(Y + 1)}{1 + \frac{1}{2} + \frac{1}{3} + \ldots + \frac{1}{i}} \approx (Y + 1)N(Y + 1)(\ln(Z) - \ln(Y)) = (Y + 1)N(Y + 1)\ln(Z/Y). \]

We also have that

\[ U = \sum_{i=0}^{Z} iN(i) = \sum_{i=Y+1}^{Z} iN(i) = (Z - Y)(Y + 1)N(Y + 1). \]

Together, we get that

\[ \frac{(Y + 1)N(Y + 1)}{\ln(Z/Y)} = \frac{U}{Z - Y'} \]

or

\[ \alpha = \frac{U}{T} = \frac{Z - Y'}{\ln(Z/Y)} = \frac{Y/Z - 1}{\ln(\alpha')} = \alpha' - 1, \]

where \( \alpha' = Y/Z \), and is given by \( \alpha' = -\alpha \cdot W \left( \frac{1}{\alpha} e^{-1/\alpha} \right) \).

Now, we deduce that for every \( Z - Y \) logical page writes, \( Z \) physical pages are written. Hence, \( P = L \cdot \frac{Z}{Z - Y} = \frac{L}{1 - \alpha'} \),

\[ E = \frac{P}{Z} = \frac{L}{Z(1 - \alpha')}, EF_2(\alpha) = \frac{E}{L/Z} = \frac{1}{1 - \alpha'}. \]

IV. ANALYSIS OF THE NAIVE-WOM SYSTEM

In this section, we take a first step in analyzing the erasure factor when using WOM codes. However, as we shall later see, this analysis depends on the specific implementation of WOM codes in the memory. In order to have a fair comparison with the baseline system, we carry out this comparison while fixing \( \alpha \), the ratio between logical and physical storage. Furthermore, in order to minimize the modifications in the architecture, we assume that the block size is fixed, however we will allow to change the size of the physical pages and accordingly the number of physical pages in a block.

Let us start by describing the conventional setup to implement WOM codes in flash. This setup was tested experimentally in several studies, see e.g. [8], [11] along with analytical derivations in [12]. Assume a two-write fixed-rate WOM code is used with individual rate \( R \) on each write, so \( R \leq 0.77 \) [9].

Here, WOM codes allow to write each block twice before an erasure. First, all pages are written sequentially in the block and after the block is chosen by GC, it is possible to write to the invalid pages. Thus, every block can be either on a first or second write. The modifications in the system setup compared to the baseline system are summarized as follows:

1. A two-write WOM code with fixed-rate \( R \) on each write is used to write all pages. Thus, the size of every physical page is \( \frac{1}{2} sKB \) so it can accommodate a write of a logical page which is encoded by the WOM code’s encoder.
2. As a result of increasing the physical page size, the number of pages in a block reduces to \( Z' = KZ \), so the block size remains the same. Accordingly, the total number of physical pages is also reduced to \( T' = RT \). Hence, the ratio \( \beta \) between the number of logical pages and physical pages is

\[ \beta = \frac{U}{T'} = \frac{U}{RT} = \frac{\alpha}{R}, \]

and thus the storage rate is \( \alpha = \beta R \).

3. We use the same greedy GC as in the baseline system, and hence the block with the minimum number of valid pages is chosen by the GC. If the block is on first write, then it is moved to second write, and since it is not erased, its valid pages remain in the block. If the block is on second write, then its valid pages are rewritten on an available block and the block is erased.

We call this method of implementing WOM codes the naive-WOM system. Since the value of \( \beta \) is at most 1, we can compare the baseline system and the naive-WOM system only for \( \alpha \leq R = 0.77 \). The next theorem states the result of the erasure factor under this setup. The proof is omitted due to the lack of space and since it is a direct application of Theorem 2.

**Theorem 3.** For any \( \alpha \leq R \), under uniform writing with greedy GC, the erasure factor \( EF_2(\alpha) \) of the naive-WOM system is given by

\[ EF_2(\alpha) = \frac{1}{2(1 - \beta')}, \]

where \( \beta' = -\beta \cdot W \left( \frac{1}{\beta} e^{-1/\beta} \right) \), and \( \beta = \frac{\alpha}{R} \).

Finally, we can compare these two systems and find the values of \( \alpha \) in which the naive-WOM system is superior to the baseline system with respect to the erasure factor.

**Corollary 4.** The naive-WOM system for \( t = 2 \) with rate \( R \) has better erasure factor than the baseline system if

\[ 1 + \alpha \cdot W \left( -\frac{1}{\alpha} e^{-1/\alpha} \right) \leq 2 \left( 1 + \frac{\alpha}{R} \cdot W \left( -\frac{R}{\alpha} e^{-\frac{R}{\alpha}} \right) \right). \]

In particular, for \( R = 0.77 \), it has better erasure factor for \( \alpha \leq 0.6442 \).

The extension to multiple writes is immediate. Assume the individual rate on each write is \( R_t \) where the optimal values of \( R_t \) are given in [9]. Then, as before, we have that the erasure factor \( EF_1(\alpha) \) is given by

\[ EF_1(\alpha) = \frac{1}{t \left( 1 + \frac{\alpha}{R_t} \cdot W \left( -\frac{R_t}{\alpha} e^{-\frac{R_t}{\alpha}} \right) \right)}. \]

In Fig. 1 we plot the curves of \( EF(\alpha) \) for \( 2 \leq t \leq 7 \), and the respective optimal \( R_t \) values.

V. ANALYSIS OF THE CAPACITY-PRESERVING-WOM SYSTEM

The main disadvantage of the naive-WOM system is its high rate penalty and thus an increase in the over-provisioning. In fact, this is a major caveat which prevented a wide adoption of WOM codes in flash memory devices. Furthermore, it also requires to either change the page or block size, which introduces another level of complication in the architecture design.

These two disadvantages were recently resolved in [13] by proposing an implementation of WOM codes which requires
Choice of a WOM code: The capacity region of a two-write WOM code with these rates exists. In practice, we take a less restrictive approach which does not affect the analysis. We call this scheme the Capacity-Preserving WOM system. We only give the main ideas of this implementation as the full details appear in [18]. For the sake of analyzing the erasure factor, we take 

\[ \begin{align*}
\alpha & = \frac{1}{3/2 - \gamma_1/2 - \gamma_2}, \\
\gamma_2 & = -\alpha W \left( \frac{1}{\alpha} \ln \left( \frac{1 + \gamma_1}{\gamma_1} \right) + \frac{1}{2} \right).
\end{align*} \]

The optimal erasure factor is given by

\[ EF_2^*(\alpha) = \min_{0 < \gamma_1 < 1} \{ EF_2^*(\alpha, \gamma_1) \}. \]

Proof: Let \( Y_1 = \gamma_1 \cdot Z \) and let us denote by \( Y_2 \) the expected number of valid pages when a block on a second write is physically erased and let \( \gamma_2 = Y_2/Z \). We will determine the relation between the values of \( Y_1 \) and \( Y_2 \). For \( 0 \leq i \leq Z \), we denote by \( N_1(i) \), \( N_2(i) \) the number of blocks with \( i \) valid pages on a first, second write, respectively. Notice first that for \( i \leq Y_1 \), \( N_1(i) = 0 \) and for \( i \leq Y_2 \), \( N_2(i) = 0 \). Furthermore, when a block is moved from first to second write, it already contains \( Y_1 \) valid pages. Since every logical page is written into 2 available pages, the total number of logical pages this block can accommodate is at most \( Y_1 + (Z - Y_1)/2 = (Y_1 + Y_2)/2 \) and thus \( N_2(i) = 0 \) for \( i > (Z - Y_1)/2 \).

We follow the same steps of the proof of Theorem 3 to have the following equations:

\[ \begin{align*}
Y_1 + 1 \quad N_1(Y_1 + 1) + \cdots &= ZN_1(Z) \\
(Y_1 + 1)N_2(Y_2 + 1) + \cdots &= \frac{Z + Y_1}{2}N_2 \left( \frac{Z + Y_1}{2} \right).
\end{align*} \]

According to these definitions, a block can accommodate \( Z \) pages on the first write and \( (Z - Y_1)/2 \) more page writes on the second write. Furthermore, on every block erasure, \( Y_2 \) pages are rewritten, so the number of erasures is given by

\[ E = \frac{L + EY_2}{Z + (Z - Y_1)/2} \]

or

\[ E = \frac{L}{3Z/2 - Y_2 - Y_1/2} = \frac{L}{Z \cdot \frac{3}{2} - \gamma_1/2 - \gamma_2}. \]

Hence, the erasure factor, as a function of both \( \gamma_1 \) and \( \gamma_2 \) is

\[ 1/(3Z/2 - \gamma_1/2 - \gamma_2). \]

Following the rest of the steps from Theorem 2 we get

\[ \begin{align*}
T/Z &= \sum_{i=0}^{Z} (N_1(i) + N_2(i)) \\
&= \sum_{i=Y_1 + 1}^{Z} N_1(i) + \sum_{i=Y_1 + 1}^{Z} N_2(i) \\
&= \sum_{i=Y_1 + 1}^{Z} \frac{Y_1 + 1}{i} N_1(Y_1 + 1) + \sum_{i=Y_1 + 1}^{Z} \frac{Y_1 + 1}{i} N_2(Y_1 + 1) \\
&= (Y_1 + 1)N_1(Y_1 + 1) \left( \sum_{i=Y_1 + 1}^{Z} \frac{1}{i} + \frac{Z + Y_1}{2Y_2} \right) \\
&= (Y_1 + 1)N_1(Y_1 + 1) \ln \left( \frac{Z + Y_1}{2Y_2} \right).
\end{align*} \]

As before, we also have

\[ \begin{align*}
U &= \sum_{i=0}^{Z} iN_1(i) + \sum_{i=0}^{Z} iN_2(i) \\
&= \sum_{i=Y_1 + 1}^{Z} iN_1(i) + \sum_{i=Y_1 + 1}^{Z} iN_2(i) \\
&= (3Z/2 - Y_1/2 - Y_2)(Y_1 + 1)N_1(Y_1 + 1).
\end{align*} \]
Thus we get

\[
(Y_1 + 1)N_1(Y_1 + 1) = \frac{U}{3Z/2 - Y_1/2 - Y_2} = \frac{T/Z}{\ln \left(\frac{1 + \gamma_1}{2\gamma_1}\right)},
\]

or

\[
\alpha = \frac{3 - 2\gamma_1/2 - \gamma_2}{\ln \left(\frac{1 + \gamma_1}{2\gamma_1}\right)},
\]

that is

\[
\gamma_2 = -\alpha W\left(\frac{1}{\alpha} e^{\left(\frac{1+\gamma_1}{2\gamma_1}\right)} + \frac{\gamma_1 - 3}{2\alpha}\right).
\]

Hence, the erasure factor, as a function of \(\alpha\) and \(\gamma_1\) is

\[
EF_2(\alpha, \gamma_1) = \frac{1}{3 - 2\gamma_1/2 - \gamma_2},
\]

where \(\gamma_2\) is given by (7). Lastly, since we can choose the threshold \(\gamma_1\), the value \(EF_2(\alpha, \gamma_1)\) is achieved by minimizing the value of \(EF_2(\alpha, \gamma_1)\) under the condition in (4).

In Fig. 2, we plot the curves of the erasure factors of the baseline, naive-WOM, and CP-WOM systems for two writes. We see that the CP-WOM system always has a better erasure factor than the baseline system. The naive-WOM system has better erasure factor than the CP-WOM system roughly for \(\alpha \leq 0.54\). This is not surprising since for small values of \(\alpha\), no pages are copied by the GC, so the best EF of the CP-WOM system is 1/1.5 = 2/3 while the best EF of the naive-system is 1/2. The extension to multiple writes follows similar steps.

**Theorem 6.** For any storage ratio \(\alpha\) and a CP-WOM system for \(t\) writes, the erasure factor is given by

\[
EF_t^* (\alpha) = \min_{0 \leq \gamma_1, \gamma_2, \ldots, \gamma_t-1 \leq 1} \left\{ EF_t^* (\alpha, \gamma_1, \gamma_2, \ldots, \gamma_t-1) \right\},
\]

where

\[
EF_t^* (\alpha, \gamma_1, \gamma_2, \ldots, \gamma_t-1) = \frac{1}{2 - \frac{1}{2^t} \frac{\sum_{j=1}^{t-1} \gamma_j}{2} - \gamma_t},
\]

and \(\gamma_t\) satisfies

\[
\alpha = \frac{2 - \frac{1}{2^t} \frac{\sum_{j=1}^{t-1} \gamma_j}{2} - \gamma_t}{\sum_{j=1}^{t} \ln \left(\frac{1+2^t-2\gamma_j+2^t-2^{t-1} \gamma_j}{2^t-1} \right)},
\]

while \(\gamma_0 = 0\).

**VI. Conclusion**

The primary goal of this paper is to establish the fundamental limits of flash memories and similar memories. We hope that similar studies will be conducted in order to analyze the performance of other coding schemes with respect to the erasure factor. Some future research directions are summarized below.

1. A tighter approximation of the erasure factor when blocks have a relatively small number of pages.
2. Analysis of other systems implementing WOM codes and additional coding schemes.
3. Analysis of non-binary WOM codes.
4. Analysis of workloads other than the uniform writing; see for example [5, 16].

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