Comparative Analysis of Two Level VSI and Three Level Neutral Point Clamped Inverter for Torque Ripple Reduction in Induction Drive

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Abstract

This paper presents the comparative analysis of various level of multilevel inverter for induction motor torque ripple minimization. The neutral point clamped (NPC) inverter is used an adequate solution in AC drives application. The Conventional inverters compared to multilevel inverters have many limitations in high-voltage and high-power applications like poor power quality, high voltage stress, EMI/EMC issue etc. Also proposed the Space Vector neutral point clamped inverter for Torque ripple reduction in induction drive. The Simple Space vector control scheme is applied to neutral point clamped inverter by reference torque estimation. In this proposed topology offer an improved performance in the form of torque ripple reduction scheme. The Classical direct torque control of two-level VSI scheme is compared with proposed three-level NPC scheme. The main aim of this paper to provide a significant method and control scheme to obtained torque ripple reduction in induction motor Drive. The result shows the performance of neutral point clamped inverter using CB-PWM on induction drive. In this paper three levels VSI based Neutral Point Clamped (NPC) multilevel inverter fed induction motor drive is simulated, analyzed and compared with two levels VSI based NPC inverter drives. Torque of the motor is controlled by using space vector based direct torque control (DTC) method. The simulation results are carried out by using MATLAB/SIMULINK environment.

Keywords: Direct Torque Control (DTC), Induction Drive, Multilevel Inverter, Neutral Point Clamped Inverter (NPC), Space Vector Pulse Width Modulation (SVPWM), Torque Ripple, Voltage Source Inverter (VSI)

1. Introduction

Recently, the growth of high power, low cost and authentic power electronic converters causes an opportunity of using medium or large induction machines in advanced industrial drive applications. The Direct torque control is a powerful control scheme for induction motor torque ripple control. The conventional hysteresis based DTC algorithm for voltage switching method has relative merits of simple structure and easy implementation.

Currently various decades of investigation, many DTC strategies have been proposed so far. The four major categories such as band-constrained technique, Space Vector Modulation (SVM)-based control of the switching frequency and strategies using predictive control schemes and finally the hybrid PWM (Pulse Width Modulation) technique. The Power Factor Correction (PFC) based control strategies used for torque ripple of BLDC Drive. Nonetheless, the improved performance is federal to significant growth of implementation schemes. A direct torque control of two-level inverter for switching frequencies control ranges 2–3 kHz, the switching frequency is operated below 1 kHz causes some limitation in torque ripple reduction. Two-level Inverter switching frequency is restricted in variable switching frequency and it is not applicable in high power drive applications.
Present work deals with comparison in performance of Torque ripple reduction fed induction motor drive using two levels VSI and three-level neutral point clamped inverter schemes. The basic problems are associated with the conventional DTC control methods because of high torque ripple, variable switching frequency and performance deterioration at low, near to zero speed basic. Limitation of classical controller has two to three control loops are presented for torque and flux control.

The standard multilevel inverter topology discussed with two-level inverter topology, multilevel inverter is a most prominent solution for induction motor drive. The Multilevel must be used to reduce the harmonics and high switching frequency with low switching losses. Three-level inverter is popularly used in high power drives application. A lot of research have been done in neutral point clamped multilevel inverter topology and a numerous of control methods have been presented in the literature stand on ripple minimization in induction motor. An active Neutral Point Clamped inverter (NPC) fed BLDC drive used to analysis the minimization of torque ripple and reduction of harmonics.

The modeling of induction machine is referred using the reference frame of theory. The total harmonic distortion of neutral point diode clamped inverter is analyzed for solar power applications. The comparative analysis of multilevel inverter is carried out under various PWM techniques. The optimized harmonic elimination is analyzed by phase disposition and phase shifting PWM method.

In this paper analyzed the suitable topology for torque ripple minimization by comparing the direct torque control of two level VSI (Voltage Source Inverter) and three level NPC-VSI (neutral point clamped voltage source inverter). In principle, DTC method based on simple space vector modulation scheme is introduced for torque ripple reduction. Additional factors of proposed control scheme are DC-Link balancing control, current ripple reduction and flux control. A simple space vector scheme is changes in same direction with respect to level of positive and negative variation over DTC two-level scheme. The rest of the paper is organized as follows: sections II described about the induction machine and DTC with two-level VSI. Sections III described about DTC with the three-level NPC-VSI topology. The DTC based SVM strategy is discussed at section IV so the comparison simulation results are exposed at the next section. Finally, conclusions are given in the last section.

2. Theory

2.1 Modeling of Machine

An induction Machine is modeled using voltage and flux equations which are referred to a general reference frame, denoted by the superscript “g” and are shown as follows:

\[ v_s^g = i_s^g r_s + j w_s \phi_s^g + p \phi_s^g. \]  

(1)

Rotor voltage equation:

\[ 0 = i_r^g R_r + j(w_s - w_r)\phi_r^g + p \phi_r^g. \]  

(2)

Stator flux equation:

\[ \lambda_s^g = L_s i_s^g + L_m i_r^g. \]  

(3)

Rotor flux equation:

\[ \lambda_r^g = L_s i_s^g + L_m i_r^g. \]  

(4)

Mechanical equation:

\[ T_e - T_L = j_m p w_r + B_m w_r \]  

(5)

\[ T_e = \frac{3P}{4} (\lambda_{dq}^g - \lambda_{dq}^g). \]  

(6)

By referring to a stationary frame, denoted by the superscript “a,” which is with d-axis attached on the stator winding of Phase “A,” the mathematical equations of induction motor can be rewritten as follows:

Stator voltage equation:

\[ v_s^a = i_s^a R_s + p \lambda_s^a. \]  

(7)

Rotor voltage equation:

\[ 0 = i_r^a R_r - j w_r \lambda_r^a + p \lambda_r^a. \]  

(8)

Stator flux equation:

\[ \lambda_s^a = L_s i_s^a + L_m i_r^a. \]  

(9)

Rotor flux equation:

\[ \lambda_r^a = L_s i_s^a + L_m i_r^a. \]  

(10)

Mechanical equation:

\[ T_e - T_L = j_m p w_r + B_m w_r \]  

(11)

\[ T_e = \frac{3P}{4} (\lambda_{dq}^a - \lambda_{dq}^a). \]  

(12)

Substituting (9) and (10) into (12), yields
Where $\sigma = \text{Total leakage factor, } \sigma \equiv 1 - \frac{L_{in}^2}{L_s L_r}$

$$T_c = \frac{3P}{4} \frac{L_m}{\sigma L_r} \left( \dot{\lambda}^s_{ds} \dot{\lambda}^s_{dr} - \dot{\lambda}^s_{qs} \dot{\lambda}^s_{qr} \right) \quad (13)$$

2.2 DTC Basis

DTC topologies allow a direct control of the motor variables through a suitable selection of the inverter control signals, the stator flux and torque are need to be increased, decreased, or maintained for fulfill the requirement of drive. These decisions are made by output $\phi$ of the flux dynamic of $\Phi_s$ is governed by the stator voltage equation expressed in the stationary reference frame, as follows

$$\frac{d}{dt} \Phi_s = V_s - r_i i_s$$ \quad (14)

where, $V_s$ stator voltage vector, $i_s$ and $r_i$ are the current vector and resistance respectively. Neglecting the voltage drop $r_i i_s$ across the stator resistance, and taking into account that the voltage vector is constant in each sampling period ($T_s$), stator flux vector variation turns is proportional to the applied voltage vector.

For Constant value of stator flux, the changing of the electromagnetic torque $T_{em}$ based on the applied voltage vector's directions, such that:

$$T_{em} = \frac{N_p M}{l_s - M} \left\| \Phi_s \right\| \left\| \Phi_r \right\| \sin \delta$$ \quad (15)

where, $\Phi_r$, named as rotor flux is referred to the stator, $\delta$ is the angular shift between the stator and rotor flux, $N_p$ is the number of poles and $l_s$, $l_r$ are the stator self-inductance and the rotor self-inductance respectively. The mutual inductance denoted by $M$.

2.3 DTC Scheme for Two Level VSI Topology

Stator flux is derived from equation $7$ is given by

$$\dot{\lambda}^s_a = \int (\dot{v}^s_a - \dot{i}^s_a R_s) dt$$ \quad (16)

where, $\dot{v}^s_a$ and $\dot{i}^s_a$ are the measured the voltage and current respectively. The classical control of DTC with two level inverter is shown in Figure 1. Electromagnetic torque is derived from equation $13$ is given by

$$T_c = \frac{3P}{4} \frac{L_m}{\sigma L_r} \left( \dot{\lambda}^s_{ds} \dot{\lambda}^s_{dr} - \dot{\lambda}^s_{qs} \dot{\lambda}^s_{qr} \right)$$ \quad (17)

**Figure 1.** Classical DTC scheme with two-level inverter.

The operating status of the switches in the two-level inverter shown in Figure 2 can be represented by switching states. Switching states are defined by torque error and flux errors shown in Table 1 and Table 2.

Reference voltage is derived from $a$ and $\beta$ reference frame below$^{18}$

$$\vec{V}_{ref} = V_a + jV_\beta = \frac{2}{3}(V_a + aV_b + a^2V_c)$$ \quad (18)

Similarly,

$$|\vec{V}_{ref}| = \sqrt{V_a^2 + V_\beta^2}, \quad a = \tan^{-1}\left(\frac{V_\beta}{V_a}\right)$$

$$V_a = jV_\beta = \frac{2}{3} \left( V_a + e^{-j\frac{2\pi}{3}} V_b + e^{j\frac{2\pi}{3}} V_c \right)$$ \quad (19)

$$v_a = jV_\beta = \frac{2}{3} \left( V_a + \cos \frac{2\pi}{3} V_b + V_a + \cos \frac{2\pi}{3} V_c \right) + j \frac{2}{3} \left( \sin \frac{2\pi}{3} V_b - \sin \frac{2\pi}{3} V_c \right)$$ \quad (20)

By equating the real and imaginary parts derived by,

$$V_a = \frac{2}{3} \left( V_a + \cos \frac{2\pi}{3} V_b + \cos \frac{2\pi}{3} V_c \right)$$

$$V_\beta = \frac{2}{3} \left( 0V_a + \sin \frac{2\pi}{3} V_b - \sin \frac{2\pi}{3} V_c \right)$$

$$\begin{bmatrix} V_a \\ V_b \\ V_c \end{bmatrix} = \frac{2}{3} \begin{bmatrix} 1 & \frac{2\pi}{3} & \frac{2\pi}{3} \\ 0 & \frac{2\pi}{3} & \frac{2\pi}{3} \end{bmatrix} \begin{bmatrix} V_a \\ V_b \\ V_c \end{bmatrix}$$ \quad (21)
3. DTC Scheme for Three level NPC Topology

Switching states that are shown in Figure 3 can constitute the operating status of the switches in the three-level inverter. When switching state is ‘1’, it is specified that upper two switches in leg A connected and the inverter terminal voltage $V_{AZ}$, which means the voltage for terminal A with respect to the neutral point $Z$, is +E, whereas ‘–1’ denotes that the lower two switches are ON, which means $V_{AZ} = –E$. When switching state ‘0’, it indicates that the inner two switches $S_4$ and $S_5$ are connected and $V_{AZ} = 0$ through the clamping diode and load current directions. Switching status for leg A. Leg B and leg C shown in Table 3.

4. A Simple SVM (Space Vector Modulation Scheme)

The three-level Voltage Vector diagram is shown in Figure 4. There are six sectors ($\Delta_1-\Delta_6$), four triangles ($\Delta_1-\Delta_4$) in a sector, and a total of 27 switching states in this space vector diagram shown in Figure 5. As a method outlined in 22 advices, proposed space vector topology in each sector is segregated by four triangles, indexed as shown in Figure 6 every triangle examine as a single sector in two level hexagon scheme with the same sacking at the origin.

In this method, the triangle that hurdle the reference vector is indentified based on correlates the tip of the ref-

![Figure 2. Space vector diagram for two level inverter.](image1)

![Figure 3. Proposed NPC inverter circuit.](image2)

![Figure 4.](image3)

![Figure 5.](image4)

![Figure 6.](image5)
While reference vector is shifted to the new set of axes that intersect at the main crest of the triangle. In single triangle, the coordinates of the tip of the shifted reference vector \(A_kP_k\), where \(P = V_{a0}V_{b0}\) is the tip off the original space vector and \(A_k\) is the origin of the triangle, are found as follows,

\[
V_{a1} = V_a - K_1 + \frac{1}{2} k_2 \tag{24}
\]

\[
V_{b1} = V_b - \frac{\sqrt{3}}{2} k_2 \tag{25}
\]

The triangle index is obtained by \(\Delta = k_1^2 + 2k_2\)

\[t_a = \text{(duration of the space-vector aligned with the } \alpha\text{-axis)}\]

\[t_b = \text{(duration of the space-vector at } 60^\circ \text{ from the } \alpha\text{-axis)}\]

\[t_z = t_a - t_a - t_b \tag{28}\]

The three neighboring space vectors are used to integrate the reference vector. These vectors resembled with vertices of the bounding triangle. The vector sequence in the first sector is determined through analysis. In other sectors, the states are obtained from the mapping of switching states between the first sector and other sectors. The switch states are remains constant for the first sector, but other sectors are negligible accordingly so that they use the available space-vector in other sectors.

In this paper the DTC-SVM scheme applied instead of hysteresis band, regulate the torque and the magnitude of flux is utilized by means of three-level inverter. Figure 7 shows the block diagram of the DTC-SVM with PI regulators

![Block Diagram of DTC-SVM with PI Regulators](image-url)
and three-level inverter. Proposed method control both the torque and the magnitude of flux, thereby generating the voltage command for inverter control, mainly no decoupling mechanism is required as the flux magnitude and the torque can be regulated by the PI controllers. DTC based two-level and DTC-SVM three level schemes are implemented in simulation shown in section 5.

5. Simulation Results

DTC scheme are introduced which is improve the induction motor performances via torque ripple reduction. Phase voltage and current waveform for two-level shown in Figure 8 (a,b). Torque and flux ripple performance and with speed and current waveform shown in Figure 8 (c,d,e). The Proposed DTC-SVM for NPC inverter fed induction motor simulation results are which is current and voltage waveform shown in Figure 9 (a,b). The Torque Ripple and flux performance with stator current and speed for three-level NPC inverter scheme shown in Figure 9 (c,d,e). The comparison performance shows that proposed scheme has an adequate solution to minimizing the torque with high speed operations.

6. Conclusions

This paper deal with comparison and study about two level inverter DTC scheme and a simple space vector based three-level NPC inverter fed Induction motor for Torque Control. Proposed three level neutral point clamped inverter obtained the better torque and flux response through a simple space vector scheme. A simple space vector is generated using torque reference generation. NPC inverter scheme is improved the torque response by controlling the phase current over two level inverter scheme and easy control of DC-Link capacitor voltage balancing and torque control loop. Simulation results are obtained in both topologies. A simple space vector Three-level NPC inverter scheme obtained better torque control over Two-level inverter scheme using single control loop.

7. Appendix

The parameters of Induction Motor

0.5hp, 50Hz, 1500rpm, p = 4

\[ R_p = 24 \, \Omega, \quad R_s = 17.9\Omega \]

\[ L_m = 914\, mH, \quad L_s = 98.4mH, \quad L_r = 914mH \]

\[ J = 2.5kg.m^2, \quad \beta = N.m.s \]

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