Single Phase Sine Wave PWM Inverter Circuit Simulation And The Design Of Filter Based On Matlab

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Abstract. Inverter circuit is the most important application of PWM control technology. This paper mainly discusses the unipolar PWM (pulse width modulation) control mode of single-phase bridge inverter circuit, and uses MATLAB visual simulation tool Simulink to model the circuit. At the same time, a filter is designed to filter the waveform of the inverted output so that the output waveform can reach a smooth sine wave shape. This simulation not only deepens the understanding of PWM inverter circuit theory, but also lay a good experimental foundation for the study of modern power electronics.

1 Introduction

In power electronics technology, converting direct current into alternating current is called inversion. Inverter circuits are widely used, for example, inverter circuits are needed when DC power supplies AC loads. Inverter circuit is also the core part of power electronic devices such as frequency converter for AC motor speed regulation, uninterruptible power supply and induction heating power supply\cite{1}. At present, the inverter technology mainly uses pulse width modulation (PWM). Because the characteristics of PWM wave determine that the output voltage of the power amplifier device contains more harmonic components\cite{2}, filters must be used in the inverter output to make the output waveform reach a smooth sine wave shape and reduce the influence of harmonics on the load.

2 Simulation of single-phase sine wave PWM inverter circuit

The simulation model of single-phase sine wave inverter circuit modulated by unipolar PWM is shown in figure 1. The DC power supply voltage in the main circuit is 200 V, which is connected to a resistive inductive load via a bridge inverter circuit formed by universal - bridge module\cite{3}. The load inductance is 2 mH and the resistance is 3 Ω.

\begin{figure}
\centering
\includegraphics[width=\textwidth]{figure1.png}
\caption{Simulation model of single-phase sinusoidal PWM inverter.}
\end{figure}

\begin{figure}
\centering
\includegraphics[width=\textwidth]{figure2.png}
\caption{PWM generator parameter setting.}
\end{figure}

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Figure 3. Simulation waveform of single-phase sinusoidal PWM inverter circuit.

The parameter settings of universal - bridge module are shown in figure 2. The simulation waveform of the circuit is shown in figure 3. It can be seen from the waveform that the result is completely consistent with the real experiment result, which proves the correctness of the simulation result.

3 Design of filter

3.1. Parameter setting of filter

When designing filters, the value of inductance is usually designed first. The selection of inductance is related to the ripple current and the power consumption of the system. In general, the ripple current on the inductor is selected to be 15% - 25% of the rated current. In the case of unipolar modulation [4]:

$$\Delta I_i = \frac{U_d - U_o(t)}{L} \times \frac{D(t)}{f_c}$$  \hspace{1cm} (1)

Under the condition that the switching frequency is far greater than the working frequency, according to the principle of unipolar PWM, the duty ratio of each switching period can be obtained as follows

$$D(t) = \frac{U_o(t)}{U_d}$$  \hspace{1cm} (2)

It can be obtained by substituting equation (2) into equation (1)

$$\Delta I_i = \frac{U_d - U_o(t)}{L} \times \frac{U_o(t)}{f_c \times U_d}$$  \hspace{1cm} (3)

As can be seen from equation (3),

$$U_o(t) = \frac{U_d}{2}$$, there is a maximum value

$$\Delta I_{imax} = \frac{U_d}{4L f_c}$$  \hspace{1cm} (4)

So there is

$$L \geq \frac{U_d}{4f_c \Delta I_{imax}}$$  \hspace{1cm} (5)

The cut-off frequency of LC filter $f_c$ is

$$f_c = \frac{1}{2\pi \sqrt{LC}}$$  \hspace{1cm} (6)

among them $10f \leq f_c \leq f_{har(min)/2}$

In the formula: $f$ the fundamental frequency; $f_{har(min)}$ is the lowest harmonic frequency.

If the current ripple of the filter inductor is approximately regarded as a sine wave and it is assumed that all of them are absorbed by the filter capacitor, then the maximum value of the output voltage ripple should be

$$\Delta U_{omax} = \Delta I_{imax} \times \frac{1}{2\pi f_c C}$$  \hspace{1cm} (7)

Formula (4) can be obtained by substituting formula (7)

$$\Delta U_{omax} = \frac{U_d}{8\pi f_c^2 LC}$$

Obtain following equation

$$LC \geq \frac{U_d}{8\pi f_c^2 \Delta U_{omax}}$$  \hspace{1cm} (8)

The value of L can be obtained from equation (5) and the value of C can be calculated from equation (6). The parameter settings for L are shown in figure 4 and the parameter settings for C are shown in figure 5.

Figure 4. Parameter setting of inductance in filter.
3.2 Establishment of simulation model

The simulation circuit of LC filter is shown in figure 6, and the output waveform is shown in figure 7.

Through the LC filter designed by the above method, the simulation experiment shows that the high frequency components can be filtered out and the relatively smooth sine wave can be output by further adjusting the parameters of L and C.

4 Harmonic analysis

The harmonic analysis of the output current obtained by the single-phase sinusoidal PWM inverter circuit without filter is carried out through the "power GUI". The result is shown in figure 8. THD = 13.62 %, which is much larger than the 8 % content of the national standard[5].

The output current obtained by the single-phase sinusoidal PWM inverter circuit is filtered by a filter and analyzed by "power GUI". The result is shown in figure 9. THD = 2.30 %, which greatly reduces the harmonic content and meets the national standard.

5 Concluding remarks

In order to make the PWM inverter have a better output waveform, the second order LC low pass filter network [6] is used in view of the high harmonic frequency of the PWM inverter.
From the simulation results, it can be seen that after LC filtering is added, the harmonic components in the output voltage are filtered out, which is closer to the sine wave shape, while the waveform of the load current is smoother and the sine degree is better. It shows that adding filter link can obviously improve the inverter output waveform.

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