ADMM LP decoding of non-binary LDPC codes in $\mathbb{F}_{2^m}$

Xishuo Liu †, Stark C. Draper ‡

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Abstract

In this paper, we develop efficient decoders for non-binary low-density parity-check (LDPC) codes using the alternating direction method of multipliers (ADMM). We apply ADMM to two decoding problems. The first problem is the linear programming (LP) decoding problem. In order to develop an efficient algorithm, we focus on non-binary codes in fields of characteristic two. This allows us to use operations in $\mathbb{F}_2$ to relax further the set of constraints into a form that has a factor graph representation. Applying ADMM to the LP decoding problem yields two types of non-trivial sub-routines. The first type of sub-routine requires solving an unconstrained quadratic program. We solve this problem efficiently by leveraging new properties on constraints of the LP decoding problem. The second type of sub-routine requires Euclidean projection onto polytopes that are studied in the literature, and which can be solved efficiently using off-the-shelf techniques. These techniques scales linearly with the dimension of the vector to project. ADMM LP decoding scales linearly with block length, linearly with check degree, and quadratically with field size. The second problem we consider is a penalized LP decoding problem. This problem is obtained by incorporating a penalty term into the LP decoding objective. The ADMM algorithm for this problem requires Euclidean projection onto a polytope formed from embeddings of the non-binary single parity-check code, which can be solved by applying ADMM a second time. Empirically, this decoder significantly outperforms LP decoding at low signal-to-noise ratios.

1 Introduction

Linear programming (LP) decoding of binary low-density parity-check (LDPC) codes is introduced by Feldman et al. in [1]. This LP decoding problem is defined by a linear objective function and a set of linear constraints. The linear objective function is constructed using the log-likelihood ratios and is the same objective as in maximum likelihood (ML) decoding. The set of constraints is obtained by first relaxing each parity-check constraint to a convex polytope called the “parity polytope” and then intersecting all constraints. Compared to classic belief propagation (BP) decoding, LP decoding is better analyzable. Many decoding guarantees (e.g., [2–6]) are shown in the literature. However, LP decoding suffers from high complexity when approached using generic

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†X. Liu is with the Dept. of Electrical and Computer Engineering, University of Wisconsin, Madison, WI 53706 (e-mail: xliu94@wisc.edu).

‡S. C. Draper is with the Dept. of Electrical and Computer Engineering, University of Toronto, ON M5S 3G4, Canada (e-mail: stark.draper@utoronto.ca).
solvers. Since [1], many works have sought to reduce the computational complexity for LP decoding of binary LDPC codes [7–13]. Of particular relevance is [11] where the authors use the alternating direction method of multipliers (ADMM) to decompose the LP decoding problem and develop an efficient decoder that has complexity comparable to the sum-product BP decoder. In a more recent work [14], Liu et al. use ADMM to try to solve a penalized LP decoding objective and term the problem *ADMM penalized decoding*. The penalized LP objective is constructed by adding a non-convex term to the LP decoding objective to penalized pseudocodewords, to which LP decoding fails. Empirically, ADMM penalized decoding outperforms ADMM LP decoding in terms of both word-error-rate (WER) and computational complexity. There are two important outcomes from [11] and [14]. First, ADMM based decoding algorithms can be efficient algorithms that are promising for practical implementation. Second, these algorithms can achieve lower WERs than BP does at both low and high signal-to-noise ratios (SNRs). In particular, simulation results in [14] show that ADMM penalized decoding can outperform BP decoding at low SNRs and does not suffer from an error-floor at WERs above $10^{-10}$ for the $[2640, 1320]$ “Margulis” LDPC code.

LP decoding of non-binary LDPC codes is introduced by Flanagan et al. in [15]. Results in [11] and [14] motivate us to extend ADMM decoding to non-binary codes. However, this extension is non-trivial. One key component of the ADMM decoder in [11] is a sub-routine that projects a length-$d$ vector onto the parity polytope – the convex hull of all length-$d$ binary vectors of even parity. The projection algorithm developed in [11] has a complexity of $O(d \log d)$. More recently, [12] and [13] propose more efficient projection algorithms, e.g., the algorithm proposed in [13] has linear complexity in dimension $d$. Unfortunately, these techniques cannot be directly applied to LP decoding of non-binary codes as the polytope induced has two major differences. First, unlike the binary case where 0, 1 can be directly embedded into the real space, elements of $\mathbb{F}_q$ need to be mapped to binary vectors of dimension $q - 1$ [15]. Second, permutations of a codeword of non-binary single parity-check (SPC) codes are not necessarily codewords whereas for binary SPC codes all permutations of a codeword are codewords.

We briefly describe our approach to developing an efficient LP decoder using ADMM. We focus on non-binary codes in fields of characteristic two and represent each element in $\mathbb{F}_{2^m}$ by a binary vector of dimension $m$ where each entry corresponds to a coefficient of the polynomial representing that element. This allows us to build a connection between an element’s embedding (i.e., those defined in [15]) and its binary vector representation. We then obtain a factor graph characterization of valid embeddings. With this characterization, the polytopes considered in [15] can be further relaxed to intersections of simplexes and parity polytopes. We develop two set of important results pertinent to this relaxation. First, we show several properties of the aforementioned factor graph of embeddings. As we will see in the main text, these properties are crucial for developing an efficient ADMM update algorithm. Further, these properties are useful in determining the computational complexity of the decoder. Second, we regain the permutation properties by introducing a rotation step similar to the BP case in [16]. This step rotates the polytope so that it has a “block permutation” property. This result is not only an interesting theoretical characterization of the polytope but also plays an important role in the algorithm.

Several other works also address the complexity of LP decoding of non-binary LDPC codes. For example, in [17] and [18], algorithms are presented that perform coordinate-ascent on an approximated dual problem to the original LP decoding problem. Using binary vectors to represent elements of $\mathbb{F}_{2^m}$ for LP decoding is considered in [19] and [20]. In particular in [19], Honda and Yamamoto introduced a subtle connection between a constant weight embedding of finite fields.
and the binary vector representation of finite fields in [19]. Although the authors of [19] did not provide an efficient algorithm to leverage this connection, we think that this work is important and inspiring.

We summarize our contributions and novelties in this paper.

- We extend the ideas of [19] and establish connections between Flanagan’s embedding of finite fields [15] and the binary vector representation of finite fields. We introduce a new factor graph representation of non-binary constraints based on embeddings (Section 3.2).

- We study the geometry of the LP decoding constraints. We show that the polytope formed from embeddings of the non-binary single parity-check code can be rotated so that the vertices have a “block permutation” property. We also show a property regards the tightness of the polytope (Section 4). A conjecture that characterizes the polytope for $F_{2^2}$ is discussed in Appendix 12.

- We conduct an extensive study of both Flanagan’s embedding method and the constant-weight embedding from [19] in the context of LP decoding. The main result here is that LP decoding using both embedding methods achieve the same WERs (Section 5.2).

- We formulate two ADMM decoding algorithms. The formulation in Section 5.3 applies to LP decoding and penalized decoding (cf. Section 6) of non-binary codes in arbitrary fields. However, in arbitrary fields we have not been successful in developing a computationally simple projection sub-routine. In Section 5.4 we limit our focus to LP decoding of codes in $F_{2^m}$ and develop a much more efficient algorithm.

- We extend the penalized decoding idea in [14] to non-binary codes (Section 6). We show through numerical results that the penalized decoder improves the WER significantly at low SNRs (Section 7).

2 Notation and definitions

In this section, we fix some notation and definitions that will be used throughout the paper.

2.1 General font conventions

We use $\mathbb{R}$ to denote real numbers and $\mathbb{F}_q$ to denote finite fields of size $q$. We denote by $\mathbb{F}_{2^m}$ fields of characteristic two. We use bold capital letters to denote matrices and bold small letters to denote vectors. We use non-bold small letters to denote entries of matrices and vectors. For example, $\mathbf{X}$ is a matrix and $x_{ij}$ is the entry at the $i$-th row and the $j$-th column. $\mathbf{x}$ is a vector and its $i$-th entry is $x_i$. We use script capital letters to denote discrete sets and use $|\cdot|$ to denote the cardinality of a set. Further, denote by $[n]$ the set $\{1,\ldots,n\}$. We use sans-serif font to represent mappings and functions, e.g. $f(\cdot)$ and $F(\cdot)$. We also explicitly define the following operations: $\text{conv}(\cdot)$ is the operation of taking the convex hull of a set in $\mathbb{R}^n$. It is defined as

$$ \text{conv}(\mathcal{S}) := \left\{ \sum_{i=1}^{|\mathcal{S}|} \alpha_i s_i \mid \sum_{i=1}^{|\mathcal{S}|} \alpha_i = 1 \text{ and } \alpha_i \geq 0 \text{ for all } i \right\}. $$
vec(·) is the operation of vectorizing a matrix, i.e., stacking all columns of a matrix to form a column vector. For example, let \( X \) be a \( m \times n \) matrix; then
\[
\text{vec}(X) = (x_{11}, x_{21}, \ldots, x_{1m}, x_{21}, x_{22}, \ldots, x_{m(n-1)}, x_{1n}, \ldots, x_{mn})^T.
\]

### 2.2 Notation and definitions of convex geometries

Throughout this paper we use roman blackboard bold symbols to denote convex sets, e.g., \( \mathbb{P} \). In particular, we define the following polytopes that will be used extensively throughout this paper:

**Definition 1** Let \( \mathbb{P}_d \) denote the parity polytope of dimension \( d \),
\[
\mathbb{P}_d := \text{conv}\{e \in \{0,1\}^d | \|e\|_1 \text{ is even}\}.
\]

Let \( \mathcal{S}_{d-1} \) denote the positive orthant of a unit \( \ell_1 \) ball of dimension \( d-1 \),
\[
\mathcal{S}_{d-1} := \left\{ (x_1, \ldots, x_{d-1}) \in \mathbb{R}^{d-1} \left| \sum_{i=1}^{d-1} x_i \leq 1, \text{ and } x_i \geq 0 \text{ for all } i \right. \right\}.
\]

Let \( \mathcal{S}'_d \) denote the standard \( d \)-simplex,
\[
\mathcal{S}'_d := \left\{ (x_0, \ldots, x_{d-1}) \in \mathbb{R}^{d-1} \left| \sum_{i=0}^{d-1} x_i = 1, \text{ and } x_i \geq 0 \text{ for all } i \right. \right\}.
\]

It is easy to verify that \((x_1, \ldots, x_{d-1}) \in \mathcal{S}_{d-1}\) implies \((x_0, \ldots, x_{d-1}) \in \mathcal{S}'_d\) where \( x_0 = 1 - (\sum_{i=1}^{d-1} x_i) \). Because of this relationship, we use \( \mathcal{S}_{d-1} \) with one dimension less than \( \mathcal{S}'_d \). Further, we let the vector index start from 0 for vectors in \( \mathcal{S}'_d \) and 1 for vectors in \( \mathcal{S}_{d-1} \).

### 2.3 Notation of non-binary linear codes

Let \( C \) be a non-binary linear code of length \( N \) with \( M \) checks. We use \( H \) to denote its parity-check matrix where each entry \( h_{ji} \in \mathbb{F}_q^2 \). Then the code \( C = \{c | Hc = 0 \ \text{in} \ \mathbb{F}_q\} \). When represented by a factor graph, the set of variable and check nodes are denoted by \( \mathcal{I} \) and \( \mathcal{J} \) respectively. Let \( \mathcal{N}_i(i) \) and \( \mathcal{N}_c(j) \) be the respective set of neighboring nodes of variable node \( i \) and of check node \( j \).

### 2.4 Integer representation of finite fields

In this paper we often consider finite fields of characteristic two. Since we will have more occasion to add in finite fields than to multiply, we represent each element of \( \mathbb{F}_{2^m} \) using an integer in \( \{0, \ldots, 2^m-1\} \). Without loss of generality, an element \( \alpha \in \mathbb{F}_{2^m} \) can be represented by a polynomial \( p(x) = \sum_{i=1}^{m} b_i x^{i-1} \). In this paper we often use the integer representation of \( \alpha \) which is given by \( p(2) = \sum_{i=1}^{m} b_i 2^{i-1} \).

**Example 2** We consider \( \mathbb{F}_{2^3} \) and let the primitive polynomial be \( x^3 + x + 1 \). Let \( \xi \) be the primitive element of the field. We list different representations of elements in \( \mathbb{F}_{2^3} \) in Table 1. Note that although we use the integer representations, multiplications are still performed using field operations. For example, using Table 1, \( 4 \cdot 6 = 5 \) in \( \mathbb{F}_{2^3} \).

In \( \mathbb{F}_{2^2} \) we let the primitive polynomial be \( x^2 + x + 1 \). Let \( \xi \) be the primitive element of the field. We then obtain different representations of elements in \( \mathbb{F}_{2^2} \) in Table 2.

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1 Note that \( \mathbb{R} \) (for real numbers) and \( \mathbb{F} \) (for finite fields) are two special cases where we use roman blackboard bold symbols.

2 We use \( j \) to represent a check node index and \( i \) to represent a variable node index.
3 Embedding methods and representations of non-binary single parity-check codes

Embedding finite fields into reals is a crucial step in LP decoding of non-binary codes. In this section, we first review two somewhat similar embedding methods. We refer these embeddings as “Flanagan’s embedding” and “the constant-weight embedding”. In Section 3.2, we express the constraints of non-binary SPC codes using these embeddings and then show properties of the constraints. In Section 4, we characterize the properties of the relaxed constraints. In particular, we introduce a rotations step that normalizes the geometry under consideration such that we only need to study the geometry induced by the SPC code defined by the all-ones check.

3.1 Embedding finite fields

One key component of LP decoding is in building a connection between finite fields and the Euclidean space. Namely, binary codes of block length $N$ live in the space of $\{0, 1\}^N$. Feldman et al. use vectors that live in $[0, 1]^N$ to transform the ML decoding problem into a linear program. This straightforward transformation cannot be applied to the ML decoding problem of non-binary linear codes, where codes live in $\mathbb{F}_q^N$. We focus on embedding methods in this section and defer our discussions of the LP decoding formulation to Section 5.

In [15], Flanagan et al. introduce a mapping that embeds elements in $\mathbb{F}_q$ into the Euclidean space of dimension $q - 1$.

**Definition 3 (Flanagan’s embedding [15])** Let $f : \mathbb{F}_q \mapsto \{0, 1\}^{q-1}$ be a mapping such that for $\alpha \in \mathbb{F}_q$,

$$f(\alpha) := x = (x_1, x_2, \ldots, x_{q-1})$$

where

$$x_\delta = \begin{cases} 1, & \text{if } \delta = \alpha, \\ 0, & \text{if } \delta \neq \alpha. \end{cases}$$

| polynomial | bits ($b_3b_2b_1$) | integer |
|------------|---------------------|---------|
| $\xi^0$    | 1                   | 001     | 1       |
| $\xi^1$    | $\xi$               | 010     | 2       |
| $\xi^2$    | $\xi^2$             | 100     | 4       |
| $\xi^3$    | $\xi + 1$           | 011     | 3       |
| $\xi^4$    | $\xi^2 + \xi$       | 110     | 6       |
| $\xi^5$    | $\xi^2 + \xi + 1$   | 111     | 7       |
| $\xi^6$    | $\xi^2 + 1$         | 101     | 5       |

Table 1: Different representations of elements in $\mathbb{F}_{2^3}$.

| polynomial | bits ($b_3b_2b_1$) | integer |
|------------|---------------------|---------|
| $\xi^0$    | 1                   | 01      | 1       |
| $\xi^1$    | $\xi$               | 10      | 2       |
| $\xi^2$    | $\xi + 1$           | 11      | 3       |

Table 2: Different representations of elements in $\mathbb{F}_{2^2}$.
We note that $f(\alpha)$ is a vector of length $q - 1$ with at most one 1. Using this mapping, $0 \in \mathbb{F}_q$ is mapped to the all-zeros vector of length $q - 1$, which has Hamming weight 0. On the other hand, all other elements of $\mathbb{F}_q$ are mapped to binary vectors of Hamming weight 1.

In a more recent work [19], the authors use a slightly different embedding:

**Definition 4 (constant-weight embedding [19])** Let $f' : \mathbb{F}_q \mapsto \{0, 1\}^q$ be a mapping such that for $\alpha \in \mathbb{F}_q$,

$$f'(\alpha) := x = (x_0, x_1, \ldots, x_{q-1})$$

where

$$x_\delta = \begin{cases} 1, & \text{if } \delta = \alpha, \\ 0, & \text{if } \delta \neq \alpha. \end{cases}$$

Now $f'(\alpha)$ is a vector of length $q$. In addition, the Hamming weight of $f'(\alpha)$ is 1 for all values of $\alpha \in \mathbb{F}_q$. For this reason, we name this embedding method constant-weight embedding.

Using Flanagan’s embedding in Definition 3, a vector $c \in \mathbb{F}_d^q$ can be mapped to a length $(q - 1)d$ vector $F_v(c) = (f(c_1)f(c_2) \cdots |f(c_n))^T$. We can also write the above vector in an equivalent matrix of dimension $(q - 1) \times d$ as: $F(c) = (f(c_1)^T | f(c_2)^T | \cdots | f(c_n)^T)$. In this representation, each column represents a coordinate of vector $c$. We use the term embedded matrix to refer to the matrix-form embedding of a codeword. Further, let $F(C)$ and $F_v(C)$ be images of set $C$ under the two mappings respectively. That is, $F(C) = \{y \mid y = F(c) \text{ for } c \in C\}$ and $F_v(C) = \{y \mid y = F_v(c) \text{ for } c \in C\}$. Similar definition also applies to the constant-weight embedding in Definition 4. Further, we use $F'(\cdot)$ and $F'_v(\cdot)$ to denote the respective operations of embedding a vector using matrices and vectors following the constant-weight embedding method. Following the convention of Definition 4, we index rows of the matrix obtained by $F'(\cdot)$ from zero.

The two embedding methods have many aspects in common. In particular, many definitions and lemmas for one embedding method can easily be extended to the other embedding. We show in Section 5.2 that both embeddings achieve the same WER for LP decoding. On the other hand, there are two important differences between the two embeddings that make each useful for particular purposes. First, Flanagan’s embedding saves exactly 1 coordinate for each embedded vector which typically translates into lower complexity. In particular, we show in Section 7 that ADMM LP decoding using the constant-weight embedding is slower than when Flanagan’s embedding is used. Second, the constant-weight embedding is symmetric to all elements in the field while Flanagan’s embedding treats 0 differently from other elements. This symmetry makes the constant-weight embedding useful when trying to solve non-convex programs, such as the penalized decoder described in Section 6.

### 3.2 Embedding of single parity-check codes in $\mathbb{F}_{2^m}$

We now restrict our discussion to fields of characteristic two. We further focus on Flanagan’s embedding for conciseness. It is easy to extend all the definitions and lemmas to the constant-weight embedding. These extensions are presented in Appendix 9.

A $d_c$-dimensional\(^3\) SPC code in $\mathbb{F}_{2^m}$ is defined as

$$C = \left\{ c \in \mathbb{F}_{2^m}^{d_c} \mid \sum_{j=1}^{d_c} c_j h_j = 0 \right\}, \quad (3.1)$$

\(^3\)We use $d_c$ to denote the length of the code because it is related to the check degree of an LDPC code.
where the addition is in \( \mathbb{F}_{2^m} \) and where \( \mathbf{h} = (h_1, h_2, \ldots, h_{d_c}) \in \mathbb{F}_{2^m}^{d_c} \) is the check vector. We are interested in characterizing \( F(C) \) because it arises in the relaxation LP decoding. In [19] Honda and Yamamoto use a set of constraints derived from (3.1) that tests whether an embedding is in \( F(C) \) to introduce their relaxation. However, the authors did not formally study these constraints (on SPC code embeddings). In this paper, we make the following contributions: First, we characterize \( F(C) \) and \( F'(C) \), i.e., SPC code embeddings for both embedding methods. Second, we explicitly state a factor graph representation of the embedding of the SPC code and derive properties of the factor graph. Third, we discuss the redundancy of these constraints. Finally, we characterize \( F(C) \) for \( \mathbb{F}_{2^2} \) in Appendix 12.

We first formally define the binary vector representation of \( \mathbb{F}_{2^m} \) that appeared in Example 2:

**Definition 5** For an element \( \alpha \in \mathbb{F}_{2^m} \), let

\[
\mathbf{b}(\alpha) = (b_1, b_2, \ldots, b_m)
\]

be the binary vector representation of \( \alpha \). In other words, the polynomial representation for \( \alpha \) is

\[
p(x) = b_m x^{m-1} + b_{m-1} x^{m-2} + \cdots + b_1.
\]

Then, for any vector \( \mathbf{c} \in \mathbb{F}_{2^m}^{d_c} \), the “bit matrix” representation of \( \mathbf{c} \) is a binary matrix of dimension \( m \times d_c \). Denote this representation as

\[
\mathbf{B}(\mathbf{c}) = (\mathbf{b}(c_1)^T | \mathbf{b}(c_2)^T | \cdots | \mathbf{b}(c_{d_c})^T).
\]

**Example 6** In \( \mathbb{F}_{2^2} \), \( \mathbf{B}((1, 2, 3)) = \begin{bmatrix} 0 & 1 & 1 \\ 1 & 0 & 1 \end{bmatrix} \).

Consider the set of embeddings defined in Definition 7 when Flanagan’s embedding is used. We show in Lemma 8 that Definition 7 characterizes \( F(C) \).

**Definition 7** Under Flanagan’s embedding let \( q = 2^m \) and denote by \( \mathcal{E} \) the set of valid embedded matrices defined by a length-\( d_c \) check \( \mathbf{h} \) where any \( (q-1) \times d_c \) binary matrix \( \mathbf{F} \in \mathcal{E} \) if and only if it satisfies the following conditions:

(a) \( f_{ij} \in \{0, 1\} \), where \( f_{ij} \) denotes the entry in the \( i \)-th row and \( j \)-th column of matrix \( \mathbf{F} \).

(b) \( \sum_{i=1}^{q-1} f_{ij} \leq 1 \) where \( q = 2^m \).

(c) For any non-zero \( h \in \mathbb{F}_{2^m} \) and \( k \in [m] \), let \( \mathcal{B}(k, h) := \{ \alpha | \mathbf{b}(h\alpha)_k = 1, \alpha \in \mathbb{F}_{2^m} \} \), where \( \cdot_k \) denotes the \( k \)-th entry of the vector. Let \( g^k_j = \sum_{i \in \mathcal{B}(k, h)} f_{ij} \),\(^4\) then

\[
\sum_{j=1}^{d_c} g^k_j = 0 \quad \text{for all } k \in [m] \tag{3.2}
\]

where the addition is in \( \mathbb{F}_2 \).

\(^4\)Note that the sum is in the real space but can only be 1 or 0. Thus \( g^k_j \) is still in \( \mathbb{F}_2 \).
Lemma 8 In $\mathbb{F}_{2^m}$, let $C$ be the set of codewords that correspond to check $h = (h_1, h_2, \ldots, h_{d_c})$. Then

$$F(C) = \mathcal{E},$$

where $\mathcal{E}$ is defined by Definition 7.

Proof See Appendix. 10.2.

Proposition 9 In $\mathbb{F}_{2^m}$, for all $k \in [m]$ and $h \neq 0$,

$$|B(k, h)| = 2^{m-1}.$$  

(3.4)

Proof See Appendix. 10.1.

We can obtain a similar definition for the constant-weight embedding by changing two statements in Definition 7. First, the dimension of valid embedded matrices become $q \times d_c$. Second, Condition (b) from Definition 7 becomes $\sum_{i=0}^{q-1} f_{ij} = 1$. Using this definition, a similar statement holds for the constant-weight embedding (cf. Lemma 35). In Appendix 9 we show the equivalent definition and lemma for the the constant-weight embedding.

Definition 10 The conditions in Definition 7 can be represented using a factor graph. In this graph, there are three types of nodes: variable nodes, parity-check nodes and at-most-one-on-check node. Each variable node corresponds to an entry $f_{ij}$ for $1 \leq i \leq 2^m - 1$ and $1 \leq j \leq d_c$. Each parity-check node corresponds to a parity-check of the $k$-th bit where $k \in [m]$; it connects all variable nodes in $B(k, h_j)$ for all $j \in [d_c]$. Each at-most-one-on-check node corresponds to a constraint $\sum_{i=1}^{q-1} f_{ij} \leq 1$. We use circles to represent variables nodes and squares to represent parity-check nodes. In addition, we use triangles to represent at-most-one-on-check nodes because the polytope we get by relaxing this constraint is $S_{q-1}$.

Example 11 Consider $\mathbb{F}_{2^2}$ and let $h = (1, 2, 3)$. Let $C$ be the SPC code defined by $h$. For a word $c \in \mathbb{F}_2^3$, denote by

$$F := F(c) = \begin{pmatrix} f_{11} & f_{12} & f_{13} \\ f_{21} & f_{22} & f_{23} \\ f_{31} & f_{32} & f_{33} \end{pmatrix}$$

and denote by

$$B := B(c) = \begin{pmatrix} b_{11} & b_{12} & b_{13} \\ b_{21} & b_{22} & b_{23} \end{pmatrix}$$

By Lemma 8, $c \in C$ if and only if $F$ satisfies the following conditions:

(a) $f_{ij} \in \{0,1\}$.

(b) $\sum_{i=1}^{3} f_{ij} \leq 1$, where addition is in $\mathbb{R}$.

(c) We can calculate from Table 2 that $B(1, 1) = \{2, 3\}$, $B(2, 1) = \{1, 3\}$, $B(1, 2) = \{1, 2\}$, $B(2, 2) = \{2, 3\}$, $B(1, 3) = \{1, 3\}$, and $B(2, 3) = \{1, 2\}$. Therefore by Definition 7 $g_1^1 = (f_{21} + f_{31}, f_{12} + f_{22}, f_{13} + f_{33})$ and $\sum_j g_j^1 = 0$ (in $\mathbb{F}_2$). Similarly, $g_2^2 = (f_{11} + f_{31}, f_{22} + f_{32}, f_{13} + f_{23})$ and $\sum_j g_j^2 = 0$ (in $\mathbb{F}_2$).
Using Definition 10, we draw the factor graph in Figure 1. Each square represents a parity-check and each triangle represents an at-most-one-on-check.

One important remark regarding Definition 7 is that it is not the unique way of defining valid embeddings. In fact in [19], the authors’ relaxation is implicitly based on the conditions in Lemma 12 below.

**Lemma 12** Consider Flanagan’s embedding and let $F$ be the set of $(q - 1) \times d_c$ binary matrices defined by the first two conditions of Definition 7 but with the third condition replaced by the Condition $(c^*)$ defined below, then $F = \mathcal{E}$. The complete set of conditions are

(a) $f_{ij} \in \{0, 1\}$, where $f_{ij}$ denotes the entry in the $i$-th row and $j$-th column of matrix $F$.

(b) $\sum_{i=1}^{q-1} f_{ij} \leq 1$ where $q = 2^m$.

(c*) Let $K$ be any nonempty subset of $[m]$. For any non-zero $h \in \mathbb{F}_2^m$, let $\tilde{B}(K, h) := \{a | \sum_{k \in K} b(ha)_k = 1\}$, where $b(ha)_k$ denotes the $k$-th entry of the vector $b(ha)$. Let $g^K_j = \sum_{i \in \tilde{B}(K, h_j)} f_{ij}$, then $\sum_{j=1}^{d_c} g^K_j = 0$ for all $K \subset [m], K \neq \emptyset$, where the addition is in $\mathbb{F}_2$.

**Proof** See Appendix 10.3.

**Lemma 13** In $\mathbb{F}_2^m$, for all $K \subset [m]$ and $h \neq 0$,

$$|\tilde{B}(K, h)| = 2^{m-1}.$$  

**Proof** See Appendix 10.4.

**Remarks**

Note that many redundant constraints are used to define $F$ in Lemma 12. Similar to redundant parity-checks for binary linear codes (see e.g., [21]), these redundant constraints can be used to tighten the polytope to be discussed in Section 4. On the other hand, adding such extra constraints generally increases computational complexity. Especially, note that there are $m$ parity-checks in Definition 7 whereas the number of parity-checks in Lemma 12 is $2^{m-1}$.

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The vector index starts from 0.
4 Geometry of the relaxation of embeddings

In this section, we consider embeddings of non-binary SPC codes and study the polytopes obtained from these embeddings. There are two ways of obtaining polytopes relevant to LP decoding. In [15], the authors study the convex hull of $F(C)$. However, this polytope is not easy to characterize. On the other hand, using the factor graph representation of SPC code embeddings from the previous section we can get a relaxation of $\text{conv}(F(C))$ following the methodology of LP decoding of binary LDPC codes in [1]. In other words, the factor graph representation of embeddings resembles a mini binary (“generalized”) LDPC code. Instead of taking the convex hull of all codewords, we can take the intersection of many local polytopes for simplicity. This idea is first seen in [19] for the constant-weight embedding. We first restate the relaxation method in [19] for Flanagan’s embedding; readers are referred to Appendix 9 for the corresponding definitions when the constant-weight embedding is used. We note that this restatement is important because it allows us to leverage off-the-shelf techniques to develop an efficient algorithm. We then show a tightness result of the parity polytope, which can imply a tightness result on the polytope of SPC code embeddings. Finally, we introduce several rotation properties of the polytope of SPC code embeddings that are useful for developing an efficient decoding algorithm.

4.1 Relaxation of single parity-check code embeddings

We first describe the relaxation considered in [15].

**Definition 14** Let $C$ be a non-binary SPC code defined by check vector $h$. Denote by $\mathbb{V}$ the “tight code polytope” for Flanagan’s embedding where

$$\mathbb{V} = \text{conv}(F(C)).$$

The following definition extends the relaxation in [19] to Flanagan’s embedding.

**Definition 15** Let $C$ be a non-binary SPC code defined by check vector $h$. In $\mathbb{F}_2^m$ denote by $\mathbb{U}$ the “relaxed code polytope” for Flanagan’s embedding where a $(q-1) \times d_c$ matrix $F \in \mathbb{U}$ if and only if the following constraints hold:

(a) $f_{ij} \in [0, 1].$

(b) $\sum_{i=1}^{q-1} f_{ij} \leq 1.$

(c) Let $\mathcal{B}(K, h)$ be the same set defined in Lemma 12. Let $g^K$ be a vector of length $d_c$ such that $g^K_j = \sum_{i \in \mathcal{B}(K, h)} f_{ij}$, then

$$g^K \in \mathbb{P}_{d_c} \quad \text{for all } K \subset [m] \text{ and } K \neq \emptyset$$

We note that in Definition 15 Conditions (a) and (b) define the simplex $S_{q-1}$. In addition, Condition (c) leverages parity polytope $\mathbb{P}_{d_c}$. Both two polytopes are studied extensively in the literature (e.g. [11] and [24]). We show how to leverage this definition in developing efficient decoding algorithms in Section 5.

---

$^6$From the visual appearances of the letters, one can think of $\mathbb{U}$ as a relaxation of $\mathbb{V}$. 
4.2 A tightness result for the relaxed code polytope

We note that Definition 15 is not the only way to relax the constraints in Lemma 12. In this subsection we study another possible relaxation and show that the relaxation in Definition 15 is tighter.

Consider \( h = (1, 2, 3) \in \mathbb{F}_2^3 \) as a motivating example. Lemma 12 requires that \( g^{(1)} \) has even parity (\( K = \{1\} \) in this case). This means \((f_{21} + f_{31}) + (f_{12} + f_{22}) + (f_{13} + f_{33}) = 0\) in \( \mathbb{F}_2 \) (cf. Example 11). Now consider the following two possible relaxations. We let \( 0 \leq f_{ij} \leq 1 \) and \( \sum_i f_{ij} \leq 1 \) for both cases.

(i) Vector \( (f_{21}, f_{31}, f_{22}, f_{32}, f_{23}, f_{33}) \in \mathbb{P}_6 \).

(ii) Vector \( (f_{21} + f_{31}, f_{22} + f_{32}, f_{23} + f_{33}) \in \mathbb{P}_3 \) (the same relaxation used in Definition 15).

One would argue that both relaxations are applicable to LP decoding because the integral points of both relaxations are embeddings that satisfy Lemma 12. We justify using Proposition 16 that statement (ii) implies statement (i) for more general cases. Thus, Definition 15 is a better relaxation method.

**Proposition 16** Let

\[ x := (x^1, x^2, \ldots, x^s) \]

be a non-negative vector obtained by concatenating \( s \) row vectors \( x^i \), where each \( x^i \) is a length-\( t \) vector. Let \( y := (\|x^1\|_1, \|x^2\|_1, \ldots, \|x^s\|_1) \). Then \( y \in \mathbb{P}_s \) implies \( x \in \mathbb{P}_{st} \).

**Proof** See Appendix 10.5.

Note that the converse of Proposition 16 does not hold. That is, if \( x \in \mathbb{P}_{st} \) and \( \|x^i\|_1 \leq 1 \) for all \( i \) \( y \) does not necessarily belong to \( \mathbb{P}_s \). As a counter example, let \( x = (0.5, 0.5, 0, 0, 0) \). Then \( x \in \mathbb{P}_6 \) but \( y = (1, 0, 0) \notin \mathbb{P}_3 \).

By applying Proposition 16 to our example, we conclude that \( (f_{21} + f_{31}, f_{22} + f_{32}, f_{23} + f_{33}) \in \mathbb{P}_3 \) implies that \( (f_{21}, f_{31}, f_{22}, f_{32}, f_{23}, f_{33}) \in \mathbb{P}_6 \).

4.3 Rotation

We describe a rotation operation that normalizes the geometry under consideration. This process simplifies the our study of the geometries because one only needs to work on a canonical object instead of different polytopes for different checks. A similar idea is also found in BP decoding in [16].

As a side effect, the normalization results in a column permutation property for valid embedded matrices. In particular, post-normalization, any permutation when applied to columns of a valid SPC code embedded matrix is a valid SPC code embedded matrix. Note that the convex hull of all permutations of a vector can be defined using (“single-variate” a.k.a., “vector”) majorization [22]. This observation is the key to the algorithm of projection onto the parity polytope [11]. Similarly, the convex hull of all column permutations of a matrix can be defined using multivariate majorization [22]. Thus it may be possible to use results for multivariate majorization to characterize \( \text{conv}(F(C)) \). We leave this as future work.

We first describe the rotation operation for \( \text{conv}(F(C)) \). We then show that the same operation can be applied to the relaxed code polytope \( U \).
4.3.1 Rotation for the tight code polytope

Consider an arbitrary finite field $\mathbb{F}_q$ and let $C^N$ be the “normalized” set of codewords defined as $C^N := \{x|\sum_{j=1}^{d_c} x_j = 0\}$. We focus on Flanagan’s embedding and let $E^N := F(C^N)$. Then for any check that is not “normalized” (i.e., contains entries not equal to 1), we can obtain the set of codewords $C$ from $C^N$ by dividing each entry by the corresponding check value in $\mathbb{F}_q$. Formally, let $h = (h_1, \ldots, h_{d_c})$ be the check vector and $h_j \neq 0$. Then

$$C = \{y | y_j = x_j/h_j \text{ for all } j; \ x \in C^N\}.$$ 

Note that in the equation above $x_j \neq 0$ and $y_j \neq 0$. We define the rotation matrix $D$ as follows:

**Definition 17** Let $h$ be a non-zero element in $\mathbb{F}_q$. Let $D(q, h)$ be a $(q-1) \times (q-1)$ permutation matrix satisfying the following equation

$$D(q, h)_{ij} = \begin{cases} 1 & \text{if } i \cdot h = j, \\ 0 & \text{otherwise}, \end{cases}$$

where the multiplication is in $\mathbb{F}_q$. For a vector $h \in \mathbb{F}_q^{d_c}$ that $h_j \neq 0$ for all $j \in [d_c]$, let

$$D(q, h) = \text{diag}(D(q, h_1), \ldots, D(q, h_{d_c})).$$

Using the permutation matrix $D(q, h)$, we can obtain $E$ by rotating every vector in $E^N$:

$$E := \{f | f = D(q, h)e, e \in E^N\}.$$ 

**Example 18** Let $q = 5$ and $h = 3$. Using division in $\mathbb{F}_5$, we obtain $1 \cdot 3^{-1} = 2$, $2 \cdot 3^{-1} = 4$, $3 \cdot 3^{-1} = 1$ and $4 \cdot 3^{-1} = 3$. Therefore

$$D(5, 3) = \begin{pmatrix} 0 & 0 & 1 & 0 \\ 1 & 0 & 0 & 0 \\ 0 & 0 & 0 & 1 \\ 0 & 1 & 0 & 0 \end{pmatrix}.$$ 

Take the equation $1 \cdot 3^{-1} = 2$ for example. The respective embedded vectors of 1 and 2 are $(1, 0, 0, 0)$ and $(0, 1, 0, 0)$. It is easy to verify that $(0, 1, 0, 0)^T = D(5, 3)(1, 0, 0, 0)^T$.

We use $\mathbb{V}_N$ to denote the tight code polytope defined by the all-ones check. The following lemma shows that we can obtain $\mathbb{V}_N$ by rotating $\mathbb{V}$ using $D(q, h)$.

**Lemma 19** Let $h$ be a check such that all entries of $h$ are non-zero. Let $\mathbb{V}$ be the convex hull of $\mathbb{E}$ and $\mathbb{V}_N$ be the convex hull of $E^N$. Then a vector $u \in \mathbb{V}$ if and only if $D(q, h)^{-1}u \in \mathbb{V}_N$. In other words, a vector $w \in \mathbb{V}_N$ if and only if $D(q, h)w \in \mathbb{V}$.

**Proof** See Appendix 10.6.

This lemma implies that having different check values is nothing but a rotation of the normalized code polytope. The following lemma states that projections onto $\mathbb{V}$ for arbitrary checks can be performed via projections onto $\mathbb{V}_N$. 

---

12
Lemma 20  Let $h$ be a check such that all entries of $h$ are non-zero. Let $\mathcal{V}$ be the convex hull of $\mathcal{E}$ and let $\mathcal{V}^N$ be the convex hull of $\mathcal{E}^N$. Let $v$ be any vector and let $v^N := D(q,h)^{-1}v$. Then, if $u^N$ is the projection of $v^N$ onto $\mathcal{V}^N$, then $u := D(q,h)u^N$ is the projection of $v$ onto $\mathcal{V}$.

**Proof**  See Appendix 10.6.

Lemma 20 shows that we can obtain the projection of a vector $v$ onto any tight code polytope $\mathcal{V}$ in three steps: First rotate the vector and get $v^N$, then project $v^N$ onto the normalized polytope $\mathcal{V}^N$, and finally rotate the projection onto $v^N$ back to the original coordinate system.

### 4.3.2 Rotation for the relaxed code polytope

In $F_{2^m}$, we can relax $\mathcal{V}$ using Definition 15 and obtain a relaxed code polytope $\mathcal{U}$. The same permutation matrix $D(q,h)$ can also be applied to $\mathcal{U}$.

Lemma 21  Let $h$ be a check such that all entries of $h$ are non-zero. Let $\mathcal{U}$ be the relaxed code polytope obtained by Definition 15 for check $h$ and let $\mathcal{U}^N$ be the relaxed code polytope obtained by Definition 15 for the all-ones check. Then a vector $v \in \mathcal{U}$ if and only if $D(q,h)^{-1}v \in \mathcal{U}^N$. In other words, a vector $w \in \mathcal{U}^N$ if and only if $D(q,h)w \in \mathcal{U}$.

**Proof**  See Appendix 10.6.

Similar to the previous case, we have the following lemma which applies to the projection operation.

Lemma 22  Let $h$ be a check such that all entries of $h$ are non-zero. Let $\mathcal{U}$ be the relaxed code polytope obtained by Definition 15 for check $h$ and let $\mathcal{U}^N$ be the relaxed code polytope obtained by Definition 15 for the all-one check. Let $v$ be any vector and let $v^N := D(q,h)^{-1}v$. Then, if $u^N$ is the projection of $v^N$ onto $\mathcal{U}^N$, then $u := D(q,h)u^N$ is the projection of $v$ onto $\mathcal{U}$.

**Proof**  See Appendix 10.6.

### 4.4 Remarks

For $F_{2^2}$, we can obtain a characterization of SPC code embeddings simpler than Definition 7. In addition, we note that it is mentioned by [19] (and we paraphrase) that $\mathcal{V} = \mathcal{U}$ for codes in $F_{2^2}$. Yet no proof was given in [19]. We validated this statement numerically and believe that it holds. These results for $F_{2^2}$ are discussed in Appendix 12.

For $F_{2^m}$ where $m \geq 3$, it is obvious that $\mathcal{V} \subset \mathcal{U}$. However, we do not have evidence that $\mathcal{V} = \mathcal{U}$. Understanding how loose $\mathcal{U}$ is compared to $\mathcal{V}$ is important future work.

### 5 LP decoding and ADMM formulations

LP decoding of non-binary codes was first introduced by Flanagan et al. in [15]. The LP decoding problem in [15] is based on the embedding method presented in Definition 3. Honda and Yamamoto proposed a different LP decoding problem in [19], one that uses the embedding method presented in Definition 4. However, no connection between the two problems have been made. We first review these two LP decoding problems and show that the choice of embedding method does not affect the results of LP decoding. We then develop two ADMM algorithms for LP decoding. The first
algorithm is generic and can be applied to arbitrary fields. However, it requires a sub-routine that can project either onto the tight code polytope or onto the relaxed code polytope. In our previous work [23], we proposed an ADMM projection algorithm to project onto the relaxed code polytope $\mathbb{U}$. The resulting LP decoding algorithm executes ADMM iterations where each iteration contains $M$ ADMM projection sub-routines, where $M$ is the number of checks. In other words there are two levels of ADMM algorithms: $M$ ADMM sub-routines per iteration of ADMM decoding. We observe that this algorithm is not especially efficient because even if the projection sub-routines converge reasonably fast, the multiplicity of sub-routines is large ($M$ per decoding iteration). To solve this problem, we introduce another ADMM formulation for codes in $\mathbb{F}_2^m$ in Section 5.4 where no ADMM sub-routine is required. This formulation leverages the factor graph representation of embeddings described in Section 3.2.

5.1 Review of LP decoding problems

We first review the LP decoding problem proposed by Flanagan et al. in [15]. We consider a linear code specified by a parity-check matrix $H$, where each entry $h_{ji} \in \mathbb{F}_q$. Let $\Sigma$ be the output space of the channel. In [15], a function $L : \Sigma \mapsto (\mathbb{R} \cup \{\pm \infty\})^{q-1}$ is introduced, it is defined as

$$L(y) := \lambda = (\lambda_1, \ldots, \lambda_{q-1}),$$

where for each $y \in \Sigma$, $\delta \in \mathbb{F}_q \setminus \{0\}$, $\lambda_\delta = \log \left( \frac{\Pr[Y=y|X=0]}{\Pr[Y=y|X=\delta]} \right)$. Similar to $F_v(\cdot)$ defined in Section 3.1, we define $L_v(y) = (L(y_1)|L(y_2)|\ldots|L(y_n))^T$. Maximum likelihood decoding can then be rewritten as (cf. [15])

$$\hat{c} = \arg\min_{c \in \mathcal{C}} \sum_{i=1}^{n} \log \left( \frac{\Pr[y_i|0]}{\Pr[y_i|c_i]} \right)$$

$$= \arg\min_{c \in \mathcal{C}} \sum_{i=1}^{n} L(y_i)f(c_i)^T$$

$$= \arg\min_{c \in \mathcal{C}} L_v(y)F_v(c)^T.$$

In [15], ML decoding is relaxed to a linear program. We can write the LP decoding problem (using the “tight” “Flanagan” representation) as

$$\text{LP-FT:} \quad \min \quad L_v(y)^T x$$

subject to $P_j x \in V_j, \forall j \in \mathcal{J},$ (5.1)

where $P_j$ selects the variables that participate in the $j$-th check and $V_j = \text{conv}(F_v(C_j))$. Here $C_j$ is the SPC code defined by the non-zero entries of the $j$-th check.

Using the relaxation of Definition 15, we can formulate a differently and further relaxed LP decoding problem. Note that this problem only applies to codes in $\mathbb{F}_2^m$. We write this (“Flanagan” “relaxed”) problem as

$$\text{LP-FR:} \quad \min \quad L_v(y)^T x$$

subject to $P_j x \in U_j, \forall j \in \mathcal{J},$ (5.2)

where $P_j$ is the same as the previous problem and $U_j$ is defined by Definition 15 for the $j$-th check.
Since the LP decoding problem in [19] uses the constant-weight embedding, both the objective function and the constraint set of this problem are different from those in [15]. Let

\[ L'(y) := \lambda = (\lambda_0, \ldots, \lambda_{q-1}), \]

where for each \( y \in \Sigma, \delta \in \mathbb{F}_q, \lambda_{\delta} = \log \left( \frac{1}{p_{\delta}} \right). \) The LP decoding problem ("constant-weight" "relaxed") in [19] is

\[
\text{LP-CR:} \quad \min \quad L'_v(y)x
\]

subject to \( P'_j x \in \mathbb{U}'_j, \forall j \in J, \quad (5.3) \)

where \( P'_j \) selects the variables that participate in the \( j \)-th check and \( \mathbb{U}'_j \) is the relaxed code polytope when the constant-weight embedding is used (cf. Definition 38). We can formulate a different "tighter" LP decoding problem using \( \mathbb{V}'_j := \text{conv}(P'_j(C_j)) \) as

\[
\text{LP-CT:} \quad \min \quad L'_v(y)^T x
\]

subject to \( P'_j x \in \mathbb{V}'_j, \forall j \in J. \quad (5.4) \)

**Example 23** In this example we demonstrate the selection matrices \( P \) and \( P' \). Consider a code in \( \mathbb{F}_{2^2} \) defined by the following parity-check matrix: \( H = \begin{pmatrix} 1 & 2 & 2 & 3 \\ 2 & 0 & 1 & 2 \end{pmatrix}. \) Then \( P_1 = I_{12 \times 12} \) and

\[
P_2 = \begin{pmatrix} I_{3 \times 3} & 0_{3 \times 3} & 0_{3 \times 3} & 0_{3 \times 3} \\ 0_{3 \times 3} & I_{3 \times 3} & 0_{3 \times 3} & 0_{3 \times 3} \\ 0_{3 \times 3} & 0_{3 \times 3} & I_{3 \times 3} & 0_{3 \times 3} \end{pmatrix}. \]

\( P'_1 = I_{16 \times 16} \) and \( P'_2 = \begin{pmatrix} I_{4 \times 4} & 0_{4 \times 4} & 0_{4 \times 4} & 0_{4 \times 4} \\ 0_{4 \times 4} & I_{4 \times 4} & 0_{4 \times 4} & 0_{4 \times 4} \\ 0_{4 \times 4} & 0_{4 \times 4} & I_{4 \times 4} & 0_{4 \times 4} \end{pmatrix}. \)

### 5.2 Equivalence properties between two embedding methods

In this section, we show several equivalence properties between Flanagan’s embedding and the constant-weight embedding in the context of LP decoding. As a reminder, we consider two LP decoding objectives and two types of LP decoding constraints. Therefore there are four LP decoding problems under consideration. We summarize the notation in Table 3.

|                      | Tight code polytope | Relaxed code polytope |
|----------------------|---------------------|-----------------------|
| Flanagan Emb.        | LP-FT               | LP-FR                 |
| Const.-weight Emb.   | LP-CT               | LP-CR                 |

Table 3: Four LP decoding problems

**5.2.1 Equivalence between LP-FT and LP-CT**

We first focus on LP decoding using the tight code polytope \( \mathbb{V} \).

**Theorem 24** Consider any finite field \( \mathbb{F}_q \) and let \( y \in \Sigma \). Let \( f_i \) be a length-(\( q - 1 \)) vector where \( i \in \mathcal{I} \). If \( \hat{f} = (f_1,f_2,\ldots,f_N)^T \) is the minimizer of LP-FT, then \( \bar{f} = (1 - \|f_1\|_1,f_1,1 - \|f_2\|_1,f_2,\ldots,1 - \|f_N\|_1,f_N)^T \) is the minimizer of LP-CT. Conversely, if \( \hat{f} = (a_1,f_1,a_2,f_2,\ldots,a_N,f_N)^T \) is the minimizer of LP-CT for some \( a_1,\ldots,a_N \), then \( \bar{f} = (f_1,f_2,\ldots,f_N)^T \) is the minimizer of LP-FT.
Proof See Appendix 10.7

This theorem builds the equivalence in the sense that, if \( \text{LP-FT} \) and \( \text{LP-CT} \) are given the same channel output vector, their decoding results have a bijective mapping between them. It is then easy to show the following corollary.

Corollary 25 Both \( \text{LP-FT} \) and \( \text{LP-CT} \) achieve the same symbol-error-rate and word-error-rate.

Proof By Theorem 24, \( \text{LP-FT} \) decodes a channel output \( y \) to a fractional solution if and only if \( \text{LP-CT} \) decodes \( y \) to a fractional solution. Thus the two decoders either both succeed or both fail.

Corollary 26 Under the channel symmetry condition in the sense of [15], the probability that \( \text{LP-CT} \) fails is independent of the codeword that was transmitted.

Proof This is due to Theorem 5.1 in [15] and Corollary 25.

5.2.2 Equivalence between LP-FR and LP-CR

We now consider the LP decoding problem for codes in fields of characteristic two when the relaxed code polytope is used in LP decoding.

Theorem 27 Consider finite fields \( \mathbb{F}_{2^m} \) and let \( y \in \Sigma \). Let \( \hat{f} \) be a length-(\( 2^m - 1 \)) vector where \( i \in I \). If \( \tilde{f} = (f_1, f_2 \ldots, f_N)^T \) is the minimizer of \( \text{LP-FR} \), then \( \tilde{f} = (1 - \|f_1\|_1, f_1, 1 - \|f_2\|_1, f_2, \ldots, 1 - \|f_N\|_1, f_N)^T \) is the minimizer of \( \text{LP-CR} \). Conversely, if \( \tilde{f} = (a_1, f_1, a_2, f_2, \ldots, a_N, f_N)^T \) is the minimizer of \( \text{LP-CR} \) for some \( a_1, \ldots, a_N \), then \( \hat{f} = (f_1, f_2, \ldots, f_N)^T \) is the minimizer of \( \text{LP-FR} \).

Proof See Appendix 10.8.

Corollary 28 Both \( \text{LP-FR} \) and \( \text{LP-CR} \) achieve the same symbol-error-rate and word-error-rate.

Corollary 29 Under the channel symmetry condition in the sense of [15], the probability that \( \text{LP-FR} \) fails is independent of the codeword that was transmitted.

Proof This is due to Theorem 2 in [19] and Theorem 27.

We also provide our direct proof of this corollary in Appendix 10.9. Our proof leverages the proof technique taken in [15]. In addition, we show a symmetry property of \( U \).

5.2.3 Remarks

- Our results imply that one has the freedom to choose either Flanagan’s embedding or the constant-weight embedding for LP decoding. Both embedding methods yield the same error rates.

- We note that the relaxed versions of LP decoding (\( \text{LP-FR} \) and \( \text{LP-CR} \)) are in general worse than the tighter versions (\( \text{LP-FT} \) and \( \text{LP-CT} \)) in terms of error rates.

- LP decoding using Flanagan’s embedding requires fewer variables. In addition, we show in Section 7 that the ADMM LP decoder in Section 5.4, which uses Flanagan’s embedding, converges faster than its constant-weight embedding counterpart.
5.3 Generic ADMM formulations of the LP decoding problem

In this section, we derive a generic ADMM formulation for LP decoding of non-binary codes. We formulate the ADMM algorithm under the assumption that a subroutine that projects a vector onto $V$ or $U$ is provided. We will use the formulation in this section to develop a penalized LP decoder in Section 6.

We use the shorthand notation $\gamma := L_v(y)$ and following the methodology of [11] to cast the LP into a form solvable using ADMM. We introduce replicas $z_j$ for all $j \in J$. Each $z_j$ is a length-$(q-1)d_c$ vector where $d_c$ is the check degree. We then express (5.1) in the following, equivalent, form:

$$
\min \gamma^T x \\
\text{subject to} \quad P_j x = z_j, \quad z_j \in V_j, \text{ for all } j \in J, \quad x_i \in S_{q-1}, \text{ for all } i \in I,
$$

where $x_i = (x_{i-1}(q-1)+1, x_{i-1}(q-1)+2, \ldots, x_{i(q-1)})$ is the sub-vector selected from the $i$-th $(q-1)$-length block of $x$. In other words, $x_i$ corresponds to the embedded vector of the $i$-th non-binary symbol. $S_{q-1}$ is the $q-1$ simplex defined in (2.2).

The augmented Lagrangian is

$$
L_\mu(x, z, \lambda) = \gamma^T x + \sum_{j \in J} \lambda_j^T (P_j x - z_j) + \frac{\mu}{2} \sum_{j \in J} \|P_j x - z_j\|^2.
$$

ADMM iteratively performs the following updates:

- $x$-update: $x^{k+1} = \text{argmin}_x L_\mu(x, z^k, \lambda^k)$,
- $z$-update: $z^{k+1} = \text{argmin}_z L_\mu(x^{k+1}, z, \lambda^k)$,
- $\lambda$-update: $\lambda_j^{k+1} = \lambda_j^k + \mu (P_j x^{k+1} - z_j^{k+1})$.

In the $x$-update we solve the following optimization problem:

$$
\min_{\forall i \in I, x_i \in S_{q-1}} L_\mu(x, z, \lambda).
$$

We first introduce some additional notation. Let $\gamma_i$ be the vector of log-likelihood ratios that correspond to the $i$-th symbol of the code. In other words,

$$
\gamma_i = (\gamma_{i-1}(q-1)+1, \gamma_{i-1}(q-1)+2, \ldots, \gamma_{i(q-1)}).
$$

Let $\lambda_j^{(i)}$ be the length-$(q-1)$ sub-vector of $\lambda_j$ that corresponds to the $i$-th symbol of the code. Similarly, we define $z_j^{(i)}$ to be the sub-vector of $z_j$ that correspond to the $i$-th symbol of the code. We then write $L_\mu(x, z, \lambda)$ as

$$
L_\mu(x, z, \lambda) = \sum_{i=1}^N \left( \gamma_i^T x_i + \sum_{j \in \mathcal{N}_i(i)} \lambda_j^{(i)T} (x_i - z_j^{(i)}) + \frac{\mu}{2} \sum_{j \in \mathcal{N}_i(i)} \|x_i - z_j^{(i)}\|^2 \right).
$$
Note that when $z_j$ and $\lambda_j$ are fixed for all $j \in \mathcal{J}$, we can decouple $x_i$ for all $i \in \mathcal{I}$ in the sense that they can be individually solved for. Therefore

$$x_i^{k+1} = \arg\min_{x_i \in \mathbb{S}_{q-1}} \gamma_i^T x_i + \sum_{j \in \mathcal{N}_v(i)} \lambda_j^{(i)} T (x_i - z_j^{(i)}) + \frac{\mu}{2} \sum_{j \in \mathcal{N}_v(i)} \|x_i - z_j^{(i)}\|_2^2$$

$$= \arg\min_{x_i \in \mathbb{S}_{q-1}} \|x_i - v_i\|_2^2,$$

where

$$v_i = \frac{1}{|\mathcal{N}_v(i)|} \left[ \sum_{j \in \mathcal{N}_v(i)} \left( z_j^{(i)} - \frac{\lambda_j^{(i)}}{\mu} \right) - \gamma_i \right].$$

Thus, the $x$-update is equivalent to a Euclidean projection onto $\mathbb{S}_{q-1}$, which is solvable in linear time (i.e., $O(q)$) using techniques proposed in [24]. Note that the $v_i$ in (5.11) is an average of the corresponding replicas (i.e., $z_j^{(i)}$) plus adjustments from the Lagrange multipliers (i.e., $\lambda_j^{(i)}$) and the log-likelihood ratios (i.e., $\gamma_i$).

In the $z$-update we can solve for each $z_j$ separately. When $x_i$ and $\lambda_j$ are fixed for all $i \in \mathcal{I}$ and $j \in \mathcal{J}$ we complete the square with respect to each $z_j$ and obtain the following update rule:

$$z_j^{k+1} = \arg\min_{z_j \in \mathbb{V}_j} \|u_j - z_j\|_2^2,$$

where $u_j = P_j x + \lambda_j / \mu$. Thus the $z$-update is equivalent to the Euclidean projection onto $\mathbb{V}_j$.

For LP-FR, the respective $z$-update is equivalent to projection onto $\mathbb{U}_j$. That is,

$$z_j^{k+1} = \arg\min_{z_j \in \mathbb{U}_j} \|u_j - z_j\|_2^2,$$

In our previous work [23], we proposed an ADMM algorithm for to solve (5.13). We show in Section 5.4 that the complexity of the decoding algorithm in [23] can be significantly reduced.

### 5.4 Improved ADMM LP decoding for LP-FR

In this section, we focus on LP-FR and propose an ADMM decoding algorithm that does not require a sub-routine that projects onto $\mathbb{U}$. We note that it is easy to extend the techniques in this section to problem LP-CR.

For an LDPC code, all entries of the embedded vector of a non-binary variable $i \in \mathcal{I}$ participate in $|\mathcal{N}_v(i)|$ non-binary checks. When embedding is used, each non-binary check can be decomposed by at-most-one-on-checks and parity-checks. We may think of these constraints having two hierarchies. The first hierarchy consists of constraints defined by the parity-check matrix of a non-binary code. The second hierarchy consists of constraints defined by the SPC code embeddings (i.e., Definition 15). We illustrated this in Example 30.

**Example 30** We build the factor graph of embeddings for an LDPC code in $\mathbb{F}_2$. Assume that the $j$-th check is connected to three variable nodes $i_1$, $i_2$ and $i_3$. Then, the embedded variables (9 binary variables) are connected to the $j$-th non-binary check. This connection is captured by the matrix $P_j$ in (5.1). We can think of this as the first hierarchy. There is another hierarchy of factor graph inside the $j$-th constraint set, namely, those constraints defined by Lemma 12. The variables are first permuted by the rotation steps defined in Section 4.3 where the permutations are captured by the matrix $D$. Then, they are connected to a normalized constraint set defined by three
at-most-one-on-checks and three parity-checks. The three at-most-one-on-checks are connected to the vector triplets $\mathbf{x}_{i_1}$, $\mathbf{x}_{i_2}$ and $\mathbf{x}_{i_3}$ respectively. The three parity-check are specified by $\tilde{B}(\mathcal{K}, 1)$ for $\mathcal{K} = \{1\}, \{2\}$ and $\{1, 2\}$; and are connected to the permuted entries of $\mathbf{x}_{i_1}$, $\mathbf{x}_{i_2}$ and $\mathbf{x}_{i_3}$. These connections are shown in Figure 2.

We note that we obtain Figure 2 by going through two hierarchies of constraints. However, the resulting graph is nothing but a factor graph of embeddings with two types of factor nodes: at-most-one-on-check nodes and parity-check nodes. The connections between variable nodes and factor nodes are specified by both the parity-check matrix of the code and the constraints specified for SPC codes (i.e., those in Lemma 12).

We now formally state the ADMM formulation based on the factor graph of embeddings. We will assume that the code has regular variable degree $d_v$ for conciseness. However, our derivation can easily extend to irregular codes. By Lemma 21, we can rewrite the LP decoding problem using the normalized polytope $\mathcal{U}^N$ and rotation matrix $\mathcal{D}$. From now on, we use $\mathcal{D}_j$ to denote the rotation matrix $\mathcal{D}(q, h_j)$, where $h_j$ is the non-zero sub-vector of the $j$-th check of the code. Then, LP-FR can be rewritten as

$$
\min \gamma^T \mathbf{x} \quad \text{subject to} \quad \mathcal{D}_j \mathcal{P}_j \mathbf{x} \in \mathcal{U}^N, \quad \forall j \in \mathcal{J},
$$

(5.14)

Note that $\mathcal{U}^N$ is defined as the intersection of simplexes (Condition (a) and (b) in Definition 15) and parity polytopes (Condition (c) in Definition 15). For Condition (c) we define a select-and-add matrix $\mathcal{T}_k$ using the following procedure. We first let $\mathcal{K}_k$, $k \in [2^m - 1]$ be all non-empty subsets of $[m]$. We then use $\mathcal{T}_k$ to denote the matrix that selects the entries in $\tilde{B}(\mathcal{K}_k, 1)$ and adds them to

![Figure 2: Factor graph of embeddings.](image-url)
form the vector $g^{k_k}$. As a result, we can rewrite (5.14) as

$$\begin{align*}
\min & \quad \gamma^T x \\
\text{subject to} & \quad T_k D_j P_j x \in P_{d_c}, \text{ for all } j \in J \text{ and } k \in [2^m - 1] \\
& \quad x_i \in S_{q-1} \text{ for all } i \in I.
\end{align*}$$

We introduce replicas $z_{j,k}$ and $s_i$. We let $z_{j,k} = T_k D_j P_j x$ and $s_i = x_i$. However, note that we draw a distinction between $z$ and $s$ only for notation purposes. Both $z$ and $s$ serve the same purpose (i.e., replicas) in the ADMM algorithm.

$$\begin{align*}
\min & \quad \gamma^T x \\
\text{subject to} & \quad \text{for all } j \in J, k \in [2^m - 1] \text{ and } i \in I,
\end{align*}$$

By defining $Z_{j,k} := T_k D_j P_j$, we can write the augmented Lagrangian as

$$\begin{align*}
\mathcal{L}_\mu(x, z, s, \lambda) &= \gamma^T x + \sum_{j,k} \lambda_{j,k}^T (Z_{j,k} x - z_{j,k}) + \frac{\mu}{2} \sum_{j,k} \| Z_{j,k} x - z_{j,k} \|_2^2 \\
& \quad + \sum_i \eta_i^T (S_i x - s_i) + \frac{\mu}{2} \sum_i \| S_i x - s_i \|_2^2,
\end{align*}$$

where $S_i$ selects the sub-vector $x_i$ from $x$. The ADMM updates for this problem are

- $x$-update: $x^* = \arg\min_x \mathcal{L}_\mu(x, z, s, \lambda)$,
- $z$-update: $z^* = \arg\min_{z \in \mathbb{P}_{d_c}} \mathcal{L}_\mu(x^*, z, s, \lambda)$,
- $s$-update: $s^* = \arg\min_{s \in S_{q-1}} \mathcal{L}_\mu(x^*, z^*, s, \lambda)$,
- $\lambda$-update: $\lambda_j^* = \lambda_j^* + \mu (Z_{j,k} x^* - z_{j,k}^*) + \mu (S_i x^* - s_i^*)$.

We make the following remarks. First, the $x$-update is an unconstrained optimization problem, which is different from the case in Section 5.3. Second, we separate the $z$-update and the $s$-update because the two types of replicas are in two different polytopes ($\mathbb{P}_{d_c}$ and $S_{q-1}$ respectively).

### 5.4.1 $x$-update

By taking the gradient of $\mathcal{L}_\mu(x, z, s, \lambda)$ and setting it to the all-zeros vector we obtain the following $x$-update

$$x^* = (Z + I)^{-1} \left( \sum_{j,k} Z_{j,k}^T (z_{j,k} - \frac{\lambda_{j,k}}{\mu}) + \sum_i S_i^T (s_i - \frac{\eta_i}{\mu}) - \frac{\gamma}{\mu} \right),$$

where $Z = \sum_{j,k} Z_{j,k}^T Z_{j,k}$ and the identity matrix $I$ comes from the fact that $\sum_i S_i^T S_i = I$. Note that $T_k^T$ has at most one 1 in each row due to constraints defined in Definition 15. Since the permutation matrix $D_j$ and the selection matrix $P_j$ all have at most one 1 in each column, $Z_{j,k}^T$
simply selects the entries in \( z_{j,k} \) that correspond to some entries in \( x \). We use the notation \( z_{j,k}^{(i)} \) to represent the length-\((q-1)\) sub-vector of \( z_{j,k} \) that corresponds to \( x_i \). Therefore, we can rewrite \( x \)-update as

\[
x^* = (Z + I)^{-1} t,
\]

where \( t = (t_1, \ldots, t_N) \) and

\[
t_i = \sum_{(j,k) \in \mathcal{N}_v(i)} \left( z_{j,k}^{(i)} - \frac{\lambda_{j,k}^{(i)}}{\mu} \right) + \frac{s_i}{\mu} - \frac{\gamma_i}{\mu}.
\]

(5.18)

Note that the matrix \((Z + I)^{-1}\) is fixed by the code. Therefore one needs to calculate it only once per code. However, a naive calculation of \((Z + I)^{-1} t\) has complexity \(O(N^2(2^m - 1)^2)\) which can be prohibitive for large \( N \). We therefore introduce a method to calculate this product in linear complexity \(O(N(2^m - 1))\).

**Lemma 31** \((Z + I)^{-1} t\) can be calculated in linear complexity \(O(N(2^m - 1))\).

**Proof** See Appendix 11.

### 5.4.2 Other ADMM updates

The \( z \)- and \( s \)-updates are the same as (5.12) except that we now perform the respective projections onto \( \mathbb{P}_{d_c} \) and \( S_2^{m-1} \).

\[
s_i^* = \arg \min_{s_i \in S_2^{m-1}} \| u_i - s_i \|_2 \text{ for } i \in \mathcal{I}
\]

\[
z_{j,k}^* = \arg \min_{z_{j,k} \in \mathbb{P}_{d_c}} \| v_{j,k} - z_{j,k} \|_2 \text{ for } j \in \mathcal{J} \text{ and } k \in [2^m - 1],
\]

where \( u_i = S_i x + \eta_i / \mu \) and \( v_{j,k} = Z_{j,k} x + \lambda_{j,k} / \mu \). Both projection operations have complexity that is linear in \( q(= 2^m) \) and \( d_c \) respectively (cf. [13] and [24]).

**Proposition 32** Let \( \hat{x} \) be the solution of the LP decoding problem (5.2). For any \( \eta > 0 \), Algorithm 1 will, in \(O(Nq^2)\) time, determine a vector \( \hat{x} \) that satisfies the constraints in (5.2) and that also satisfies the following bound:

\[
L_v(y)^T \hat{x} - L_v(y)^T \tilde{x} < qNd_c\eta,
\]

where \( y \) is the channel output, \( q = 2^m \) is the field size and \( d_c \) is the check degree.

**Proof** This proposition is similar to Proposition 1 in [11]. We omit the details but point out that ADMM takes \(O(1)\) iterations to converge to a point with \(qNM\eta\) gap to optimal. For each iteration, the complexity of the \( x \)-update is \(O(Nq^2)\). The complexity of each \((z, s)\)-update is \(O(Nq + M(q-1)d_c)\). Since \( d_c \) does not scale, we deduce that the complexity of each iteration is \(O(Nq^2)\). Therefore due to the \(O(1)\) requirement on the number of iterations, the overall complexity is \(O(Nq^2)\).

We summarize ADMM LP decoding in Algorithm 1.

---

7Recall that \( Z_{j,k} \) maps \( x \) to the replica \( z_{j,k} \). In addition, there is at most one 1 per column in \( Z_{j,k} \). This implies that \( Z_{j,k}^T \) has at most one 1 per row. Thus, \( Z_{j,k}^T \) simply selects entries from \( z_{j,k} \).
Algorithm 1 ADMM LP decoding of LDPC codes in $\mathbb{F}_{2^m}$

1: Construct the $d_c \times (2^m - 1)N$ matrices $Z_{j,k}$ for all $j \in J$ and $k \in [2^m - 1]$ based on the factor graph of embeddings.
2: Construct the $(2^m - 1) \times (2^m - 1)N$ matrices $S_i$ for all $i \in I$ based on the at-most-one-on constraints.
3: Initialize all entries of $\lambda_{j,k}$ and $\eta_i$ to 0. Initialize all entries of $z_{j,k}$ and $s_i$ to $\frac{1}{2^m}$. Initialize iterate $\delta = 0$. To simplify the notation, we drop iterate $\delta$ except when determining the stopping criteria.
4: repeat
5:   for all $i = 1, \ldots, N$ do
6:       Calculate vector $t_i$ using equation (5.18). Store $\|t_i\|_1$.
7:       Calculate the variables $a$ and $b$ per Lemma 59.
8:       Update $x_i \leftarrow (a - b)t_i + b\|t_i\|_1$.
9:   end for
10:  for all $j = 1, \ldots, M$ and $k = 1, \ldots, 2^m - 1$ do
11:     Set $v_{j,k} = Z_{j,k}x + \lambda_{j,k}/\mu$.
12:     Update $z_{j,k} \leftarrow \Pi_{\mathbb{F}_{2^m}d_c}(v_{j,k})$ where $\Pi_{\mathbb{F}_{2^m}d_c}(\cdot)$ means project onto the parity polytope of dimension $d_c$.
13:     Update $\lambda_{j,k} \leftarrow \lambda_{j,k} + \mu (Z_{j,k}x - z_{j,k})$.
14: end for
15:  for all $i = 1, \ldots, N$ do
16:     Set $u_i = S_ix + \eta_i/\mu$.
17:     Update $s_i \leftarrow \Pi_{S_q-1}(u_i)$ where $\Pi_{S_q-1}(\cdot)$ means project onto $S_{q-1}$.
18:     Update $\eta_i \leftarrow \eta_i + \mu (S_i x - s_i)$.
19: end for
20: $\delta \leftarrow \delta + 1$.
21: until $\sum_i \|S_i x^{\delta} - s_i^{\delta}\|_2^2 + \sum_{j,k} \|Z_{j,k} x^{\delta} - z_{j,k}^{\delta}\|_2^2 < \epsilon^2((2^m - 1)N + M(2^m - 1)d_c)$
    and $\sum_i \|s_i^{\delta - 1} - s_i^{\delta - 1}\|_2^2 + \sum_{j,k} \|z_{j,k}^{\delta - 1} - z_{j,k}^{\delta - 1}\|_2^2 < \epsilon^2((2^m - 1)N + M(2^m - 1)d_c)$

return $x$. 

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5.4.3 Early termination and over-relaxation

In practice, ADMM can be terminated whenever a codeword is found. Doing so often reduces the number of iterations needed to decode. We observe empirically that early termination does affect the WER for some very short block length codes. However, it does not create observable effects for the codes we study in Section 7. We use the vectors \( s_i \) for all \( i \in I \) to determine whether or not the corresponding non-binary vector is a codeword. This decision process is as follows. For each \( i \in I \), we first obtain the vector \( \hat{x} := (\hat{x}_0, \hat{x}_1, \ldots, \hat{x}_{2^m-1}) \) by letting \( \hat{x}_0 = 1 - \|s_i\| \) and \( \hat{x}_k = s_{i,k} \) for all \( k \in [2^m - 1] \). Then, the \( i \)-th non-binary symbol is determined by \( \hat{c}_i = \arg\max_k \hat{x}_k \). After we have obtained the length-\( N \) non-binary vector \( \hat{c} \), we terminate ADMM if \( H\hat{c} = 0 \) in \( \mathbb{F}_{2^m} \) where \( H \) is the parity-check matrix of the code.

Another useful technique in practice is over-relaxation, which is already used in [11]. Readers are referred to [25] and references therein for details on over-relaxation. We observe that over-relaxation is effective for Algorithm 1. Experiments surrounding the choices of over-relaxation parameter \( \rho \) are presented in Section 7.

6 ADMM penalized decoding of non-binary codes

6.1 ADMM penalized decoding algorithm

In this section, we introduce an ADMM penalized decoder for non-binary codes that is analogous to the penalized decoder for binary codes introduced in [14]. It is shown in [14] that adding a non-convex penalty term to the LP decoding objective can improve the low SNR performance of LP decoding. The penalty term penalizes fractional solutions because of the fact that correct solutions should be integral. We extend this idea to embeddings of non-binary symbols.

We note an important aspect of non-binary penalized decoder: The probability that the decoder fails should be independent of the codeword that is transmitted. We use the constant-weight embedding for this purpose because all non-binary symbols are embedded symmetrically with respect to each other. As a reminder, for Flanagan’s embedding, symbol 0 is treated differently from others symbol in the field. We discuss this issue in details in Section 6.2.

Formally, we consider the following decoding problem:

\[
\min \gamma^T x - \alpha \sum_i \|x_i - r\|_2^2 \\
\text{subject to } P_j' x \in U_j', \forall j \in J,
\]

where \( U_j' \) is the relaxed polytope under the constant-weight embedding (Definition 38), \( \gamma' := \mathbb{L}_v'(y) \) and

\[
r = \left( \frac{1}{q'}, \frac{1}{q'}, \ldots, \frac{1}{q'} \right) .
\]

We pick the \( \ell_2 \) norm as a penalty in (6.1) because it yields simple ADMM updates and because it has good empirical performance (cf. Section 7). Note that the \( \ell_1 \) norm and other penalties are also considered in [23]. For both the \( x \)-update and the \( z \)-update, we complete squares and then perform minimizations. As a result, the \( x \)-update can be expressed as a projection onto \( S_q' \); and
the \( z \)-update can be expressed as a projection onto \( U_j' \). In particular, the \( x \)-update rule is

\[
x_i = \Pi_{S_q} \left[ \frac{1}{d_i - 2\alpha} \left( \sum_{j \in \mathcal{N}_v(i)} \left( z_j(i) - \frac{\lambda_j(j)}{\mu} \right) - \frac{\gamma_j'}{\mu} - 2\alpha r \right) \right].
\]

The \( z \)-update can be written as a projection onto \( U'_j \). For this projection operation, we use the rotate-and-project technique introduced in [23]. The ADMM penalized decoder is summarized in Algorithm 2.

**Algorithm 2** ADMM penalized decoding. **Input:** Received vector \( y \in \Sigma^N \). **Output:** Decoded vector \( x \).

1: Construct the \( qd_j \times qN \) selection matrix \( P_j' \) for all \( j \in \mathcal{J} \) based on the parity-check matrix \( H \).
2: Construct the log-likelihood ratio \( L_v'(y) \).
3: For all \( j \in \mathcal{J} \), initialize all entries of \( \lambda_j \) to 0 and initialize all entries of \( z_j \) to 0.
4: repeat
   5: for all \( i \in \mathcal{I} \) do
      6: Update \( x_i \) by
      7: end for
   8: for all \( j \in \mathcal{J} \) do
      9: Set \( v_j \leftarrow P_j x + \lambda_j/\mu \).
   10: Update \( z_j \leftarrow \Pi_{U_j'}(v_j) \) where \( \Pi_{U_j'}(\cdot) \) means project onto the relaxed code polytope defined by the \( j \)-th check.
   11: Update \( \lambda_j \leftarrow \lambda_j + \mu \left( P_j x - z_j \right) \).
   12: end for
   13: \( \delta \leftarrow \delta + 1 \).
   14: until \( \sum_j \| P_j x^\delta - z_j^\delta \|_2^2 < \epsilon^2 qM_{d_j} \)
   and \( \sum_j \| z_j^\delta - z_j^{\delta-1} \|_2^2 < \epsilon^2 qM_{d_j} \)

return \( x \).

**Theorem 33** Under the channel symmetry condition in the sense of [15], the probability that Algorithm 2 fails is independent of the codeword that was transmitted.

**Proof** See Appendix 10.10

**6.2 Discussions**

The penalized decoder derived in this section requires a sub-routine that projects onto the code polytope. This means that the computational complexity of the penalized decoder is much higher than the ADMM LP decoder in Algorithm 1. We observe empirically that in our implementation of ADMM penalized decoding is around 20 times slower than the ADMM LP decoder in Algorithm 1\(^8\).

\(^8\)Measured in execution time (sec).
We note that, indeed, Algorithm 1 can be used to try to solve the penalized objective (6.1). However, we observe empirically that the resulting decoder does not have the symmetry property in the sense of Theorem 33. We briefly discuss why the proof technique of Theorem 33 cannot be applied to this decoder. First, note that the penalized decoder is trying to solve a non-convex program. As a result, the output of the penalized decoder cannot be determined solely by the optimization problem. It depends on the ADMM update rules (the \( x^- \), \( z^- \) and \( \lambda^- \)-update), which are determined by the way the optimization problem is stated\(^9\). In the proof of Theorem 33, we show that the decoding process when decoding the all-zeros codeword is “symmetric” with respect to any other codeword’s decoding process in the sense of Lemma 54 (see rigorous discussions in Appendix 10.10). This symmetry behavior is due to the symmetric structure of \( U' \) (Lemma 52). If we were to use Algorithm 1 to try to solve (6.1), the polytopes used to describe the constraint set are parity polytopes and simplexes, not \( U' \). As a result, we cannot have the symmetry behavior for parity polytopes and simplexes in the sense of Lemma 52.

On the other hand, we note that applying Algorithm 1 to (6.1) results in a significant improvement in terms of decoding efficiency when compared to Algorithm 2. Furthermore, we observe empirically that this technique can achieve a significantly lower error rate when compared to LP decoding (LP-FR and LP-CR) for the all-zeros codeword. The open questions are whether all codewords receive some gain and how much gain each codeword receives. These questions are non-trivial because of the non-convexity of the penalized objective.

### 7 Numerical results and discussions

In this section we present numerical results for ADMM LP decoding (Algorithm 1) and ADMM penalized LP decoding (Algorithm 2). First, we show the error rate performance of the two decoders as a function of SNR. In addition, we compare our decoders with to the low-complexity LP (LCLP) decoding technique proposed by Punekar \textit{et al.} in [18]. Next, we focus on choosing a good set of parameters for Algorithm 1 in Section 7.2. Finally, we show how the penalty coefficient \( \alpha \) (cf. (6.1)) affects the WER performance of ADMM penalized decoding.

#### 7.1 Performance of the proposed decoders

In this subsection we simulate three codes and demonstrate their error rate performance as a function of SNR. The first code we simulate is derived from a binary length-2048 progressive edge growth (PEG) code that can be obtained from [26]. The other two codes are derived from two binary Tanner codes obtained from [27]. We select these codes for the following reasons. First, we select codes that are of different block lengths (2048, 1055 and 755) and in different fields (\( \mathbb{F}_2 \) and \( \mathbb{F}_{2^3} \)). Second, the two Tanner codes have been studied in [18]. Therefore we can make direct comparisons with the results therein. Last but not least, the parity-check matrices of these codes are easy to obtain (e.g., from [26]). Our intent is to make our experiments using these codes easy to repeat and thus to compare to.

Figure 3 plots the word-error-rate (WER) and symbol-error-rate (SER) of a length-2048 LDPC code when decoded using ADMM LP decoding and ADMM penalized decoding. This code is derived from the PEG [2048, 1018] binary LDPC code obtained from [26]. We use the same parity-check matrix as the original PEG code except that we let each non-zero check value be \( 1(= \xi^0) \in \mathbb{F}_{2^2} \).

\(^9\)That is, (6.1) can be rewritten into other equivalent forms. Each form may result in a different ADMM algorithm.
The code symbols are modulated using quaternary phase-shift keying (QPSK) and transmitted over an AWGN channel. Denote by \((x, y)\) the in-phase and quadrature components. We modulate the symbols in the following way: \(0 \mapsto (1, 0), \xi^0 \mapsto (0, 1), \xi^1 \mapsto (-1, 0)\) and \(\xi^2 \mapsto (0, -1)\). We use energy per information symbol \((E_s/N_0)\) as our unit of SNR. We show explicitly how we calculate \(E_s/N_0\) in Appendix 13. In this experiment, we decode using ADMM LP decoding (Algorithm 1) and ADMM penalized decoding (Algorithm 2). We note that both algorithms require many parameters. We show how to choose parameters in Sections 7.2 and 7.3. For ADMM LP decoding we use the following parameter settings: the “step size” of ADMM \(\mu = 2\) (cf. (5.17)); the maximum number of iterations \(T_{\text{max}} = 200\); the ending tolerance \(\epsilon = 10^{-5}\) (cf. Algorithm 1), and the over-relaxation parameter \(\rho = 1.9\) (cf. Section 5.4.3). For ADMM penalized decoding, we let \(\mu = 4\), \(T_{\text{max}} = 200\), \(\rho = 1.5\), \(\epsilon = 10^{-5}\), and \(\alpha = 0.8\) (cf. (6.1)). For each data point, we collect more than 100 word errors.

We make the following observations. First, both decoding algorithms display a “waterfall” behavior in terms of error rates. However, penalized decoding initiates the waterfall at a much lower SNR (about 0.7dB lower in this example). This shows that ADMM penalized decoding significantly improves LP decoding at low SNRs. Second, ADMM penalized decoding displays an error-floor at WER \(10^{-4}\) (SER \(10^{-6}\)). However, LP decoding does not display an error-floor for WER above \(10^{-5}\) (SER above \(10^{-8}\)). Both two observations are consistent with binary LP decoding and binary penalized decoding [14]. We note that unlike the case with binary decoders where the binary ADMM penalized decoder outperforms the binary ADMM LP decoding in terms of both the number of iterations and execution time, the average decoding time of Algorithm 2 using our implementation is much longer than that of Algorithm 1 due to the reasons discussed in Section 6. Although Algorithm 2 is competitive in terms of WER performance, it is not competitive in terms of execution time.

![Figure 3: ADMM LP decoding and ADMM penalized decoding error rates plotted as a function of SNR for the \([2048, 1018]\) PEG code in \(\mathbb{F}_2^2\).](image-url)

The second code we simulate is the length-1055 code used in [18]. This code is derived from the Tanner \([1055, 424]\) binary LDPC code introduced by Tanner in [27]. Similar to the previous case,
each binary non-zero check is replaced by the value $1 \in \mathbb{F}_{2^2}$. We keep all other simulation settings the same as the previous case, i.e., we use the same AWGN channel with QPSK modulation and the same parameter settings for Algorithm 1. For the penalized decoder, we change the parameter settings. We use the following settings: $\mu = 4$, $\rho = 1.5$, $T_{\text{max}} = 100$, $\epsilon = 10^{-5}$, and $\alpha = 0.6$. We first plot the WER performance of the code in Figure 4. The performance of LCLP and sum-product algorithm (SPA) decoders is plotted for comparison. The data on these decoders is obtained from [18].

We make the following observations. First, we observe that ADMM LP decoding outperforms both SPA and LCLP in terms of WER. ADMM LP decoding has a 0.6dB SNR gain when compared to the LCLP algorithm of [18]. This result is interesting because ADMM LP decoding uses the relaxed code polytope $\mathbb{U}$. Thus one might expect, in general, that it would perform worse than Flanagan’s LP decoding algorithm. However, we recall that we validated numerically that $\mathbb{U} = \mathbb{V}$ for $\mathbb{F}_{2^2}$ (cf. Appendix 12). Figure 4 suggests that LCLP may have a 0.6dB SNR loss due to the approximations made in the algorithm (see [18] for details). Second, the penalized decoder improves the WER performance by 0.4dB. This effect is consistent with Figure 3. Third, we note that none of the decoders used in Figure 4 displays an error-floor.

![Figure 4: WER plotted as a function of SNR for the Tanner [1055, 424] code in $\mathbb{F}_{2^2}$.](image)

In Figure 5, we plot the average number of iterations for erroneous decoding events, all decoding events, and correct decoding events. We observe that of all SNRs the average number of iterations for decoding that is correct is always less than 100. This indicates that we can lower $T_{\text{max}}$ without greatly impacting WER. In fact, we observe that there is less than a 0.05dB loss if we use $T_{\text{max}} = 100$ (data not shown).

In Figure 6, we plot execution time statistics of the decoding events. In this experiment, the decoder is implemented using C++ and data is collected on a 3.10GHz Intel(R) Core(TM) i5-2400 CPU. We make the following observations: First, the average time for decoding that is correct decreases as the SNR increases. However, the average time for decoding that is erroneous does not change much with SNR. Second, the average time per decoding decreases rapidly at low SNRs. This is due to the waterfall behavior of the error rate. Third, we note that ADMM LP decoding
(Algorithm 1) is a much faster algorithm in terms of execution time than ADMM penalized decoding (Algorithm 2). As an example, the average decoding time for ADMM LP decoding at $E_s/N_0 = 5\text{dB}$ is 0.033s. In contrast, the average decoding time for ADMM penalized decoding at $E_s/N_0 = 5\text{dB}$ is 0.61s (data not shown).

![Figure 5: Number of iterations plotted as a function of SNR for the Tanner [1055, 424] code in $\mathbb{F}_{2^2}$.

![Figure 6: Software implementation execution time plotted as a function of SNR for the Tanner [1055, 424] code in $\mathbb{F}_{2^2}$.

In Figure 7, we show results for the length-755 code also studied in [18]. This code has symbols in $\mathbb{F}_{2^3}$ and is derived from the Tanner [755, 334] binary LDPC code [27]. We use the same non-binary parity-check matrix and the same modulation method as in [18]. We make the following
remarks. First, ADMM LP decoding performs worse than LCLP in our simulations. This is due to the fact that ADMM LP decoding uses the relaxed code polytope. Unlike in the $\mathbb{F}_2$ case, relaxing the polytope using Definition 15 induces a loss in terms of SNR performance. We observe that ADMM LP decoding is about 0.3dB worse than LCLP. Second, and similar to the simulations for the previous two codes, the penalized decoder improves the low-SNR performance of LP decoding by around 1.5dB for $E_s/N_0 \leq 9$dB. However, the WER curve displays a strong error-floor. We observe that the optimal value for the penalty coefficient $\alpha$ decreases as SNR increases (data not shown). At $E_s/N_0 = 10$dB, adding a positive penalty does not lead to an observable improvement in WER. Third, in comparison to Figure 4 in which no error-floor is observed for any decoder, we believe that the $[755, 344]$ Tanner code is a problematic code. Thus, we think designing a good code for ADMM decoding is important future work.

![Figure 7: WER plotted as a function of SNR for the Tanner $[755, 344]$ code in $\mathbb{F}_2^3$.](image)

We make two concluding remarks. First, Algorithm 1 solves the relaxed LP decoding problem efficiently. It is competitive with earlier decoders in terms of WER performance, especially for $\mathbb{F}_2$. Second, the penalized decoder is a very promising decoder because it improves the low-SNR performance of LP decoding consistently. However, it suffers from high-complexity and may suffer from an early error-floor. It is important in future work to improve the complexity of the penalized decoder.

### 7.2 Parameter choices for ADMM LP decoding

In this section we study the effects of the choice of parameters for Algorithm 1. We conduct experiments based on the $[1055, 424]$ Tanner code described in the previous subsection, and use the early termination technique described in Section 5.4.3. For each data point, we collect more than 100 word errors.

The ADMM algorithm has many parameters: the “step size” of ADMM $\mu$ (cf. (5.17)), the maximum number of iterations $T_{\text{max}}$, the ending tolerance $\epsilon$ (cf. Algorithm 1), and the over-relaxation parameter $\rho$ (cf. Section 5.4.3). We fix the number of iterations to be $T_{\text{max}} = 200$ and
ending tolerance to be $\epsilon = 10^{-5}$ because the decoder is less sensitive to the settings of these two parameters than it is to $\mu$ or $\rho$.

In Figures 8 and 9 we study the effects of the choice of parameter $\mu$ on the decoder. In Figure 8 we plot the WER as a function of $\mu$ when $\rho = 1$, 1.5 and 1.9. We run experiments for $E_s/N_0 = 5$dB and $E_s/N_0 = 5.5$dB. In Figure 13 we plot the corresponding average number of iterations. We make the following observations. First, the WER does not change much as a function of $\mu$ as long as $\mu \in [1, 3]$. In addition, the lowest number of iterations is attained when $\mu \in [1, 3]$. This means that a choice of $\mu$ within the range $[1, 3]$ is good for practical reasons. Second, the trends of curves for different values of $\rho$ are similar. In other words, the choice of $\rho$ does not strongly affect the choice of $\mu$, at least empirically. This is useful because it simplifies the task of choosing good parameters. Third, we observe that the WER curves for both SNRs flatten when $\mu \in [1, 3]$.

This indicates the parameter choice for this data point allows ADMM to converge fast enough to achieve the LP decoding solutions within the set number of iterations (in most cases). In particular, if we increase $T_{\text{max}}$, the range of the flat region increases. We observe that for $E_s/N_0 = 5$dB, $T_{\text{max}} = 1000$, and $\rho = 1$, the WERs are between 0.122 and 0.128 for $\mu \in [0.3, 3.1]$. This also shows that in this case increasing $T_{\text{max}}$ does not greatly improve WER performance. Fourth, we observe that the WER performance of the decoder can be improved by assigning a large value to $\rho$. This effect is demonstrated further in Figures 10 and 11. Finally, we observe that the average numbers of iterations are small for both SNRs. Note that the SNRs in Figures 8 and 9 are low SNRs and the corresponding WERs are high. Recall that in Figure 5 the average number of iterations is much smaller than $T_{\text{max}} = 200$ for high SNRs. This means that $T_{\text{max}}$ does not need to be large.

![Figure 8: ADMM step size $\mu$: WER plotted as a function of $\mu$ for the Tanner [1055, 424] code in $\mathbb{F}_2$, $T_{\text{max}} = 200$, and $\epsilon = 10^{-5}$.](image)

In Figures 10 and 11, we study the effects of the choice of parameter $\rho$ on the decoder. In Figure 10 we plot WER as a function of $\rho$ when $\mu$ takes on values 1.5 and 2. Again we run experiments for $E_s/N_0 = 5$dB and $E_s/N_0 = 5.5$dB. In Figure 11, we plot the corresponding average number of iterations as a function of $\rho$. In these two figures we observe consistent results.

---

10We use the same seed to generate noises for different values of $\mu$.  

with respect to Figures 8 and 9, i.e., the number of iterations decreases as $\rho$ increases. However, the WER does not decrease significantly for $\rho > 1$.

Finally, we compare Flanagan’s embedding and the constant-weight embedding in terms of decoding performance. For Flanagan’s embedding we use Algorithm 1. For the constant-weight embedding we apply the method in Algorithm 1 to the LP decoding problem (5.3). Due to the similarity between Flanagan’s embedding and the constant-weight embedding, results in Section 5.4 can be easily extended to the constant-weight embedding. In Figures 12 and 13, we compare both
WER and the average number of iterations for the two decoders. In this set of experiments we let $E_s/N_0 = 5$dB, $T_{\text{max}} = 200$, and $\epsilon = 10^{-5}$. In Figure 12 we observe that the WER for the constant-weight embedding is higher than it is for Flanagan’s embedding in most cases. The exception is the data points at $\rho = 1.5$ and $\mu \in [0.7, 1.7]$, where both embeddings attain the same WERs. This exception is because that the parameter choices allow ADMM to converge fast enough to obtain the LP decoding solution. Recall that we proved that the two embedding methods are equivalent in terms of LP decoding (cf. Corollary 28). It is not surprising that both decoders can achieve the same WERs when sufficient number of iterations are allowed. From Figure 13 we observe that the number of iterations for the constant-weight embedding is greater than that required for Flanagan’s embedding. Recall that Flanagan’s embedding saves exactly 1 coordinate for each embedded vector when compared to the constant-weight embedding (cf. Section 3.1). As a result, the constant-weight embedding requires slightly more computer memories than Flanagan’s embedding. Thus, in practice, Flanagan’s embedding for LP decoding would be preferred.

To summarize the above discussions, we emphasize four points

• $\mu \in [1, 2]$ and $\rho = 1.9$ are good parameter choices for the Tanner [1055, 424] code.

• The error performance of the decoder only weakly depends on parameter settings.

• Flanagan’s embedding is slightly better than the constant-weight embedding in terms of computational complexity and memory usage.

• The effects of the choice of parameters for the PEG [2048, 1018] code discussed in Section 7.1 are similar to that for the Tanner [1055, 424] code. We observe that $\mu \in [1, 3.3]$ and $\rho = 1.9$ are good parameter choices for the PEG [2048, 1018] code.
Figure 12: Choice of embedding method: WER plotted as a function of ADMM step size $\mu$ for the Tanner $[1055, 424]$ code in $\mathbb{F}_2^2$. The SNR is $E_s/N_0 = 5$dB and $T_{\text{max}} = 200$. “CW” stands for “constant weight”.

Figure 13: Choice of embedding method: Number of iterations plotted as a function of ADMM step size $\mu$ for the Tanner $[1055, 424]$ code in $\mathbb{F}_2^2$. The SNR is $E_s/N_0 = 5$dB and $T_{\text{max}} = 200$. “CW” stands for “constant weight”.
7.3 Parameter choices for penalized decoding

We now study the ADMM penalized decoder and show how to choose a good penalty parameter $\alpha$. We use the same Tanner [1055, 424] code from Section 7.1 and simulate for an AWGN channel. For this experiment, we do not perform an extensive grid search over the possible parameter choices because of limited computational resources. However, the following parameters are good enough to produce good decoding results: $\mu = 4$, $\rho = 1.5$, and $T_{\text{max}} = 100$.

In Figure 14 we plot WER as a function of the penalty parameter $\alpha$. We observe that the WER first decreases as the value of $\alpha$ increases. But when we penalize too heavily (e.g. when $\alpha > 0.5$), the WER starts to increase again. A similar effect can be observed in Figure 15, i.e., the average number of iterations decreases for $\alpha$ small, and then starts to increase once $\alpha$ becomes sufficiently large ($\alpha > 0.8$ in Figure 15). We make two remarks. First, we observe that $\alpha = 0.5$ is optimal in terms of WER for all SNRs we simulated. This is not necessarily the case for other codes or other SNRs. We do observe that for some codes (e.g., the Tanner [755, 334] code studied in Section 7.1) the optimal value for $\alpha$ is a (non-constant) function of SNR. Second, we observe that the optimal $\alpha$ in terms of WER is not the optimal in terms of the average number of iterations. As a result, there is a trade-off between the optimal computational complexity and the optimal WERs in choosing $\alpha$.

![Figure 14](image-url)

**Figure 14:** Penalty coefficient $\alpha$: ADMM penalized decoding WER plotted as a function of $\alpha$ for the Tanner [1055, 424] code in $\mathbb{F}_2$.

8 Conclusions

In this paper, we develop two types of ADMM decoders for decoding non-binary codes in fields of characteristic two. We start by understanding the embeddings of non-binary SPC codes. We study both the embedding method used by Flanagan et al. in [15] and the method used by Honda and Yamamoto in [19]. We found that the two embeddings are equivalent in terms of error performance of LP decoding. However, Flanagan’s embedding is slightly more efficient in terms of computational
complexity and memory usage. We extend the connections found in [19] to Flanagan’s embedding method and propose a factor graph characterization for SPC code embeddings in $\mathbb{F}_{2^m}$. Further, we show that the polytope formed from SPC code embeddings in $\mathbb{F}_{2^m}$ can be normalized such that we only need to study the geometry induced by the all-ones check. We use these properties to develop new ADMM decoding algorithms.

We present two types of ADMM algorithms in this paper. The first is a natural extension of the binary ADMM decoder introduced by Barman et al in [11]. This ADMM algorithm requires a sub-routine that projects a vector onto the polytope formed by the embeddings of a SPC code. The second ADMM algorithm leverages the factor graph representation of the $\mathbb{F}_{2^m}$ SPC code embedding and does not require the projection sub-routine that is necessary for the first ADMM algorithm. Instead, the projection sub-routines required by the second ADMM algorithm can be performed using off-the-shelf techniques. We further improve the efficiency of this ADMM algorithm using new properties of the embedding of SPC codes that we have discovered. The first ADMM algorithm has two major advantages when compared with the second ADMM algorithm. First, it can be applied to arbitrary fields. Second, it can be used to develop a penalized LP decoder that, empirically, improves the error performance of LP decoding. However, it is not as efficient as the second ADMM algorithm. Although the second ADMM algorithm is restricted to field of characteristic two, it is useful in practice because $\mathbb{F}_{2^m}$ are most commonly used non-binary fields. In addition, we demonstrate that the second ADMM algorithm is efficient and can outperform BP decoding in terms of error performance. Thus we think both algorithms are competitive decoding algorithms and are promising in application to practice.

We summarize three important future directions relevant to this topic. First, the relaxed code polytope considered in this work is conjectured to be tight for $\mathbb{F}_{2^2}$. The proof of this conjecture is still open. To the best of the authors’ knowledge, there has been no known characterization of $\mathbb{F}_{2^2}$ pseudocodewords. Therefore we believe this conjecture, if proved, is an important theoretical result to develop a route to understanding pseudocodewords of non-binary LP decoding. Second,
as mentioned many times in the paper, improving the projection sub-routine of ADMM decoding is important future work (i.e., for the “first” type of ADMM algorithm per the previous paragraph). Finally, we show through execution time statistics that the proposed ADMM LP decoder is efficient and could be interesting in practical (hardware) implementation. However, many missing pieces remain to be developed before the decoder can be applied in practice. In particular, there has been no approximation algorithms for ADMM LP decoding that are analogous to the relationship between the min-sum algorithm and the sum-product algorithm. Such algorithms may substantially reduce computational complexity and are hence important future work. Furthermore, studying finite precision effects on ADMM LP decoding is another crucial direction to pursue which is important to hardware implementation.

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Appendix

9 Definitions and Lemmas for the constant-weight embedding

In this section, we state the definitions and lemmas for the constant-weight embedding that are omitted in Section 3.

**Definition 34** Under the constant-weight embedding let \( q = 2^m \) and denote by \( \mathcal{E}' \) the set of valid embedded matrices defined by check \( \mathbf{h} \) where any \( q \times d_c \) binary matrix \( \mathbf{F} \in \mathcal{E}' \) if and only if it satisfies the following conditions:

(a) \( f_{ij} \in \{0,1\} \), where \( i = 0, \ldots, 2^m-1 \) and \( j = 1, \ldots, d_c \).

(b) \( \sum_{i=0}^{q-1} f_{ij} = 1 \) where \( q = 2^m \).

(c) For any non-zero \( h \in \mathbb{F}_{2^m} \) and \( k \in [m] \), let \( \mathcal{B}(k,h) := \{ \alpha | \langle h_\alpha \rangle_k = 1, \alpha \in \mathbb{F}_{2^m} \} \), where \( \cdot_k \) denotes the \( k \)-th entry of the vector. Let \( g^k_j = \sum_{i\in\mathcal{B}(k,h_j)} f_{ij} \), then

\[
\sum_{j=1}^{d_c} g^k_j = 0 \quad \text{for all } k \in [m]
\]  

(9.1)

where the addition is in \( \mathbb{F}_2 \).

Note that Condition (c) is the same for both embeddings. This is due to two facts. First, \( 0 \notin \mathcal{B}(k,h) \). Second, the constant-weight embedding is indexed starting from 0 (cf. Definition 4). Thus entries from Definition 7 that participate in \( \mathcal{B}(k,h) \) remain unchanged.

**Lemma 35** In \( \mathbb{F}_{2^m} \), let \( \mathcal{C} \) be the set of codewords that correspond to check \( \mathbf{h} = (h_1,h_2,\ldots,h_{d_c}) \). Then

\[ F'(\mathcal{C}) = \mathcal{E}', \]  

(9.2)

where \( \mathcal{E}' \) is defined by Definition 34.
Proof See Appendix. 10.2.

Definition 36 The conditions in Definition 34 can be represented using factor graphs. In this graph, there are three types of nodes: variable nodes, parity-check nodes and one-on-check node. Each variable node corresponds to an entry $f_{ij}$ for $0 \leq i \leq 2^m - 1$ and $1 \leq j \leq d_c$. Each parity-check node corresponds to a parity-check of the $k$-th bit where $k \in [m]$; it connects all variable nodes in $B(k, h_j)$ for all $j \in [d_c]$. Each one-on-check node corresponds to a constraint $\sum_{i=0}^{q-1} f_{ij} = 1$.

Lemma 37 Consider the constant-weight embedding and let $F'$ be the set of $q \times d_c$ binary matrices defined by the first two conditions of Definition 34 but with the third condition replaced by the Condition $(c^*)$ defined below, then $F' = E'$. The complete set of conditions are

(a) $f_{ij} \in \{0, 1\}$.

(b) $\sum_{i=0}^{q-1} f_{ij} = 1$ where $q = 2^m$.

(c) Let $K$ be any nonempty subset of $[m]$. For any non-zero $h \in F_2^m$, let $\bar{B}(K, h) := \{a| \sum_{k \in K} b(ha)_k = 1\}$, where $b(ha)_k$ denotes the $k$-th entry of the vector $b(ha)$. Let $g^K_j = \sum_{i \in \bar{B}(K, h_j)} f_{ij}$, then $\sum_{j=1}^{d_c} g^K_j = 0$ for all $K \subset [m], K \neq \emptyset$, where the addition is in $F_2$.

Proof See Appendix. 10.3

Definition 38 Let $C$ be a non-binary SPC code defined by check vector $h$. Denote by $V'$ the “tight code polytope” for the constant-weight embedding where

$$V' = \text{conv}(F'(C)).$$

In $F_2^m$ denote by $U'$ the “relaxed code polytope” for the constant-weight embedding where a $q \times d_c$ matrix $F \in U'$ if and only if the following constraints hold:

(a) $f_{ij} \in [0, 1]$.

(b) $\sum_{i=0}^{q-1} f_{ij} = 1$.

(c) Let $\bar{B}(K, h)$ be the same set defined in Lemma 37. Let $g^K$ be a vector of length $d_c$ such that $g^K_j = \sum_{i \in \bar{B}(K, h_j)} f_{ij}$, then

$$g^K \in F_{d_c}$$

for all $K \subset [m]$ and $K \neq \emptyset$ (9.3)

Definition 39 Let $h$ be a non-zero element in $F_q$. Let $D'(q, h)$ be a $q \times q$ permutation matrix indexed starting from row 0 and column 0, and satisfying the following equation

$$D'(q, h)_{ij} = \begin{cases} 1 & \text{if } i \cdot h = j, \\ 0 & \text{otherwise}, \end{cases}$$

where the multiplication is in $F_q$. For a vector $h \in F_q^{d_c}$ that $h_j \neq 0$ for all $j \in [d_c]$, let

$$D'(q, h) = \text{diag}(D'(q, h_1), \ldots, D'(q, h_{d_c})).$$

We note that Lemma 19, Lemma 20, Lemma 21 and Lemma 22 all hold for the constant-weight embedding when $D'$ is used. The proofs for the constant-weight embedding can be extended from the proofs for Flanagan’s embedding easily.
10 Proofs

10.1 Proof of Proposition 9
First, \(|\mathcal{B}(k, 1)| = 2^{m-1}\) because the \(k\)-th bit is fixed to 1. Since dividing by a fixed \(h\) only permutes the elements in finite fields, \(|\mathcal{B}(k, h)|\) is also \(2^{m-1}\).

10.2 Proof of Lemma 8 and Lemma 35

Proof of Lemma 8
We first prove that for a vector \(c \in \mathbb{F}_2^{d_c}\) and its embedded matrix \(F\), \(g_j^k = 1\) if and only if \(b(h_jc_j)_k = 1\) for all \(j\) and \(k\). For a fixed \(j\), if \(b(h_jc_j)_k = 1\), then \(c_j \in \mathcal{B}(k, h_j)\) by the definition of \(\mathcal{B}(k, h_j)\). Note that the \(j\)-th column of \(F\), denoted by \(f_j\), is a vector such that \(f_{c_j} = 1\) and \(f_i = 0\) for all \(i \neq c_j\). Hence \((g_j^k :=) \sum_{i \in \mathcal{B}(k, h_j)} f_{ij} = f_{c_j} = 1\). If \(b(h_jc_j)_k = 0\), then \(c_j \notin \mathcal{B}(k, h_j)\). Since \(f_{ij} = 0\) for all \(i \neq c_j\), \(\sum_{i \in \mathcal{B}(k, h_j)} f_{ij} = 0\).

Using this result we show \(F \in \mathcal{F}(C)\) implies \(F \in \mathcal{E}\). If \(F \in \mathcal{F}(C)\), then there exists a vector \(c \in C\) such that \(F = F(c)\). By the definition of \(\mathcal{F}(C)\), Condition (a) and (b) per Definition 7 are satisfied by \(F\). For Condition (c), note that \(c\) is a codeword. This implies that \(v := \sum_{j=1}^{d_c} h_j c_j = 0\) in \(\mathbb{F}_{2^m}\). Further, due to the fact that we are working in extension fields of 2, \(b(v)\) is an all-zero vector of length \(m\). This means that for all \(k = 1, \ldots, m\), \(b(v)_k = \sum_{j=1}^{d_c} b(h_jc_j)_k = 0\). Thus, \(\sum_{j=1}^{d_c} g_j^k = \sum_{j=1}^{d_c} b(h_jc_j)_k = 0\). This implies that Condition (c) is satisfied.

Conversely, let \(F \in \mathcal{E}\). Then by Condition (a) and (b), we know that there is a unique vector \(c \in \mathbb{F}_2^{d_c}\) such that \(F = F(c)\). By Condition (c), we know that for any fixed \(j\) and \(k\), \(\sum_{i \in \mathcal{B}(k, h_j)} f_{ij} = b(h_jc_j)_k = 0\). Therefore \(b(v)_k = 0\) for all \(k\) where \(v := \sum_{i=1}^{d_c} h_j c_i\). This implies that \(c\) is a valid codeword, or equivalently, \(c \in C\) and hence \(F \in \mathcal{F}(C)\).

Proof of Lemma 35
This is a simple consequence due to the bijection relationship between the constant-weight embedding and Flanagan’s embedding. Formally, if a matrix \(F' \in \mathcal{E}'\), we construct \(F\) by eliminating the first row of \(F'\). Then it is simple to verify that \(F \in \mathcal{E}\). By Lemma 8, \(F\) is the embedding for some codeword \(c\). This shows that \(F' \in \mathcal{F}(C)\). Conversely, for each codeword \(c \in C\), we can reverse the process and find a \(F'\) that satisfies Definition 34.

10.3 Proof of Lemma 12 and Lemma 37

Proof of Lemma 12
We first show that \(\sum_{k \in \mathcal{K}} b(h_jc_j)_k = 1\) if and only if \(g_j^F = 1\). Suppose \(g_j^F = 1\), then there exists a unique \(i\) such that \(i \in \mathcal{B}(K, h_j)\) and \(f_{ij} = 1\). Since \(f_{c_j} = 1\) and Condition (b) holds, \(i = c_j\). Therefore \(c_j \in \mathcal{B}(K, h_j)\). By the definition of \(\mathcal{B}(K, h_j)\), \(\sum_{k \in \mathcal{K}} b(h_jc_j)_k = 1\). On the other hand, if \(\sum_{k \in \mathcal{E}} b(h_jc_j)_k = 1\), then \(c_j \in \mathcal{B}(K, h_j)\). This implies \(g_j^F := \sum_{i \in \mathcal{B}(K, h_j)} f_{ij} = 1\). Next, note that Condition (c*) only adds more constraints to Definition 7. Thus \(F \subset \mathcal{E}\). Conversely, using the same definition in the proof in Appendix 10.2, \(b(v)_k = \sum_{j=1}^{d_c} b(h_jc_j)_k = 0\). This means that for
any subset $\mathcal{K}$, \( \sum_{k \in \mathcal{K}} (\sum_{j=1}^{d_c} b(h_j c_j)_k) = 0 \). In addition,
\[
\sum_{k \in \mathcal{K}} (\sum_{j=1}^{d_c} b(h_j c_j)_k) = \sum_{j=1}^{d_c} (\sum_{k \in \mathcal{K}} b(h_j c_j)_k).
\]

Using the result we just proved, we deduce that \( \sum_{j=1}^{d_c} g^K_j = 0 \). This shows that \( \mathcal{E} \subset \mathcal{F} \). In other words, we have shown that any valid codeword \( c \in \mathcal{C} \) satisfies Condition \((c^*)\) in Lemma 12.

**Proof of Lemma 37**

It is easy to verify this lemma following the same logic in Appendix 10.2.

**10.4 Proof of Lemma 13**

First consider all binary vectors of length \(|\mathcal{K}|\) that sum up to 1. The number of such vectors is \( 2^{|\mathcal{K}|}-1 \). The number of binary vectors of length \( m - |\mathcal{K}| \) is \( 2^{m-|\mathcal{K}|} \). Thus \( |\mathcal{B}(\mathcal{K}, h)| = 2^{|\mathcal{K}|-1} \times 2^{m-|\mathcal{K}|} = 2^m \).

**10.5 Proof of Proposition 16**

We first introduce a notation that we use in this proof. Let “\( \prec \)” denote the relationship of majorization. Let \( x \) and \( y \) be length-\( n \) vectors, then \( y \) majorizes \( x \) written as \( x \prec y \). We refer readers to Definition 1 in [11] for the definition of majorization. We use Theorem 1 in [11] in this proof.

Let \( y \in \mathbb{P}_n \) and consider the length-\( nd \) vector \( y^* = (y, 0_{1 \times (nd-n)}) \). First, we show \( y^* \in \mathbb{P}_{nd} \). This immediately implies that all permutations of \( y^* \) is in \( \mathbb{P}_{nd} \). Then we show \( x \prec y^* \). By Theorem 1 in [11], this implies that \( x \) is in the convex hull of all permutations of \( y^* \), each of which is in \( \mathbb{P}_{nd} \). Therefore \( x \in \mathbb{P}_{nd} \).

Since \( y \in \mathbb{P}_n \), there exists a set \( S \) of length-\( n \) binary vectors with even parity such that \( y = \sum_{i=1}^{|S|} w_i s_i \) for some \( w_i \geq 0 \) and \( \sum_{i=1}^{|S|} w_i = 1 \). Let \( s_i^* = (s_i, 0_{1 \times (nd-n)}) \), then \( s_i^* \) is a binary vector of length \( nd \) with even parity. Also note that \( y^* = \sum_{i=1}^{|S|} w_i s_i^* \). This implies \( y^* \in \mathbb{P}_{nd} \).

In order to show \( x \prec y^* \), we need to consider partial sums based on the sorted vectors of \( x \) and \( y^* \). Let \( Q_k \) (resp. \( U_k \)) be the set indices of the \( k \) largest entries in \( x \) (resp. \( y^* \)). Let operator \( \text{cover}() \) be defined as \( i := \text{cover}(j) \) if \( j \in \{(i-1)n+1, (i-1)n+2, \ldots, in\} \). Since all entries of \( x \) and \( y^* \) are non-negative, \( x_j \leq y_{\text{cover}(j)}^* \) for all \( j \). Therefore \( \sum_{j \in Q_k} x_j \leq \sum_{j \in Q_k} y_{\text{cover}(j)}^* \leq \sum_{j \in U_k} y_j^* \), where the second inequality is because of the fact that \( U_k \) contains the largest \( k \) entries of \( y^* \). In addition, \( \|x\|_1 = \|y^*\|_1 \). By the definition of majorization, these imply that \( x \prec y^* \).
10.6 Proof of Lemmas on rotation

10.6.1 Proof of Lemma 19
We first show that if \( u^N \in \mathbb{V}^N \) then \( u = D(q, h)u^N \in \mathbb{V} \). Let \( e^N_k, k = 1, \ldots, |\mathcal{E}^N| \) be vectors in \( \mathcal{E}^N \), then

\[
D(q, h)u^N = D(q, h) \sum_k \alpha_k e^N_k
\]

\[
= \sum_k \alpha_k (D(q, h)e^N_k)
\]

\[
= \sum_k \alpha_k e_k,
\]

where \( e_k \) are vectors in \( \mathcal{E} \). Therefore \( v \in \mathbb{V} \).

Conversely, if \( u \in \mathbb{V}, u^N = D(q, h)^{-1}u \). Let \( e_k, k = 1, \ldots, |\mathcal{E}| \) be vectors in \( \mathcal{E} \), then

\[
D(q, h)^{-1}u = D(q, h)^{-1} \sum_k \alpha_k e_k
\]

\[
= \sum_k \alpha_k (D(q, h)^{-1}e_k)
\]

\[
= \sum_k \alpha_k e^N_k,
\]

where \( e^N_k \) are vectors in \( \mathcal{E}^N \). Therefore \( u \in \mathbb{V}^N \).

10.6.2 Proof of Lemma 21
First, by Lemma 19, \( u \in \mathbb{V} \). Therefore we need to show that there is no vector in \( \mathbb{V} \) that has less Euclidean distance to \( v \) than \( u \). Suppose that there is a vector \( p \) such that \( \|p - v\|_2^2 < \|D(q, h)u^N - v\|_2^2 \). Then \( \|p - v\|_2^2 = \|D(q, h)^{-1}(p - v)\|_2^2 = \|D(q, h)^{-1}p - D(q, h)^{-1}v\|_2^2 = \|D(q, h)^{-1}p - v^N\|_2^2 \), where the second equality is due to the fact that \( D(q, h) \) is a permutation matrix. We then deduce that

\[
\|D(q, h)^{-1}p - v^N\|_2^2 < \|u^N - D(q, h)^{-1}v\|_2^2,
\]

which contradicts with the fact that \( u^N \) is the projection of \( v^N \) onto \( \mathbb{V}^N \).

10.6.3 Proof of Lemma 21
Let \( w \in \mathbb{U}^N \), we need to show \( u := D(q, h)w \) is in \( \mathbb{U} \). Let \( U \) and \( W \) be the matrix form for \( u \) and \( w \) respectively (i.e. \( \text{vec}(U) = u \) and \( \text{vec}(W) = w \)). Note that the rotations are done per embedded vector. In other words, columns of \( U \) are permuted independently with respect to each other. By the definition of the rotation matrix, for each column \( j \),

\[
u_{ij} = w_{ij} \text{ for all } l = i \cdot h,
\]

(10.1)

where the multiplication is in \( \mathbb{F}_{2^m} \). Since \( w \in \mathbb{U}^N \), \( W \) satisfies Definition 15 for check \((1,1,1,\ldots,1)\). We first verify that \( U \) satisfies Conditions \((a) \) and \((b) \) in Definition 15. Note that these two
conditions are defined for each column. Thus a permutation within each column does not violate these two conditions. For Condition (c), note that \( \tilde{B}(K, h) \) can be obtained by \( \tilde{B}(K, 1) \) as follows:

\[
\tilde{B}(K, h) = \{ y| y = x/h, x \in \tilde{B}(K, 1) \}. \tag{10.2}
\]

Then, \( i \in \tilde{B}(K, h_j) \) if \( l \in \tilde{B}(K, 1) \), provided that \( l = i \cdot h_j \). Let \( f_{\tilde{B}(K, h_j)_j} \) be the sub-vector constructed by selecting entries \( f_{ij} \) where \( i \in \tilde{B}(K, h_j) \). Combining (10.1) and (10.2), we deduce that for fixed \( j, K \) and \( l \), if \( w_{ij} \) is in the vector \( f_{\tilde{B}(K, 1)_j} \), then \( u_{ij} \) is in the vector \( f_{\tilde{B}(K, h_j)_j} \) and \( u_{ij} = w_{ij} \). This shows that \( g^K_j = \|f_{\tilde{B}(K, h_j)_j}\|_1 \) is not changed under rotation. Therefore \( u \) satisfies Condition (c). This implies that \( u \in U \).

Conversely, let \( u \in U \), we need to show \( w := D(q, h)^{-1} u \) is in \( U^N \). Note that \( D(q, h) \) is a permutation matrix and therefore \( D(q, h)^{-1} = D(q, h)^T \). Same as the previous case, the columns of \( U \) and \( W \) are permuted independently. Therefore Condition (a) and (b) hold for \( W \). We now verify Condition (c). First, permutation using \( D(q, h)_T \) implies that \( u_{ij} = w_{ij} \) for all \( l = i \cdot h \). Second, \( \tilde{B}(K, 1) \) can be obtained by \( \tilde{B}(K, h) \) as follows:

\[
\tilde{B}(K, 1) = \{ y| y = x \cdot h, x \in \tilde{B}(K, h) \}. \tag{10.3}
\]

Thus, for fixed \( j, K \) and \( l \), if entry \( u_{ij} \) is contained in the vector \( f_{\tilde{B}(K, h_j)_j} \), then entry \( w_{ij} \) is contained in the vector \( f_{\tilde{B}(K, 1)_j} \) and \( u_{ij} = w_{ij} \). Therefore the vector \( g^K \) still remains unchanged, which implies that Condition (c) is satisfied by \( W \).

### 10.6.4 Proof of Lemma 22

Using Lemma 21, we can follow exactly the same logic as in the proof of Lemma 21. The details are omitted.

### 10.7 Proof of Theorem 24

We first prove several lemmas that will be useful for proving the theorem.

**Lemma 40** For any \( \alpha \in \mathbb{F}_q \), \( f'(\alpha) = (1 - \|f(\alpha)\|_1, f(\alpha)) \).

**Proof** If \( \alpha \neq 0 \), \( \|f(\alpha)\|_1 = 1 \) and \( f'(\alpha) = (0, f(\alpha)) \). If \( \alpha = 0 \), \( \|f(\alpha)\|_1 = 0 \) and \( f'(\alpha) = (1, 0, 0, \ldots, 0) \).

**Lemma 41** Let \( C \) be the SPC code defined by check \( h \). If

\[
\tilde{f} = (f_1|\ldots|f_{d_c})^T \in \text{conv}(\mathbb{F}_v(C)),
\]

then

\[
\bar{f} = (a_1, f_1|\ldots|a_{d_c}, f_{d_c})^T \in \text{conv}(\mathbb{F}_v'(C)),
\]

where \( a_i = 1 - \|f_i\|_1 \) for all \( i = 1, \ldots, d_c \). Conversely, if

\[
\bar{f} = (a_1, f_1|\ldots|a_{d_c}, f_{d_c})^T \in \text{conv}(\mathbb{F}_v'(C))
\]

for some \( a_1, \ldots, a_{d_c} \), then

\[
\tilde{f} = (f_1|\ldots|f_{d_c})^T \in \text{conv}(\mathbb{F}_v(C)).
\]
Proof Let $c_k$, $k = 1, \ldots, |C|$ be all codewords in $C$. Let $\hat{f} \in \text{conv}(F_v(C))$, then there exists a set of coefficient $0 \leq \omega_k \leq 1$, $k = 1, \ldots, |C|$, satisfying $\sum_{k=1}^{|C|} \omega_k = 1$ such that $\hat{f} = \sum_{k=1}^{|C|} \omega_k F_v(c_k)$. Then $f_i = \sum_{k=1}^{|C|} \omega_k f(c_k^i)$ for all $i$. On the other hand, $\sum_{k=1}^{|C|} \omega_k F_v(c_k) \in \text{conv}(F_v(C))$ by the definition of convex hull. Using Lemma 40, \[
\sum_{k=1}^{|C|} \omega_k F'_v(c_k) = \left( \sum_{k=1}^{|C|} \omega_k f'(c_k^i), \ldots, \sum_{k=1}^{|C|} \omega_k f'(c_{d_i}) \right) \]

\[
= \left( \sum_{k=1}^{|C|} \omega_k (1 - \|f(c_k^1)\|_1, f(c_k^i)), \ldots, \sum_{k=1}^{|C|} \omega_k (1 - \|f(c_{d_i})\|_1, f(c_{d_i})) \right) \]

\[
= (1 - \|f_1\|_1, f_1, \ldots, 1 - \|f_{d_i}\|_1, f_{d_i})^T = \bar{f}.
\]

Thus we conclude $\bar{f} \in \text{conv}(F'_v(C))$.

Conversely, if $\bar{f} \in \text{conv}(F'_v(C))$, then there exists a set of coefficient $0 \leq \omega_k \leq 1$, $k = 1, \ldots, |C|$, satisfying $\sum_{k=1}^{|C|} \omega_k = 1$ such that $\bar{f} = \sum_{k=1}^{|C|} \omega_k F'_v(c_k)$. By definition, $\sum_{k=1}^{|C|} \omega_k F_v(c_k^i) \in \text{conv}(F_v(C))$. Similar to the previous case, it is easy to verify using Lemma 40 that $\sum_{k=1}^{|C|} \omega_k F_v(c_k^i) = \hat{f}$.

Lemma 42 If $\hat{f} = (f_1, \ldots, f_N)^T$ is feasible for LP-FT, then $\bar{f} = (1 - \|f_1\|_1, f_1, \ldots, 1 - \|f_N\|_1, f_N)^T$ is feasible for LP-CT. Conversely, if $\hat{f} = (f_1, f_1, \ldots, f_N, f_N)^T$ is feasible to LP-CT, then $\bar{f} = (f_1, \ldots, f_N)^T$ is feasible to LP-FT.

Proof For LDPC codes, check $j \in J$ selects a sub-vector $\hat{f}_j$ from $\hat{f}$ (or $\bar{f}_j$ from $\bar{f}$) that participates in that check. Therefore we apply Lemma 41 to each sub-vector of $\hat{f}$ (and $\bar{f}$). In addition, $\hat{f}$ (or $\bar{f}$) is feasible if and only if $\hat{f}_j \in \text{conv}(F_v(C_j))$ ($\bar{f}_j \in \text{conv}(F'_v(C_j))$) for all $j \in J$, where $C_j$ is the SPC code defined by the $j$-th check. Therefore we conclude that the lemma holds.

Lemma 43 Let $y \in \Sigma$, $\hat{f} = (f_1, \ldots, f_N)^T$ be a vector with non-negative entries and such that $\|f_i\|_1 \leq 1$. Further, let $\bar{f} = (1 - \|f_1\|_1, f_1, \ldots, 1 - \|f_N\|_1, f_N)^T$. Then

\[
L'_v(y) \hat{f} = L_v(y) \bar{f} - \sum_{i=1}^N \log(\text{Pr}[y_i|0]).
\]

Proof For simplicity, we index entries in $\hat{f}$ starting from 0. By definitions of $\hat{f}$ and $\bar{f}$, we have

\[
f_i = (f(i-1)q+1, f(i-1)q+2, \ldots, f(i-1)q+q-1) = (\hat{f}(i-1)q+1, \hat{f}(i-1)q+2, \ldots, \hat{f}(i-1)q+q-1),
\]

and

\[
\hat{f}(i-1)q = 1 - \|f_i\|_1.
\]
Note that $\hat{f}_{(i-1)q+\delta} = \hat{f}_{(i-1)(q-1)+\delta}$ for all $i \in \mathcal{I}$ and $\delta \in [q-1]$. Then

$$L'_v(y) \hat{f} = \sum_{i=1}^{N} \sum_{\delta=0}^{q-1} \log \left( \frac{1}{Pr[y_i|\delta]} \right) \hat{f}_{(i-1)q+\delta}$$

$$= \sum_{i=1}^{N} \left[ \sum_{\delta=1}^{q-1} \log \left( \frac{1}{Pr[y_i|\delta]} \right) \hat{f}_{(i-1)q+\delta} + (1 - \|f_i+1\|_1) \log \left( \frac{1}{Pr[y_i|0]} \right) \right]$$

$$= \sum_{i=1}^{N} \left[ \sum_{\delta=1}^{q-1} \log \left( \frac{Pr[y_{i}\delta]}{Pr[y_i|\delta]} \right) \hat{f}_{(i-1)q+\delta} + \log \left( \frac{1}{Pr[y_i|0]} \right) \right]$$

$$= \sum_{i=1}^{N} \left[ \sum_{\delta=1}^{q-1} \log \left( \frac{Pr[y_{i}\delta]}{Pr[y_i|\delta]} \right) \hat{f}_{(i-1)(q-1)+\delta} \right] + \sum_{i=1}^{N} \log \left( \frac{1}{Pr[y_i|0]} \right)$$

$$= L_v(y) \hat{f} - \sum_{i=1}^{N} \log(Pr[y_i|0]).$$

Now we state the proof of Theorem 24: **Proof** By Lemma 42, $\hat{f} = (a_1, f_1|\ldots|a_N, f_N)^T$ is feasible for LP-CT if and only if $\hat{f} = (f_1|\ldots|f_N)^T$ is feasible for LP-FT. By Lemma 43, we can subtract a constant, $\sum_{i=1}^{N} \log(Pr[y_i|0])$, from the objective function of LP-CT to obtain the same objective function as LP-FT.

We now prove the theorem by contradiction. Suppose $\hat{f}$ is the minimizer of LP-CT, but $\hat{f}$ is not the minimizer of LP-FT. Then there exists a feasible point $\bar{f}$ that attains less cost than $\hat{f}$. Using Lemma 42, we can construct a feasible point for LP-CT that attains less cost than $\hat{f}$. This contradicts with the assumption that $\hat{f}$ is the minimizer of LP-CT. Similar arguments also hold for the converse statement.

10.8 **Proof of Theorem 27**

We first restate Lemma 41 and Lemma 42 for the constant-weight embedding:

**Lemma 44** Let $\mathbb{U}$ (resp. $\mathbb{U}'$) be the relaxed code polytope for Flanagan’s embedding (resp. the constant-weight embedding) for check $h$. If $\hat{f} = (f_1|\ldots|f_{d_c})^T \in \mathbb{U}$, then $\hat{f} = (a_1, f_1|\ldots|a_{d_c}, f_{d_c})^T \in \mathbb{U}'$ where $a_i = 1 - \|f_i\|_1$ for all $i = 1, \ldots, d_c$. Conversely, if $\hat{f} = (a_1, f_1|\ldots|a_{d_c}, f_{d_c})^T \in \mathbb{U}'$ for some $a_1, \ldots, a_{d_c}$, then $\hat{f} = (f_1|\ldots|f_{d_c})^T \in \mathbb{U}$.

**Proof** For any value of $i \in [d_c]$, $f_{bi}$ does not participate in the Condition (c) in Definition 38. Thus $\hat{f}$ satisfies Condition (c) in Definition 15 if and only if $\hat{f}$ satisfies Condition (c) in Definition 38. Further, it is easy to verify that $\hat{f}$ satisfies Condition (a) and (b) in Definition 15 if and only if $\hat{f}$ satisfies Condition (a) and (b) in Definition 38.

**Lemma 45** If $\hat{f} = (f_1|\ldots|f_N)^T$ is feasible for LP-FR, then $\hat{f} = (1-\|f_1\|_1, f_1|\ldots|1-\|f_N\|, f_N)^T$ is feasible for LP-CR. Conversely, if $\hat{f} = (f_1, f_1|\ldots|f_N, f_N)^T$ is feasible to LP-CR, then $\hat{f} = (f_1|\ldots|f_N)^T$ is feasible to LP-FR.

**Proof** The proof is the same as Lemma 42 except it is based on Lemma 44.

Then we can prove Theorem 27 using Lemma 43 and Lemma 45. The logic is the same as Theorem 24.
10.9 Proof of Corollary 29

\textbf{LP-FR} has the same embedding and objective as \textbf{LP-FT}. Thus we can follow the same proof as in [15, Thm. 5.1]. The only difference being the constraint set. Thus we only need to prove that the “relative matrix” (defined below) to all-zero matrix satisfies the constraint set in \textbf{LP-FR}. The rest of the proof is identical to that in [15]. The following definition rephrases Eq. (19) in [15].

\begin{definition}
Let \( C \) be a SPC code defined by a length-\( d \) check \( h \). Let \( F \) and \( \tilde{F} \) be \((2^m - 1) \times d\) matrices. We say that \( \tilde{F} \) is the “relative matrix” for \( F \) based on a codeword \( c \in C \) if

\[
\tilde{f}_{ij} = \begin{cases} 
1 - \sum_i f_{ij} & \text{if } i + c_j = 0 \\
 f_{(i+c_j)j} & \text{otherwise}
\end{cases},
\]

where the sums are in \( \mathbb{F}_{2^m} \). (Note that in \( \mathbb{F}_{2^m} \), \( p + q = 0 \) means \( q = p \).) Denote this mapping \( R_c(\cdot) \). Note that it is a bijective mapping. Its inverse is given by

\[
f_{ij} = \begin{cases} 
1 - \sum_i \tilde{f}_{ij} & \text{if } i - c_j = 0 \\
 \tilde{f}_{(i-c_j)j} & \text{otherwise}
\end{cases}.
\]

Denote by \( R_c^{-1}(\cdot) \) this inverse mapping.
\end{definition}

\begin{lemma}
Let \( c \) be a valid SPC for length-\( d \) check \( h \). Then \( F \in U \) if and only if \( R_c(F) \in U \).
\end{lemma}

\begin{proof}
We first show that if \( F \in U \), then \( R_c(F) \in U \). In other words, we need to verify that \( R_c(F) \) satisfies all three conditions in Definition 15. The first two conditions are obvious. We focus on the third condition. Let \( \mathcal{K} \) be a non-empty subset of \([m]\) and let \( j \) be an integer in \([d]\). Let \( \tilde{B}(\mathcal{K}, h_j) \) be the same set defined in Lemma 12 for the \( j \)-th check entry \( h_j \). There are two cases: (i) \( c_j \notin \tilde{B}(\mathcal{K}, h_j) \) and (ii) \( c_j \in \tilde{B}(\mathcal{K}, h_j) \).

If \( c_j \notin \tilde{B}(\mathcal{K}, h_j) \) then for all \( i \in \tilde{B}(\mathcal{K}, h_j) \),

\[
\sum_{k \in \mathcal{K}} b(h_j(i + c_j))_k = \sum_{k \in \mathcal{K}} b(h_{ji} + h_j c_j)_k
= \sum_{k \in \mathcal{K}} [b(h_{ji})_k + b(h_j c_j)_k]
= 1 + 0 = 1,
\]

where the second equality is because that the addition in \( \mathbb{F}_{2^m} \) are equivalent to the addition of the corresponding binary vectors. The third equality is because \( i \in \tilde{B}(\mathcal{K}, h_j) \) and \( c_j \notin \tilde{B}(\mathcal{K}, h_j) \). Similarly, for all \( i \notin \tilde{B}(\mathcal{K}, h_j) \), \( \sum_{k \in \mathcal{K}} b(h_j(i + c_j))_k = 0 \). Thus \( i + c_j \in \tilde{B}(\mathcal{K}, h_j) \) if and only if \( i \in \tilde{B}(\mathcal{K}, h_j) \).

In addition, since \( c_j \notin \tilde{B}(\mathcal{K}, h_j) \), \( c_j \neq i \) for all \( i \in \tilde{B}(\mathcal{K}, h_j) \). Then by Definition 46, \( \tilde{f}_{ij} = \)
Let $g^K_j := \sum_{i \in \tilde{\mathcal{B}}(K, h_j)} f_{ij}$ and $\tilde{g}^K_j := \sum_{i \in \tilde{\mathcal{B}}(K, h_j)} \tilde{f}_{ij}$, then

$$
\sum_{i \in \mathcal{B}(K, h_j)} \tilde{f}_{ij} = \sum_{i \in \mathcal{B}(K, h_j)} f_{(i+c_j)j} = \sum_{(i+c_j) \in \mathcal{B}(K, h_j)} f_{(i+c_j)j} = \sum_{i \in \mathcal{B}(K, h_j)} \tilde{f}_{ij} = g^K_j,
$$

where the second equality is because $i + c_j \in \tilde{\mathcal{B}}(K, h_j)$ if and only if $i \in \mathcal{B}(K, h_j)$. Therefore we conclude that $g^K_j = \tilde{g}^K_j$.

For case (ii), $c_j \in \tilde{\mathcal{B}}(K, h_j)$. Using the same argument as above, we can show that $i + c_j \in \tilde{\mathcal{B}}(K, h_j)$ if and only if $i \notin \tilde{\mathcal{B}}(K, h_j)$. Therefore

$$
\sum_{i \in \mathcal{B}(K, h_j)} \tilde{f}_{ij} = \tilde{f}_{c_j} + \sum_{i \in \mathcal{B}(K, h_j) \text{ and } i \neq c_j} \tilde{f}_{ij} = \left(1 - \sum_{i=1}^{2^m-1} f_{ij}\right) + \sum_{l \notin \mathcal{B}(K, h_j) \text{ and } l \neq 0} f_{ij} = 1 - \sum_{l \in \mathcal{B}(K, h_j)} f_{ij} = 1 - g^K_j.
$$

Combining the two cases, we conclude that for the vector $\tilde{g}^K$ satisfies the following conditions:

$$
\tilde{g}^K_j = \begin{cases} 
g^K_j & \text{if } c_j \in \tilde{\mathcal{B}}(K, h_j) \\
1 - g^K_j & \text{if } c_j \notin \tilde{\mathcal{B}}(K, h_j) \end{cases}.
$$

(10.6)

We can rephrase this condition by introducing the following notation: Let $\tilde{\mathbf{F}} = \mathbf{F}(c)$. Let $\mathbf{g}^{K,c}$ be a binary vector for $K$ and $c$ defined by $g^{K,c}_j := \sum_{i \in \tilde{\mathcal{B}}(K, h_j)} \tilde{f}_{ij}$. By Lemma 12, $\mathbf{g}^{K,c}$ is a binary vector with even parity. By its definition, $g^{K,c}_j = 1$ if and only if $c_j \in \tilde{\mathcal{B}}(K, h_j)$. Thus we can rewrite (10.6) as

$$
\tilde{g}^K_j = \begin{cases} 
g^{K,c}_j & \text{if } g^{K,c}_j = 0 \\
1 - g^{K,c}_j & \text{if } g^{K,c}_j = 1 \end{cases}.
$$

(10.7)

These conditions satisfy the definition of “relative solution” defined in [1]. When applying Lemma 17 in [1] to the case of binary single parity-check code, we conclude that $\mathbf{g}^K \in \mathbb{P}_d$ if $\mathbf{g}^{K,c} \in \mathbb{P}_d$. This conclude our verification for the third condition of Definition 15.

- Next we need to show that if $\tilde{\mathbf{F}} \in \mathbb{U}$, then $R^{-1}_c(\tilde{\mathbf{F}}) \in \mathbb{U}$. Note that in (10.5), $i - c_j = 0$ is equivalent to $i + c_j = 0$ for $\mathbb{F}_{2^m}$. Therefore the proof is identical to the previous case.
10.10 Proof of Theorem 33

10.10.1 Sketch of the proof

We need to prove \( \Pr[\text{error}|0] = \Pr[\text{error}|c] \), where \( c \) is any non-zero codeword. Let

\[ B(c) := \{ y | \text{Decoder fails to recover } c \text{ if } y \text{ is received} \}. \]

Then \( \Pr[\text{error}|c] = \sum_{y \in B(c)} \Pr[y|c] \).

We first rephrase the symmetry condition in [15]. This definition introduces a one-to-one mapping from the received vector \( y \) to a vector \( y^0 \) such that the following two statements hold:

(a) \( \Pr[y|c] = \Pr[y^0|0] \),

(b) \( y \in B(c) \) if and only if \( y^0 \in B(0) \).

Statement (a) is directly implied by the definition of symmetry condition. We show in the following that statement (b) is also true. Once we have both results,

\[
\Pr[\text{error}|c] = \sum_{y \in B(c)} \Pr[y|c] = \sum_{y^0 \in B(0)} \Pr[y^0|c] = \Pr[\text{error}|0].
\]

10.10.2 Symmetry condition

A symmetry condition for rings is defined in [15]. We rephrase that definition for fields:

Definition 48 (Symmetry condition) For \( \beta \in \mathbb{F}_q \), there exists a bijection

\[ \tau_\beta : \Sigma \mapsto \Sigma, \]

such that the channel output probability conditioned on the channel input satisfies

\[ P(y|\alpha) = P(\tau_\beta(y)|\alpha - \beta), \quad (10.8) \]

for all \( y \in \Sigma, \alpha \in \mathbb{F}_q \). In addition, \( \tau_\beta \) is isometric in the sense of [15].

10.10.3 Proof of statement (b)

We define the concept of relative matrices for the constant-weight embedding.

Definition 49 Let \( \alpha \in \mathbb{F}_{2^m} \) and let \( \mathbf{x} \) be a vector of length \( 2^m \). We say that \( \mathbf{\tilde{x}} \) is the “relative vector” for \( \mathbf{x} \) based on \( \alpha \) if

\[ \tilde{x}_i = x_{i + \alpha}, \quad (10.9) \]

where the sum is in \( \mathbb{F}_{2^m} \). This mapping is denoted by \( \tilde{x} = R'_\alpha(\mathbf{x}) \). Its inverse is given by

\[ x_i = \tilde{x}_{i - \alpha}. \quad (10.10) \]

Denote by \( R'^{-1}_\alpha(\cdot) \) this inverse mapping.
We reuse this notation in the context of non-binary vectors and let \( c \in \mathbb{F}_{2^m}^d \). Let \( F' \) and \( \tilde{F}' \) be \( 2^m \times d \) matrices. We say that \( \tilde{F}' \) is the “relative matrix” for \( F' \) based on \( c \) if for all \( j \)

\[
\tilde{f}'_{ij} = f'_{(i+c_j)j},
\]

where the sum is in \( \mathbb{F}_{2^m} \). This mapping is denoted by \( \tilde{F}' = R_c'(F') \). Its inverse is given by

\[
f'_{ij} = \tilde{f}'_{(i-c_j)j}.
\]

Denote by \( R_c'^{-1}(\cdot) \) this inverse mapping. Finally, we note that for any vector \( \lambda \) of length \( 2^m \cdot d \), we can think of this vector as \( \lambda = \text{vec}(F) \). Then we let \( \tilde{\lambda} = R_c'(\lambda) \) where \( \tilde{\lambda} := \text{vec}(\tilde{F}) \) and \( \tilde{F} := R_c'(F) \).

**Lemma 50** The relative operation is linear. That is,

\[
R'(\alpha x + \phi y) = \phi R'(x) + \phi y R'(y).
\]

Further, it is norm preserving. That is, \( \|R'(x)\| = \|x\| \).

**Proof** The linearity is easy to verify. We note that \( R'_c(\cdot) \) permutes the input vector based on \( \alpha \), therefore the norm is preserved.

**Lemma 51** Let \( y \in \Sigma \). Then \( L'(y) = R'_\beta(L'(\tau_\beta(y))) \).

**Proof** For all \( j = 0, \ldots, 2^m - 1 \), \( P(y|j) = P(\tau_\beta(y)|j - \beta) \). Therefore

\[
\log \left( \frac{1}{P(y|j)} \right) = \log \left( \frac{1}{P(\tau_\beta(y)|j - \beta)} \right).
\]

This means that

\[
L'(y)_j = L'(\tau_\beta(y))_{j-\beta},
\]

satisfying the definition of relative vector in (10.10). Therefore \( L'(\tau_\beta(y))) = R'_\beta(L'(y)) \).

**Lemma 52** Let \( c \) be a valid SPC for length-\( d \) check \( h \). Then \( F' \in U' \) if and only if \( R_c'(F') \in U' \).

**Proof** We can use the same logic in the proof of Lemma 47. Thus we omit the details.

**Lemma 53** Suppose a convex set \( C \) is such that \( x \in C \) if and only if \( R'_\alpha(x) \in C \) for some \( \alpha \), then \( \Pi_C(R'_\alpha(v)) = R'_\alpha(\Pi_C(v)) \).

**Proof** We prove by contradiction. Suppose that the projection of \( R'_\alpha(v) \) onto \( C \) is \( u \neq R'_\alpha(\Pi_C(v)) \). Then \( R'^{-1}_\alpha(u) \in C \) and \( R'^{-1}_\alpha(u) \neq \Pi_C(v) \). Due to Lemma 50, we have

\[
\|R'^{-1}_\alpha(u) - v\|_2 = \|u - R'_\alpha(v)\|_2 < \|R'_\alpha(\Pi_C(v)) - R'_\alpha(v)\|_2 = \|\Pi_C(v) - v\|_2.
\]

This contradicts with the fact that \( \Pi_C(v) \) is the projection of \( v \) onto \( C \).
Lemma 54 In Algorithm 2, let \( x^{(\delta)}, z_j^{(\delta)} \) and \( \lambda_j^{(\delta)} \) be the vectors after the \( \delta \)-th iteration when decoding \( y \). Let \( x_0^{(\delta)}, z_j^{0,(\delta)} \) and \( \lambda_j^{0,(\delta)} \) be the vectors after the \( \delta \)-th iteration when decoding \( y^0 \).
If \( x_0^{(\delta)} = R'_c(x^{(\delta)}) \), \( z_j^{0,(\delta)} = R'_c(z_j^{(\delta)}) \) and \( \lambda_j^{0,(\delta)} = R'_c(\lambda_j^{(\delta)}) \) then \( x_0^{(\delta+1)} = R'_c(x^{(\delta+1)}) \), \( z_j^{0,(\delta+1)} = R'_c(z_j^{(\delta+1)}) \) and \( \lambda_j^{0,(\delta+1)} = R'_c(\lambda_j^{(\delta+1)}) \).

**Proof** We drop the iterate \((\delta)\) for simplicity and denote \( x^{\text{new}}, z_j^{\text{new}} \) and \( \lambda_j^{\text{new}} \) to be the updated vector at \((\delta+1)\)-th iteration. Let \( x^0 = R'_c(x) \), \( z_j^0 = R'_c(z_j) \) and \( \lambda_j^0 = R'_c(\lambda_j) \). Also let \( \gamma_j^0 \) be the log-likelihood ratio for the received vector \( y^0 \). From Algorithm 2,

\[
x_0^{\text{new},i} = \Pi_{S_i} \left[ \frac{1}{d_i} \left( \sum_{j \in \mathcal{N}_c(i)} \left( z_j^{0,(i)} - \frac{\lambda_j^{0,(i)}}{\mu} \right) - \frac{\gamma_j^0}{\mu} - \frac{2\alpha r}{\mu} \right) \right]. \tag{10.14}
\]

By Lemma 51, \( \gamma_j^0 = R'_{c'}(\gamma_i) \). Then (10.14) can be rewritten as

\[
x_0^{\text{new},i} = \Pi_{S_i} \left[ R'_{c'}(u) \right],
\]

where

\[
u = \frac{1}{d_i} \left( \sum_{j \in \mathcal{N}_c(i)} \left( z_j^{(i)} - \frac{\lambda_j^{(i)}}{\mu} \right) - \frac{\gamma_j^{(i)}}{\mu} - \frac{2\alpha r}{\mu} \right).
\]

By Lemma 53,

\[
x_0^{\text{new},i} = R'_{c'}(x^{(\delta)}).
\]

Let \( v_j = P_j x + \lambda_j / \mu \) and \( v_j^0 = P_j x^0 + \lambda_j^0 / \mu \), then \( v_j^{0,(i)} = R'_{c'} (v_j^{(i)}) \). In addition, \( z_j^{\text{new},i} = \Pi_{\mathcal{U}} (v_j^0) \) and \( z_j^{\text{new},i} = \Pi_{\mathcal{U}_c} (v_j) \). By Lemma 53, \( z_j^{0,(i)} = R'_{c'} (z_j^{\text{new},i}) \).

It remains to verify one more equality:

\[
\lambda_j^{0,(i)} = \lambda_j^{0,(i)} + \mu \left( (P_j x^{0,(i)} - z_j^{0,(i)}) \right) = R'_{c'} (\lambda_j^{(i)}) + \mu \left( (P_j R'_{c'}(x^{\text{new},i}) - R'_{c'}(z_j^{\text{new},i})) \right) = R'_{c'} (\lambda_j^{\text{new},(i)}).
\]

Lemma 55 Let \( \hat{x} = \mathcal{D}(y) \) be the output of the decoder if \( y \) is received and \( \hat{x}^0 = \mathcal{D}(y^0) \). Then \( \hat{x}^0 = R'_{c'}(\hat{x}) \).

**Proof** We note that since we initialize Algorithm 2 such that the conditions in Lemma 54 is satisfied. By induction on Lemma 54, we always obtain relative vectors at each iteration. It is easy to verify that both decoding processes stop at the same iteration. Therefore \( \hat{x}^0 = R'_{c'}(\hat{x}) \).

Due to Lemma 55, if the decoder recovers a codeword \( c \) from \( y \), it means that the decoded vector \( x \) is the embedding of \( c \) in the sense of Definition 4. Therefore by (10.9), \( R'_{c'}(\hat{x}) \) is the embedding of \( 0 \) in the sense of Definition 4. This means that the decoder can recover \( 0 \) from \( y^0 \). One the other hand, if the decoder fails to recover codeword \( c \) from \( y \), then \( R'_{c'}(\hat{x}) \) is not an integral vector. This means that the decoder cannot recover \( 0 \) from \( y^0 \). Combining both arguments, we deduce that the decoder can recover \( c \) from \( y \) if and only if it can recover \( 0 \) from \( y^0 \), which completes the proof.
11 Linear time $x$-update algorithm

First note that

$$Z_{j,k}^T Z_{j,k} = P_j^T D_j^T T_k^T T_k D_j P_j.$$  

Therefore,

$$Z = \sum_{j,k} Z_{j,k}^T Z_{j,k} = \sum_{j,k} P_j^T D_j^T T_k^T T_k D_j P_j = \sum_j P_j^T D_j^T \left( \sum_k T_k^T T_k \right) D_j P_j.$$  

Let $T = \sum_k T_k^T T_k$. Then

$$Z = \sum_j P_j^T D_j^T T D_j P_j.$$  

We first prove the following Lemma that is useful in studying the structure of $T$.

**Lemma 56** For an integer $n \geq 2$, let $\tilde{K}_n = \{ K | K \subset [n] \text{ and } K \neq \emptyset \}$. Let $u, v$ be arbitrary binary vectors of length $n$ not equal to $0$. Then

1. The number of sets $K \in \tilde{K}_n$ such that $\sum_{i \in K} v_i = 1$ in $\mathbb{F}_2$ is $2^n - 1$.

2. If $v \neq u$, the number of sets $K \in \tilde{K}_n$ such that $\sum_{i \in K} u_i = 1$ and $\sum_{i \in K} v_i = 1$ in $\mathbb{F}_2$ is $2^n - 2$.

**Proof** We prove both parts of the lemma using inductions.

1. **Prove by induction:** When $n = 2$, the statement holds. Assume that the statement holds for $n = l$. Denote by $\tilde{K}_{l,0}(v)$ (resp. $\tilde{K}_{l,1}(v)$) the set of $K \in \tilde{K}_l$ such that $\sum_{i \in K} v_i = 0$ (resp. $\sum_{i \in K} v_i = 1$). This implies

$$|\tilde{K}_{l,0}(v)| = |\tilde{K}_{l,0}(v)| = 2^{l-1}.$$  

When $n = l + 1$, we need to consider binary vectors of length $n + 1$. We use $v$ to denote the first $n$ bits of the vector. Let $x \in \{0, 1\}$ be the $n + 1$-th bit of the vector. If $x = 0$, then $\tilde{K}_{l+1,0}((v, 0))$ contains the following sets: (a) all sets $K \in \tilde{K}_{l,0}(v)$ and (b) all sets $K \cup \{ l + 1 \}$ where $K \in \tilde{K}_{l,0}(v)$. Therefore $|\tilde{K}_{l+1,0}((v, 0))| = 2^l$. Since $\tilde{K}_{l+1,1}((v, 0))$ is the complement of $\tilde{K}_{l+1,0}((v, 0))$, $|\tilde{K}_{l+1,1}((v, 0))| = 2^l$. If $x = 1$, then $\tilde{K}_{l+1,0}((v, 1))$ contains the following sets: (a) all sets $K \in \tilde{K}_{l,0}(v)$ and (b) all sets $K \cup \{ l + 1 \}$ where $K \in \tilde{K}_{l,1}(v)$. Therefore, we get the same result, i.e.

$$|\tilde{K}_{l+1,0}((v, 1))| = |\tilde{K}_{l+1,1}((v, 1))| = 2^l.$$  

Combining the two cases, we conclude that

$$|\tilde{K}_{l+1,0}((v, 1))| = 2^l,$$

which completes the proof.
Let $b$ be an $n \times n$ permutation matrix. Let $T$ be an $n \times n$ matrix whose entries are $t_{ii} = a$ for all $i \in [n]$ and some constant $a$, $t_{ij} = b$ for all $i \in [n]$, $j \in [n]$ and $i \neq j$, and some constant $b$. Then, $D^T T D = T$.

**Proof** Without loss of generality, let $t_{11} = a$ and $t_{12} = b$. Let $X = D^T T D$, we need to prove $x_{ii} = a$ for all $i$ and $x_{ij} = b$ for all $i \neq j$.

Let $Y = D^T T$, then $X = Y D$. Therefore, $x_{ij} = \sum_{k=1}^{n} y_{ik} d_{kj}$. Since $D$ is a permutation matrix, there is one 1 in the $j$-th column. Without loss of generality assume $d_{aj} = 1$. Then $x_{ij} = y_{ia}$. Further, $y_{ia} = \sum_{k=1}^{n} d_{ik} t_{ka}$. If $i \neq j$, then there exists a $\beta \neq \alpha$ such that $\sum_{k=1}^{n} d_{ik} t_{ka} = t_{ja} = t_{12}$.

Thus $x_{ij} = t_{12} = b$. If $i = j$, note that $d_{ai} = 1$ and $d_{ki} = 0$ for all $k \neq i$. Therefore $y_{ia} = \sum_{k=1}^{n} d_{ik} t_{ka} = t_{i\alpha} = t_{11} = a$.

By Lemma 58 we conclude that $D_j^T T D_j = T$ for all $D_j$. As a result,

$$Z = \sum_{j} P_j^T D_j^T T D_j P_j$$

$$= \sum_{j} P_j^T T P_j$$

$$= \text{diag}(d_v T, d_v T, \ldots, d_v T),$$

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where $d_v$ is the variable degree, i.e, $d_v = |\mathcal{N}_v(i)|$. Then,

$$Z + I = \text{diag}(d_vT + I, d_vT + I, \ldots, d_vT + I).$$

Since $d_vT + I = (d_vT + I)^T$ and its entries only have two values, we can calculate its inverse explicitly. Namely,

**Lemma 59** Let $T$ be a $(2^m-1)d \times (2^m-1)d$ block diagonal matrix denoted by $\text{diag}(\Phi, \Phi, \ldots, \Phi)$, where each $\Phi$ is the same $(2^m-1) \times (2^m-1)$ matrix in (11.1). Then $(Z + I)^{-1} = \text{diag}((d_vT + I)^{-1}, (d_vT + I)^{-1}, \ldots, (d_vT + I)^{-1})$ and

$$(d_vT + I)^{-1} = \begin{pmatrix}
  a & b & \ldots & b \\
  b & a & \ldots & b \\
  \ldots & \ldots & \ldots & \ldots \\
  b & b & \ldots & a
\end{pmatrix},$$

where $a = \frac{1}{r-s} + b$, $b = \frac{r+1}{(r-s)(r-1)/2}$, $r = 2^m d_v/2 + 1$ and $s = 2^m d_v/4$.

**Proof** It is easy to verify that the product of the two matrices is the identity matrix.

To summarize, $(Z + I)^{-1}t$ can be obtained by calculating each block multiplication due to the fact that $(Z + I)^{-1}$ is a block diagonal matrix. For each block we first calculate $b\|t_i\|_1$, where $t_i$ is the $i$-th block of vector $t$. We then calculate $(a - b)t_i + b\|t_i\|_1$. This algorithm for $x$-update has complexity $O(qN)$.

12 Results and a conjecture for $\mathbb{F}_2$

**Lemma 60** In $\mathbb{F}_2$, $F$ is a valid embedded matrix for the all-ones check if and only if $F$ satisfies the first two conditions of Definition 7 and the rows of $F$ either all have odd parity or all have even parity.

**Proof** Let $\mathcal{E}^1$ be the set of binary matrices satisfying conditions in this lemma. First, we show $F \in \mathcal{E}^1$ implies $F \in \mathcal{E}$, where $\mathcal{E}$ is defined in Definition 7. Since the field under consideration is $\mathbb{F}_2$, $F$ have three rows. Denote by $f_i^R$ the $i$-th row of $F$. Then $g^1 = f_1^R + f_3^R$ and $g^2 = f_2^R + f_3^R$. If $F \in \mathcal{E}^1$,

$$\sum_{j=1}^{d_e} g^1_j = \sum_{j=1}^{d_e} (f_{1j} + f_{3j}) = \sum_{j=1}^{d_e} f_{1j} + \sum_{j=1}^{d_e} f_{3j}.$$ 

If $\sum_{j=1}^{d_e} f_{1j} = 1$, then $\sum_{j=1}^{d_e} f_{3j} = 1$ by the definition of $\mathcal{E}^1$. Then their sum is 0. If the both have even parity, then sum is also 0. It is easy to verify that the same thing is true for $g^2$. Thus $F \in \mathcal{E}$.

Now we show that $F \in \mathcal{E}$ implies $F \in \mathcal{E}^1$. Let $F \in \mathcal{E}$, we need to show that if $f_1$ has odd (even) parities, $f_2$ and $f_3$ must also have odd (even) parities. This can be proved by contradiction. Suppose $f_1$ has odd parity and $f_2$ (or $f_3$) has even parity, then $g^1 = f_1 + f_3$ (or $g^2 = f_2 + f_3$)
does not have even parity, contradicting with $F \in \mathcal{E}$. Similarly, if $f_1$ has even parity, the other two rows have to have even parity.

In $\mathbb{F}_{2^2}$, considering the all-ones check, the polytope in Definition 15 can be simplified to the following conditions:

**Definition 61** Denote by $\mathbb{U}_4$ the relaxed code polytope for $\mathbb{F}_{2^2}$ for the all-ones checks where any $3 \times d_c$ matrix $F \in \mathbb{U}_4$ if the following constraints hold:

1. $f_{ij} \in [0, 1]$.
2. $\sum_{i=1}^{3} f_{ij} \leq 1$.
3. Let $f_i$ be the $i$-th row of $F$. Let $g^1 = f_1 + f_2$, $g^2 = f_1 + f_3$ and $g^3 = f_2 + f_3$. Then $g^1 \in \mathbb{P}_{d_c}$, $g^2 \in \mathbb{P}_{d_c}$ and $g^3 \in \mathbb{P}_{d_c}$.

**Conjecture 62** Let $\mathcal{E}$ be defined by Lemma 12 for $\mathbb{F}_{2^2}$ and the all-ones check of length $d$. Let $\mathbb{U}_4$ be defined by Definition 61. Then $\mathbb{U}_4 = \text{conv}(\mathcal{E})$.

We validate this result numerically using the following approach. We randomly generate $10^6$ vectors of length $d$ where the entries of each vector are iid uniformly distributed in $[0, 4]$. We then project these vectors onto both $\mathbb{U}_4$ and $\text{conv}(\mathcal{E})$ using CVX (cf. [28]). What we observe is that the projections onto these two polytopes are the same for every vector. Therefore we believe that the conjecture should hold. However, a proof of the conjecture remains open.

### 13 Calculating $E_s/N_0$

In QPSK, the coded symbols are $0, 1, \xi^1, \xi^2$. We use vectors $(1, 0)$, $(0, 1)$, $(-1, 0)$ and $(0, -1)$ to represent the modulated signals. Let $x$ be the modulated vector of length $2N$, where $N$ is the block length. Then the received signal is $y = x + n$. Where $n$ is iid Gaussian of variance $\sigma^2$.

Let $N_0$ be the noise spectrum density, then the noise is equivalent to variance of $\sigma^2 = N_0/2$ per dimension. Let $R$ be the symbol rate and fix $E_s = 1/R$. Let $\gamma = 10^{E_s/N_0}$ be the (non dB) value for $E_s/N_0$, then

$$\sigma^2 = \frac{1}{2\gamma R},$$

Suppose we use the length-80 code\(^{11}\) defined in [15], the block length is 80 and rate is $R = 0.6$. Then $E_s/N_0 = 4\text{dB}$ translates into $\sigma = \sqrt{\frac{1}{2 \times 10^{4\text{dB}} \times 0.6}} = 0.5760$.

### References

[1] J. Feldman, M. Wainwright, and D. Karger, “Using linear programming to decode binary linear codes,” *IEEE Trans. Inf. Theory*, vol. 51, no. 3, pp. 954–972, Mar. 2005.

[2] J. Feldman, T. Malkin, R. A. Servedio, C. Stein, and M. J. Wainwright, “LP decoding corrects a constant fraction of errors,” *IEEE Trans. Inf. Theory*, vol. 53, no. 1, pp. 82–89, Jan. 2007.

\(^{11}\)The parity-check matrix of this code is denoted by $\mathcal{H}^{(2)}$ in [15].
[3] C. Daskalakis, A. Dimakis, R. Karp, and M. Wainwright, “Probabilistic analysis of linear programming decoding,” IEEE Trans. Inf. Theory, vol. 54, no. 8, pp. 3565–3578, Aug. 2008.

[4] S. Arora, C. Daskalakis, and D. Steurer, “Message-passing algorithms and improved LP decoding,” IEEE Trans. Inf. Theory, vol. 58, no. 12, pp. 7260–7271, Dec. 2012.

[5] N. Halabi and G. Even, “LP decoding of regular LDPC codes in memoryless channels,” IEEE Trans. Inf. Theory, vol. 57, no. 2, pp. 887–897, Feb. 2011.

[6] L. Bazzi, B. Ghazi, and R. Urbanke, “Linear programming decoding of spatially coupled codes,” IEEE Trans. Inf. Theory, vol. 60, no. 8, pp. 4677–4698, Aug. 2014.

[7] P. O. Vontobel and R. Koetter, “Towards low complexity linear programming decoding,” in Proc. Int. Symp. Turbo Codes and Related Topics, Munich, Germany, Apr. 2006.

[8] M.-H. Taghavi and P. Siegel, “Adaptive methods for linear programming decoding,” IEEE Trans. Inf. Theory, vol. 54, no. 12, pp. 5396–5410, Dec. 2008.

[9] D. Burshtein, “Iterative approximate linear programming decoding of LDPC codes with linear complexity,” IEEE Trans. Inf. Theory, vol. 55, no. 11, pp. 4835–4859, Nov. 2009.

[10] M. Taghavi, A. Shokrollahi, and P. Siegel, “Efficient implementation of linear programming decoding,” IEEE Trans. Inf. Theory, vol. 57, no. 9, pp. 5960–5982, Sept. 2011.

[11] S. Barman, X. Liu, S. Draper, and B. Recht, “Decomposition methods for large scale LP decoding,” IEEE Trans. Inf. Theory, vol. 59, no. 12, pp. 7870–7886, Dec. 2013.

[12] X. Zhang and P. H. Siegel, “Efficient iterative LP decoding of LDPC codes with alternating direction method of multipliers,” in IEEE Int. Symp. Inf. Theory (ISIT), July 2013, pp. 1501–1505.

[13] G. Zhang, R. Heusdens, and W. Kleijn, “Large scale LP decoding with low complexity,” Communications Letters, IEEE, vol. 17, no. 11, pp. 2152–2155, Nov. 2013.

[14] X. Liu and S. C. Draper, “The ADMM penalized decoder for LDPC codes,” submitted to IEEE Trans. Inf. Theory, 2014.

[15] M. Flanagan, V. Skachek, E. Byrne, and M. Greferath, “Linear-programming decoding of nonbinary linear codes,” IEEE Trans. Inf. Theory, vol. 55, no. 9, pp. 4134–4154, Sept. 2009.

[16] D. Declercq and M. Fossorier, “Decoding algorithms for nonbinary LDPC codes over GF(q),” IEEE Trans. Commun., vol. 55, no. 4, pp. 633–643, Apr. 2007.

[17] D. Goldin and D. Burshtein, “Iterative linear programming decoding of nonbinary LDPC codes with linear complexity,” IEEE Trans. Inf. Theory, vol. 59, no. 1, pp. 282–300, Jan. 2013.

[18] M. Punekar, P. Vontobel, and M. Flanagan, “Low-complexity LP decoding of nonbinary linear codes,” IEEE Trans. Commun., vol. 61, no. 8, pp. 3073–3085, Aug. 2013.

[19] J. Honda and H. Yamamoto, “Fast linear-programming decoding of LDPC codes over GF(2^m),” in Int. Symp. Inf. Theory and its Applications (ISITA), 2012, pp. 754–758.
[20] B. Touri, “Some new results on linear programming decoding,” Master’s thesis, Jacobs University Bremen, Bremen, Germany, 2008.

[21] X. Zhang and P. Siegel, “Adaptive cut generation algorithm for improved linear programming decoding of binary linear codes,” *IEEE Trans. Inf. Theory*, vol. 58, no. 10, pp. 6581–6594, Oct. 2012.

[22] A. W. Marshall, I. Olkin, and B. C. Arnold, *Inequalities: Theory of Majorization and Its Applications*. Springer, 2009.

[23] X. Liu and S. C. Draper, “ADMM decoding of non-binary LDPC codes in \( \mathbb{F}_{2^m} \),” in *IEEE Int. Symp. Inf. Theory (ISIT)*. IEEE, 2014.

[24] J. Duchi, S. Shalev-Shwartz, Y. Singer, and T. Chandra, “Efficient projections onto the \( \ell_1 \)-ball for learning in high dimensions,” in *Proceedings of the 25th Int. Conf. on Machine Learning*. ACM, 2008, pp. 272–279.

[25] S. Boyd, N. Parikh, E. Chu, B. Peleato, and J. Eckstein, “Distributed optimization and statistical learning via the alternating direction method of multipliers,” *Machine Learning*, vol. 3, no. 1, pp. 1–123, 2010.

[26] D. J. C. MacKay, “Encyclopedia of sparse graph codes,” available online at http://www.inference.phy.cam.ac.uk/mackay/codes/data.html.

[27] R. M. Tanner, D. Sridhara, and T. Fuja, “A class of group-structured LDPC codes,” in *ICSTA*, Ambleside, UK, 2001.

[28] M. Grant and S. Boyd, “CVX: Matlab software for disciplined convex programming, version 2.1,” http://cvxr.com/cvx, Mar. 2014.