Magnetic-free non-reciprocity based on staggered commutation

Negar Reiskarimian1 & Harish Krishnaswamy1

Lorentz reciprocity is a fundamental characteristic of the vast majority of electronic and photonic structures. However, non-reciprocal components such as isolators, circulators and gyrators enable new applications ranging from radio frequencies to optical frequencies, including full-duplex wireless communication and on-chip all-optical information processing. Such components today dominantly rely on the phenomenon of Faraday rotation in magneto-optic materials. However, they are typically bulky, expensive and not suitable for insertion in a conventional integrated circuit. Here we demonstrate magnetic-free linear passive non-reciprocity based on the concept of staggered commutation. Commutation is a form of parametric modulation with very high modulation ratio. We observe that staggered commutation enables time-reversal symmetry breaking within very small dimensions (\(\lambda/1,250 \times \lambda/1,250\) in our device), resulting in a miniature radio-frequency circulator that exhibits reduced implementation complexity, very low loss, strong non-reciprocity, significantly enhanced linearity and real-time reconfigurability, and is integrated in a conventional complementary metal-oxide-semiconductor integrated circuit for the first time.
Reciprocity in electronics or, equivalently, the principle of time reversibility in optics is a fundamental property of any linear system or material described by symmetric and time-independent permittivity and permeability tensors. Non-reciprocity, however, enables new applications that span radio frequencies (RF) to optical frequencies. Optical isolators are critical to on-chip all-optical information processing systems for the protection of lasers and amplifiers, and the mitigation of multipath reflections. RF circulators enable full-duplex wireless, an emerging wireless communication paradigm that has been historically considered impractical, where the transmitter and the receiver operate simultaneously on the same frequency band, potentially doubling network capacity at the physical layer while offering numerous other benefits at the network layer.

Non-reciprocal components today are almost exclusively realized through the magneto-optic Faraday effect (Fig. 1a–c)—the application of a magnetic field bias parallel to the direction of propagation rotates the polarization vector of light due to the different propagation velocities of left- and right-circularly polarized waves. Despite significant research efforts in the optical and RF domains, non-reciprocal components based on magneto-optic materials remain incompatible with complementary metal–oxide–semiconductor (CMOS) integrated circuit (IC) fabrication processes due to material incompatibilities and the need for a magnetic field bias, significantly restricting their impact.

As early as 50 years ago, magnetic-free non-reciprocity and circulators had been investigated at microwave frequencies through the use of the inherent non-reciprocal nature of active devices such as direct-current/voltage-biased transistors. More recently, metamaterials with embedded active transistors have been explored at RF and microwave frequencies. However, such approaches are fundamentally limited by the noise and nonlinear distortion generated by the active transistors. Indeed, for non-reciprocal components used at the front end of RF communication systems (such as circulators), and for front-end components more broadly (such as filters and diplexers), passive approaches with superior linearity and noise performance are paramount. Techniques for non-reciprocity leveraging nonlinearity have also been proposed. These techniques, although potentially useful and extensively explored in optical applications, exhibit non-reciprocity over certain signal power levels only and have limited applicability to scenarios where linearity to the signal is required, such as RF communication. Lorentz reciprocity may also be broken by introducing time dependency into the material or system. Techniques at optical frequencies based on electro-optic phase modulators have been investigated but require complex electro-optic modulation networks. Approaches based on spatio-temporal parametric modulation of waveguides have been investigated in. In such approaches, a travelling-wave modulation of the waveguide’s properties produces direction-dependent mode conversion of the desired signal. In the optical domain, the size of the structure is limited by the weak electro-optic or acousto-optic effect which results in an extremely low modulation ratio (modulation ratio is defined as the ratio between the maximum and minimum values of the modulated parameter and extremely weak modulation corresponds to a modulation ratio of practically unity). At RF, varactors are able to achieve modulation ratios of around two to four, resulting in structures that are of the order of a wavelength. A second disadvantage of these approaches is that the mode/frequency conversion is undesirable in some applications and necessitates the use of filters or diplexers. In recent times, non-reciprocity through spatio-temporal parametric modulation of resonant rings, resulting in angular momentum biasing, has been demonstrated in the acoustic domain and at

**Figure 1** | Comparison between non-reciprocity induced by the magneto-optic Faraday effect and staggered commutation. (a) A wave propagating in a Faraday-active magneto-optic material experiences no Faraday rotation in the absence of magnetic bias. (b) In the presence of magnetic bias in the positive z direction, a wave travelling in the positive z direction experiences Faraday rotation due to the difference in the propagation velocities of right-handed and left-handed circularly polarized waves, whereas (c) a wave travelling in the negative z direction experiences an opposite rotation. (d) A wave propagating through a commutated network with no staggering experiences no phase shift. (e) Staggered commutation acts as a bias that breaks time reversibility, producing a phase shift for waves travelling from left to right and (f) an opposite phase shift for waves travelling from right to left.
RF\(^{31}\), resulting in magnetic-free non-reciprocal circulators at or well below the wavelength scale and with low loss\(^{32}\). A key challenge with these spatio-temporal parametric modulation approaches in general is that the property that enables modulation (for instance, varactors\(^{27,31}\) or opto-acoustic interactions\(^{25}\)) often represents a non-linearity to the signal itself, in particular when the modulation ratio is high, resulting in nonlinear distortion at higher signal levels. There has also been theoretical work on non-reciprocity and a microwave circulator based on parametric modulation of coupled resonators\(^{33}\).

Here we introduce magnetic-free, linear and passive phase non-reciprocity based on staggered commutation and a highly-miniaturized RF circulator that embeds the phase-nonreciprocal component within a ring resonator. Commutation may be seen as a form of parametric modulation with very high modulation ratio (practically infinite in our prototype), making the phase-nonreciprocal component effectively a point parametric modulator. This enables its specific placement in the ring relative to the three circulator ports such that the signal across it is suppressed for excitations from one port, providing significantly enhanced linearity to that port. Furthermore, the need for spatio-temporal modulation in the ring is eliminated, easing modulation complexity. Implementation complexity is further eased as no frequency conversion is seen at the circulator ports. In addition, when compared with prior art employing spatio-temporal parametric modulation of waveguide\(^{-24-27}\) or electro-optic phase modulation\(^{22,23}\), these concepts allow miniaturization of the unmodulated ring to deeply subwavelength scales (\(\approx 2/80\) in our prototype). When compared with prior art employing angular-momentum biasing in parametrically modulated resonant rings\(^{31,32}\), linearity enhancement is realized due to the ability to suppress the signal across the point parametric modulator, although it is interesting to consider synergies between the approaches offering the dual benefits of both.

**Results**

Phase non-reciprocity through staggered commutation. Commutated networks are a class of linear, periodically time-varying (LPTV) networks where the signal is periodically commutated through a bank of linear, time-invariant (LTI) networks (or media as in Fig. 1d). The first commutated networks relied on mechanical commutation through a rotating brush that periodically contacted a bank of capacitors\(^{34,35}\) to realize narrow comb filters around harmonics of the commutation frequency. More recently, electronic commutation using passive transistor-based switches has resulted in high quality-factor (Q) comb filters, commonly called N-path filters, which operate at RF, exhibit significantly lower noise and higher linearity when compared with active RF filters, are compatible with conventional CMOS IC technology and exhibit the potential to replace front-end off-chip surface acoustic wave filters for RF communication applications\(^{3,36-40}\). A key requirement is the availability of a high-quality switch with high ON/OFF transmission ratio (or modulation ratio when viewed as parametric modulation) and sufficient switching speed. Modern CMOS transistor switches boast ON/OFF conductance ratios as high as 1,000–100,000 (ref. \(41\)), enough to be practically infinite from the perspective of commutated network operation, and switching speeds that enable such commutated networks to operate well into the RF frequency range and continuously improve with CMOS technology scaling. It should be emphasized that the transistors are used as reciprocal, highly linear, passive switches without direct-current bias. In other words, the switches cannot provide power gain to the desired signal by sourcing power from the modulating clock signals that control the commutation. These clocking signals that control the commutation are easily implemented in CMOS and the associated power consumption is due to the charging and discharging of parasitic capacitance in the clock path. This power consumption also reduces with CMOS technology scaling due to reducing parasitics.

An interesting property of commutated networks that we observe here is that staggering the commutation on either side of the bank of LTI networks results in time-reversal symmetry breaking and phase non-reciprocity (Fig. 1e,f). Waves travelling in the forward and reverse direction see a different ordering of the first and second commutating switches in time and experience opposite shifts in phase, essentially analogous to the magnetooptic Faraday effect. Here, the staggered commutation plays the role of the magnetic Faraday bias.

When the first and second set of switches are staggered by \(+90^\circ\), forward and reverse travelling waves at or near the commutation frequency experience phase shifts of \(+90^\circ\) and \(-90^\circ\), respectively. When a transmission line or waveguide of length \(3\pi/4\) is wrapped around the staggered commutated network, non-reciprocal wave propagation is achieved as waves may propagate in only one direction (Fig. 2a,b). In that direction, the \(-270^\circ\) phase delay through the \(3\pi/4\) ring adds with \(-90^\circ\) phase shift of the staggered commutated network to satisfy the boundary condition, enabling wave propagation. In the other direction, the \(-270^\circ\) phase delay adds with the \(+90^\circ\) phase shift of the staggered commutated network to prohibit wave propagation.

Unidirectional wave propagation not only requires phase non-reciprocity but also requires (near-)perfect transmission through the commutated network. In Fig. 3, we examine the requirements on the media across which commutation is being performed. We consider electrically short transmission-line media, in line with our goal of achieving a point parametric modulator. In the depicted simulations, eight-way commutation is considered, the transmission-line characteristic impedance (\(Z_{\text{medium}}\)), wave velocity (\(v\)) and length (\(l\)) are varied, and ideal sets of switches are
used for commutation at 750 MHz frequency, with each switch active for 12.5% of the time period. With +90° staggering, phase non-reciprocity is always observed (phase of the scattering or \(S\)-parameters \(S_{21}\) and \(S_{12}\) at the commutation frequency always have a 180° difference). The magnitudes of \(S_{21}\) and \(S_{12}\) at the commutation frequency are always reciprocal; however, low \(Z_{\text{medium}}\), low velocity \(v\) and higher lengths \(l\) are required for significant signal transmission. In other words, the media must have a significant capacitance, similar to the (reciprocal) comb filter implementations described earlier, which use commutation across capacitor banks without staggering.

The need for capacitance may be intuitively understood by the fact with +90° staggering, no direct connection exists between the input and output at any instant assuming at least four paths. Therefore, a certain amount of capacitance is required for the media to store sufficient energy for subsequent transmission. A formal analysis of a commutated network with transmission-line media based on LPTV network theory is challenging. In Supplementary Note 1, with the aid of Supplementary Figs 1 and 2, we have completed an analysis of a staggered commutated network with \(N\) capacitors and ideal switches. As electrically short transmission lines are being considered, the approximation with capacitors is expected to be accurate. Figure 3 also depicts the magnitudes of \(S_{21}\) and \(S_{12}\) at the commutation frequency based on the exact analytical solution in Supplementary Note 1 for the case of \(l = 80\mu\text{m}\) and an excellent agreement is seen to the simulations.

The analytical formulation quantifies the capacitance condition for substantial transmission as \(C >> \frac{1}{2\pi f_s Z_0}\) where \(f_s\) is the commutation frequency and \(Z_0\) is the reference impedance. Under this condition, at \(f_s\) for +90° staggering, the \(S\)-parameters of the staggered commutated network reduce to

\[
S(f_s) \approx \begin{bmatrix}
\frac{N^2(1 - \cos(\phi))}{N^2(1 - \cos(\phi)) + N^2(1 - \cos(\phi)) + e^{-j\phi/2}} - 1 & N^2(1 - \cos(\phi)) \\
N^2(1 - \cos(\phi)) e^{j\phi/2} & N^2(1 - \cos(\phi)) - 1
\end{bmatrix} \approx \left[\begin{array}{cc}
0 & e^{-j\phi/2} \\
e^{j\phi/2} & 0
\end{array}\right].
\]

Figure 3 | Signal transmission in a staggered commutated network. Simulated \(S\)-parameters of an eight-way staggered commutated network are depicted assuming electrically short transmission line media of varied length \(l\), characteristic impedance \(Z_{\text{medium}}\) and velocity \(v\). Ideal switches are commutated at 750 MHz, with each switch active for 12.5% of the time period, and the reference impedance is assumed to be 50\(\Omega\). Theoretical calculations based on the analytical formulation presented in Supplementary Note 1, where the electrically short transmission lines are approximated by their capacitance, are also shown and agree very well with simulations. The phases of \(S_{21}\) and \(S_{12}\) are always non-reciprocal and differ by 180° for 90° staggering. The magnitudes are always reciprocal; however, for substantial transmission, high \(l\), low \(Z_{\text{medium}}\) and low \(v\), or equivalently large capacitance, are required.
clearly showing the non-reciprocity in the phase of \( S_{21} \) and \( S_{12} \). The (reciprocal) magnitudes of \( S_{21} \) and \( S_{12} \) approach zero as \( N \to \infty \) but even \( N = 4 \) or \( N = 8 \) are sufficient to achieve low loss, resulting in \(-1.8\) and \(-0.45\) dB transmission, respectively. The magnitudes of \( S_{11} \) and \( S_{22} \) approach zero for \( N \to \infty \), implying perfect impedance matching.

**A frequency up-/down-conversion and filtering-based explanation.**

The behaviour may also be explained by viewing each set of commutating switches as an in-phase and quadrature reciprocal modulator that performs a frequency up-conversion and down-conversion, similar to the approach described in prior literature\(^{33}\). Indeed, such commutating transistor-based switches are regularly used as frequency converters in RF and microwave integrated electronics\(^{40}\). Figure 4 depicts a graphical view of signal propagation through the staggered commutated network in the forward and reverse directions. In each direction, a sinusoidal input signal, \( \cos(\omega_{in} t) \), is assumed at a frequency \( \omega_{in} \) near the commutation frequency \( \omega_c \). Each set of switches is modelled as an in-phase and quadrature reciprocal modulator that multiplies the input signal with cosine and sine versions of the pump signal at \( \omega_s \). The second set of pump signals (2 and 4) are assumed to lead the first set (1 and 3) by \( +90^\circ \), representing the staggering. The capacitances of the media are assumed to effectively form a low-pass filter in conjunction with the source and load impedances. The frequency translation of this low-pass filtering to RF by the commutating switches is the basis for the use of unstaggered commutated networks to realize (reciprocal) comb filters\(^{35,36–40}\). This low-pass filter effect substantially attenuates the up-converted signal at \( \omega_{in} + \omega_{m} \) and \( -\omega_{in} - \omega_{m} \) after the first modulation. It is seen that this leads to signal transmission with non-reciprocal phase response \((+90^\circ / -90^\circ)\) in the forward and reverse directions. It should be noted that although the up-converted frequency components are filtered away, this does not result in any power loss for the desired signal as the commutated media are purely capacitive. Consequently, the staggered commutated network is lossless for \( N \to \infty \), as discussed earlier (for finite \( N \), there is loss due to harmonic conversion, albeit small even for \( N = 4 \) or \( N = 8 \) as discussed earlier). The reader may also verify that in the absence of low-pass filtering, all frequency components at the outputs cancel and the transmission of the structure in both directions is identically zero, which agrees with the theory and simulations in Fig. 3 that show very weak transmission for high \( Z_{medium} \), low \( l \) and high \( v \) (that is, low capacitance in the commutated media), and our expectation given a lack of a direct connection between the input and the output.

A comparison may be made with multi-arm optical isolators based on tandem electro-optic phase modulation\(^{42}\), where back-to-back phase modulation with quarter-wavelength optical

**Figure 4 | A frequency up-/down-conversion and filtering-based explanation of phase non-reciprocity in staggered commutated networks.**

A commutator with at least four paths can be viewed as a reciprocal in-phase and quadrature frequency up-downconverter. The capacitance of the commutated media acts as a low-pass filter that attenuates the up-converted components after the first commutation. As a result, phase non-reciprocity is seen for signals travelling in the forward and reverse directions through a staggered commutated network. It may be verified that in the absence of low-pass filtering, all frequency components at the outputs cancel, leading to zero transmission in either direction, which agrees with the theory and simulations in Fig. 3 that show very weak transmission for high \( Z_{medium} \), low \( l \) and high \( v \) (that is, low capacitance in the commutated media).
delays in between results in non-reciprocity. In our approach, commutation is essentially amplitude modulation with very high modulation ratio. Staggered commutation results in phase non-reciprocity and the filtering effect that is unique to commutation across media with appreciable capacitance eliminates the need for quarter-wavelength delays, allowing the phase non-reciprocity to be achieved within dimensions as small as $\lambda/1,250 \times \lambda/1,250$ in our device.

A highly-miniaturized RF circulator with enhanced linearity. A three-port circulator matched at all ports to a reference impedance of $Z_0$ may be realized by contacting the $3\lambda/4$ transmission line (also of characteristic impedance $Z_0$) at three points as long as the three ports are spaced $\lambda/4$ apart along the ring circumference, as shown in Fig. 5a. To analyse this structure, we consider the case of a voltage excitation $V_{in}$ at port 1. Using conventional microwave circuit analysis techniques along with the S-parameters of the staggered commutated network in equation (1) for the case of $N \to \infty$, the various node voltages can be determined to be

$$V_1 = \frac{1}{2} V_{in}, \quad V_2 = -j \frac{1}{2} V_{in}, \quad V_3 = 0, \quad V_x = \frac{\sin(\beta l)}{2} V_{in}, \quad V_y = \frac{\sin(\beta l)}{2} V_{in}.$$  \hspace{1cm} (2)

where $V_1$, $V_2$ and $V_3$ are the three port voltages, $V_x$ and $V_y$ are the voltages on the left and right sides of the staggered commutated network, $\beta$ is the propagation constant of the $3\lambda/4$ ring and $l$ is the circumferential distance between port 3 and the commutated network. Based on these node voltages and by repeating the calculations with excitations at the other two ports, the S-parameter matrix of the structure can be derived to be:

$$S_{\text{circ}}(f_b) = \begin{bmatrix} 0 & 0 & -1 \\ -j & 0 & 0 \\ 0 & -j & 0 \end{bmatrix}. \hspace{1cm} (3)$$

This matches the S-parameters of an ideal 3-port circulator. It is interesting to note that the S-parameter matrix does not depend on $l$, meaning that the position of the staggered commutated network relative to ports 1 and 3 does not have an impact on $S_{\text{circ}}$. However, the voltages seen at the two ends of the staggered commutated network ($V_x$ and $V_y$) when port 1 is excited are functions of $l$ in equation (2). Interestingly, these voltages become 0 when $l = 0$. This is a direct consequence of the fact that when $l = 0$, $V_x$ coincides with port 3 and naturally remains quiet for excitations at port 1 due to the isolation of the circulator (Fig. 5b). The S-parameters of the staggered commutated network force $V_x$ and $V_y$ to have the same magnitude, making $V_x$ also a quiet node (Fig. 5b). As a result, voltage swings across the point parametric modulator are suppressed, yielding very high linearity to excitations at port 1.

A more comprehensive analysis of the circulator may be performed using the S-parameters of the staggered commutated network in equation (1) for the case of finite $N$. Interestingly, for $l = 0$, we once again arrive at the ideal S-parameter matrix of equation (3) and $V_x = V_y = 0$ for excitations at port 1. This implies that when port 3 coincides with $V_{in}$, the presence of a finite number of commutated paths will not limit circulator performance. In the presence of finite switch ON resistance, however, there will be finite voltage swing across the commutating switches for excitations at port 1, leading to finite linearity. However, this linearity to port 1 excitations will be vastly superior to the linearity to excitations at the other ports that do not enjoy this suppression mechanism.

We constructed a prototype at RF using conventional 65 nm CMOS IC technology to implement the electronic commutation across a bank of $N = 8$ capacitors, and three lumped C-L-C networks to miniaturize the $3\lambda/4$ transmission line ring (Fig. 6). Miniaturization is eased by the fact that the $3\lambda/4$ ring is unmodulated, resulting in an overall structure that has a maximum dimension of $\approx \lambda/80$ at the operating frequency of 750 MHz. The capacitors of the C-L-C networks are also incorporated in the IC, leaving the three inductors as the only off-chip components. The commutated capacitors are chosen to be 26 pF each, roughly six times $\frac{1}{2\pi f_{\text{op}} Z_0}$ ($Z_0 = 50 \Omega$), and are each realized on chip as a pair of $80 \mu m \times 80 \mu m$ metal–insulator–metal capacitors. The total size of the staggered commutated network is $\approx 320 \mu m \times 320 \mu m$ ($\lambda/1,250 \times \lambda/1,250$). The control signals for the staggered commutation are generated using on-chip clock generation circuitry that is described in additional detail in the Methods section.

**Experimental results.** S-parameter measurements of the three-port circulator were performed using a measurement setup described in the Methods section. In the absence of commutation, with a pair of transistor switches on either side of the capacitor bank permanently closed, the circuit is perfectly reciprocal (Fig. 7a,d,g). In this configuration the high reciprocal isolations from port 3 to ports 1 and 2 are seen because port 3 is shunted to ground by one of the 26-pF capacitors. The quarter-wave transmission lines from port 1 to the staggered commutated network and from port 2 to port 3 transform this near-short-circuit impedance to open circuits at ports 1 and 2, effectively disconnecting port 3 from the rest of the circuit and resulting in a reciprocal structure that exhibits low-loss transmission between port 1 and port 2. Similarly, another reciprocal configuration without commutation is depicted, where all switches are permanently open. Here, simple circuit analysis reveals that port 1 is effectively disconnected from the rest of the circuit, which now exhibits low-loss reciprocal transmission between ports 2 and 3.

**Figure 5 | Circulator architecture.** (a) A 3-port circulator may be realized by adding ports at three points along the ring as long as the circumferential spacing between the ports is $\lambda/4$. The S-parameters of such a structure match that of an ideal three-port circulator and are independent of $l$, the circumferential distance between port 3 and the staggered commutated network. (b) However, setting $l$ to 0 suppresses the voltages across the commutated network for excitations at port 1, significantly enhancing the linearity of the structure to port 1 excitations.
Under commutation at a frequency of 750 MHz with staggering for clockwise circulation, strong non-reciprocity is measured (Fig. 7b,c,h) with low-loss transmission in the direction of circulation ($S_{23}$, $S_{32}$ and $S_{33}$ are $-1.7, -1.7$ and $-3.3$ dB, respectively, at 750 MHz) and strong isolation in the reverse direction ($S_{12}$, $S_{23}$ and $S_{31}$ are $-9.6, -10.4$ and $-17.4$ dB, respectively, at 750 MHz). This represents, on an average, an order of magnitude of non-reciprocity between any two ports and is limited by the imperfect impedance matching at the third port (as is the case with all circulators) caused due to parasitics at the chip-package-board interfaces. If the impedance at the third port is slightly tuned, non-reciprocity of 40–50 dB or four to five orders of magnitude is seen in $S_{12}, S_{23}$ and $S_{31}$ at 750 MHz with negligible impact on transmission in the circulation direction. It should be noted that independent tuning is required at the three ports to achieve very high non-reciprocity in all paths, given the inherent asymmetry in the circulator structure. Figure 7c,f,i depict the simulated S-parameters under commutation, demonstrating an excellent match to the measurements.

Although the analysis in this study has restricted itself to response at the commutation frequency, it is noteworthy that a filtering profile is observed in $S_{23}$ and $S_{33}$ across frequency due to the comb-filter functionality inherent to commutation across a capacitor bank. The LPTV analysis in Supplementary Note 1 may be extended to determine the response of the staggered commutated network and the circulator across frequency, and confirms this observed (and simulated) filtering profile.

Another unique feature of the fabricated prototype is its real-time reconfigurability. By changing the staggering between $+90^\circ$ and $-90^\circ$, the direction of circulation can be altered, as is seen in Fig. 8a. The frequency of operation of the circulator can also be tuned by changing the frequency of commutation within the limits dictated by the bandwidth of the $3\lambda/4$ transmission line ring. In Fig. 8b, four to five orders of magnitude (40–50 dB) of non-reciprocity in $S_{31}$ is maintained across 700–800 MHz.

In Fig. 8c, we present experimental evidence of the enhanced linearity to excitations at port 1. Two-tone intermodulation distortion tests were performed on the prototype for transmission from port 1 to port 2, and from port 2 to port 3. The input-referred third-order intercept point (IIP3) for transmission from port 1 to port 2 is $+27.5$ dBm ($\approx 560$ mW), a remarkable number for a 65 nm CMOS IC implementation and nearly two orders of magnitude higher than that from port 2 to port 3 ($+8.7$ dBm or $7.4$ mW) due to the suppression of the signal across the point parametric modulator. Aside from intermodulation distortion caused due to third-order non-linearity, other spurious effects to consider include image signals due to quadrature mismatch and modulation feedthrough. The level of the measured spurious image signals produced due to quadrature mismatch for port 1 to port 2 transmission is $51$ dB below the main signal. This level of image rejection is high enough so that it is not a serious issue when compared with the third-order intermodulation distortion produced for reasonable port 1 power levels. The measured modulation feedthrough at port 2 is $-57$ dBm. Although these levels are already quite low, they can be cancelled further using integrated calibration circuits that are commonly implemented in CMOS RF ICs.

Passive LTI systems have a noise figure (NF) that is equal to their loss. Passive LPTV systems can exhibit noise folding. The measured NF for port 2 to port 3 transmission is $4$ dB, higher than the $1.7$ dB loss. Simulations indicate that $2$ dB degradation arises from modulation path phase noise due to a poor

Figure 6 | RF CMOS IC implementation of the circulator. (a) A simplified circuit diagram of the circulator is shown. Electronic commutation across a bank of $N=8$ capacitors is performed using reciprocal, passive transistor-based switches without direct-current bias. The staggered commutated network enables miniaturization of the unmodulated $3\lambda/4$ ring using three C-L-C sections. (b) The microphotograph of the fabricated IC is shown along with a close-up photograph of the fabricated printed circuit board with the IC housed in a quad-flat no-leads (QFN) package and interfaced with the off-chip inductors. The largest dimension of the prototype is $5$ mm or $\lambda/80$ at the operating frequency of $750$ MHz.
implementation of the modulation path phase shifter. A better implementation of the modulation path phase shifter restores the NF to 2 dB in simulation, close to the 1.7 dB loss level and indicative of only minor NF degradation due to harmonic noise folding.

**Discussion**

The ability to integrate magnetic-free passive linear non-reciprocal components in CMOS has the potential to revolutionize RF communications. The CMOS circulator described in this study exhibits extremely low insertion loss (< 2 dB) and strong non-reciprocity, is compact and features reconfiguration capabilities. Furthermore, the enhanced linearity to port 1 is particularly useful for full-duplex or simultaneous transmit-and-receive communication and radar applications, where a high-power transmitter (port 1) and a highly sensitive receiver (port 3) operate simultaneously at the same frequency, and must be interfaced with a shared antenna (port 2). In such a configuration, \(S_{21}\) (transmitter to antenna loss), \(S_{12}\) (antenna to receiver loss) and \(S_{23}\) (transmitter to receiver isolation) are the most important performance metrics, where our device performs particularly well. Full-duplex communication is drawing significant interest for emerging 5G communication networks due to its potential to double network capacity compared with half-duplex communication at the physical layer while offering numerous other benefits at the network layer. There has been active research on fully integrated CMOS transceiver ICs supporting full duplex. The ability to include the circulator with the transceiver on the same CMOS IC would significantly reduce the cost and form factor, and enhance the performance of full-duplex systems. The filtering profile in \(S_{11}\) is also very useful in protecting the highly sensitive receiver from interference signals outside the frequency band of operation.

For such applications, the ability to reconfigure the device between reciprocal and non-reciprocal operation is also very useful. We demonstrated reconfigurable modes of operation in the absence of commutation where the structure becomes reciprocal with low-loss transmission between the antenna (port 2) and the transmitter (port 1), and the antenna and the receiver (port 3). In other words, the prototype can be reconfigured between operation as a non-reciprocal circulator for full-duplex communication and a reciprocal low-loss transmit/receive switch for half-duplex communication.

Although the design of this prototype is deliberately asymmetric with respect to the three ports to prioritize linearity to port 1 excitations for communication applications, a symmetric design may also be envisioned where a non-reciprocal...
phase component is incorporated in all three arms, that is, between all three ports. Such a symmetric circulator structure would enable extension of the concept to non-reciprocal metamaterials that support topologically protected wave propagation modes.\(^4\)

The bandwidth of the circulator’s isolation is limited by the matching between the frequency responses of the \(3\pi/4\) ring and the staggered commutated network. Currently, the \(-20\) dB isolation bandwidth in \(S_{11}\) after tuning is 32 MHz or 4.3% of the operating frequency, useful for commercial cellular LTE and WiFi applications. Dispersion engineering techniques to match their responses over a wider frequency range, thus enhancing the isolation amount versus bandwidth trade-off, represent an interesting topic for future research.

Commutated networks are known to have response at harmonics of the commutation frequency. Response at harmonics can lead to susceptibility to interference near harmonic frequencies and also leads to noise folding. Although we have seen that the effect of noise folding on the NF is very small in this circulator structure, a formal analysis of the harmonic response and noise of the overall circulator structure, as well as techniques to suppress the same, also represent an interesting topic for future investigation.

It is also interesting to consider the application of these concepts to other domains where a high-quality switch is available, such as optical waves. Compact optical switches with one to two orders of magnitude ON/OFF transmission ratio\(^2\)\(^5\)\(^6\)\(^1\) open the door to optical non-reciprocity and isolation through commutation-based parametric modulation. The nanosecond-scale switching speed implies GHz-range commutation frequencies, much smaller than the optical carrier frequency, which can be accommodated by commutating across high-Q optical filters that eliminate one of the modulation sidebands, similar to the low-pass filter effect used in our prototype.

### Methods

**IC implementation details.** A block diagram of the IC and component values are provided in Supplementary Fig. 3 and Supplementary Table 1, respectively. The circulator consists of three lumped C-L-C sections that miniaturize the \(3\pi/4\) transmission line and a staggered electronically commutated network of eight capacitors. All components, except for the three inductors, are integrated on the CMOS IC. The switches of the commutated network are implemented using 65 nm CMOS transistors and are driven with two sets of eight non-overlapping clock signals with 12.5% duty cycle. These clock signals are generated from two differentiators (0/180\(^\circ\)) input clocks that run at four times the desired commutation frequency. A divide-by-2 frequency-divider circuit generates four quadrature clocks with 0/90/180/270\(^\circ\) phase relationship. These four clock signals drive two parallel paths for the two sets of switches. One of the paths features a programmable phase shifter that allows for arbitrary staggering between the two commutating switch sets. This enables switching between +90\(^\circ\) and −90\(^\circ\) staggering, which allows dynamic reconfiguration of the circulation direction. The phase shifter also allows for fine tuning of the staggered phase shift, to optimize the transmission loss in the circulation direction and isolation in the reverse direction. This feature has been exploited in the measurements shown in the main text. After phase shifting, another divide-by-2 circuit and a non-overlapping 12.5% duty-cycle clock generation circuit create the clock signals that control the commutating transistor switches.

**Experimental setups.** Diagrams of the experimental setups are provided in Supplementary Figs 4 and 5, respectively. A list of the equipment used in the two setups is provided in Supplementary Table 2. A 180\(^\circ\) hybrid is used to generate two differential (0/180\(^\circ\)) signals from a signal generator to drive the clock inputs of the implemented circulator. A voltage bias signal is added to the clock signals through bias tees to drive the clock inputs. A two-port vector network analyser is used to measure the \(S\)-parameters of the circulator two ports at a time, whereas the third port is terminated with a variable impedance to tune the port impedance. For the input-referred third-order intercept point linearity test, a two-way power combiner is used to combine two sinusoidal signals generated from two additional signal generators and feed the input port under consideration. A spectrum analyser is used to monitor the fundamental signals and the third-order intermodulation distortion products of the circulator at the output port under consideration, whereas the third port is terminated with a 50-\(\Omega\) termination.

### References

1. Jdas, D. et al. What is—and what is not—an optical isolator. *Nat. Photonics* 7, 579–582 (2013).
2. Goldsmith, A. *Wireless Communications* (Cambridge Univ. Press, 2005).
3. Bharadia, D., McMilen, E. & Katti, S. Full duplex radios. ACM SIGCOMM Computer Communication Review vol. 43, 373–386 (ACM, 2013).
4. Saharanawale, A. et al. In-band full-duplex wireless: Challenges and opportunities. IEEE J. Select. Areas Commun. 32, 1637–1652 (2014).
5. Pozar, D. M. Microwave Engineering (Addison-Wesley, 1990).
6. Bi, L. et al. On-chip optical isolation in monolithically integrated non-reciprocal optical resonators. Nat. Photonics 5, 758–762 (2011).
7. Shoji, Y., Mizumoto, T., Yokoi, H., Hsieh, I.-W. & Ogsood, R. M. Magneto-optical isolator with silicon waveguides fabricated by direct bonding. Appl. Phys. Lett. 92, 071117 (2008).
8. Adam, J., Davis, L., Dionne, G. F., Schloemann, E. & Stitzer, S. Ferrite devices and materials. IEEE Trans. Microw. Theory Techn. 50, 726–737 (2002).
9. Adam, J. et al. Monolithically integrated an X-band circulator with GaAs MiMics. IEEE MTT-S International Microwave Symposium Digest 1, 97–98 (1995).
10. Qin, S., Xu, Q. & Wang, Y. Nonreciprocal components with distributedly photonic transitions. Nat. Commun. 6, 245–247 (2015).
11. Yu, Z. & Fan, S. Complete optical isolation created by indirect interband photonic transition on a silicon chip. Nat. Phys. 10, 114–121 (2014).