The design and testing issues of radiation tolerant microwave amplifiers implemented in the domestic GaAs pHEMT 0.5 μm process

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Abstract. The investigation results of the possibility of manufacturing radiation tolerant microwave amplifiers implementing in domestic GaAs D-mode pHEMT 0.5 μm process are presented in this work. The amplifier with an operating frequency range from 0.1 GHz to 3.5 GHz, gain above 15 dB, noise figure below 2.2 dB, output linearity above 17 dBm is designed, produced and measured. The characteristic property of the amplifier is a single positive supply voltage and extended frequency range up to 100 MHz provided by the external capacitor circuit. Transient radiation effects in the amplifier are investigated up to the dose rate value of 4.9×10^9 a.u./s. The recovery time does not exceed 4 μs according to the experimental results.

1 Introduction

Microwave amplifier integrated circuits (IC) implemented in GaAs pseudo high-electron mobility transistor (pHEMT) processes are widely used in main radio-frequency wireless systems for industrial, space and science applications because its wide operating frequency range, high amplification (gain), low noise figure (NF), high output power [1]. Traditionally, amplifiers ICs are implemented in the depletion mode (D-mode) pHEMT process, which required additional bias circuits and negative voltage supply for biasing [1, 2].

Enhancement mode (E-mode) pHEMT processes are becoming increasingly popular for various microwave amplifier implementation due to the following advantages: high efficiency, high transconductance, gain, cut-off frequency (fT) and maximum oscillation frequency (Fmax), no negative voltage requirement, compatibility with D-mode pHEMT process (E/D-mode pHEMT process) [3]. Meanwhile, the conventional low cost and widely available D-mode pHEMT process can also be used to design high performance microwave amplifiers even with single positive supply operation [4].

The purpose of this work is to show the applicability of the domestic GaAs D-mode pHEMT 0.5 μm process in the implementation of the radiation tolerant microwave amplifiers with the operating frequency range from 0.1 GHz to 3.5 GHz, gain above 15 dB,

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NF below 2.2 dB, output linearity above 17 dBm and single positive supply voltage operation. The comparison results of the proposed amplifier characteristics with those for its analog TGA5108 (TriQuint Semiconductor) [5] implemented in GaAs E/D-mode pHEMT 0.5 µm process [6] are also presented.

2 GaAs pHEMT 0.5 µm process

The domestic GaAs D-mode pHEMT 0.5 µm process with Ft up to 35 GHz and minimal NF (NFmin) of 1.2 dB at 8 GHz [7] has been used for the amplifier implementation. According to the results provided in [8], the presented GaAs D-mode pHEMT 0.5 µm process allows producing a microwave amplifier with immunity to the total ionizing dose and the destructive single event effects up to 300 krad and 60 MeV·cm²/mg respectively. Meanwhile, transient radiation effects should be investigated in this work [9].

TGA5108 implemented in the «TQPED» E/D-mode pHEMT 0.5 µm process (TriQuint Semiconductor) that has Ft of 30/25 GHz [6]. The main process parameters for both amplifiers are summarized in Table 1.

Table 1. GaAs pHEMT 0.5 µm process parameters.

| Process | Domestic D-mode 0.5 µm | «TQPED» E/D-mode 0.5 µm (TriQuint Semiconductor) [6] |
|---------|------------------------|--------------------------------------------------|
| Transistor type | D-mode | D-mode | E-mode |
| Pinch-off voltage (D-mode) / Threshold voltage (E-mode), V | -1.55 | -0.8 | 0.35 |
| Transconductance, mS/mm | 350 | 350 | 625 |
| Cut-off frequency, GHz | 35 | 25 | 30 |
| Minimum noise figure, dB | 1.2 @8GHz | – | – |
| Metal layers | 2 | 3 |

3 Amplifier circuit

A simplified schematic of the proposed amplifier is shown in Fig. 1. The main features of this amplifier are: (1) wideband operation due to the use of cascode configuration with resistive parallel feedback (resistor R1); (2) single positive supply mode operation due to the use of self-bias circuit (resistor R3, capacitor C2); (3) external (connect to pad “C”) capacitors can be used to improve amplifier performance at frequencies below 0.5 GHz.

TGA5108 is implemented based on E-mode pHEMT and has a cascode configuration with resistive parallel feedback without a self-bias circuit.

4 Amplifier performances

The measurements of the amplifier proposed in the paper were performed on the wafer using Cascade semi-automated probe station, vector network analyzer (VNA) and signal (spectrum) analyzer with noise figure measurement option. The experimental setup used for amplifier dies testing is shown in Fig. 2. The measurement results of TGA5108 are presented in [5] (TGA5108 has been tested using the evaluation board).
Fig. 1. Simplified circuit of the proposed amplifier.

Fig. 2. Experimental setup for amplifier performance measurements.

Fig. 3. Frequency dependence of the gain and noise figure (a) and gain and output power versus input power (b) for the proposed amplifier and TGA5108.

The simulated and measured results of the proposed amplifier and measured TGA5108 characteristics are shown in Fig. 3. The proposed amplifier has similar to TGA5108 noise and linearity performance, however smaller (1...4 dB less) small-signal gain due to less
transconductance of the D-mode pHEMT and/or other feedback factor. Meanwhile, TGA5108 has significant (7 dB) gain flatness due to the impact of the evaluation board and/or other feedback factor.

To provide measurements of the proposed amplifier performance in the low-frequency range (with and without external capacitors) and the temperature range from -60 °C to +125 °C, a special measurement test fixture has been designed and implemented (see Fig. 4).

![Fig. 4. Special measurement test fixture schematic (a), photograph (b).](image)

The measured low-frequency performance of the amplifier with and without mounted 1 nF SMD capacitors C2 and C3 are shown in Fig. 5. The dependencies are shown in Fig. 5 demonstrate that the gain and noise figure have been improved by more than 10 dB and 5 dB, respectively, at 100 MHz through using the external capacitors.

The measurement results of the proposed amplifier parameters in the ambient temperature range from -60 °C to +125 °C and frequency range from 0.5 GHz to 3.5 GHz are shown in Fig. 6. The gain value monotonically decreases as the ambient temperature increases, and the gain change does not exceed 2 dB. The noise figure monotonically increases with the increase of the ambient temperature, the variation of the noise figure does not exceed 1.3 dB. The output linearity (output $P_{1dB}$) monotonically decreases with the increase of the ambient temperature, the output $P_{1dB}$ shift in the temperature range is not higher than 1.5...2 dB. The change in the current consumption in the considered ambient temperature range does not exceed 3.8 mA. The measured performance of the presented amplifier is summarized in Table 2 along with its counterpart from TriQuint Semiconductor.

![Fig. 5. Measured gain (a) and noise figure (b) versus frequency for the proposed amplifier.](image)
Fig. 6. Measured gain and noise figure versus frequency (a) and gain and output power versus input power (b) for the proposed amplifier in the ambient temperature range from -60 °C to +125 °C.

Table 2. Measured performance summary of the proposed amplifier and its counterpart (T = 25 °C).

| Microwave Amplifier | This Work | TGA510S |
|---------------------|-----------|---------|
| Process             | 0.5 μm D-pHEMT | 0.5 μm E/D-pHEMT |
| Operating frequency, GHz | 0.5 (0.1)...3.5 | 0.5...3.5 |
| Gain, dB            | 15.3      | 15      |
| Gain flatness, dB   | 4         | 7       |
| Noise figure, dB    | 2.2       | 2.2     |
| Output $P_{1\text{dB}}$, dBm | 17      | 20      |
| Gain temperature coefficient, dB/°C | -0.011 | -0.011 |
| Noise figure temperature coefficient, dB/°C | 0.007 | 0.005 |
| Power consumption, mW | 325     | 425     |
| Chip size, mm²      | 2.15×1.65 | 1.49×0.85 |

The investigation of transient radiation effects in the proposed amplifier has been performed in SPELS and MEPhI test center [10-12] at the dose rate (P) value from $6 \times 10^6$ a.u./s to $4.9 \times 10^9$ a.u./s. The amplifier gain at the frequency of 1.5 GHz has been controlled during irradiation. Waveforms of the amplifier gain ionizing response under irradiation at three different P values are shown in Fig. 7. It has been determined that the recovery time (limit: the gain decreases by 1 dB or more) does not exceed 4 µs. The dependence of the gain response amplitude on P value is shown in Fig. 8. The figure shows that the gain monotonically decreases with the increase of P value.

Fig. 7. Waveforms of the gain ionizing response under irradiation at three P values: $9.6 \times 10^6$ a.u./s (a); $2.1 \times 10^7$ a.u./s (b); $4.9 \times 10^9$ a.u./s (c).
Fig. 8. The dependence of the gain ionizing response amplitude on P value.

5 Conclusion

The investigation results of the possibility of manufacturing radiation tolerant microwave amplifiers implementing in domestic GaAs D-mode pHEMT 0.5 μm process are presented. The amplifier with an operating frequency range from 0.1 GHz to 3.5 GHz, gain above 15 dB, noise figure below 2.2 dB, output linearity above 17 dBm is designed, produced and measured. The characteristic property of the amplifier is a single positive supply voltage and extended frequency range up to 100 MHz provided by the external capacitor circuit. The amplifier has immunity to total ionizing dose and destructive single event effects up to 300 krad and 60 MeV·cm²/mg, respectively, and has not long-term radiation effects. The obtained in this work results confirms the applicability of the conventional low cost and widely available D-mode pHEMT process in high performance radiation tolerant microwave amplifiers implementation.

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