Collaborative Cloud and Edge Mobile Computing in C-RAN Systems with Minimal End-to-End Latency

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Abstract—Mobile cloud and edge computing protocols make it possible to offer computationally heavy applications to mobile devices via computational offloading from devices to nearby edge servers or more powerful, but remote, cloud servers. Previous work assumed that computational tasks can be fractionally offloaded at both cloud processor (CP) and at a local edge node (EN) within a conventional Distributed Radio Access Network (D-RAN) that relies on non-cooperative ENs equipped with one-way uplink fronthaul connection to the cloud. In this paper, we propose to integrate collaborative fractional computing across CP and ENs within a Cloud RAN (C-RAN) architecture with finite-capacity two-way fronthaul links. Accordingly, tasks offloaded by a mobile device can be partially carried out at an EN and the CP, with multiple ENs communicating with a common CP to exchange data and computational outcomes while allowing for centralized precoding and decoding. Unlike prior work, we investigate joint optimization of computing and communication resources, including wireless and fronthaul segments, to minimize the end-to-end latency by accounting for a two-way uplink and downlink transmission. The problem is tackled by using fractional programming (FP) and matrix FP. Extensive numerical results validate the performance gain of the proposed architecture as compared to the previously studied D-RAN solution.

Index Terms—Mobile cloud computing, edge computing, C-RAN, constrained fronthaul, end-to-end latency minimization, (matrix) fractional programming.

I. INTRODUCTION

Mobile cloud and edge computing techniques enable computationally heavy applications such as gaming and augmented reality (AR) by offloading computation tasks from battery-limited mobile user equipments (UEs) to cloud or edge servers which are located respectively at cloud processor (CP) or edge nodes (ENs) of a cellular architecture [11]-[7]. In systems with both cloud and edge computing capabilities, computation tasks can be opportunistically offloaded either to ENs or to the CP [8]. For example, it may be desirable to offload latency-sensitive and computationally heavy tasks to a CP, while relatively light tasks with more stringent latency constraints can be offloaded to edge servers in ENs.

The optimization of the offloading decision policy was studied in [9], [10] by focusing on the application layer and without including constraints imposed by the Radio Access Network (RAN). To the best of our knowledge, reference [3] for the first time studied the joint optimization of computation and communication resources for mobile wireless edge computing systems, with follow-up works including [4]. Both papers [3], [4] aimed at minimizing energy expenditure under constraints on the end-to-end latency that encompass the contributions of both communication and computation. While [3] accounts only for uplink transmission, reference [4] also includes the contribution of downlink communication, which is required to feed back the results of the remote computations. To overcome the inherent non-convexity of the resulting optimization problems, the authors in [3], [4] applied successive convex approximation (SCA) [11], [12], which efficiently finds a locally optimal solution for constrained non-convex problems. Extensions in [13], [14] studied edge computing-based AR applications [13] and edge computing via an unmanned aerial vehicle (UAV) mounted cloudlet [14].

In a system with both cloud and edge computing capabilities, computation tasks can be partially offloaded to CP and ENs [8]. Reference [8] tackled the problem of jointly optimizing communication and computational resources with the goal of minimizing a weighted sum of per-UE end-to-end latency metrics within a distributed RAN (D-RAN) architecture [15] Sec. III. The authors in [8] developed closed-form solutions for optimal resource allocation and task splitting ratios by focusing on the design of uplink communication from UEs to ENs and CP while assuming orthogonal time-division multiple access (TDMA) on wireless access uplink channel and a fixed allocation of fronthaul capacity across the UEs. Reference [16] also addressed the design of the task splitting ratios under the assumption that the task of each UE can be split into multiple subtasks that are offloaded to multiple ENs.

In a D-RAN, ENs perform local signal processing for channel encoding and decoding. Thus, the overall performance...
can be degraded by interference in dense networks. In this paper, we propose integrating collaborative fractional cloud-edge offloading within a cloud radio access network (C-RAN) architecture [17], while accounting for the contributions of both uplink and downlink. In a C-RAN, as illustrated in Fig. 1, joint signal processing, in the form of cooperative precoding and detection, at the CP enables effective interference management. Unlike the case of D-RANs, the design of C-RAN systems entails the additional challenge of optimizing the use of ENs–CP fronthaul links [18]–[20]. In this regard, we note that, although fronthaul constraints were also considered in [8] for the design within a D-RAN system, a simple data forwarding model was assumed with fixed capacity allocation among the UEs. In [21], the authors tackled the optimization of functional split for collaborative computing systems equipped with a packet-based fronthaul network. However, it was assumed in [21] that the physical-layer (PHY) functionalities, which include channel encoding and decoding, are located only at ENs. In [22], the authors addressed the task allocation and traffic path planning problem for a C-RAN system under the assumption that the service latency consists of task processing delay and path delay only on fronthaul links.

In this work, we address the optimization of C-RAN signal processing for the purpose of enabling collaborative cloud and edge mobile computing with minimal end-to-end two-way latency. We proceed by first reviewing the design of collaborative cloud and edge computing system within a D-RAN architecture. Unlike [8], [23], which considered one-way uplink design with inter-UE TDMA and fixed fronthaul capacity allocation, we address the design of two-way communications with both TDMA and non-orthogonal multiple access strategies and we treat the fronthaul capacity allocation as optimization variables. Then, we address the design of C-RAN system for collaborative offloading. For all the design problems, we consider the criterion of minimizing two-way end-to-end latency for computation offloading as in [8], [24]–[26]. To tackle the formulated problems, which turn out to be non-convex, we adopt fractional programming (FP) and matrix FP [27], [28]. We present extensive numerical results that confirm the convergence of the proposed optimization algorithms, the advantages of C-RAN architecture as compared to D-RAN [8], and the impact of collaborative cloud and edge computing on latency with C-RAN.

The paper is organized as follows. In Sec. II, we describe the system model including the computational tasks, computational capabilities, wireless channel and fronthaul transmission models. In Sec. III, we discuss the design of collaborative cloud and edge mobile computing system within the D-RAN architecture, and the design for a C-RAN system is discussed in Sec. IV. We provide extensive numerical results in Sec. V to validate the performance gain of the proposed architecture as compared to the D-RAN solution. We conclude the paper in Sec. VI.

Notations: We denote the set of all $M \times N$ complex matrices by $\mathbb{C}^{M \times N}$. The notation $x \sim \mathcal{CN}(\mu, \Omega)$ indicates that $x$ is a column vector following circularly symmetric complex Gaussian distribution with mean vector $\mu$ and covariance matrix $\Omega$. We also use the notation $I(x; y)$ to represent the mutual information between random vectors $x$ and $y$. A block diagonal matrix, whose diagonal blocks are given as $A_1, \ldots, A_L$, is denoted by $\text{diag}(\{A_i\}_{i \in \{1, \ldots, L\}})$. Lastly, $\mathbb{E}[\cdot]$ represents the expectation operator, and $\|x\|$ denotes the Euclidean 2-norm of a vector $x$.

II. System Model

As illustrated in Fig. 1, we consider a collaborative cloud and edge mobile computing system, in which $N_U$ single-antenna mobile UEs offload their computational tasks to a network consisting of $N_E$ ENs and a CP. In order to exchange computational input information, the UEs communicate with the ENs over a wireless uplink channel, and each EN is connected to the CP through dedicated fronthaul link of finite capacity $C_{ul}^E$ bits per second (bps). For communication in the reverse direction from CP to each EN, the fronthaul has capacity of $C_{dl}^E$ bps, and the ENs transmit to the UEs in a wireless downlink channel. For convenience, we define the sets $N_U \triangleq \{1, 2, \ldots, N_U\}$ and $N_E \triangleq \{1, 2, \ldots, N_E\}$ of indices of UEs and ENs, respectively. We denote the number of antennas of EN $i$ as $n_{E,i}$, and the number of all ENs’ antennas is $n_E = \sum_{i \in N_E} n_{E,i}$. The bandwidths of uplink and downlink channels are $W^{ul}$ and $W^{dl}$, respectively, which are measured in Hz.

A. Computational Tasks and Collaborative Computing Model

As in [4], [8], we assume that the UEs have limited computing powers, and hence offload their whole tasks to ENs or CP without local processing. We define $b_{i,k}$ and $b_{O,k}$ as the numbers of input and output bits for the task of UE $k$. We assume that $V_k$ CPU cycles are required to process one bit of the task of UE $k$ so that the task of UE $k$ requires $b_{i,k}V_k$ CPU cycles in total. The computing powers of each EN $i$ and CP are denoted by $F_{E,i}$ and $F_C$, respectively, whose units are CPU cycles per second.
For each UE $k$, we allow for a collaborative cloud and edge computing [4], [8]. This means that a part of the task of UE $k$ is processed by a predetermined EN $i_k$, while the rest of the task is offloaded to the CP. We define a variable $c_k \in [0, 1]$ which controls the fraction of the task of UE $k$ that is processed by EN $i_k$. Accordingly, EN $i_k$ receives the input information of $c_k b_{i,k}$ bits from UE $k$, runs $c_k b_{i,k} V_k$ CPU cycles, and reports the resulting output information of $c_k b_{O,k}$ bits back to UE $k$. Similarly, the CP receives $(1-c_k) b_{i,k}$ input bits from UE $k$, runs $(1-c_k) b_{i,k} V_k$ CPU cycles, and sends $(1-c_k) b_{O,k}$ output bits to UE $k$.

We define $N_{U,i}$ as the set of UEs that are associated with EN $i$, i.e.,
\[ N_{U,i} = \{ k \in N_U | i_k = i \}. \]

Therefore, if we denote as $F_{E,i,k}$ the computing power of EN $i$ assigned for UE $k$, the variables $F_{E,i,k}, k \in N_{U,i}$, are subject to the constraint
\[ \sum_{k \in N_{U,i}} F_{E,i,k} \leq F_{E,i}. \]

The edge computation latency $\tau_{E,i,k}^{ex}$ for UE $k$ at EN $i$ with $k \in N_{U,i}$ is given as
\[ \tau_{E,i,k}^{ex} = \frac{c_k b_{i,k} V_k}{F_{E,i,k}}. \]

Similarly, denoting the computing power allocated to UE $k$ by the CP as $F_{C,k}$, the variables $F_{C,k}, k \in N_U$, should satisfy the constraint
\[ \sum_{k \in N_U} F_{C,k} = F_C. \]

The cloud computing latency $\tau_{C,k}^{ex}$ for UE $k$ at the CP is given as
\[ \tau_{C,k}^{ex} = \frac{(1-c_k) b_{i,k} V_k}{F_{C,k}}. \]

### B. Wireless Channel Model for Edge Link

Assuming the flat fading channel model for both the uplink and downlink wireless edge links, the received signal vector $y_i^u \in \mathbb{C}^{N_{E,i} \times 1}$ of EN $i$ on the uplink is given as
\[ y_i^u = \sum_{k \in N_U} h_{i,k}^u x_k^u + z_i^u, \]

where $h_{i,k}^u \in \mathbb{C}^{N_{E,i} \times 1}$ denotes the channel vector from UE $k$ to EN $i$; $x_k^u \in \mathbb{C}^{1 \times 1}$ indicates the transmit signal of UE $k$; and $z_i^u \sim \mathcal{C}\mathcal{N}(0, \sigma^2_{z,u})$ is the additive noise vector. Similarly, the received signal $y_i^d \in \mathbb{C}^{1 \times 1}$ of UE $k$ on the downlink can be written as
\[ y_i^d = \sum_{i \in N_E} h_{i,k,d}^u x_i^d + z_i^d, \]

where $h_{i,k,d}^u \in \mathbb{C}^{N_{E,i} \times N_{E,i}}$ represents the channel vector from EN $i$ to UE $k$; $x_i^d \in \mathbb{C}^{N_{E,i} \times 1}$ denotes the transmit signal vector of EN $i$; and $z_i^d \sim \mathcal{C}\mathcal{N}(0, \sigma^2_{z,d})$ denotes the additive noise.

The transmit powers of each UE $k$ and EN $i$ are limited as
\[ \mathbb{E} \left[ \|x_k^u\|^2 \right] \leq P_{ul}, \quad \mathbb{E} \left[ \|x_k^d\|^2 \right] \leq P_{dl}, \]

where $P_{ul}$ and $P_{dl}$ represent the maximum transmit powers at each UE and EN, respectively. We define the maximum signal-to-noise ratios (SNRs) of the uplink and downlink channels as $SNR_{\text{ul}}^{\text{max}} = P_{ul}/\sigma^2_{z,u}$ and $SNR_{\text{dl}}^{\text{max}} = P_{dl}/\sigma^2_{z,d}$, respectively. The symbols described in this section are summarized in Table I.

### III. OPTIMIZATION FOR THE D-RAN ARCHITECTURE

In this section, we discuss the design of the collaborative cloud and edge mobile computing system under a D-RAN architecture [15], Sec. III. Unlike [8], which considered one-way uplink design with inter-UE TDMA and fixed fronthaul capacity allocation, we address the design of two-way communications with both TDMA and non-orthogonal multiple access strategies while treating the fronthaul capacity allocation as optimization variables.

In D-RAN, each EN $i$ locally decodes the uplink input information transmitted by the associated UEs $N_{U,i}$, without cooperating with nearby ENs. Also, in the downlink, the computation output information for UEs $N_{U,i}$ is solely encoded and transmitted by the serving EN $i$. We discuss the designs with orthogonal TDMA and non-orthogonal multiple access strategies in Sec. III-A and III-B respectively.

#### A. Orthogonal TDMA

With TDMA, $N_U$ UEs communicate with $N_E$ ENs on the wireless edge link while being assigned different time slots so that there is no inter-UE interference on wireless channel. We define $u_k^u \in [0, 1]$ and $u_k^d \in [0, 1]$ as the uplink and downlink time fractions allocated to UE $k$. Thus, the defined fraction variables $u \triangleq \{u_k^u, u_k^d \}_{k \in N_U}$ should satisfy the constraint
\[ \sum_{k \in N_U} u_k^u = \sum_{k \in N_U} u_k^d = 1. \]

In the uplink, UE $k$ transmits a baseband signal which encodes the input information for its task. Assuming that Gaussian channel codebooks are used, the transmitted signal $x_k^u$ of UE $k$ is distributed as $x_k^u \sim \mathcal{CN}(0, p_{k}^u)$. Since there is no co-channel interference with orthogonal TDMA, the transmit power $p_k$ of UE $k$ is set to $p_k^u = P_{ul}$ without loss of optimality.

| Symbol | Meaning |
|--------|---------|
| $N_U, N_E$ | Numbers of UEs and ENs |
| $N_U, k_E$ | Sets of UEs and ENs’ indices |
| $n_{i,E}$ | Number of antennas of EN $i$ |
| $C_{l,ul}, C_{l,dl}$ | Capacity of uplink and downlink fronthaul links |
| $W_{ul}, W_{dl}$ | Bandwidths of uplink and downlink channels |
| $b_{i,k}, b_{O,k}$ | Numbers of input and output bits for UE $k$ |
| $V_k$ | Number of CPU cycles per input bit for UE $k$ |
| $P_{E,i}, P_{C}$ | CPU frequencies of EN $i$ and CP |
| $c_k$ | Fraction of the task of UE $k$ processed by EN $i_k$ |
| $N_{U,i}$ | Set of UEs associated with EN $i$ |
| $\sigma^2_{z,u}, \sigma^2_{z,d}$ | Noise powers per receive antenna at ENs and UEs |
| $SNR_{\text{ul}}^{\text{max}}, SNR_{\text{dl}}^{\text{max}}$ | Maximum SNRs of uplink and downlink channels |
| $h_{i,k}^u, h_{i,k}^d$ | Uplink & downlink channels between UE $k$ and EN $i$ |
| $x_k^u, y_k^u$ | Received signals of EN $i$ and UE $k$ |
| $x_k^d, z_k^d$ | Transmitted signals of UE $k$ and EN $i$ |
| $\bar{y}_k^u$ | Noise signals at EN $i$ and UE $k$ |

Table I: Table summarizing important symbols used throughout the paper.
With the described transmission model, the achievable data rate $R_{dl}^i$ between UE $k$ and EN $i$ in the uplink channel is given as $R_{dl}^i = \frac{1}{2}W_{dl}^i I(x_{dl}^i \parallel \hbar_{dl}^i)$, where the mutual information $I(x_{dl}^i \parallel \hbar_{dl}^i)$ is calculated as

$$I(x_{dl}^i \parallel \hbar_{dl}^i) = \log_2 \left( 1 + \frac{(P_{dl}^i/\sigma_{dl}^2)||\hbar_{dl}^i||^2}{1} \right).$$

(11)

The uplink latency $\tau_{dl}^i$ on the wireless edge link for UE $k$ is then given as

$$\tau_{dl}^i = \frac{b_{dl}^i}{R_{dl}^i}.$$  

(12)

Among the received $b_{dl}^i$ bits from UE $k$ in $N_{U,i}$, EN $i$ processes only $c_{dl} b_{dl}^i$ bits using its edge server and forwards the remaining $(1-c_{dl}) b_{dl}^i$ bits to the CP on the fronthaul link for cloud computing. We denote the partial capacity of the fronthaul link between EN $i$ and CP that is used for transferring the $(1-c_{dl}) b_{dl}^i$ bits input bits for UE $k$ by $C_{dl}^i, k \geq 0$, so that $C_{dl}^i, k \in N_{U,i}$, as $C_{dl}^i, k \geq 0$ for all $i \in N_E.$ For given $C_{dl}^i, k$ of the uplink fronthaul latency $\tau_{dl}^i$ of UE $k$ is given as

$$\tau_{dl}^i = \frac{(1-c_{dl}) b_{dl}^i}{C_{dl}^i, k}.$$  

(14)

The CP processes the received $(1-c_{dl}) b_{dl}^i$ bits for UE $k$ producing output information of $(1-c_{dl}) b_{dl}^i$ bits. The output bits are transmitted to EN $i_k$ that serves UE $k$. We denote by $C_{dl}^i, k \geq 0$ the partial capacity of the fronthaul link from CP to EN $i_k$ that is used to transfer the $(1-c_{dl}) b_{dl}^i$ bits for UE $k$. Thus, the following constraint should be satisfied:

$$\sum_{k \in N_{U,i}} C_{dl}^i, k \leq C_{dl}^i, k \leq C_{dl}^i, k.$$  

(15)

for all $i \in N_E.$ The downlink fronthaul latency $\tau_{dl}^i$ of UE $k$ for given $C_{dl}^i$ is given as

$$\tau_{dl}^i = \frac{(1-c_{dl}) b_{dl}^i}{C_{dl}^i, k}.$$  

(16)

In the downlink, each EN $i$ reports the computation output information of $b_{dl}^i$ bits to UE $k$ in $N_{U,i}$. To this end, EN $i$ encodes the output information with Gaussian channel codebook producing an encoded baseband signal $s_{dl}^i \sim C\mathcal{N}(0, Q_{dl}^i)$ with \(\|s_{dl}^i\|^2 = tr(Q_{dl}^i) \leq P_{dl}^i.\) Therefore, EN $i$ transmits the encoded signal $s_{dl}^i$ during a fraction $u_{dl}^i$ of the downlink time slot. For given $Q_{dl}^i$, the achievable downlink data rate $R_{dl}^i$ is given as $R_{dl}^i = u_{dl}^i W_{dl}^i I(s_{dl}^i \parallel \hbar_{dl}^i)$ with $I(s_{dl}^i \parallel \hbar_{dl}^i)$ computed as

$$I(s_{dl}^i \parallel \hbar_{dl}^i) = \log_2 \left( 1 + \frac{(1/\sigma_{dl}^2)||\hbar_{dl}^i||^2}{1} \right).$$

(17)

where $\hat{h}_{dl}^i = h_{dl}^i/||h_{dl}^i||$. By substituting $(18)$ into $(17)$, we obtain the maximized mutual information value $I(s_{dl}^i \parallel y_{dl}^i)$ as

$I(s_{dl}^i \parallel y_{dl}^i) = \log_2 \left( 1 + \frac{(P_{dl}^i/\sigma_{dl}^2)||\hbar_{dl}^i||^2}{1} \right)$.  

(19)

The downlink latency $\tau_{dl}^i$ for UE $k$ on the wireless edge link is hence given as

$$\tau_{dl}^i = \frac{b_{dl}^i}{R_{dl}^i}.$$  

(20)

Finally, the overall latency $\tau_{T,k}$ for each UE $k$ is given as

$$\tau_{T,k} = \tau_{dl}^i + \max \{\tau_{\text{exe},E,k}, \tau_{\text{dl}^i, E,k} + \tau_{\text{exe},C,k} + \tau_{dl}^i, E,k \} + \tau_{dl}^i, E,k,$$

(21)

where the second term indicates that local edge computing at EN $k$ and fronthaul transmissions can take place simultaneously. As a result, the total latency required for completing the tasks of all the participating UEs is given as

$$\tau_T = \max_{k \in N_U} \tau_{T,k}.$$  

(22)

We tackle the problem of optimizing the variables $c \in \{c_k; k \in N_U\}$, $u \in \{u_k; k \in N_U\}$, $F \in \{F_k, k \in N_U\}$, and $C_{F} = \{C_{F,k}, k \in N_U\}$ with the goal of minimizing the total latency $\tau_T$. We formulate this problem as

$$\text{minimize}_{c \in \{c_k; k \in N_U\}, u \in \{u_k; k \in N_U\}} \text{max}_{k \in N_U} \tau_{T,k}$$

(23a)

s.t. $\tau_{dl}^i, E,k \geq \frac{b_{dl}^i}{u_{dl}^i} , k \in N_U,$  

(23b)

$$\tau_{dl}^i, F,k \geq \frac{(1-c_{dl}) b_{dl}^i}{C_{dl}^i, k} , k \in N_U,$$  

(23c)

$$\tau_{dl}^i, E,k \geq \frac{b_{dl}^i}{u_{dl}^i} , k \in N_U,$$  

(23d)

$$\tau_{dl}^i, F,k \geq \frac{(1-c_{dl}) b_{dl}^i}{C_{dl}^i, k} , k \in N_U,$$  

(23e)

$$\tau_{\text{exe},E,k} \geq \frac{c_k \cdot b_{dl}^i V_{dl}^i}{F_{E,k}} , k \in N_U,$$  

(23f)

$$\tau_{\text{exe},C,k} \geq \frac{(1-c_{dl}) b_{dl}^i V_{dl}^i}{F_{C,k}}, k \in N_U,$$  

(23g)

for $c_k \in [0,1]$ for all $k \in N_U$.

$$\sum_{k \in N_U} u_{dl}^i = \sum_{k \in N_U} u_{dl}^i = 1,$$  

(23h)

$$\sum_{k \in N_{U,i}} F_{E,i,k} \leq F_{E,i}, i \in N_E,$$  

(23i)

$$\sum_{k \in N_{U,i}} F_{C,k} \leq F_{C},$$  

(23k)

$$\sum_{k \in N_{U,i}} C_{F,k} \leq C_{F}, i \in N_E,$$  

(23l)

$$\sum_{k \in N_{U,i}} C_{F,k} \leq C_{F}, i \in N_E,$$  

(23m)

with the notations $\tilde{R}_{dl}^i = W_{dl}^i I(s_{dl}^i \parallel y_{dl}^i)$, $\tilde{R}_{dl}^i = W_{dl}^i I(s_{dl}^i \parallel y_{dl}^i)$, and $\tau = \{\tau_{dl}^i, E,k, \tau_{dl}^i, F,k, \tau_{exe},E,k, \tau_{exe},C,k \} \in N_U$.}

The problem (23) is non-convex due to the constraints (23b) and (23e). We can tackle the non-convex problem by coordinate descent approach [30 Sec. 1.8], since the problem becomes convex if we fix one of the variable sets $c$ and $\{F, C_F\}$. However, the coordinate descent approach cannot
be directly applied to the problems that will be discussed in Sec. III-B and IV and hence we consider FP [27] as a solution method, which can overcome this limitation.

We observe that all the constraints (23c) and (23e)-(23g), that induce the non-convexity of the problem (23), can be expressed as a function of ratios of optimization variables. It was shown in [27] that FP is suitable for approximating those constraints by convex constraints. In more detail, based on [27], Cor. 1, we can show that, for any real values $\lambda_{F,k}$, $\lambda_{E,k}$, $\lambda_{C,k}$ and $\lambda_{E,k}$, the following constraints are stricter than (23c) and (23e)-(23g):

$$2\lambda_{F,k} \sqrt{\alpha_{F,k}} \left(1 - c_{k}\right) \geq b_{F,k} / \sqrt{\alpha_{F,k}}, \quad k \in N_U,$$ (24a)

$$2\lambda_{F,k} \sqrt{\alpha_{F,k}} \left(1 - c_{k}\right) \geq b_{F,k} / \sqrt{\alpha_{F,k}}, \quad k \in N_U,$$ (24b)

$$2\lambda_{E,k} \sqrt{\alpha_{E,k}} \left(1 - c_{k}\right) \geq b_{E,k} V_{E,k} / \sqrt{\alpha_{E,k}}, \quad k \in N_E,$$ (24c)

$$2\lambda_{C,k} \sqrt{\alpha_{C,k}} \left(1 - c_{k}\right) \geq b_{C,k} V_{C,k} / \sqrt{\alpha_{C,k}}, \quad k \in N_C,$$ (24d)

The above constraints have the following desirable properties: they are convex constraints, if the auxiliary variables $\lambda_{F,k}$, $\lambda_{E,k}$, $\lambda_{C,k}$ and $\lambda_{E,k}$ are fixed. And they become equivalent to (23c) and (23e) in (23g), if the variables $\lambda_{F,k}$, $\lambda_{E,k}$, $\lambda_{C,k}$ and $\lambda_{E,k}$ are given as:

$$\lambda_{F,k} = \frac{\sqrt{\alpha_{F,k}}}{1 - c_{k}}, \quad \lambda_{E,k} = \frac{\sqrt{\alpha_{E,k}}}{1 - c_{k}}, \quad \lambda_{C,k} = \frac{\sqrt{\alpha_{C,k}}}{c_{k}}, \quad \lambda_{E,k} = \frac{\sqrt{\alpha_{E,k}}}{c_{k}}.$$ (25)

Based on the above observation, we consider the problem obtained by replacing the constraints (23c) and (23e)-(23g) with (24c) in (23d) and adding $\lambda = \{\lambda_{F,k}, \lambda_{E,k}, \lambda_{C,k}, \lambda_{E,k}\} \in N_U$ as optimization variables. To tackle the obtained problem, which has the same optimal value as (23), we propose an iterative algorithm, in which the variables $\{c, u, F, C, \tau, \lambda\}$ are alternately updated. Since the optimization of $\{c, u, F, C, \tau, \lambda\}$ for fixed $\lambda$ is a convex problem, standard convex solvers, such as the CVX software [31], can be used. The optimal $\lambda$ for fixed $\{c, u, F, C, \tau, \lambda\}$ can be obtained as (25), which make the constraints (24a)-(24d) equivalent to the original constraints (23c) and (23e)-(23g). We describe the detailed algorithm in Algorithm 1.

The convex problem solved at Step 4 of each $t$th iteration in Algorithm 1 has stricter constraints than the original problem (23). Also, the feasible space of the convex problem contains the solution obtained at the $(t-1)$th iteration. Thus, the solution of the convex problem at the $t$th iteration belongs to the feasible space of problem (23) and achieves a lower latency value than the solution of the $(t-1)$th iteration. Therefore, Algorithm 1 produces monotonically decreasing latency values with respect to the iteration index $t$ so that it converges to a locally optimal point. For more formal proof of the convergence of SCA and FP algorithms, we refer to [31]. [27]. We can operate Algorithm 1 with an arbitrary initial point that satisfies the conditions (23b)-(23m). In the simulation section, we initialize the variables $\{c, u, F, C\}$ at Step 1 as:

$$u_k \leftarrow 1 / N_U, \quad k \in N_U,$$ (26a)

$$c_k \leftarrow 1 / 2, \quad k \in N_U,$$ (26b)

$$F_{E,k} \leftarrow |N_U|, \quad k \in N_{U,i}, i \in N_E,$$ (26c)

$$F_{C,k} \leftarrow F_{C} / N_U, \quad k \in N_U,$$ (26d)

$$C_{C,k} \leftarrow C_{C} / |N_{U,i}|, \quad k \in N_{U,i}, i \in N_E, m \in \{ul, dl\}.$$ (26e)

For the given $\{c, u, F, C\}$, we compute an initial value for $\tau$ according to (12), (14), (16), and (20).

The complexity of Algorithm 1 is given by the number of iterations multiplied by the complexity of solving the convex problem at each iteration (i.e., Step 4). The complexity of solving a generic convex problem is upper bounded by $O(n^3 + M \log(1/\epsilon))$ [32], p. 4, where $n$ denotes the number of optimization variables, $M$ is the number of arithmetic operations required to compute the objective and constraint functions, and $\epsilon$ represents the desired error tolerance. The numbers $n$ and $M$ equal $n = 13N_U$ and $M = 45N_U$, respectively, for the convex problem solved at Step 4 of Algorithm 1. However, to the best of our knowledge, the analysis of the convergence rate of general SCA algorithms is still an open problem. Instead, we provide some numerical evidence of the fast convergence of Algorithm 1 in Sec. V.

**Algorithm 1 Alternating optimization algorithm that tackles problem (23)**

1. **Initialize** $\{c, u, F, C, \tau\}$ as arbitrary values that satisfy the constraints (23b)-(23m), and set $t \leftarrow 1$.
2. **Calculate** the total latency $\tau_T$ in (22) with the initialized $\{c, u, F, C, \tau\}$, and set $\tau_T^{(0)} \leftarrow \tau_T$.
3. **Set** $\lambda$ according to (25).
4. **Update** the variables $\{c, u, F, C, \tau\}$ as a solution of the convex problem which is obtained by replacing the constraints (23c) and (23e)-(23g) with (24a)-(24d) and then by fixing $\lambda$.
5. **Calculate** the total latency $\tau_T$ with the updated $\{c, u, F, C, \tau\}$, and set $\tau_T^{(t)} \leftarrow \tau_T$.
6. **Stop** if $|\tau_T^{(t)} - \tau_T^{(t-1)}| \leq \delta$ or $t > t_{\text{max}}$. Otherwise, set $t \leftarrow t + 1$ and go back to Step 2.

**B. Non-Orthogonal Multiple Access**

In this subsection, we discuss the design with non-orthogonal multiple access. With non-orthogonal access, $N_U$ UEs communicate simultaneously with $N_E$ ENs on the same time and frequency resource. Therefore, the uplink and downlink communications on the wireless edge link are impairedd by inter-UE interference signals, while benefiting from transmission on a larger time interval. The computation and fronthaul transmission models are the same as the one described in Sec. III-A and we detail here only the uplink and downlink communication phases and the resulting latency performance.

As in Sec. III-A we assume that each UE $k$ uses a Gaussian channel codebook so that its transmitted signal $x_{k}^{ul}$ is distributed as $x_{k}^{ul} \sim \mathcal{CN}(0, p_k^{ul})$. The transmit power $p_k^{ul}$ is
subject to the constraint $p_k^u \in [0, P^u]$. Due to the presence of inter-UE interference signals, full power transmission at all UEs may cause an optimality loss. This suggests that we need to carefully design the transmit power variables $p_k^u$, $k \in \mathcal{N}_U$, by adapting to channel state information (CSI).

Each EN $i$ needs to decode the signals $\{x_k^u\}_{k \in \mathcal{N}_U}$, based on the received signal $y_k^d$. We assume that the signals $\{x_k^u\}_{k \in \mathcal{N}_U}$ are detected in parallel without successive interference cancellation (SIC) as in [33], [34] in order to minimize the decoding delay. We leave the design and analysis with SIC decoding [35] while taking into account the decoding delay for future work.

Under the assumption of parallel decoding, the achievable rate $R_k^u$ of UE $k$ in the uplink channel is given as $R_k^u = W^u I(x_k^u; y_k^d)$ with the mutual information value computed as

$$ I(x_k^u; y_k^d) = f_{E,k}^u (p) = \Psi \left( p_k^u \sigma_{z,ul}^2 \mathbf{I} + \sum_{l \in \mathcal{N}_U} p_l^u \mathbf{h}_{k,ul} \mathbf{h}_{l,ul}^H \right). $$

(27)

Here we have defined the notation $p \triangleq \{p_k^u\}_{k \in \mathcal{N}_U}$, and the function

$$ \Psi (\mathbf{A}, \mathbf{B}) = \log_2 \det (\mathbf{I} + \mathbf{B}^{-1} \mathbf{A}). $$

(28)

For given $R_k^d$, the uplink edge latency $\tau_{E,k}^u$ for UE $k$ is given as (22).

For the downlink edge link, each EN $i$ transmits a superposition of the signals $s_k^d$, $k \in \mathcal{N}_U$, where $s_k^d \sim \mathcal{CN}(0, Q_k^d)$ encodes the task output of UE $k$. The transmit signal of EN $i$ is written as

$$ x_i^d = \sum_{k \in \mathcal{N}_U, i} s_k^d. $$

(29)

With the above transmission model, the downlink transmit power constraint (9) can be expressed as

$$ \sum_{k \in \mathcal{N}_U, i} \text{tr} (Q_k^d) \leq P^d, $$

and the achievable rate $R_k^d$ of UE $k$ on the wireless edge link is given as $R_k^d = W^d I(s_k^d; y_k^d)$ with

$$ I(s_k^d; y_k^d) = f_{E,k}^d (Q) = \Psi \left( \mathbf{h}_{k,dl} \mathbf{Q}_k^d \mathbf{h}_{dl,k}^H, \sigma_{z,dl}^2 + \sum_{l \in \mathcal{N}_U, k} \mathbf{h}_{l,dl}^H \mathbf{Q}_k^d \mathbf{h}_{dl,l} \mathbf{h}_{k,dl}^H \right), $$

where $Q \triangleq \{Q_k^d\}_{k \in \mathcal{N}_U}$. For given $R_k^d$, the downlink edge latency $\tau_{E,k}^d$ of UE $k$ is given as (20).

For the non-orthogonal multiple access scheme as described above, we aim at jointly optimizing the variables $p$, $Q$, $c$ and $F$ with the goal of minimizing the total latency $\tau_T$ in

$$ \text{minimize} \quad \max_{k \in \mathcal{N}_U} \tau_{E,k} \quad \text{subject to} \quad \tau_{E,k} \geq \frac{b_k}{P_{ul}^{\text{ul}}}, \quad k \in \mathcal{N}_U, $$

$$ \tau_{E,k} \geq \frac{b_{ol,k}}{P_{dl}^{\text{dl}}}, \quad k \in \mathcal{N}_U, $$

(31a)

(31b)

(31c)

(23a)-(23i)

(31d)

(31e)

(31f)

(31g)

(31h)

(31i)

(23j)-(23m)

where we have defined $R_k^u \triangleq \{R_k^u, R_k^d\}_{k \in \mathcal{N}_U}$.

We note that it is more challenging to tackle problem (31) than (23) due to the presence of inter-UE interference signals on the wireless edge links. Accordingly, the uplink and downlink transmission strategies on edge links, which are characterized by the variables $p$ and $Q$, need to be jointly optimized. Also, the constraints (31c) and (31i) on the edge throughputs, which involve matrix variables $Q$, are not convex. To address these complications, we employ FP [27] as well as matrix FP [28], which is a generalized version of [27].

We first observe that the constraints (31d), that are expressed as a function of ratios of scalar optimization variables, can be handled by FP [27] as in Sec. III-A. Based on [27, Cor. 1], we replace the constraints (31d) with stricter constraints (24a)-(24d), which become equivalent to (31d) if the variables $\lambda_{F,k}^u$, $\lambda_{E,k}^u$, $\lambda_{F,k}^d$ and $\lambda_{C,k}^d$ equal (25).

The other non-convex constraints (31e) and (31f) contain ratios of matrix variables. Thus, we need to employ matrix FP [28], which generalizes scalar or vector version of FP in [27]. From [28, Cor. 1], the following constraints are stricter than (31e) and (31f) for any $\Gamma_{E,k} \in \mathbb{C}^{n_{E,k} \times n_{E,k}}$ and $\theta_{E,k} \in \mathbb{C}^{1 \times n_{E,k}}$,

$$ R_k^u \leq \phi \left( \Gamma_{E,k}, \theta_{E,k}, \mathbf{h}_{k,ul}^H \mathbf{Q}_k^d \mathbf{h}_{ul,k}, \sigma_{z,ul}^2 \mathbf{I} + \sum_{l \in \mathcal{N}_U} \mathbf{h}_{l,ul}^H \mathbf{Q}_k^d \mathbf{h}_{dl,l} \mathbf{h}_{k,dl}^H \right), $$

(32a)

$$ R_k^d \leq \phi \left( \Gamma_{E,k}, \theta_{E,k}, \mathbf{h}_{k,dl} \mathbf{Q}_k^d \mathbf{h}_{dl,k}^H, \sigma_{z,dl}^2 + \sum_{l \in \mathcal{N}_U} \mathbf{h}_{l,dl}^H \mathbf{Q}_k^d \mathbf{h}_{dl,l} \mathbf{h}_{k,dl}^H \right), $$

(32b)

where we have defined the variables $\bar{p}_k = \sqrt{p_k} \in [0, \sqrt{P^u}]$, $\bar{Q}_k^d = Q_k^d^{1/2}$, and the function

$$ \phi (\mathbf{A}, \mathbf{B}, C, D) = \log_2 \det (\mathbf{I} + \mathbf{A}) - \frac{1}{\ln 2} \text{tr} (\mathbf{A}) $$

$$ + \frac{1}{\ln 2} \text{tr} (\mathbf{I} + \mathbf{A}) (2C^H \mathbf{B} - \mathbf{B}^H \mathbf{D} \mathbf{C}). $$

(33)
Also, the above constraints are equivalent to (31e) and (31f) if
\[
\Gamma_{E,k}^{dl} = \frac{p_{dl}^{\text{ul}} h_{i,k}^{H}}{\sum_{l \in N_U \setminus \{k\}} p_{dl}^{\text{ul}} h_{i,l}^{H}} \left( \sigma_{z,ul}^2 I + \sum_{l \in N_U \setminus \{k\}} p_{dl}^{\text{ul}} h_{i,l}^{H} \right)^{-1} h_{i,k,k},
\]
\[
\theta_{E,k}^{dl} = \frac{\rho_{dl}^{\text{ul}}}{p_{dl}^{\text{ul}}} \left( \sigma_{z,ul}^2 I + \sum_{l \in N_U} p_{dl}^{\text{ul}} h_{i,l}^{H} \right)^{-1} h_{i,k,k},
\]
\[
\Gamma_{E,k}^{dl} = \frac{Q_{E,k}^{dl} h_{i,k}^{\text{ul}}}{\sum_{l \in N_U} p_{dl}^{\text{ul}} h_{i,l}^{H}} \left( \sigma_{z,dl}^2 I + \sum_{l \in N_U} h_{i,l}^{H} Q_{E,k}^{dl} h_{i,k,l} \right)^{-1} h_{i,k,k},
\]
\[
\theta_{E,k}^{dl} = \frac{Q_{E,k}^{dl} h_{i,k}^{\text{ul}}}{\sum_{l \in N_U} p_{dl}^{\text{ul}} h_{i,l}^{H}} \left( \sigma_{z,dl}^2 I + \sum_{l \in N_U} h_{i,l}^{H} Q_{E,k}^{dl} h_{i,k,l} \right)^{-1} h_{i,k,k}. \]

Using the alternative representations (24) and (32) to the non-convex constraints (31d)–(31f), we restate the problem (31) with additional optimization variables \( \lambda, \Gamma \triangleq \{\Gamma_{E,k}^{dl}, \Gamma_{E,k}^{ul}\} \subseteq N_U \) and \( \theta \triangleq \{\theta_{E,k}^{dl}, \theta_{E,k}^{ul}\} \subseteq N_U \). We tackle the obtained problem by alternately optimizing the variables \( \{\lambda, \rho, Q, F, C, P, \tau, R\} \) and \( \{\lambda, \Gamma, \theta\} \). The detailed algorithm is summarized in Algorithm 2. Similarly to Algorithm 1, Algorithm 2 achieves monotonically decreasing latency with respect to the number of iterations, where its solution converges to a locally optimal point of (31) due to its non-convexity. In Sec. V, we initialize the variables \( \{C, F, P\} \) and \( \{\rho, Q\} \) as arbitrary values/matrices that satisfy the constraints (31b)–(31l), and set \( t \leftarrow 1 \).

1. Initialize \( \{c, \rho, Q, F, C, P, \tau, R\} \) as arbitrary values/matrices that satisfy the constraints (31b)–(31l), and set \( t \leftarrow 1 \).
2. Calculate the total latency \( \tau_T \) in (22) with the initialized \( \{c, \rho, Q, F, C, P, \tau, R\} \), and set \( \tau_T^{(0)} \leftarrow \tau_T \).
3. Set \( \{\lambda, \Gamma, \theta\} \) according to (25) and (34).
4. Update the variables \( \{c, \rho, Q, F, C, P, \tau, R\} \) as a solution of the convex problem which is obtained by replacing the constraints (31d)–(31f) with (24)–(24) in (23), (32a) and (32d) and then by fixing \( \{\lambda, \Gamma, \theta\} \).
5. Calculate the total latency \( \tau_T \) in (22) with the updated \( \{c, \rho, Q, F, C, P, \tau, R\} \), and set \( \tau_T^{(t)} \leftarrow \tau_T \).
6. Stop if \( \tau_T^{(t)} - \tau_T^{(t-1)} \leq \delta \) or \( t > t_{\text{max}} \). Otherwise, set \( t \leftarrow t + 1 \) and go back to Step 2.

IV. OPTIMIZATION FOR THE C-RAN ARCHITECTURE

In this section, we investigate the design of collaborative cloud and edge mobile computing system within a C-RAN architecture [18–20]. In C-RAN, the baseband signals of distributed ENs are processed by the CP in a centralized manner for the purpose of effective interference management. In the following subsections, we describe the uplink and downlink communication phases and the total end-to-end latency required for completing all the tasks, and discuss the joint optimization of C-RAN signal processing and computational resource allocation strategies.

A. Uplink Communication and Latency

As illustrated in Sec. II-A, each UE \( k \) splits its computation input information into two parts of \( c_k b_{1,k} \) and \( (1-c_k) b_{1,k} \) bits, and sends the former and latter parts to its serving EN \( i_k \) and the CP, respectively. In the D-RAN protocol detailed in Sec. IIIB both parts were encoded into a single codeword, since all the input information had to be decoded by the serving EN \( i_k \). However, in the C-RAN scheme, only one part is decoded by EN \( i_k \), and the other codeword is decoded by the CP based on the fronthaul received signals. To accommodate this requirement, we leverage superposition coding as discussed next.

We denote the encoded signals for the two parts of \( c_k b_{1,k} \) and \( (1-c_k) b_{1,k} \) bits by \( s_{E,k}^{\text{ul}} \) and \( s_{C,k}^{\text{ul}} \) respectively. Under independent Gaussian channel codebooks, the two signals are independent of each other and distributed as \( s_{E,k}^{\text{ul}} \sim \mathcal{CN}(0, P_{E,k}^{\text{ul}}) \) and \( s_{C,k}^{\text{ul}} \sim \mathcal{CN}(0, P_{C,k}^{\text{ul}}) \). UE \( k \) transmits a superposition of the encoded signals so that the transmit signal \( x_{E,k}^{\text{ul}} \) is given as
\[
x_{E,k}^{\text{ul}} = s_{E,k}^{\text{ul}} + s_{C,k}^{\text{ul}},
\]
and the transmit power constraint (3) can be written as \( P_{E,k}^{\text{ul}} + P_{C,k}^{\text{ul}} \leq P_{\text{ul}}^{\text{ul}} \).

Based on the uplink received signal \( y_{E,k}^{\text{ul}} \), EN \( i \) detects the signals \( s_{E,k}^{\text{ul}} \) transmitted by its serving UEs \( k \in N_{U,i} \). The achievable rate \( R_{E,k}^{\text{ul}} \) of each signal \( s_{E,k}^{\text{ul}} \) in bps is given as
\[
R_{E,k}^{\text{ul}} = W^{\text{ul}} I\left(s_{E,k}^{\text{ul}}; y_{E,k}^{\text{ul}}\right)
\]
where
\[
I\left(s_{E,k}^{\text{ul}}; y_{E,k}^{\text{ul}}\right) = E_{E,k}^{\text{ul}} \left( P_{E,k}^{\text{ul}} p_{E,k}^{\text{ul}} \right) =
\]
\[
\Psi \left( P_{E,k}^{\text{ul}} h_{i,k}^{\text{ul}} h_{i,k}^{H}, \left( \sigma_{z,\text{ul}}^2 I + \sum_{l \in N_U \setminus \{k\}} P_{E,k}^{\text{ul}} h_{i,l}^{H} \right) \right).
\]
Here we have defined \( p_{E,k}^{\text{ul}} \triangleq \{\frac{p_{E,k}^{\text{ul}}}{p_{C,k}^{\text{ul}}}\} k \in N_{U} \).
The quantization model (39), the compression rate \( \hat{\gamma} \), modeled as \( \gamma \) channel as in [19], [20]. Then, the quantized signal \( \hat{\gamma} \) is approximately link of capacity \( W \) bit/s/Hz for uploading the input information of the UEs on the uplink channel is given as \( \gamma_E^d = \max_{k \in \mathcal{N}_U} \left\{ c_k b_{1,k} \left( W_{ul}^d I_{E,k}^d(p) + W_{ul}^d I_{C,k}^d(p, \Omega_d^d) \right) \right\} \). (44)

\section{Downlink Communication and Latency}

After completing the computation tasks, the CP encodes the computation output information of (1 - \( c_k b_{1,k} \)) bits for each UE \( k \) with Gaussian channel codebook and obtains an encoded baseband signal \( s_{C,k}^d \). The CP computes a signal vector \( \tilde{x}_i^d \) in \( \mathcal{C}_N^e \times 1 \) by superimposing the encoded signals as \( \tilde{x}_i^d = \sum_{k \in \mathcal{N}_i} s_{C,k}^d \). (45)

The ith subvector \( \tilde{x}_i^d \) is transferred to \( i \) on the fronthaul link. To this end, it is quantized, and we model the quantized signal \( \hat{x}_i^d \) under the Gaussian test channel [19], [20] as \( \hat{x}_i^d = \tilde{x}_i^d + q_i^d \), (46)

where the quantization distortion noise \( q_i^d \) is independent of \( \tilde{x}_i^d \) and is distributed as \( q_i^d \sim \mathcal{C}\mathcal{N}(0, \Omega_d^d) \). Under the quantization model [39], the compression rate \( \hat{\gamma}_i^d \), that equals the number of bits representing the quantized signal \( \hat{\gamma}_i^d \) per baseband sample, is given as \( \hat{\gamma}_i^d = \log_2 \det \left( \sum_{k \in \mathcal{N}_i} c_k b_{1,k} \Omega_d^d \right) \). (40)

\[ \hat{\gamma}_i^d = \frac{W_{ul}^d I_{E,k}^d(p) + W_{ul}^d I_{C,k}^d(p, \Omega_d^d)}{C_F} \] (41)

The CP recovers the quantized signals \( \hat{\gamma}_1^d, \hat{\gamma}_2^d, \ldots, \hat{\gamma}_{\mathcal{N}_i}^d \) from the bit streams received on the fronthaul link. The vector \( \hat{\gamma}^d = [\hat{\gamma}_1^d H; \hat{\gamma}_2^d H; \ldots; \hat{\gamma}_{\mathcal{N}_i}^d H] \), which stacks the quantized signals from all ENs, can be written as

\[ \hat{\gamma}^d = \sum_{i \in \mathcal{N}_i} \tilde{h}_i^d H + \epsilon_{E,i}^d H + \epsilon_{C,i}^d H \] by \( \epsilon_{E,i}^d \) and \( \epsilon_{C,i}^d \) are distributed as \( \epsilon_{E,i}^d \sim \mathcal{C}\mathcal{N}(0, \Omega_{E,i}^d) \) and \( \epsilon_{C,i}^d \sim \mathcal{C}\mathcal{N}(0, \Omega_{C,i}^d) \), respectively, with \( \Omega^d = \text{diag}(\{\Omega_{E,i}^d\}_{i \in \mathcal{N}_i}) \).

Using the recovered quantized signal vector \( \hat{\gamma}^d \), the CP detects all the signals \( s_{C,k}^d \), which are necessary for cloud computing. The achievable rate \( P_{C,k}^d \) of the signal \( s_{C,k}^d \) is given as \( P_{C,k}^d = W_{ul}^d I_{C,k}^d(\hat{\gamma}^d) \), where the mutual information value is computed as

\[ I_{C,k}^d(\hat{\gamma}^d) = I_{E,k}^d(\hat{\gamma}^d) \] (43)

Consequently, the latency \( \tau_E^d \) for uploading the input information of the UEs on the uplink channel is given as

Each EN \( i \) also encodes the edge computation output information for UE \( k \in \mathcal{N}_U \) of \( c_k b_{1,k} \) bits producing an encoded baseband signal \( s_{C,k}^d \) in \( \mathcal{C}_N^e \times 1 \sim \mathcal{C}\mathcal{N}(0, \Omega_{C,k}^d) \). EN \( i \) then transmits a superposition of the locally encoded signals \( s_{C,k}^d \), \( k \in \mathcal{N}_U \), and the quantized signal \( \tilde{x}_i^d \), which was received on fronthaul, over the downlink channel to UEs. Thus, the signal \( \tilde{x}_i^d \) transmitted by \( i \) is given as

\[ \tilde{x}_i^d = \sum_{k \in \mathcal{N}_U} s_{C,k}^d + \tilde{x}_i^d \] (49)

With (49), the transmit power constraint (3) at EN \( i \) can be written as

\[ \sum_{k \in \mathcal{N}_U} \left( \epsilon_{E,k}^d \right)^2 + \sum_{k \in \mathcal{N}_U} \left( \epsilon_{C,k}^d \right)^2 + \left( \Omega_{C,k}^d \right)^2 \leq P_{ul}^d \] (50)

The first term in the left-hand side (LHS) measures the power of the signals \( \{s_{C,k}^d\}_{k \in \mathcal{N}_U} \), which encode the computation output information processed by EN \( i \). The sum of the second and third terms is the power of the signal \( \tilde{x}_i^d \), which is a quantized version of \( \hat{x}_i^d \) that encodes the signals \( \{s_{C,k}^d\}_{k \in \mathcal{N}_U} \) processed by the CP.
Each UE $k$ detects the signals $s^d_{E,k}$ and $s^d_{C,k}$ based on
the downlink received signal $y^d_k$. The achievable rates of $s^d_{E,k}$ and $s^d_{C,k}$ are given as $R^d_{E,k} = W^d I(s^d_{E,k}; y^d_k)$ and $R^d_{C,k} = W^d I(s^d_{C,k}; y^d_k)$, respectively, with

$$I(s^d_{E,k}; y^d_k) = f^d_{E,k}(Q^d, \Omega^d)$$

$$\Psi_{\Omega^d}(h^d_{E,k}, \Omega^d_{E,k}) = \left( \sum_{i \in \Omega^d_{E,k} \setminus \{k\}} h^d_{E,k,i} + \sum_{i \in \Omega^d_{E,k} \setminus \{k\}} h^d_{E,k,i} + h^d_{E,k,k} \right)$$

$$I(s^d_{C,k}; y^d_k) = f^d_{C,k}(Q^d, \Omega^d) = \Psi_{\Omega^d}(h^d_{C,k}, \Omega^d_{C,k})$$

Here, we have defined $h^d_{E,k} = [h^d_{E,k,1}, h^d_{E,k,2}, \ldots, h^d_{E,k,N_E}]^H$ and $\Omega^d = \text{diag}(\{\Omega^d_i\}_{i \in N_E})$.

With the downlink rates described above, the latency $\tau^d_k$ for downloading the output information on the downlink channel is given as

$$\tau^d_k = \max_{k \in N_U} \left\{ \frac{c_k b_{O,k}}{W^d f^d_{E,k}(Q^d, \Omega^d)} \left(1 - \frac{c_k b_{O,k}}{W^d f^d_{C,k}(Q^d, \Omega^d)} \right) \right\}.$$  

C. Total End-to-End Latency With C-RAN

The total end-to-end latency $\tau_T$ for completing all the tasks within the described C-RAN architecture is modeled as

$$\tau_T = \tau^d_k + \max_{k \in N_U} \left\{ \tau^e_{E,k} + \tau^e_{C,k} + \tau^f_k \right\} + \tau^d_k,$$

where the fronthaul latency $\tau^d_k$, $\tau^d_k$, and $\tau^d_k$ and the edge latency $\tau^e_{E,k}$, $\tau^e_{C,k}$ are defined in (41), (48), (44) and (52), respectively. Also, $\tau^e_{E,k}$ and $\tau^e_{C,k}$ represent the latency for executing the computation tasks at the ENs and CP which are given as

$$\tau^e_{E,k} = \max_{k \in N_U} \left\{ \tau^e_{E,k} \right\} \quad \text{and} \quad \tau^e_{C,k} = \max_{k \in N_U} \left\{ \tau^e_{C,k} \right\}.$$  

D. Optimization

We aim at jointly optimizing the task splitting variables $c$, the uplink $\{p^d, \Omega^d\}$ and downlink communication strategies $\{Q^d, \Omega^d\}$ with the goal of minimizing the end-to-end latency $\tau_T$ in (53). The problem at hand can be stated as

$$\min_{p^d, \Omega^d} \tau^d_k + \max_{k \in N_U} \left\{ \tau^e_{E,k} + \tau^e_{C,k} + \tau^d_k \right\} + \tau^d_k$$

s.t. $\tau^d_k \geq \frac{c_k b_{O,k}}{R^d_{E,k}}$, $k \in N_U$,  

$$\tau^d_k \geq \frac{(1 - c_k) b_{O,k}}{R^d_{C,k}}$$

$$\tau^d_k \geq \frac{W^d f^d_{E,k}(p^d, \Omega^d)}{C^d}$$

$$\tau^d_k \geq \frac{c_k b_{O,k}}{R^d_{E,k}}, \quad k \in N_U$$

$$\tau^d_k \geq \frac{(1 - c_k) b_{O,k}}{R^d_{C,k}}, \quad k \in N_U$$

$$\tau^d_k \geq \frac{W^d f^d_{E,k}(p^d, \Omega^d)}{C^d}, \quad i \in N_E$$

$$\tau^d_k \geq \frac{W^d f^d_{E,k}(p^d, \Omega^d)}{C^d}, \quad i \in N_E$$

$$\tau^d_k \geq \frac{W^d f^d_{E,k}(p^d, \Omega^d)}{C^d}, \quad i \in N_E$$

$$\tau^d_k \geq \frac{W^d f^d_{E,k}(p^d, \Omega^d)}{C^d}, \quad i \in N_E$$

$$R^d_{E,k} \leq W^d f^d_{E,k}(p^d), \quad k \in N_U$$

$$R^d_{C,k} \leq W^d f^d_{C,k}(p^d, \Omega^d)$$

$$R^d_{E,k} \leq W^d f^d_{E,k}(Q^d, \Omega^d), \quad k \in N_U$$

$$R^d_{C,k} \leq W^d f^d_{C,k}(Q^d, \Omega^d), \quad k \in N_U$$

We note that it is more difficult to solve problem (55) than problems (23) and (31) for D-RAN, since (55) involves more optimization variables including the fronthaul quantization strategies $\Omega^d$ and $\Omega^d$, and the constraints (55b) and (55c) on the fronthaul latency have a more complicated form than (23) and (24a) for D-RAN systems. To address these complications, we apply FP and matrix FP [27], [28] as in the methodology outlined above for D-RAN as well as the convex approximation method introduced in [19] Lem. 1.

To this end, we first replace the constraints (55b) with (24c) and (24d) which are convex for fixed $\lambda^e_{E,k}$ and $\lambda^e_{C,k}$ and become equivalent to (55h) when $\lambda^e_{E,k}$ and $\lambda^e_{C,k}$ are given as (25). Similarly, based on [27] Cor. 1, we consider the following constraints which are stronger than (55b), (55c), (55e) and (55f):

$$2 \lambda^d_{E,k} \sqrt{\tau^d_k - (\lambda^d_{E,k})^2} \geq \frac{b_{O,k}}{R^d_{E,k}}, \quad k \in N_U$$

$$2 \lambda^d_{C,k} \sqrt{\tau^d_k - (\lambda^d_{C,k})^2} \geq (1 - c_k) \frac{b_{O,k}}{R^d_{C,k}}$$

$$2 \lambda^d_{E,k} \sqrt{\tau^d_k - (\lambda^d_{E,k})^2} \geq \frac{b_{O,k}}{R^d_{E,k}}, \quad k \in N_U$$

$$2 \lambda^d_{C,k} \sqrt{\tau^d_k - (\lambda^d_{C,k})^2} \geq (1 - c_k) \frac{b_{O,k}}{R^d_{C,k}}, \quad k \in N_U.$$
The above constraints become equivalent to (55b), (55c), (55d) and (55f) if
\[
\lambda_{m,k}^m = \frac{\sqrt{r_{F}^m}}{c_k} \quad \text{and} \quad \lambda_{C,k}^m = \frac{\sqrt{r_{F}^m}}{1 - c_k},
\]
for \(m \in \{ul, dl\}\).

Now, we discuss the non-convex constraints (55d) and (55f). Using the epigraph form, the constraint (55d) can be restated as
\[
\tau_d^i \geq \frac{W_{ul,i}^m}{C_F} \quad \text{if} \quad i \in N_E,
\]
and
\[
\gamma_i^u \geq g_{ul,i}^u \left(p_i^u, \Omega_i^u\right), \quad i \in N_E.
\]

From [27, Cor. 1] and [19, Lem. 1], the following constraints are stricter than (58):

\[
\frac{W_{ul,i}^m}{C_F} \leq \frac{\alpha^u}{\tau_d^i} - (\alpha^u)^2 \tau_d^i, \quad i \in N_E,
\]
and
\[
\gamma_i^u \geq \log_2 \det \left(\Sigma_i^u\right) + \frac{1}{\ln 2} \times \left(\sum_{k \in N_U \setminus N_i} p_{E,E,k}^u h_{i,k}^u h_k^H + \right.
\]
\[
\left.\sum_{k \in N_U} p_{C,k}^u h_{i,k}^u h_k^H + \alpha^u \gamma_i^u \bar{I} + \Omega_i^u\right), \quad i \in N_E.
\]

If we fix the auxiliary variables \(\alpha^u\) and \(\Sigma_i^u\), the constraints (59) are convex. Also, by replacing the constraints (58) with (59), we can restate the non-convex constraints (55) as a solution of the convex problem which is obtained from (55) by replacing the constraints (55b)-(55l) with (24c), (56), (59), (61) and (63), and fixing the variables \(\lambda, \alpha, \Sigma, \Gamma, \Theta\).

Lastly, using [28, Cor. 1], we replace the remaining non-convex constraints (55i)-(55l) with the following stricter constraints:

\[
\frac{R_{ul,E,k}^l}{W_{ul}^m} \leq \left(\frac{\sum_{k \in N_U \setminus N_i} p_{E,E,k}^u h_{i,k}^u h_k^H + \alpha^u \gamma_i^u \bar{I} + \Omega_i^u}{C_F} \right), \quad \text{for} \quad k \in N_U.
\]

The above constraints are equivalent to (55a) if
\[
\alpha^u = \frac{\sqrt{r_{F}^m}}{\tau_d^i} \quad \text{and} \quad \Sigma_i^u = \sum_{k \in N_U \setminus N_i} h_{i,k}^u h_k^H + \sum_{k \in N_U} p_{C,k}^u h_{i,k}^u h_k^H + \sigma_{ul,i}^2 \bar{I} + \Omega_i^u.
\]

Similarly, instead of (55g) for downlink, we consider the following stricter constraints:

\[
\frac{W_{dl,i}^m}{C_F} \leq \frac{\alpha^d}{\tau_d^i} - (\alpha^d)^2 \tau_d^i, \quad i \in N_E,
\]
and
\[
\gamma_i^d \geq \log_2 \det \left(\Sigma_i^d\right) + \frac{1}{\ln 2} \times \left(\sum_{k \in N_U \setminus N_i} E_i^H \hat{Q}_{C,k}^d \hat{Q}_{C,k}^d E_i + \Omega_i^d\right) - \frac{n_{E,i}}{\ln 2} \log_2 \det \left(\Omega_i^d\right) \quad \text{if} \quad i \in N_E.
\]

The above constraints are equivalent to (55f) if
\[
\alpha^d = \frac{\sqrt{r_{F}^m}}{\tau_d^i} \quad \text{and} \quad \Sigma_i^d = \sum_{k \in N_U} E_i^H \hat{Q}_{C,k}^d \hat{Q}_{C,k}^d E_i + \Omega_i^d.
\]

Algorithm 3 Alternating optimization algorithm that tackles problem (55)

1. Initialize \(\{p, c, Q, \Omega, \tau, R\}\) as arbitrary matrices/values that satisfy the constraints (55b)-(55l), and set \(t \leftarrow 1\).
2. Calculate the total latency \(T_r\) in (55) with the initialized \(\{p, c, Q, \Omega, \tau, R\}\), and set \(t \leftarrow t + 1\).
3. Set \(\{\lambda, \alpha, \Sigma, \Gamma, \Theta\}\) according to (25), (57), (60), (62) and (64).
4. Update \(\{p, c, Q, \Omega, \gamma, \tau, R\}\) as a solution of the convex problem which is obtained from (55) by replacing the constraints (55b)-(55l) with (24c), (24d), (56), (59), (61) and (63), and fixing the variables \(\lambda, \alpha, \Sigma, \Gamma, \Theta\).
5. Calculate the total latency \(T_r\) in (55) with the updated \(\{p, c, Q, \Omega, \gamma, \tau, R\}\), and set \(t \leftarrow t + 1\).
6. Stop if \(|T_r^t - T_r^{t-1}| \leq \delta\) or \(t > t_{\text{max}}\). Otherwise, set \(t \leftarrow t + 1\) and go back to Step 3.
of downlink signals \( Q \) and quantization noise signals \( \Omega \), we first set

\[
Q_{E,k} \leftarrow \mathbf{V}_{E,k} \mathbf{V}_{E,k}^H, \quad k \in \mathcal{N}_U, \quad i \in \mathcal{N}_E, \tag{65a}
\]

\[
Q_{C,k} \leftarrow \mathbf{V}_{C,k} \mathbf{V}_{C,k}^H, \quad k \in \mathcal{N}_U, \tag{65b}
\]

\[
\Omega_{i} \leftarrow \mathbf{V}_{\Omega,i} \mathbf{V}_{\Omega,i}^H, \quad i \in \mathcal{N}_E, \tag{65c}
\]

where the elements of \( \mathbf{V}_{E,k} \in \mathbb{C}^{n_{E} \times n_{E}} \), \( \mathbf{V}_{C,k} \in \mathbb{C}^{n_{E} \times n_{E}} \) and \( \mathbf{V}_{\Omega,i} \in \mathbb{C}^{n_{E} \times n_{E}} \) follow \( \mathcal{CN}(0,1) \). The covariance matrices obtained in (65) may not satisfy the power constraints (50). To resolve this issue, we repeatedly multiply a scalar \( \eta < 1 \) to the matrices \( Q \) and \( \Omega \) until the constraints (50) are satisfied. In the simulation, we set \( \eta = 1/2 \). Once the variables \( \{ p, c, Q, \Omega \} \) are fixed, the rate variables \( R \) can be computed using (37), (43) and (51), and the latency variables \( \tau \) are initialized as (41), (44), (48), and (52).

As discussed in Sec. III, the complexity of Algorithm 3 is given by the number of iterations multiplied by the complexity of solving the convex problem at Step 4. The complexity of the latter is upper bounded by \( \mathcal{O}(n^3 + M) \log(1/\epsilon) \) (22) p. 4, where the numbers \( n \) and \( M \) equal \( n = N_U(4\eta^2(N_E^2 + 1) + 10) + N_E(8\eta^2 + 2) + 6 \) and \( M = (8\eta^2 N_U + D_{rE}) N_E + 4 N_U N_E^2 (8\eta^2 N_E + 3 N_U) + 50 N_U + 5 N_E \), respectively. Here \( D_L \) is defined as the number of arithmetic operations needed to calculate the determinant of an \( L \times L \) matrix, which is given as \( D_L = \mathcal{O}(L^3) \) with Gaussian elimination (37) p. 1. We discuss the convergence rate of Algorithm 3 in Sec. V.

\[
\Gamma_{E,k}^{ul} = p_{E,k}^{ul} \mathbf{h}_{k,k}^{ulH} \left( \sigma_{z,ul}^2 \mathbf{I} + \sum_{l \in \mathcal{N}_U \setminus \{ k \}} p_{l,E}^{ul} \mathbf{h}_{k,l}^{ulH} \mathbf{h}_{i,k,l}^{ul} + \sum_{l \in \mathcal{N}_E} p_{E,l}^{ul} \mathbf{h}_{i,k,l}^{ulH} \mathbf{h}_{k,l}^{ul} \right)^{-1} \mathbf{h}_{k,k}^{ul}, \tag{64a}
\]

\[
\Theta_{E,k}^{ul} = p_{E,k}^{ul} \left( \sigma_{z,ul}^2 \mathbf{I} + \sum_{l \in \mathcal{N}_U} p_{l,E}^{ul} \mathbf{h}_{k,l}^{ulH} \mathbf{h}_{i,k,l}^{ul} + \sum_{l \in \mathcal{N}_E} p_{E,l}^{ul} \mathbf{h}_{i,k,l}^{ulH} \mathbf{h}_{k,l}^{ul} \right)^{-1} \mathbf{h}_{k,k}^{ul}, \tag{64b}
\]

\[
\Gamma_{C,k}^{dl} = p_{C,k}^{dl} \mathbf{h}_{k,k}^{dlH} \left( \sigma_{z,dl}^2 \mathbf{I} + \Omega_{dl} + \sum_{l \in \mathcal{N}_U} p_{E,l}^{dl} \mathbf{h}_{k,l}^{dlH} \mathbf{h}_{k,l}^{dl} + \sum_{l \in \mathcal{N}_E} p_{C,l}^{dl} \mathbf{h}_{k,l}^{dlH} \mathbf{h}_{k,l}^{dl} \right)^{-1} \mathbf{h}_{k,k}^{dl}, \tag{64c}
\]

\[
\Theta_{C,k}^{dl} = p_{C,k}^{dl} \left( \sigma_{z,dl}^2 \mathbf{I} + \Omega_{dl} + \sum_{l \in \mathcal{N}_U} p_{E,l}^{dl} \mathbf{h}_{k,l}^{dlH} \mathbf{h}_{k,l}^{dl} + \sum_{l \in \mathcal{N}_E} p_{C,l}^{dl} \mathbf{h}_{k,l}^{dlH} \mathbf{h}_{k,l}^{dl} \right)^{-1} \mathbf{h}_{k,k}^{dl}, \tag{64d}
\]

\[
\Gamma_{E,k}^{dl} = \mathbf{Q}_{E,k}^{dlH} \mathbf{Q}_{E,k}^{dl} \left( \sigma_{z,dl}^2 \mathbf{I} + \mathbf{h}_{k,k}^{dlH} \Omega_{dl} \mathbf{h}_{k,k}^{dl} + \sum_{l \in \mathcal{N}_U} \mathbf{h}_{k,l}^{dlH} \mathbf{Q}_{E,l} \mathbf{h}_{k,l}^{dl} + \sum_{l \in \mathcal{N}_E} \mathbf{h}_{k,l}^{dlH} \mathbf{Q}_{C,l} \mathbf{h}_{k,l}^{dl} \right)^{-1} \mathbf{h}_{k,k}^{dl}, \tag{64e}
\]

\[
\Theta_{E,k}^{dl} = \left( \sigma_{z,dl}^2 + \mathbf{h}_{k,k}^{dlH} \Omega_{dl} \mathbf{h}_{k,k}^{dl} + \sum_{l \in \mathcal{N}_U} \mathbf{h}_{k,l}^{dlH} \mathbf{Q}_{E,l} \mathbf{h}_{k,l}^{dl} + \sum_{l \in \mathcal{N}_E} \mathbf{h}_{k,l}^{dlH} \mathbf{Q}_{C,l} \mathbf{h}_{k,l}^{dl} \right)^{-1} \mathbf{h}_{k,k}^{dl}, \tag{64f}
\]

\[
\Gamma_{C,k}^{dl} = \mathbf{Q}_{C,k}^{dlH} \mathbf{Q}_{C,k}^{dl} \left( \sigma_{z,dl}^2 + \mathbf{h}_{k,k}^{dlH} \Omega_{dl} \mathbf{h}_{k,k}^{dl} + \sum_{l \in \mathcal{N}_U} \mathbf{h}_{k,l}^{dlH} \mathbf{Q}_{E,l} \mathbf{h}_{k,l}^{dl} + \sum_{l \in \mathcal{N}_E} \mathbf{h}_{k,l}^{dlH} \mathbf{Q}_{C,l} \mathbf{h}_{k,l}^{dl} \right)^{-1} \mathbf{h}_{k,k}^{dl}, \tag{64g}
\]

\[
\Theta_{C,k}^{dl} = \left( \sigma_{z,dl}^2 + \mathbf{h}_{k,k}^{dlH} \Omega_{dl} \mathbf{h}_{k,k}^{dl} + \sum_{l \in \mathcal{N}_U} \mathbf{h}_{k,l}^{dlH} \mathbf{Q}_{E,l} \mathbf{h}_{k,l}^{dl} + \sum_{l \in \mathcal{N}_E} \mathbf{h}_{k,l}^{dlH} \mathbf{Q}_{C,l} \mathbf{h}_{k,l}^{dl} \right)^{-1} \mathbf{h}_{k,k}^{dl}. \tag{64h}
\]

V. Numerical Results

In this section, we validate via numerical results the performance gain of the proposed C-RAN architecture as compared to the D-RAN reference system. We assume that the locations of \( N_U \) UEs and \( N_E \) ENs are independently and uniformly sampled from a square area with side length of 500 m. We impose the minimum separation of 10 m between any pair of UE and EN. We consider a path-loss model \( p_{0}(d_{0})^{-\eta} \) [38], [39], where \( p_0 \) is the path-loss at a reference distance \( d_0 \), \( d \) denotes the distance between the transmitting and receiving nodes, and \( \eta \) is the path-loss exponent. We set \( d_0 = 30 \) m, \( p_0 = 10 \) dB and \( \eta = 3 \), and assume independent Rayleigh small-scale fading channel model for all the channel coefficients. We consider a symmetric system between uplink and downlink with \( \text{SNR}_{ul} = \text{SNR}_{dl} = \text{SNR}_{max} \), \( W_{ul} = W_{dl} = W \), and \( C_{F} = C_{ul} = C_{dl} \). The computation capabilities of CP and ENs are set to \( F_{C} = 10^{11} \) [41] and \( F_{E,i} \in \{1,0.25\} \times 10^{10} \) [13], [40], respectively, unless stated otherwise. We also assume that there are \( b_{i,k} = b_{O,k} = 10^6 \) input and output bits for each UE and that the task of each UE \( k \) requires \( V_k = 700 \) CPU cycles per input bit [8]. To solve the convex problems at Step 4 of Algorithms 1, 2 and 3, CVX software [31] with SDPT3 solver [41] is adopted. Without claim of optimality, we associate each UE \( k \) with the closest EN, so that \( i_k \) is set to

\[
i_k = \arg\min_{i \in \mathcal{N}_E} \text{dist}_{i,k}, \tag{66}
\]

with \( \text{dist}_{i,k} \) represents the geographical distance between UE \( k \) and EN \( i \).
A. Convergence of the Proposed Algorithm

The convergence rate of FP is analyzed in [27] with a focus on single-ratio problems, and reference [28] discusses the convergence rate of matrix FP via numerical examples. Similar to [28], we provide numerical evidence of the fast convergence of the proposed algorithms in Fig. 2. In the figure, we plot the end-to-end latency $\tau_T$ versus the number of iterations ($N_U = 4$, $N_E = 2$, $n_{E,i} = 2$, $W = 20$ MHz, $C_F = 1$ Gbps, $F_{E,i} = 10^{10}$ and $SNR_{\text{max}} = \{0, 20\}$ dB). We plot both the snapshots and average latency, where the latter is averaged over 100 channel samples. The figure shows that, regardless of the SNR, the proposed algorithms converge reliably within a few iterations. We leave the analysis of the convergence rate of the proposed algorithms for future work. Throughout the following experiments, we set the threshold value for convergence as $\delta = 10^{-4}$ and limit the maximum number of iterations to $t_{\text{max}} = 30$.

B. Performance Gains of the C-RAN Architecture

In this subsection, we investigate the performance gains of the C-RAN architecture introduced in Sec. IV for collaborative cloud and edge mobile computing as compared to benchmark D-RAN systems described in Sec. III. To this end, in Fig. 3, we plot the average end-to-end latency $\tau_T$ versus the fronthaul capacity $C_F$ for $N_U = 4$, $N_E = 2$, $n_{E,i} = 2$, $W = 20$ MHz, $F_{E,i} = 10^{10}$ and $SNR_{\text{max}} = 20$ dB. The figure shows that deploying C-RAN architecture is not advantageous when the fronthaul capacity $C_F$ is small due to the large latency caused by the fronthaul transmission. However, as $C_F$ increases, the C-RAN scheme significantly outperforms the benchmark D-RAN schemes, since it enables more effective interference management by means of centralized encoding and decoding at CP.

In Fig. 4, we examine the average energy consumption at UEs under
the same set-up considered in Fig. 3. We calculate the energy consumption at UE $k$ as $E_k = E_{k}^{\text{ul}} + E_{k}^{\text{dl}}$, where the uplink and downlink energy expenditures are defined as $E_{k}^{\text{ul}} = \tau_{E,k}^{\text{ul}} p_{k}^{\text{ul}}$ and $E_{k}^{\text{dl}} = \tau_{E,k}^{\text{dl}} d_{k}^{\text{dl}}$, respectively. Here, $d_{k}^{\text{ul}}$ indicates the mobile receiving energy expenditure per second in downlink, and is set to $d_k = 0.625 \text{ J/s}$ as in [13]. The uplink transmit power $p_{k}^{\text{ul}}$ of UE $k$ is respectively given as $p_{k}^{\text{ul}} = p_{k}^{\text{ll}}$ and $\tilde{p}_{k}^{\text{ul}} = \tilde{p}_{E,k}^{\text{ul}} + \tilde{p}_{C,k}^{\text{ul}}$ for the D-RAN and C-RAN systems. Unlike D-RAN, the energy consumption of UEs with C-RAN decreases with $C_F$. This is because the ENs and CP can exchange quantized baseband signals of better resolution for larger $C_F$, and hence the latency on edge links becomes lower.

Fig. 5 plots the average end-to-end latency $\tau_T$ with respect to the number $n_{E,i}$ of antennas of each EN for $N_U = 3$, $N_E = 2$, $W = 20 \text{ MHz}$, $F_{E,i} = 10^{10}$, $C_F = 3 \text{ Gbps}$ and $\text{SNR}_{\text{max}} = 5 \text{ dB}$. Comparing the performance of D-RAN with different access techniques, we see that TDMA shows a lower latency than non-orthogonal access when the ENs use a small number of antennas. However, when the ENs are equipped with sufficiently many antennas, the non-orthogonal scheme outperforms the TDMA scheme, since the co-channel interference signals can be suppressed by local array processing at the ENs. In this case, each EN can suppress interference signals only with local processing, and hence C-RAN does not provide performance benefits, while significant gains are observed for lower values of $n_{E,i}$.

In Fig. 6 we plot the average end-to-end latency $\tau_T$ versus the number $N_E$ of ENs for $N_U = 8$, $n_{E,i} = 2$, $W = 50 \text{ MHz}$, $F_{E,i} = 2.5 \times 10^{10}$, $C_F = 2 \text{ Gbps}$ and $\text{SNR}_{\text{max}} = 20 \text{ dB}$. When the network has a single EN, i.e., $N_E = 1$, there is no advantage of deploying the C-RAN architecture in Sec. IV compared to D-RAN in Sec. III. This is because the noise signals caused from fronthaul quantization degrade the spectral efficiency for both uplink and downlink. However, as $N_E$ increases, C-RAN shows significantly improved latency performance than the D-RAN schemes. These gains are achieved by the centralized signal processing at the CP on behalf of the connected ENs, which enables effective interference management.

C. Performance Gains of Collaborative Cloud-Edge Computing

In this subsection, we study the performance gains of the collaborative cloud and edge computing system with optimized computational resource allocation as compared to benchmark schemes that rely only on edge computing (i.e., by setting $c_k = 1$ for all $k \in N_U$) or cloud computing (i.e., $c_k = 0$ for all $k \in N_U$). Note that the optimization of these benchmark schemes can be addressed by adopting the proposed algorithm with minor modifications. For reference, we also evaluate the performance of a hybrid strategy that selects between the two benchmark schemes. We adopt the optimized C-RAN architecture in Sec. IV for all cases except for edge computing, for which the C-RAN system is not applicable and hence we select D-RAN with non-orthogonal multiple access.

In Fig. 7 we plot the average end-to-end latency $\tau_T$ versus the fronthaul capacity $C_F$ for $N_U = 4$, $N_E = 2$, $n_{E,i} = 2$, $W = 50 \text{ MHz}$, $F_{E,i} = 2.5 \times 10^{10}$ and $\text{SNR}_{\text{max}} = 10 \text{ dB}$. Since edge computing does not utilize the fronthaul links, its performance is not affected by $C_F$. In contrast, the latency of cloud computing scheme decreases as $C_F$ increases. While selecting between edge and cloud computing schemes does not yield significant benefits, the proposed collaborative cloud and edge scheme achieves notable gains, particularly in the intermediate regime of $C_F$.

In Fig. 8 we plot the average end-to-end latency $\tau_T$ versus the maximum SNR for $N_U = 4$, $N_E = 2$, $n_{E,i} = 2$, $W = 100 \text{ MHz}$, $F_{E,i} = 2.5 \times 10^{10}$ and $C_F = 250 \text{ Mbps}$. The figure shows that, although increased SNR levels are beneficial for all the schemes, the performance of cloud computing is more significantly affected by the SNR than that of edge computing. This is because the edge latency of edge computing is limited by interference, and hence its performance saturates as the
we observe from the figure that, as the fronthaul task ratio variables are obtained from the proposed algorithm versus the fronthaul capacity scheme with optimized task allocation provides a notable gain.

tasks to ENs can improve the performance, and the proposed F4 is desired to choose F instead limited by the fronthaul capacity as SNR grows larger.

In Fig. [10] we plot the average task ratio c_k assigned to ENs versus the fronthaul capacity C_F for N_U ∈ {2, 4}, N_E = 2, n_E,i = 1, W = 100 MHz and F_E,i ∈ {0.1, 0.5} × 10^{10}. The task ratio variables are obtained from the proposed algorithm in Sec. [IV-D] We observe from the figure that, as the fronthaul capacity C_F increases, more tasks are assigned to CP due to reduced fronthaul latency. Similarly, as the ENs are equipped with stronger computing power F_E,i, they process a larger portion of tasks. Moreover, increasing the number N_U of UEs results in smaller ratios c_k, since the ENs with limited computing power offload more tasks to the CP when N_U is larger.

VI. CONCLUSIONS

We have studied the design of collaborative cloud and edge mobile computing within a C-RAN architecture for minimal end-to-end latency. We have tackled the joint design of computational resource allocation and C-RAN signal processing strategies with the goal of minimizing end-to-end latency required for completing the computational tasks of all the participating UEs in the network. To tackle the non-convex

SNR increases. The performance of the C-RAN scheme is instead limited by the fronthaul capacity as SNR grows larger.
optimization problem, we have applied FP and matrix FP. Via extensive numerical results, we have validated the convergence of the proposed optimization algorithms, the performance gain of C-RAN architecture as compared to D-RAN, and the impact of optimized computational resource allocation of collaborative cloud and edge computing. As future work, we mention the extension to collaborative AR [13], heterogeneous C-RAN and mobile computing integrated systems [42][44], the robust design with imperfect CSI [45], and the energy-efficient design [3][4] for energy-limited mobile UEs. Also, it would be relevant to verify the effectiveness of the proposed algorithms by deriving a tight lower bound on the optimal latency values.

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