Simultaneous Voltage and Current Measurement Instrumentation Amplifier for ECG and PPG Monitoring

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Abstract: An instrumentation amplifier (IA) capable of sensing both voltage and current at the same time has been introduced and applied to electrocardiogram (ECG) and photoplethysmogram (PPG) measurements for cardiovascular health monitoring applications. The proposed IA can switch between the voltage and current sensing configurations in a time-division manner faster than the ECG and PPG bandwidths. The application-specific integrated circuit (ASIC) of the proposed circuit design was implemented using 180 nm CMOS fabrication technology. Input-referred voltage noise and current noise were measured as 3.9 µVrms and 172 pArms, respectively, and power consumption was measured as 34.9 µA. In the current sensing configuration, a current noise reduction technique is applied, which was confirmed to be a 25 times improvement over the previous version. Using a single IA, ECG and PPG can be monitored in the form of separated ECG and PPG signals. In addition, for the first time, a merged ECG/PPG signal is acquired, which has features of both ECG and PPG peaks.

Keywords: reconfigurable instrumentation amplifier; electrocardiogram (ECG); photoplethysmogram (PPG)

1. Introduction

Cardiovascular disease has been the top leading cause of death for the past 30 years [1]. It is defined as a set of diseases that affect the heart and blood vessels and includes hypertension and atherosclerosis as typical diseases. Monitoring blood pressure and vessel stiffness in daily living, therefore, is essential for prophylactic healthcare. Heart and vascular health can be monitored using electrocardiogram (ECG) and photoplethysmogram (PPG) signals. An ECG signal represents an electrical signal that drives the heart. A PPG signal represents an optical signal that reflects the contraction and expansion of blood vessels. The degree of blood vessel stiffness can be estimated from the PPG waveform analysis [2]. In the case of a rigid blood vessel, a one peak waveform is formed due to a rapid reflection wave; on the other hand, when the blood vessel is soft, a two peaked waveform is formed due to a slow reflection wave. In the case of blood pressure measurement, it is accurate to measure using the volume-oscillometric method using a cuff on the upper arm; however, for convenience reasons for users, many researchers are trying to achieve cuffless blood pressure measurement [3–6]. Therefore, ECG and PPG are essential signals for cardiovascular health monitoring.

Conventionally, various dedicated chips have been used for various bio-signal sensing as shown in Figure 1a. For example, ADS1291 or AD8233 can be used for ECG sensing, and AFE4400 or ADPD105 can be used for PPG sensing. In addition, for body composition or breathing detection, AFE4300 or AD5933 can be used, and for measurement of blood glucose, a dedicated circuit is also required. Alternatively, a customized application-specific integrated circuit (ASIC) has been announced with multiple dedicated readout channels for ECG, PPG, and bio-Z applications [7]. Chip manufacturers prefer to produce a few items in large volume rather than many things in small quantities. To enhance large volume production, it should be able to measure ECG, PPG, body composition, and blood...
glucose using a common chip. A reconfigurable circuit is required. Inputs of various sensing applications are eventually classified as voltage and current signals. Therefore, a reconfigurable IA capable of voltage and current sensing is required. A reconfigurable IA based on a current-balanced IA structure was published and showed various applications as shown in Figure 1b [8]. A reconfigurable IA based on a three OP amp IA structure was also published in [9].

Figure 1. Difference between dedicated and reconfigurable AFE (Analog Front-End): (a) applications with conventional dedicated AFE; (b) applications with novel reconfigurable AFE.

In wearable and mobile environments, health monitoring devices use batteries. A low power consumption circuit is necessary to operate for a long time under battery power while avoiding the hassle of frequent charging. In this article, we propose a method to measure ECG and PPG at the same time by improving the reconfigurable IA in [8,10]. In the new method, two analog outputs are generated for ECG and PPG using a time-division method with a single IA and two sample and hold blocks. In the PPG measurement, a new noise reduction method is suggested and compared with the previous version. Most simply, a merged signal of ECG and PPG can be generated in one channel.

The developed chip can be mounted on two types of platforms used in mobile environments for cardiovascular health monitoring. One is a patch-type platform attached to the chest, and the other is a wristwatch-type platform.

Simple and miniaturized ECG patches to replace the existing complex and lined Holter monitors have been continuously developed [11–14]. In addition, in order to acquire various bio-signal data, there was also a patch study in which other types of bio-signal measurement functions such as PPG and bio-impedance were added in addition to ECG [15]. ECG and PPG measurements using chest patches allow continuous cardiovascular health monitoring as shown in Figure 2a. The patch platform with one reconfigurable readout channel introduced in this article is capable of four operational scenarios: patch-scenario (1), measuring only ECG or PPG; patch-scenario (2), alternately measuring ECG and PPG with a time-division method at the level of obtaining multiple pulses; patch-scenario (3), separating ECG and PPG signals after measuring by temporally multiplexing at a level faster than the bandwidth of the ECG and PPG; and patch-scenario (4), measuring ECG and PPG by synthesizing them into one signal at the same time.

ECG and PPG measurement functions can also be embedded in wristwatch-type platforms [7]. In a wristwatch platform as shown in Figure 2b, PPG can be measured continuously, but ECG can only be measured intermittently. PPG can be measured from your wrist using a PPG sensor mounted on the back of the watch. Two ECG electrodes are required for ECG measurement. The first ECG electrode is placed where the watch and wrist are in contact. The second ECG electrode is installed on the outer surface of the watch that is not in contact with the wrist. It should be positioned so that it can be easily touched with at least one finger of the opposite hand where the watch is not worn. On a wristwatch-type platform with one reconfigurable readout channel as introduced in this
article, the following operational scenarios are possible. The readout channel measures PPG normally in the current measurement mode, and when a touch occurs on the second ECG electrode, the readout channel switches to the voltage measurement mode to measure ECG.

In either of the above two platforms, the electrode connection lines will not be long and poorly conditioned like in a conventional ECG measurement system. If you want to analyze the QRST feature points of the ECG in detail, you can use the reconfigurable readout channel in this article dedicated to ECG, and if you want to analyze the feature points of the PPG in detail, you can set the readout channel to PPG only. For cuff-free blood pressure estimation applications, ECG and PPG can be measured using one reconfigurable readout channel by the time–division method or synthetic simultaneous measurement method.

This article is organized into the following sections: Section 2 details the working principle and design of the proposed circuit for a cardiovascular health monitoring application; Section 3 provides the measurement results of the fabricated IC; finally, Section 4 concludes this article.

2. Circuit Details and Working Principle

The top-level architecture of an ASIC measuring ECG and PPG is shown in Figure 3. There are two reconfigurable readout channels (CHANNEL1 and CHANNEL2) and 12-bit SAR ADC. Each readout channel has a reconfigurable IA (IA1 and IA2), which has a voltage sensing input and current sensing input. The TC1 stage can share its output to TI2 in CHANNEL2 via MUX1, and the TI1 stage can also share its output to SH2 in CHANNEL2 via MUX2.
Figure 3. ASIC top architecture and sensing interface connection.

2.1. Reconfigurable IA Capable of Voltage and Current Sensing

The structure of the proposed reconfigurable IA is based on the structure of the current balanced IA, which consists of a trans-conductance stage (TC stage) and a trans-impedance stage (TI stage) as shown in Figure 4. The TC stage of the IA includes two kinds of differential inputs for voltage sensing (IA\textsubscript{vp} and IA\textsubscript{vn}) and current sensing (IO\_IIN\textsubscript{p} and IO\_IIN\textsubscript{n}). The offset voltage (V\textsubscript{offset}) in the TC stage was designed using a source follower structure in the same way as [8].

In the voltage-sensing configuration of the IA, the chopper2 disconnects external current inputs (IO\_IIN\textsubscript{p} and IO\_IIN\textsubscript{n}). The voltage sensing input selection switches (SW\textsubscript{vp} and SW\textsubscript{vn}) select high-pass filtered signals (IA\textsubscript{vp} and IA\textsubscript{vn}) as the inputs (TC\textsubscript{VIN\textsubscript{p}} and TC\textsubscript{VIN\textsubscript{n}}) of PM\textsubscript{1p} and PM\textsubscript{1n}, respectively. The input resistance connection control switch (SW\textsubscript{Ri}) connects two input resistors (R\textsubscript{i}/2). In this configuration, the differential value of the voltage input signals (IA\textsubscript{vp} and IA\textsubscript{vn}) is transferred across the input resistor (R\textsubscript{i}) and the current flowing through the input resistor (R\textsubscript{i}) is mirrored to the differential current of the input PMOS (PM\textsubscript{3p} and PM\textsubscript{3n}) of the TI stage. The mirrored currents in the TI stage are demodulated through the chopper3 and converted into voltage signals (IA\textsubscript{op} and IA\textsubscript{on}) by an output resistor (R\textsubscript{o}).

Small signal modeling of IA can be performed as shown in Figure 4b. By applying Kirchhoff’s current law at nodes A, B and C, and by making some assumptions, the voltage gain (GV\textsubscript{IA}) of the IA in the voltage sensing configuration can be derived as

\[
GV_{IA} = \frac{(W/L)_{PM3}}{(W/L)_{PM2}} \times \frac{R_o}{R_i},
\]

(1)
in which \((W/L)_{PM3}\) and \((W/L)_{PM2}\) are the ratio of the gate width to the gate length of PM3 and PM2, respectively, and \((R_o/R_i)\) is the resistance ratio between the output resistor and the input resistor. In the following cases, the gain of IA can be different from Equation (1). If the transconductance of the input MOS (PM1) is not large enough, the error in Equation (1) occurs. If the output resistance R\textsubscript{o} is significant compared to the size of the output resistances r\textsubscript{ds3} and r\textsubscript{ds4} of PM3 and PM4, the gain error from Equation (1) occurs. The analysis of the current balanced instrumentation amplifier (CBIA) can be found in more detail in Firat’s thesis and Dwobeng’s thesis [16,17].
In the current sensing configuration, the voltage sensing input selection switches (SW_{vp} and SW_{vn}) select a fixed-bias voltage V_{b} as the inputs (TC_{VINp} and TC_{VINn}) of PM1p and PM1n. The chopper2 connects the external current inputs (IO_{IINp} and IO_{IINn}) to the internal nodes. The input resistance connection control switch (SW_{Ri}) separates the input resistor (R_{i}) into two parts (R_{i}/2 and R_{i}/2). In the previous version [8] there was no separation of the input resistor R_{i} in the current sensing configuration. For this reason, noise from the input PMOS of the TC stage is propagated to the TI stage and added...
to the output. Therefore, in this article, the input resistor has been separated to avoid unnecessary noise added to the current sensing configuration. If the chopper2 runs as a modulator, the chopper3 would run as a demodulator. If the chopper2 runs only with a simple switch, the chopper3 also runs with only a simple switch. The input currents (IO\textsubscript{IIN}\textsubscript{p} and IO\textsubscript{IIN}\textsubscript{n}) are mirrored to PM3\textsubscript{p} and PM3\textsubscript{n}, respectively. The mirrored currents in the TI stage are converted to voltage signals (IA\textsubscript{op} and IA\textsubscript{on}) by an output resistor (R\textsubscript{o}). The trans-impedance gain (G\textsubscript{IA}) of the IA in the current sensing configuration can be derived as

\[ G_{IA} = \frac{(W/L)_{PM3}}{(W/L)_{PM2}} \times R_o, \]

in which \((W/L)_{PM3}\) and \((W/L)_{PM2}\) are the ratio of the gate width to the gate length of PM3 and PM2, respectively, and \(R_o\) is the output resistance.

2.2. Static Configuration of Readout Channel for Either ECG or PPG Sensing

Static configuration means that the readout channel is configured continuously to the voltage sensing mode for ECG or current sensing mode for PPG.

Figure 5a shows static configuration of the readout channel for voltage sensing. The external voltage inputs (IO\textsubscript{VIN}\textsubscript{p} and IO\textsubscript{VIN}\textsubscript{n}) are high-pass filtered in the INPUT\_STAGE. The high-pass filter consists of an input capacitor (C\textsubscript{IN}) and an equivalent resistor. The equivalent resistor is formed by the switching action of the chopper1 and the parasitic capacitors in the input nodes of the IA. The cutoff frequency of the high pass filter can be adjusted by changing the switching frequency. The chopper1 not only forms the equivalent resistance, but also acts as a modulator. The input signals (IO\textsubscript{VIN}\textsubscript{p} and IO\textsubscript{VIN}\textsubscript{n}) are modulated into the high frequency band and then amplified; therefore, 1/f noise of the amplifying circuit can be avoided. In the TC stage, the current inputs (IO\textsubscript{IIN}\textsubscript{p} and IO\textsubscript{IIN}\textsubscript{n}) are disconnected, the input resistor \(R_i\) is connected, and the voltage inputs (TC\textsubscript{VIN}\textsubscript{p} and TC\textsubscript{VIN}\textsubscript{n}) are connected to the outputs of the INPUT\_STAGE. In a static voltage sensing configuration, the input and output of the sample and hold (SH) block are always connected. In the low pass filer (LPF), the high frequency noise of the amplified ECG signal is removed.

Figure 5b shows a static configuration of the readout channel for current sensing. In the TC stage, a fixed bias \(V_b\) is connected to the voltage inputs (TC\textsubscript{VIN}\textsubscript{p} and TC\textsubscript{VIN}\textsubscript{n}), the input resistor \(R_i\) is separated, and the current inputs (IO\textsubscript{IIN}\textsubscript{p} and IO\textsubscript{IIN}\textsubscript{n}) are connected to the photo detector. In static current sensing configuration, the input and output of the sample and hold (SH) block are also always connected. The photocurrent signal consists of a large DC signal and a small AC signal. The current compensation (CC) block compensates the large DC current from the photo current to prevent the signal from saturation.

Through the programmable gain amplifier (PGA) and low pass filter (LPF) in the ANA\_POST, the output of the IA can be further amplified and filtered. The architecture of the LPF is the 4th order Bessel filter. The large resistors in the LPF are implemented using switched capacitors. The adjustable cutoff frequency of the LPF ranges from 30 Hz to 250 Hz.
Figure 5. Static readout channel configuration: (a) voltage sensing; (b) current sensing.

The architecture and detailed circuit diagram of the PGA are shown in Figure 6a. The PGA has a feedback instrumentation amplifier architecture as in [18]. The gain of the PGA can be adjustable from 1, 2, 4, and 10.

When measuring PPG, most of the current flowing from the photodiode is a DC component, and only a small percentage is an AC component according to the heartbeat. To sufficiently amplify only the AC component of interest, the DC component must be removed to prevent saturation. The current compensation (CC) block monitors the output of the readout channel and compensates the DC current of the input. The detailed architecture of the CC block is shown in Figure 6b. The DC component $V_{lpf}$ of the readout channel output signal is extracted through a low pass filter. By the two comparators (CMP), when the $V_{lpf}$ signal is higher than the upper threshold $V_{H}$, high is output to the $V_{cmph}$ signal, and if it is lower than the lower threshold $V_{L}$, high is output to $V_{cmph}$. The comparator is a dynamic comparator operated by clock $f_{cc}$. When a rising edge occurs on the clock $f_{cc}$, the comparison operation is performed, and when the clock $f_{cc}$ is low, the comparator output is reset to low. If saturation occurs when $V_{lpf}$ is greater than the threshold $V_{H}$, the counter output (DOUT[7:0]) increases and, accordingly, the current DAC’s compensation current output ($i_{cc}$) increases. If saturation occurs when $V_{lpf}$ is smaller than the threshold $V_{L}$, the counter output (DOUT[7:0]) decreases and, accordingly, the current DAC’s compensation current output ($i_{cc}$) is decreased. Whether to up count or down count is determined by
RS_Latch. A delay element (delay) is inserted so that the counting occurs after the counting direction is determined.

The analog output (AO) of the ANA_POST is fed to a 12-bit successive approximation (SAR) ADC and converted to a digital signal. The ADC input signal is sampled and converted at 500 Hz for ECG and 60 Hz for PPG. Then a 13-bit data packet is generated with the 12-bit ADC data and 1-bit channel ID. The channel ID contains information about whether the data belongs to the CHANNEL1 or the CHANNEL2. Packets are stored in memory until the memory is full to a preset level. When the buffered size reaches a preset level, a pulse signal indicating a memory-full condition is generated and output to the outside. Then, the external master chip reads the packet data using the serial peripheral interface (SPI) communication method.

2.3. Dynamic Configuration of Readout Channel for ECG and PPG Sensing

Dynamic configuration means that a single IA configuration periodically changes between voltage and current sensing configurations at a faster rate than the bio signal bandwidth.

Figure 7 shows the dynamic readout channel configuration and the clock operation of the switches. The ECG signal (AO1) from the CHANNEL1 and the PPG signal (AO2) from the CHANNEL2 can be acquired simultaneously by alternately operating the sensing configuration of the IA in a time–division method. During the period of T1~T2 and the period of T3~T4, the ECG signal output from the IA1 in the voltage sensing configuration is stored in the sample and hold block (SH1) of the ANA_POST1. During the period of T0~T1 and the period of T2~T3, the PPG signal output from the IA1 in the voltage sensing configuration is stored in the sample and hold block (SH2) of the ANA_POST2. The output resistor R0 setting value of the TI stage when measuring ECG and the output resistor R0 setting value of the TI stage when measuring PPG are different. It is designed to be able to set a different output resistance R0 value for each time–division section.
Ro setting value of the TI stage when measuring PPG are different. It is designed to be able to set a different output resistance Ro value for each time division section.

Figure 7. Dynamic readout channel configuration using a single IA for electrocardiogram (ECG) output at CHANNEL1 and photoplethysmogram (PPG) output at CHANNEL2: (a) voltage (ECG) sensing phase; (b) current (PPG) sensing phase.

In addition, the ECG signal can be merged into the PPG signal using one readout channel configured as shown in Figure 8. In the merged ECG/PPG signal, the R peak of ECG and the peak of PPG required for the pulse transit time analysis are included. In merge mode, both the voltage input terminal and the current input terminal are connected to the TC stage at the same time. The transconductance current generated by the ECG signal and the current generated by the photodiode are combined at the TC stage. The combined current signal of the two signals is transferred to the TI stage and converted into a voltage signal. It is then further amplified or filtered and converted into a digital signal. In this process, current compensation is performed at the TC stage by the CC block so that saturation is not caused by the DC current of the photodiode.

Figure 8. Readout channel configuration for merged ECG/PPG signal at the output of CHANNEL1.
3. Fabrication and Measurement Results

The ASIC with the proposed circuit design was implemented using 180 nm CMOS fabrication technology, as shown in Figure 9. The boundaries of major blocks such as IA, TC stage, TI stage and ANA_POST are marked on the ASIC photo. The measured frequency response characteristics and noise characteristics in the voltage sensing configuration are shown in Figure 10a,b, respectively. An input-equivalent voltage noise level integrated from 1.0 Hz to 200 Hz was measured as 3.9 µVrms. The measured frequency response characteristics and noise characteristics in the current sensing configuration are shown in Figure 10c,d, respectively. An input-equivalent current noise level integrated from 0.25 Hz to 15 Hz was measured as 172 pArms. The efficacy of the proposed noise reduction method in the current sensing phase is shown in Figure 10d. The current noise with the input resistor $R_i$ disconnected operation in the IA is improved by 25 times when compared to the current noise with the input resistor $R_i$ connected operation. The circuit operated at 1.6 V and the measured current consumption including IA, ANA_POST1 and ANA_POST2 was 34.9 µA. In the voltage measurement mode using a chopping frequency of 2 kHz, the input impedance was measured to be about 880 MΩ.

![Figure 9. Photograph of fabricated ASIC.](image)

![Figure 10. Measured readout channel characteristics in a dynamic configuration: (a) voltage gain and frequency response; (b) input referred voltage noise corresponding to the voltage sensing path; (c) transimpedance and frequency response; (d) input referred current noise corresponding to the current sensing path.](image)
Figure 11 shows the measured ECG signal at the output of the CHANNEL1 and the PPG signal at the output of the CHANNEL2 using the dynamic sensing configuration. ECG was measured at the position corresponding to the standard ECG Lead II using a wet electrode. PPG was measured on the index finger using a commercial PPG probe (Nellcor DS100A). It can be seen that the main peak of the PPG follows after the occurrence of the R peak of the ECG.

Figure 12 shows a measured merged ECG/PPG signal using a single reconfigurable IA. For clear vision and easy feature extraction, an ECG signal with a downward R peak is added to a PPG signal with an upward peak. The points marked with blue solid and red dot circles correspond to the ECG and the PPG peaks, respectively. The ECG feature and PPG feature are extracted from a single waveform. Especially, the time interval between the blue solid circled peak and the red dot circled peak is correlated with the pulse transit time.

The performance of the proposed circuit is summarized in Table 1 and compared with the previous works. In Konijnenburg’s 2016 paper, a dedicated ECG channel and a dedicated PPG channel were integrated into one ASIC chip. It is cited as an example of a typical dedicated bio-signal acquisition ASIC. It can be seen that the performance of the dedicated bio-signal acquisition ASIC is relatively good. Since the reconfigurable architecture presented in this article has a current balanced IA (CBIA) architecture, it can fundamentally achieve the previously announced CBIA voltage noise level [7,19,20]. An appropriate paper to compare with the ideas pursued in this article is IMEC Xu’s 2018 paper. Xu also proposed a reconfigurable readout circuit for ECG and PPG measurements. Xu’s reconfigurable IA is based on a commonly known form of IA consisting of three amps. Two
operational amplifiers that receive input signals have the architecture of folded cascode type class-AB amplifier, and the third amplifier has the architecture of CBIA used in this paper. So, architecturally, Xu’s reconfigurable IA requires two more operational amplifiers compared to the reconfigurable IA of this article. This means that the area and power consumption of two operational amplifiers are additionally required. The IA in this article consumes 18 µA, while the 3 amplifiers of Xu’s IA consume 34 µA. In terms of current noise, the IA of this article has 44.8 pArms/Hz$^{0.5}$ and the IA of Xu has 41.0 pArms/Hz$^{0.5}$, which is similar. In terms of voltage noise, the IA of this paper is 276 nVrms /Hz$^{0.5}$ and the IA of Xu is 80 nVrms/Hz$^{0.5}$. Since the IA architecture of this paper is CBIA, and the ECG IA architecture of the 2016 IMEC paper is also CBIA, therefore, fundamentally, the voltage noise of the IA in this article can be reduced a similar level of 50 nVrms/Hz$^{0.5}$. In addition, IMEC’s reconfigurable IA can only be used in one fixed sensing mode. ECG and PPG cannot be measured dynamically and simultaneously with a single IA with a time–division multiplexing method.

Table 1. Performance comparison.

|                        | This Work | Konijnenburg (IMEC, 2016) [7] | Xu (IMEC, 2018) [9] |
|------------------------|-----------|-------------------------------|---------------------|
| Fabrication technology | 180 nm    | 180 nm                        | 180 nm             |
| Supply voltage         | 1.6 V     | 1.2 V                         | 1.2 V              |
| Reconfigurable IA for  | Yes       | No                            | Yes                |
| voltage/current sensing|           |                               |                    |
| Simultaneous sharing IA for voltage/current sensing | Yes | No | No |
| Readout analog current consumption | 34.9 µA (ECG/PPG) (including readout channel and ADC) | 40.8 µA (ECG) (including readout channel and ADC) | 27.5 µA (ECG) (only two OP amps for inputs) |
| Voltage noise          | 3.9 µV$_{\text{rms}}$ (1~200 Hz) | 0.6 µV$_{\text{rms}}$ (0.5~150 Hz) | 0.8 µV$_{\text{rms}}$ (~100 Hz) |
| Voltage noise floor    | 276 nVrms / Hz$^{0.5}$ | 49 nVrms / Hz$^{0.5}$ | 80 nVrms / Hz$^{0.5}$ |
| Current noise          | 172 pA$_{\text{rms}}$ (0.25~15 Hz) | 15.4 pA$_{\text{rms}}$ (1~64 Hz) | 82 pA$_{\text{rms}}$ (~4 Hz) |
| Current noise floor    | 44.8 pA$_{\text{rms}}$/Hz$^{0.5}$ | 1.9 pA$_{\text{rms}}$/Hz$^{0.5}$ | 41.0 pA$_{\text{rms}}$/Hz$^{0.5}$ |
| ADC                    | 12 bit SAR | 15 bit SD                     | 12 bit SAR         |

$^{1}$ Estimated from GSR set. For visibility, the ‘This Work’ column is displayed with a gray background and key functional specifications are shown in bold style.

4. Conclusions

In this article, a single, low-power, reconfigurable IA that can simultaneously measure ECG and PPG for cardiovascular health monitoring in a mobile low-power environment is introduced. The proposed and designed circuit is implemented with CMOS 180 nm fabrication technology, and the proposed function has been verified through testing and measurements.

The proposed reconfigurable IA can switch its configuration between voltage and current sensing modes statically or dynamically. Using a single reconfigurable IA with the dynamic reconfiguration, the ECG signal is acquired at the output of the CHANNEL1 and the PPG signal is acquired at the output of the CHANNEL2. To reduce current noise in the current sensing configuration for PPG measurement, an input resistor $R_i$ disconnection method in the IA is proposed and verified that it has an efficacy of 25 times reduction in the current noise.
For the first time, a merged ECG/PPG signal is showed with one readout channel using only single reconfigurable IA. The merged ECG/PPG signal includes both ECG and PPG features. The time interval between the R peak of the ECG and the peak of the PPG in the merged ECG/PPG signal corresponds to the pulse transit time in the traditional pulse transit time (PTT) analysis.

Based on these verified results, we might be able to expect the followings. The proposed re-configurable architecture between voltage and current sensing can give the benefit of large volume production to chip manufacturers by reducing production variety. The features of sharing a single IA or readout channel for different sensing modalities can give an architectural approach the chance for low power consumption in battery operated applications.

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**Informed Consent Statement:** Informed consent was obtained from only one subject (the author) involved in the study.

**Data Availability Statement:** The data presented in this study are available upon a reasonable request from the corresponding author.

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