Approximate Bisimulation Relations for Neural Networks and Application to Assured Neural Network Compression

Weiming Xiang, Senior Member, IEEE and Zhongzhu Shao

Abstract—In this paper, we propose a concept of approximate bisimulation relation for feedforward neural networks. In the framework of approximate bisimulation relation, a novel neural network merging method is developed to compute the approximate bisimulation error between two neural networks based on reachability analysis of neural networks. The developed method is able to quantitatively measure the distance between the outputs of two neural networks with same inputs. Then, we apply the approximate bisimulation relation results to perform neural networks model reduction and compute the compression precision, i.e., assured neural networks compression. At last, using the assured neural network compression, we accelerate the verification processes of ACAS Xu neural networks to illustrate the effectiveness and advantages of our proposed approximate bisimulation approach.

I. INTRODUCTION

Deep neural networks (DNN) are now widely used in a variety of contemporary applications, such as image processing [1], pattern recognition [2], [3], adaptive control, [4], [5] autonomous vehicles [6], and other fields, showing the powerful capabilities solving complex and challenging problems that traditional approaches fail to deal with. To cope with complex tasks and different environments, neural network models have been being developed with increasing scale and complexity, which aim to provide better performance and higher accuracy. However, the increasing scale and complexity of the neural network models also mean that neural networks require a large amount of resources for real-world implementation such as higher memory, more computational power, and higher energy consumption [7]. Therefore, neural network model compression methods were developed to reduce the complexity of neural networks at the least possible price of performance deterioration. For instance, in [8], four compression methods for deep convolutional neural networks are summarized, but some problems were pointed out such as a sharp drop in the accuracy of the network when compressing. More neural network compression results can be found in the recent survey [9] and references therein. Moreover, it has been observed that well-trained neural networks on abundant data are sometimes sensitive to updates, and react in unexpected and incorrect ways to even slight changes of the parameters [10]. The neural network compression inevitably introduces changes to the neural network. Therefore, an approach is needed to formally characterize the changes between the original neural network model and its compressed version.

In this paper, we propose an approximate bisimulation relation between two neural networks, which formally characterize the maximal difference between the outputs of two neural networks generated from same inputs. Based on the framework of the approximate bisimulation relation, we propose a neural network merging algorithm to calculate the approximate bisimulation error, measuring the distance between two neural networks. Applying this approximate bisimulation method to neural network model compression, we can obtain the precision of neural network model compression, which is able to provide assurance to perform tasks using compressed neural networks on behalf of original ones. To illustrate the feasibility of the approximate bisimulation method, we apply it to accelerate verification processes of the ACAS Xu neural networks using the compressed neural networks.

The remainder of the paper is organized as follows: Preliminaries are given in Section II. The approximate bisimulation relation and approximate bisimulation error computation are presented in Section III. Assured neural network compression and examples are given in Section IV. The conclusion is presented in Section V.

Notations: For the rest of paper, $0_{n \times m}$ denotes denotes a matrix of $n$ rows and $m$ columns with all elements zero, $1_n$ denotes the $n$-dimensional unit matrix. $\text{purelin}(\cdot)$ is linear transfer function, i.e., $x = \text{purelin}(x)$.

II. PRELIMINARIES

In this paper, we consider a class of feedforward neural networks which generally consist of one input layer, multiple hidden layers and one output layer. Each layer consists of one or multiple neurons. The action of a neuron depends on its activation function, which is in the description of

$$y_i = \phi\left(\sum_{j=1}^{n} w_{ij} x_j + b_i\right) \quad (1)$$

where $y_i$ is the output of the $i$th neuron, $x_j$ is the $j$th input of the $i$th neuron, $w_{ij}$ is the weight from the $j$th input to the $i$th neuron, $b_i$ is the bias of the $i$th neuron, $\phi(\cdot)$ is the activation function. Each layer $\ell$ ($1 \leq \ell \leq L$) of a feedforward neural network has $n_\ell$ neurons. Layer $\ell = 0$ denote the input layer, $n_0$ denote the number of the input layer. For the layer $\ell$, the input vector is denoted by $x^{(\ell)}$, respectively, the weight matrix and the bias vector are

$$W^{(\ell)} = [w_1^{(\ell)}, \ldots, w_{n_\ell}^{(\ell)}]^T \quad (2)$$

$$b^{(\ell)} = [b_1^{(\ell)}, \ldots, b_{n_\ell}^{(\ell)}]^T \quad (3)$$
where $w^{(l)}$ is the weight vector, $b^{(l)}$ is the bias value. The output vector of layer $\ell$ is $y^{(\ell)}$ defined by

$$y^{(\ell)} = \phi^{(\ell)}(W^{(\ell)}x^{(\ell)} + b^{(\ell)})$$  \hspace{1cm} (4)

where $\phi^{(\ell)}(\cdot)$ is the activation function of layer $\ell$.

For the whole neural network, the input and output layer are $x^{[0]}$ and $y^{[L]}$ respectively, the input of the layer $\ell$ is the output of the layer $\ell - 1$, the mapping relation from the input to the output is denoted by

$$y^{(L)} = \Phi(x^{[0]})$$  \hspace{1cm} (5)

where $\Phi(\cdot) \triangleq \phi^{(L)} \circ \phi^{(L-1)} \cdots \phi^{[1]}(\cdot)$. The mapping relation $\Phi$ includes not only the activation function of the neural network, but also the weight matrix and the bias vectors, which represent the structural information of the neural network.

Given an input set $\mathcal{X}$, the output reachable set of a neural network is stated by the definition below.

**Definition 1:** Given a neural network in the form of (5) and input set $\mathcal{X} \in \mathbb{R}^{n^{[0]}}$, the following set

$$\mathcal{Y} = \left\{ y^{(L)} \in \mathbb{R}^{n^{(L)}} \mid y^{(L)} = \Phi(x^{[0]}), x^{[0]} \in \mathcal{X} \right\}$$  \hspace{1cm} (6)

is called the output reachable set of neural network (5).

The safety specification of a neural network is expressed by the set defined in the output space, describing the safety requirement.

**Definition 2:** Safety specification $\mathcal{S}$ formalizes the safety requirement for output $y^{(L)}$ of neural network (5), and is a predicate over output $y^{(L)}$ of neural network (5). The neural network (5) is safe if and only if the following condition is satisfied:

$$\mathcal{Y} \cap \neg \mathcal{S} = \emptyset$$  \hspace{1cm} (7)

where $\mathcal{Y}$ is the output set defined by (6), and $\neg$ is the symbol for logical negation.

The above safety verification concept is reachability-based and will be used in Section IV for safety verification of neural networks of Airborne Collision Avoidance Systems in [11].

**III. APPROXIMATION SIMULATION RELATIONS OF NEURAL NETWORKS**

**A. Approximation Bisimulation Relations**

In order to characterize the difference of two feedforward neural networks in terms of outputs, we defined the following metric which measures the distance between the outputs of two neural networks in the framework of the reachable set defined in Definition 1.

**Definition 3:** Consider two neural networks $y^{(L)} = \Phi_j(x^{[0]}), j \in \{1, 2\}$, input set $\mathcal{X} \in \mathbb{R}^{n^{[0]}}$, and output sets $\mathcal{Y}_j \in \mathbb{R}^{n^{(L)}}, j \in \{1, 2\}$, we define $\mathcal{N}_j = (\mathcal{X}, \mathcal{Y}_j, \Phi_j)$, $j \in \{1, 2\}$, and

$$d(\Phi_1(x_1^{[0]}), \Phi_2(x_2^{[0]})) = \begin{cases} \rho(y_1^{(L)}, y_2^{(L)}) & \text{if } x_1^{[0]} = x_2^{[0]} \\ +\infty & \text{otherwise} \end{cases}$$  \hspace{1cm} (8)

where

$$\rho(y_1^{(L)}, y_2^{(L)}) = \sup_{y_1^{(L)} \in \mathcal{Y}_1, y_2^{(L)} \in \mathcal{Y}_2} \left\| y_1^{(L)} - y_2^{(L)} \right\|.$$  \hspace{1cm} (9)

It is noted that $d(\Phi_1(x_1^{[0]}), \Phi_2(x_2^{[0]}))$ defined in (8) characterizes the maximal difference between the outputs of two neural networks generated from a same input, which quantifies the discrepancy between two neural networks $\Phi_1$ and $\Phi_2$ in terms of outputs. Based on Definition 3, we will be able to establish the approximate bisimulation relation of two neural networks.

**Definition 4:** Consider $\mathcal{N}_j = (\mathcal{X}, \mathcal{Y}_j, \Phi_j), j \in \{1, 2\}$, and let $\epsilon \geq 0$, a relation $\mathcal{R}_{\epsilon} \in \mathbb{R}^{n^{[0]}} \times \mathbb{R}^{n^{(L)}}$ is called an approximate simulation relation between $\mathcal{N}_1$ and $\mathcal{N}_2$, of precision $\epsilon$, if for all $(y_1^{(L)}, y_2^{(L)}) \in \mathcal{R}_{\epsilon}$

1. $d(\Phi_1(x_1^{[0]}), \Phi_2(x_2^{[0]})) \leq \epsilon, \forall x^{[0]} \in \mathcal{X}$;
2. $\forall x^{[0]} \in \mathcal{X}, \forall \Phi_1(x_1^{[0]}), \exists \Phi_2(x_2^{[0]}) \in \mathcal{Y}_2$ such that $(\Phi_1(x_1^{[0]}), \Phi_2(x_2^{[0]})) \in \mathcal{R}_{\epsilon}$;
3. $\forall x^{[0]} \in \mathcal{X}, \forall \Phi_2(x_2^{[0]}), \exists \Phi_1(x_1^{[0]}) \in \mathcal{Y}_1$ such that $(\Phi_1(x_1^{[0]}), \Phi_2(x_2^{[0]})) \in \mathcal{R}_{\epsilon}$;

and we say neural networks $\mathcal{N}_1$ and $\mathcal{N}_2$ are approximately bisimilar with precision $\epsilon$, denoted by $\mathcal{N}_1 \sim_{\epsilon} \mathcal{N}_2$.

**Remark 1:** The meaning of approximate bisimulation between two neural networks $\mathcal{N}_1$ and $\mathcal{N}_2$ with precision $\epsilon$, which denoted by $\mathcal{N}_1 \sim_{\epsilon} \mathcal{N}_2$, is as follows: Considering two neural networks $\mathcal{N}_1$ and $\mathcal{N}_2$ and any output of neural network $\mathcal{N}_1$, we can find one output generated by the same corresponding input out of neural network $\mathcal{N}_2$, and vice versa. The two outputs of two neural networks always satisfy that the distance between them is bounded by $\epsilon$. In the case of $\epsilon = 0$, we can define that the two neural networks have an exact simulation relation.

Then, we define metrics measuring the distance between the observed behaviors of neural network $\mathcal{N}_1$ and $\mathcal{N}_2$. Based on the defined notion of approximate bisimulation, we can define the approximate bisimulation error to represent the distance between two neural networks.

**Definition 5:** Given two neural networks $\mathcal{N}_1$ and $\mathcal{N}_2$, the approximate bisimulation error of them are defined by

$$d(\mathcal{N}_1, \mathcal{N}_2) = \sup \{ \epsilon \mid \mathcal{N}_1 \sim_{\epsilon} \mathcal{N}_2 \}$$  \hspace{1cm} (10)

where $\epsilon \geq 0$.

The key to establish the approximation bisimulation relation between two neural networks is how to efficiently compute the approximation bisimulation error defined by (10). In the next subsection, a reachability-based method is proposed to compute the approximate bisimulation error.

**B. Approximate Bisimulation Error Computation**

In order to compute the approximate bisimulation error $\epsilon$ between two neural network outputs, the set-valued reachability methods can be used. First, consider two neural networks with same input set $\mathcal{X}$, a feedforward neural network $\mathcal{N}_2$ with $L$ hidden layers and $n_l^{(l)}, l = 1, \ldots, L$ neurons in each layer, and its bisimilar feedforward neural network $\mathcal{N}_2$ with $S$ hidden layers and $n_s^{(l)}, l = 1, \ldots, S$ neurons in each hidden layer.
Without loss of generality, the following assumption is given for neural networks $\mathcal{N}_L$ and $\mathcal{N}_S$:

**Assumption 1:** The following assumptions hold for two neural networks $\mathcal{N}_L$ and $\mathcal{N}_S$:

1. The number of inputs of two neural networks are same, i.e., $n^{(0)}_L = n^{(0)}_S$;
2. The number of outputs of two neural networks are same, i.e., $n^{(L)}_L = n^{(L)}_S$;
3. The number of hidden layers of neural network $\mathcal{N}_L$ is greater than or equal the number of hidden layers of neural network $\mathcal{N}_S$, i.e., $L \geq S$.

According to (9), (10), the approximate bisimulation error between $\mathcal{N}_L$ and $\mathcal{N}_S$ can be expressed by

$$d(\mathcal{N}_L, \mathcal{N}_S) = \sup_{x^{(0)} \in \mathcal{X}} \| \Phi_L(x^{(0)}) - \Phi_S(x^{(0)}) \|. \quad (11)$$

To obtain the approximate bisimulation error of the two neural networks, i.e., $d(\mathcal{N}_L, \mathcal{N}_S)$, we propose to merge the two neural networks in a non-fully connected structure $\mathcal{N}_M$, which is able to generate the output $y^{(M)}_M$ exactly characterizing the difference of the outputs of $\mathcal{N}_L$ and $\mathcal{N}_S$, i.e., $y^{(M)}_M = y^{(L)}_L - y^{(S)}_S$.

**Merger Neural Network $\mathcal{N}_M$:** To begin with, we consider two neural networks $\mathcal{N}_L$ and $\mathcal{N}_S$ with same input $x^{(0)}$. We use $W^{(m)}_M$ and $b^{(m)}_M$ to denote the weight matrix and bias vector of the $m$th layer of the merged neural network $\mathcal{N}_M$, $x^{(m)}_M$ and $y^{(m)}_M$ are input and output vectors of $m$th layer of $\mathcal{N}_M$. The structure of the merged neural network $\mathcal{N}_M$ with $L + 1$ layers is recursively defined as below:

$$\begin{align*}
(y^{(m)}_M, x^{(m)}_M) &= \left( \phi^{(m)}_M(W^{(m)}_M x^{(m-1)}_M + b^{(m)}_M), x^{(m)}_M \right) \\
(y^{(L+1)}_M, x^{(L+1)}_M) &= \left( \phi^{(L+1)}_M (W^{(L+1)}_M x^{(L)}_M + b^{(L+1)}_M), x^{(L+1)}_M \right)
\end{align*} \quad (12)$$

where $m = 1, 2, \ldots, L + 1$. The input is $x^{(0)}_M = x^{(0)}$, output is $y^{(L+1)}_M$, weight matrices $W^{(m)}_M$ and bias vectors $b^{(m)}_M$, and activation functions $\phi^{(m)}_M(\cdot)$ are categorized as the following five cases:

1. When $m = 1$, $W^{(1)}_M$, $b^{(1)}_M$, and $\phi^{(1)}_M(\cdot)$ are

$$W^{(1)}_M = \begin{bmatrix} W^{(1)}_L \\ W^{(1)}_S \end{bmatrix}, \quad b^{(1)}_M = \begin{bmatrix} b^{(1)}_L \\ b^{(1)}_S \end{bmatrix}, \quad \phi^{(1)}_M(\cdot) = \begin{bmatrix} \phi^{(1)}_L(\cdot) \\ \phi^{(1)}_S(\cdot) \end{bmatrix}. \quad (13)$$

2. When $1 < m \leq S - 1$, $W^{(m)}_M$, $b^{(m)}_M$, and $\phi^{(m)}_M(\cdot)$ are

$$W^{(m)}_M = \begin{bmatrix} W^{(m)}_L \\ 0_{n^{(m)}_L \times n^{(m-1)}_L} \end{bmatrix}, \quad b^{(m)}_M = \begin{bmatrix} b^{(m)}_L \\ b^{(m)}_S \end{bmatrix}, \quad \phi^{(m)}_M(\cdot) = \begin{bmatrix} \phi^{(m)}_L(\cdot) \\ \phi^{(m)}_S(\cdot) \end{bmatrix}. \quad (14)$$

3. When $S - 1 < m \leq L - 1$, $W^{(m)}_M$, $b^{(m)}_M$, and $\phi^{(m)}_M(\cdot)$ are

$$W^{(m)}_M = \begin{bmatrix} W^{(m)}_L \\ 0_{n^{(m)}_L \times n^{(m-1)}_L} \end{bmatrix}, \quad b^{(m)}_M = \begin{bmatrix} b^{(m)}_L \\ b^{(m)}_S \end{bmatrix}, \quad \phi^{(m)}_M(\cdot) = \begin{bmatrix} \phi^{(m)}_L(\cdot) \\ \phi^{(m)}_S(\cdot) \end{bmatrix}. \quad (15)$$

4. When $m = L$, $W^{(L)}_M$, $b^{(L)}_M$, and $\phi^{(L)}_M(\cdot)$ are

$$W^{(L)}_M = \begin{bmatrix} W^{(L)}_L \\ 0_{n^{(S)}_L \times n^{(S-1)}_L} \end{bmatrix}, \quad b^{(L)}_M = \begin{bmatrix} b^{(L)}_L \\ b^{(L)}_S \end{bmatrix}, \quad \phi^{(L)}_M(\cdot) = \begin{bmatrix} \phi^{(L)}_L(\cdot) \\ \phi^{(L)}_S(\cdot) \end{bmatrix}. \quad (16)$$

5. When $m = L + 1$, $W^{(L+1)}_M$, $b^{(L+1)}_M$, and $\phi^{(L+1)}_M(\cdot)$ are

$$W^{(L+1)}_M = \begin{bmatrix} I_{n^{(L)}_L} \\ -I_{n^{(L)}_L} \end{bmatrix}, \quad b^{(L+1)}_M = \begin{bmatrix} 0_{n^{(L)}_L \times 1} \end{bmatrix}, \quad \phi^{(L+1)}_M(\cdot) = \text{purelin}(\cdot). \quad (17)$$

**Remark 2:** In the merging process of neural networks $\mathcal{N}_L$ and $\mathcal{N}_S$, (13)–(15) ensures that merged neural network $\mathcal{N}_M$ takes the one input $x^{(0)}$ for the subsequent calls involving both processes of $\mathcal{N}_L$ and $\mathcal{N}_S$. Then, for $1 < m \leq S - 1$, $\mathcal{N}_M$ conducts the computation of $\mathcal{N}_L$ and $\mathcal{N}_S$ parallely for the hidden layers of $1 < m \leq S - 1$. When $S - 1 < m \leq L - 1$, the hidden layers of neural network $\mathcal{N}_S$ which has a less hidden layers are expanded to match the number of layers of neural network $\mathcal{N}_L$ with larger number of hidden layers, but the expanded layers are forced to be pass the information to subsequent layers without any changes, i.e., the weight matrices of the expanded hidden layers are identity matrices, and the bias vector is the zero vectors. This expansion is formalized as (19)–(21). Moreover, as $m = L$, this layer is a combination of output layers of both $\mathcal{N}_L$ and $\mathcal{N}_S$ to generate the same outputs of $\mathcal{N}_L$ and $\mathcal{N}_S$. At last, a comparison layer $L + 1$ is added to compute the exact difference between two bisimilar neural networks.

With the merged neural network $\mathcal{N}_M$ in the description of (12)–(27), we are ready to propose the main contribution of this work in Proposition 1.

**Proposition 1:** Given two neural networks $\mathcal{N}_L$ with $L$ layers and $\mathcal{N}_S$ with $S$ layers under Assumption 1, the output $y^{(L+1)}_M$ of their merged network $\mathcal{N}_M$ defined by (12)–(27) equals the difference of the output $y^{(L)}_L$ of $\mathcal{N}_L$ and the output $y^{(S)}_S$ of $\mathcal{N}_S$, i.e.,

$$y^{(L+1)}_M = y^{(L)}_L - y^{(S)}_S \quad (28)$$
holds for any input $x^{[0]} \in \mathcal{X}$, where $y^{[L]}_L = \Phi_L(x^{[0]})$ and $y^{[S]}_S = \Phi_S(x^{[0]})$.

Proof: Considering an input $x^{[0]} \in \mathcal{X}$ and according to (13)–(15), the following results for output of layer $m = 1$ of merged neural network $\mathcal{N}_M$ can be obtained
\[ y^{[1]}_M = \begin{bmatrix} \varphi^{[1]}_L(W^{[1]}_L x^{[0]} + b^{[1]}_L) \\ \varphi^{[1]}_S(W^{[1]}_S x^{[0]} + b^{[1]}_S) \end{bmatrix} = \begin{bmatrix} x^{[1]}_L \\ x^{[1]}_S \end{bmatrix} \] (29)

Further considering layers $1 < m \leq S - 1$ of $\mathcal{N}_M$, and using (16) and (17), it leads to
\[ W^{[m]}_M x^{[m-1]}_M + b^{[m]}_M = \begin{bmatrix} W^{[m]}_L x^{[m-1]}_L + b^{[m]}_L \\ W^{[m]}_S x^{[m-1]}_S + b^{[m]}_S \end{bmatrix} \] (30)

where $1 < m \leq S - 1$. Then based on (18), recursively we can obtain
\[ y^{[S-1]}_M = \begin{bmatrix} \varphi^{[S-1]}_L(W^{[1]}_L x^{[0]} + b^{[1]}_L) \\ \varphi^{[S-1]}_S(W^{[1]}_S x^{[0]} + b^{[1]}_S) \end{bmatrix} = \begin{bmatrix} x^{[S-1]}_L \\ x^{[S-1]}_S \end{bmatrix} \] (31)
\[ y^{[L-1]}_M = \begin{bmatrix} x^{[L-1]}_L \\ x^{[L-1]}_S \end{bmatrix} \] (32)

Moreover, considering $S - 1 < m \leq L - 1$ and using (19) and (20), one can obtain
\[ W^{[m]}_M x^{[m-1]}_M + b^{[m]}_M = \begin{bmatrix} W^{[m]}_L x^{[m-1]}_L + b^{[m]}_L \\ W^{[m]}_S x^{[m-1]}_S + b^{[m]}_S \end{bmatrix} \] (33)

where $S - 1 < m \leq L - 1$. From (21), it yields
\[ y^{[L-1]}_M = \begin{bmatrix} x^{[L-1]}_L \\ x^{[L-1]}_S \end{bmatrix} \] (34)
in which $x^{[L-1]}_M$ is defined as
\[ x^{[L-1]}_M = \begin{bmatrix} \varphi^{[L-1]}_L(W^{[1]}_L x^{[0]} + b^{[1]}_L) \\ \varphi^{[L-1]}_S(W^{[1]}_S x^{[0]} + b^{[1]}_S) \end{bmatrix} = \begin{bmatrix} x^{[L-1]}_L \\ x^{[L-1]}_S \end{bmatrix} \] (35)

Then, as $m = L$ with (22) and (23) as well as $x^{[L-1]}_M = y^{[L-1]}_M$, it leads to
\[ W^{[L]}_M x^{[L-1]}_M + b^{[L]}_M = \begin{bmatrix} W^{[L]}_L x^{[L-1]}_L + b^{[L]}_L \\ W^{[L]}_S x^{[L-1]}_S + b^{[L]}_S \end{bmatrix} \] (36)

Also due to (24), we can have
\[ y^{[L]}_M = \begin{bmatrix} \varphi^{[L]}_L(W^{[1]}_L x^{[0]} + b^{[1]}_L) \\ \varphi^{[L]}_S(W^{[1]}_S x^{[0]} + b^{[1]}_S) \end{bmatrix} = \begin{bmatrix} y^{[L]}_L \\ y^{[L]}_S \end{bmatrix} \] (37)

At last, when $m = L + 1$ with (25)–(27), the following result can be obtained
\[ y^{[L+1]}_M = W^{[L+1]}_M x^{[M]}_M + b^{[L+1]}_M = \begin{bmatrix} I_{n^{[L]}} - I_{n^{[L]}} \\ y^{[L]}_L - y^{[S]}_S \end{bmatrix} \] (38)

where $y^{[L]}_L = \Phi_L(x^{[0]})$ and $y^{[S]}_S = \Phi_S(x^{[0]})$. The proof is complete.

Proposition 1 implies that, for any individual input $x^{[0]}$, we can compute the difference of the outputs between two bisimilar neural networks via generating the output of their merged neural network of $x^{[0]}$. This lays the foundation of computing the approximate bisimulation error in the description of (11), i.e., the computation of the maximum discrepancy between two bisimilar neural networks subject to an input set $\mathcal{X}$ can be converted to the output reachable set $Y_M$ of merged neural network $\mathcal{N}_M$.

Proposition 2: Given an input set $\mathcal{X}$, two neural networks $\mathcal{N}_L$ with $L$ layers and $\mathcal{N}_S$ with $S$ layers under Assumption 1, their merged neural network $\mathcal{N}_M$ can be defined by (12)–(27). Then, the approximate bisimulation error between $\mathcal{N}_L$ and $\mathcal{N}_S$ can be computed by
\[ d(\mathcal{N}_L, \mathcal{N}_S) = \sup_{y^{[L+1]}_M \in Y_M} \left\| y^{[L+1]}_M - y^{[L]}_L \right\| \] (39)

where $y^{[L+1]}_M = \Phi_M(x^{[0]})$ is the output of $\mathcal{N}_M$ and $Y_M$ is the output reachable set of $\mathcal{N}_M$.

Proof: The result can be obtained straightforwardly from the result in Proposition 1, i.e., $y^{[L+1]}_M = y^{[L]}_L - y^{[S]}_S$. The proof is complete. ■
tool, the reachable sets are in the form of a family of polyhedral sets [12], and in IGGNV tool, the output reachable set is a family of interval sets [13], [14]. With the reachable set $\mathcal{Y}_M$, the approximate bisimulation error $d(\mathcal{N}_L, \mathcal{N}_S)$ can be easily obtained by searching for the maximal value of $\|y^{(L+1)}_M\|$ in $\mathcal{Y}_M$, e.g., testing throughout a finite number of vertices in polyhedral sets.

IV. APPLICATION TO ASSURED NEURAL NETWORK COMPRESSION

A. Assured Neural Network Compression

In practical applications, neural networks are usually large in size, and it could be computationally expensive and time-consuming to perform those tasks requiring a large amount of computation resources. A promising method to mitigate the computation burden is to compress large-scale neural networks into small-scale ones and provide the approximate bisimulation error between two neural networks. With the approximate bisimulation error, we can infer the outputs of the original large-scale neural network via running its corresponding small-scale compressed one plus the approximate bisimulation error. The assured neural network compression is stated as below.

**Definition 6:** Given a large-scale neural network $\mathcal{N}_L$ with input set $\mathcal{X}$, a small-scale neural network $\mathcal{N}_S$ is called its assured compressed version with precision $\varepsilon$ if the approximate bisimulation error of two neural networks is not greater than $\varepsilon$, i.e.,

$$d(\mathcal{N}_L, \mathcal{N}_S) \leq \varepsilon \quad (39)$$

where $\varepsilon \geq 0$.

**Remark 3:** There exist a number of neural network compression methods [9] to obtain small-scale neural network $\mathcal{N}_S$. In this paper, our focus is on how to compute the assured neural network compression precision $\varepsilon$ using the framework of approximate bisimulation relations proposed in the previous sections.

**Example 1:** We verify the effectiveness of the approximate bisimulation approach in neural network compression by a numerical example. In the numerical case, we aim to soundly simulate a neural network $\mathcal{N}_L$ (large-scale) with 5 hidden layers and 50 neurons in each hidden layer using a neural network $\mathcal{N}_S$ (small-scale) with 2 hidden layers and 10 neurons in each hidden layer. To facilitate the visualization of the simulation results, the output outputs of both neural networks are selected one-dimensional.

First, a neural network $\mathcal{N}_L$ is randomly generated, and then a neural network $\mathcal{N}_S$ is trained out of the input-output data of $\mathcal{N}_L$. All activation functions are ReLU functions. Using the merged neural network method and computing reachable set with NNV tool, the approximate bisimulation error $\varepsilon = 26.1227$ of the two neural networks can be obtained. With the help of $\varepsilon = 26.1227$, the upper and lower bounds of output $\mathcal{Y}_L$ of $\mathcal{N}_L$ can be obtained via the outputs $\mathcal{Y}_S$ of $\mathcal{N}_S$ with a smaller size, i.e., upper bound $\mathcal{Y}_L = \mathcal{Y}_S + \varepsilon$ and lower bound $\mathcal{Y}_L = \mathcal{Y}_S - \varepsilon$.

Output data of the original neural network and the compressed neural network, as well as the upper and lower bounds, are represented in Fig. 2. It can be observed that all the outputs $\mathcal{Y}_L$ are within the upper bound $\mathcal{Y}_L$ and lower bound $\mathcal{Y}_S$, i.e., $\mathcal{Y}_S \leq \mathcal{Y}_L \leq \mathcal{Y}_L$.

B. Application of ACAS Xu Network Verification

In this subsection, we apply the neural network model compression method to ACAS Xu network in [11] to accelerate the verification processes. ACAS Xu system has been developed using a large lookup table that maps sensor measurements to warning signals, see Fig. 3. It has been shown that DNNs can significantly reduce memory (replacing a 2GB lookup table with an efficient DNNs of less than 3MB). The DNN method of ACAS Xu system consists of 45 DNNs, and each neural network contains 5 inputs and 5 outputs, with 6 hidden layers and 50 neurons with ReLU activation functions in each layer.

In practical applications, calculating the exact output reachable set of a neural network with 6 hidden layers and 50 neurons per layer requires huge computational effort and computational time [15]. Therefore, we compress the original neural networks into smaller neural networks and compute the assured precision by the approximate bisimulation method. Then, we can perform verification of properties.
based on those reduced-scale neural networks and approximate bisimulation error $\varepsilon$, i.e., expand the unsafe region $-S$ in Definition 3 by the approximate bisimulation error $\varepsilon$.

In this example, we use neural networks with two hidden layers and 10 neurons in each layer as the compressed version for the compression of the DNNs of the ACAS Xu system. Then, we verify Property $\phi_3$ on 27 neural networks in the ACAS Xu system using their assured compressed versions. The verification results and computational time are listed in Table I. In Table I, $\varepsilon$ is the approximate bisimulation error. $T_L$ is the verification time (seconds) using original neural networks and $T_S$ is the verification time (seconds) using compressed neural networks. $V_L$ is the verification results on original neural networks, and $V_S$ is the verification results on compressed neural networks.

As explicitly shown in Table I, the verification time can be significantly reduced using compressed neural networks. It is worth mentioning that since the approximate bisimulation error is an over-approximation of the exact difference between the outputs of two neural networks, the safety conclusions based on compressed networks are only able to derive safe conclusions for original networks in safe cases. As to uncertain cases, we have to perform verification on original neural networks to ascertain the safety property. It can be found that the safety of 18 of the compressed neural networks can be used to conclude the safety of original neural networks. The remaining 9 unsafe verification results based on compressed neural networks are insufficient to derive safe or unsafe conclusions of original neural networks. This is mainly because the approximate bisimulation error is too large to meet the accuracy of the safety verification. Despite the 9 uncertain cases that need to be verified through original neural networks, the total verification time has been significantly reduced for these 27 neural networks.

### V. Conclusion

This work proposed approximate bisimulation relations for feedforward neural networks. The approximate bisimulation relation formally define the maximal difference between the outputs of two bisimilar neural networks from same inputs. An reachability-based computation procedure is developed to efficiently compute the approximation error via a novel neural network merging approach. Then, the approximation bisimulation approach is applied to assure neural network compression. With the approximate bisimulation error, the perform tasks using the compressed network on behalf of original one such as verification of neural networks, which has been demonstrated by an ACAS Xu example.

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