Discovering faster matrix multiplication algorithms with reinforcement learning
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(Supplementary Information)

Alhussein Fawzi∗ Matej Balog∗ Aja Huang∗ Thomas Hubert∗
Bernardino Romera-Paredes† Mohammadamin Barekatain† Alexander Novikov†
Francisco J. R. Ruiz† Julian Schrittwieser† Grzegorz Swirszcz† David Silver
Demis Hassabis Pushmeet Kohli

DeepMind, London, UK

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∗Equal contributors. Authors listed alphabetically by last name, after corresponding author.
†Equal contributors. Authors listed alphabetically by last name.
A.1 Details on network components

Algorithms A.1-A.9 show the details of each component. In Algorithm A.10, we show how the different components are connected together for training the neural network in AlphaTensor. In Algorithm A.11, we show how AlphaTensor’s neural network is used at inference time.

A.1.1 Torso

Algorithm A.1 Basic attention function.

```python
def Attention (x ∈ R^{N_x × c}, y ∈ R^{N_y × c}, causal_mask = False, N_heads = 16, d = 32, w = 4):
    1: x_norm = LayerNorm (x)
    2: y_norm = LayerNorm (y)
    3: q_h^i = Linear (x_norm, d)
    4: k_h^j = Linear (y_norm, d)
    5: v_h^j = Linear (y_norm, d)
    6: a_h^i,j = softmax_h \left( \frac{1}{\sqrt{d}} q_h^i k_h^j \right)
    7: if causal_mask
    8: w_i^h = \sum_{j>i} a_h^i,j v_h^j
    9: else
    10: w_i^h = \sum_j a_h^i,j v_h^j
    11: x = x + Linear (concat_h (w_i^h), c_1)
    # Dense block:
    12: x = x + Linear (GeLU (Linear (LayerNorm (x), c_1 w)_1), c_1)
    13: return x
```

Algorithm A.2 Attentive modes.

```python
def AttentiveModes (x_1 ∈ R^{S × S × c}, x_2 ∈ R^{S × S × c}, x_3 ∈ R^{S × S × c})
1: g = [x_1, x_2, x_3]
2: for [m_1, m_2] ∈ \{[0, 1], [2, 0], [1, 2]\} do
3:   a = Concatenate ([g [m_1], g [m_2]]^T, axis = 1)
   # Parallel loop
4:   for i = 0, . . . , S − 1 do
5:     c = Attention (a [i, ; ;], a [i, ; ;])
6:     g [m_1] [i, ; ;] = c [; S, ;]
7:     g [m_2] [i, ; ;] = c [S, ;]^T
8: return g
```
Algorithm A.3 Torso.

```python
def Torso(x ∈ R^{T×S×S×S}, s ∈ R^s, c ∈ N)
1: x₁ = Reshape (Transpose (x, (1, 2, 3, 0)), (S × S × ST))
x₂ = Reshape (Transpose (x, (3, 1, 2, 0)), (S × S × ST))
x₃ = Reshape (Transpose (x, (2, 3, 1, 0)), (S × S × ST))
g = [x₁, x₂, x₃]
for i = {0, . . . , 2} do
  p = Reshape (Linear (s, S^2), (S, S, 1))
g[i] = Concatenate ([g[i], p], -1)
g[i] = Linear (g[i], c)
[x₁, x₂, x₃] = g
for i = {1, . . . , 8} do
  e = AttentiveModes (x₁, x₂, x₃)
return e
```

A.1.2 Policy head

Algorithm A.4 Function used in the policy head to map from the learned embedding obtained from the torso, e, and the piece of action produced so far (or ground truth if training), α, to the logits of the remainder of the action.

```python
def predict_action_logits (α ∈ R^{N_{steps}×N_{logits}}, e ∈ R^{m×c}, N_{heads} = 32, N_{layers} = 2)
x = Linear (α, N_{features} × N_{heads})
x = x + LearnablePositionEncoding (x)
for i = {1 . . . N_{layers}} do
  x = LayerNorm(x)
# Causal self attention:
  if is training then
    c = Dropout(c)
  x = x + c
  x = LayerNorm(x)
# Cross attention:
  c = Attention (x, e, N_{heads}, causal_mask = False)
  if is training then
    c = Dropout(c)
x = x + c
o = Linear(ReLU(x), N_{logits})
return o, x
```

Algorithm A.5 Policy head behaviour at training time. It returns the logits (and the embeddings of the first step).

```python
def PolicyHead_training (e ∈ R^{m×c}, g ∈ {1, . . . , N_{logits}}^{N_{steps}})
# Training by teacher-forcing:
1: o, z = predict_action_logits(onehot(shifted(g), N_{logits}), e)
# Returns only z₁ because it is the only one not conditioned on ground truth.
2: return o, z₁
```
Algorithm A.6 Policy head behaviour at inference time. It returns the sampled action, and its estimated probability (and the embeddings of the first step).

```python
def PolicyHead_inference (e ∈ R^{m×c}, N_{samples} = 32, N_{steps}, N_{logits})
1: a_s^i = 0  s ∈ {1, . . . , N_{samples}}, i ∈ {1, . . . , N_{steps}}
2: p_s^i = 1  s ∈ {1, . . . , N_{samples}}
3: for s ∈ {1, . . . , N_{samples}} do
4:     for i ∈ {1, . . . , N_{steps}} do
5:         o^s, z^s = predict_logits(onehot(a^s, N_{logits}), e)
6:         a^s, p_i = sample_from_logits(o^s)
7:     p^s = p^s ⊙ p_i
# Returns only z_1 because it is the only one not conditioned on any sampled action.
8: return a, p, z_1  a ∈ {1, . . . , N_{logits}}^{N_{samples}×N_{steps}}, p ∈ [0, 1]^{N_{samples}}, z_1 ∈ R^{2048}
```

A.1.3 Value head

Algorithm A.7 Value head network.

```python
def ValueHead (x ∈ R^c, n = 8)
1: for i ∈ {1, 2, 3} do
2:     x = ReLU (Linear (x, 512))
3: q = Linear (x, n)  q ∈ R^n
4: return q
```

Algorithm A.8 Quantile loss used in the value head. q are the predicted values of the quantiles at equally spaced intervals, g is the ground truth.

```python
def Quantile_loss (q ∈ R^n, g ∈ R^n, δ = 1)
# Getting the n quantiles, τ.
1: τ = ([0 . . . n] + 0.5) / n  τ ∈ [0, 1]^n
2: d = g - q  d ∈ R^n
3: h = Huber_loss (d, δ)  h ∈ R^n
4: k = abs (τ - float (d < 0))  k ∈ R^n
5: return mean (k ⊙ h)
```

Algorithm A.9 Obtaining the predicted value at inference.

```python
def ValueRiskManagement (q ∈ R^n, u_q = 0.75)
1: j = [u_q n]  j ∈ [1, . . . , n]
2: return mean (q [j], q [j + 1], . . . , q [n])
```
A.1.4 Training and inference with AlphaTensor

Algorithm A.10 Training AlphaTensor’s neural network.

def AlphaTensor_Net_training \( \mathcal{X} \in \mathbb{R}^{T \times S \times S \times S}, s \in \mathbb{R}^s, g_{\text{action}} \in \{1, \ldots, N_{\text{logits}}\}^{N_{\text{steps}}}, g_{\text{value}} \in \mathbb{R}^n, c \in \mathbb{N} \):

1: \( e = \text{Torso}(\mathcal{X}, s, c) \)
2: \( a, z_1 = \text{PolicyHead\_training}(e, g_{\text{action}}, N_{\text{logits}}) \)
3: \( l_{\text{policy}} = \text{Sum}(\text{CrossEntropy}(a, g_{\text{action}})) \)
4: \( q = \text{ValueHead}(z_1) \)
5: \( l_{\text{value}} = \text{Quantile\_loss}(q, g_{\text{value}}) \)
6: return \( l_{\text{policy}}, l_{\text{value}} \)

Algorithm A.11 Using AlphaTensor’s neural network for inference.

def AlphaTensor_Net_inference \( \mathcal{X} \in \mathbb{R}^{T \times S \times S \times S}, s \in \mathbb{R}^s, c \in \mathbb{N}, N_{\text{samples}} \in \mathbb{N}, N_{\text{steps}} \in \mathbb{N}, N_{\text{logits}} \in \mathbb{N} \):

1: \( e = \text{Torso}(\mathcal{X}, s, c) \)
2: \( a, p, z_1 = \text{PolicyHead\_inference}(e, N_{\text{samples}}, N_{\text{steps}}, N_{\text{logits}}) \)
3: \( q = \text{ValueHead}(z_1) \)
4: \( q = \text{ValueRiskManagement}(q) \)
5: return \( a, p, q \)

Algorithm A.12: Generation of synthetic demonstrations

**Input:** Tensor size \( S \), maximum rank \( R_{\text{limit}} \), factor entry probability distribution \( p_{\text{entry}} \), desired number of demonstrations \( N \), random seed.

**Output:** \( N \) pairs \( \{\mathcal{T}, \{u(r), v(r), w(r)\}\} \) \( R_{\text{limit}} \) such that in each \( \mathcal{T} = \sum_{r=1}^{R} u(r) \otimes v(r) \otimes w(r) \).

1: for \( n = 1 \) to \( N \) do
2: \( R \sim \text{Uniform}\{1, 2, \ldots, R_{\text{limit}}\} \) \( \triangleright \) Sample the rank of the synthetic demonstration.
3: for \( r = 1 \) to \( R \) do
4: repeat
5: for \( s = 1 \) to \( S \) do
6: \( u_s(r) \sim p_{\text{entry}} \)
7: \( v_s(r) \sim p_{\text{entry}} \)
8: \( w_s(r) \sim p_{\text{entry}} \)
9: until \( u_s(r) \otimes v_s(r) \otimes w_s(r) \neq 0 \)
10: \( \mathcal{T} \leftarrow \sum_{r=1}^{R} u(r) \otimes v(r) \otimes w(r) \) \( \triangleright \) Reconstruct tensor corresponding to the random factors.
11: yield \( \{\mathcal{T}, \{u(r), v(r), w(r)\}\} \)

Algorithm A.12 shows the procedure we used for generating the synthetic demonstrations. The \( N \) data points are sampled i.i.d., corresponding to the outer loop on Line 1. When generating demonstrations in a ring \( \mathcal{E} \), factor entries are sampled from the set \( \mathcal{E} \) and the arithmetic (multiplications and additions in Line 10) is performed in this ring, e.g., in \( \mathbb{Z}_2 \) this corresponds to applying an additional modulo operation.

**Hyper-parameters.** In all experiments we used \( N = 5,000,000 \) synthetic demonstrations. We use the factor entry probability distribution \( p_{\text{entry}}(-2) = 0.001, p_{\text{entry}}(-1) = 0.099, p_{\text{entry}}(0) = 0.8, p_{\text{entry}}(1) = 0.099, p_{\text{entry}}(2) = 0.001 \) for standard arithmetic, and \( p_{\text{entry}}(0) = p_{\text{entry}}(1) = 0.5 \) for modular arithmetic in \( \mathbb{Z}_2 \).
A.3 Algorithm for generating changes of basis

**Algorithm A.13: Generation of basis change matrices**

**Input:** Tensor size \( S \), matrix entry distribution \( p_{\text{cob-entry}} \), desired number of basis changes \( N_{\text{cob}} \), random seed.

**Output:** \( N_{\text{cob}} \) i.i.d. invertible unimodular \( S \times S \) matrices.

1. for \( n = 1 \) to \( N_{\text{cob}} \) do
2. \( P \leftarrow \) zero \( S \times S \) matrix \( \triangleright \) Will be upper-triangular.
3. \( L \leftarrow \) zero \( S \times S \) matrix \( \triangleright \) Will be lower-triangular.
4. for \( i = 1 \) to \( S \) do
5. \( P_{ii} \sim \text{Uniform}\([-1, +1]\) \( \text{Uniform}\([-1, +1]\)
6. \( L_{ii} \sim \text{Uniform}\([-1, +1]\)
7. for \( i = 1 \) to \( S \) do
8. for \( j = 1 \) to \( i - 1 \) do
9. \( L_{ij} \sim p_{\text{cob-entry}} \)
10. for \( j = i + 1 \) to \( S \) do
11. \( P_{ij} \sim p_{\text{cob-entry}} \)
12. yield \( PL \)

Algorithm A.13 shows the procedure for generating basis change matrices. We chose to generate unimodular change of bases for numerical stability of the obtained factorizations after converting them back into the canonical basis. The matrices generated by Algorithm A.13 are unimodular because the determinant of a triangular matrix equals the product of its diagonal elements \((-1\) or \(+1\) in this case), and so \( \det(PL) = \det P \det L \in \{-1, +1\} \) by construction. In our experiments we used \( p_{\text{cob-entry}}(0) = 0.985 \) and \( p_{\text{cob-entry}}(-1) = p_{\text{cob-entry}}(1) = 0.0075 \).

In acting, an actor is given a basis in which it should try to decompose the tensor (see Figure 2 in the main paper). With probability \( p_{\text{canonical}} \) the basis given is the original (canonical) basis (i.e., no basis change is applied), and with probability \( 1 - p_{\text{canonical}} \) one of the \( N_{\text{cob}} \) generated basis changes is chosen uniformly at random. In our experiments \( N_{\text{cob}} = 100,000 \) and \( p_{\text{canonical}} = \frac{250}{100,000} \approx 0.0017 \).

A.4 Converting modular arithmetic algorithms into general ones

As for the targets \( \mathcal{T}_4 \) and \( \mathcal{T}_5 \) AlphaTensor discovered algorithms of lower rank in modular arithmetic \( \mathbb{Z}_2 \) than in standard arithmetic, we attempted to convert these modular arithmetic algorithms into general ones using a SAT solver. Specifically, given a factorization with entries \( F = \{0, 1\} \) valid in the ring \( \mathbb{E} = \mathbb{Z}_2 \), we set up the problem of finding a factorization with entries \( F = \{-1, 0, 1\} \) valid in the standard arithmetic \( \mathbb{E} = \mathbb{Z} \) such that it has the same sparsity pattern but each 1 entry in the \( \mathbb{Z}_2 \) factorization is turned into either a \(-1\) or \(+1\) entry in the \( \mathbb{Z} \) factorization. This follows the approach of [1]. Unfortunately, the SAT solver was able to prove that it is not possible to convert our rank-47 algorithms for \( \mathcal{T}_4 \) nor the rank-96 algorithms for \( \mathcal{T}_5 \) from \( \mathbb{Z}_2 \) into standard arithmetic in this way. This of course does not preclude that other algorithms of this rank may exist for these targets.

B Symmetries in matrix multiplication algorithms

In Section B.1, we define the equivalence relation between matrix multiplication algorithms. Section B.2 details the algorithm used for determining whether two algorithms are equivalent. Section B.3 provides details on the different non-equivalent factorizations found using AlphaTensor.

**Notation.** Recall that \( \otimes \) defines the tensor product operation. That is, if \( T_1 \) and \( T_2 \) are two tensors with \( n \) and \( m \) dimensions, respectively, then \( T_1 \otimes T_2 \) is the tensor with \( n + m \) dimensions where

\[
(T_1 \otimes T_2)[i_1, i_2, \ldots, i_n, j_1, \ldots, j_m] = T_1[i_1, \ldots, i_n]T_2[j_1, \ldots, j_m].
\]
A slight variation of the tensor product is the Kronecker product \( \otimes_K \), which we define between two tensors with the same number of dimensions: if \( \mathbf{T}_1 \) and \( \mathbf{T}_2 \) have sizes \([s_1, \ldots, s_n]\) and \([t_1, \ldots, t_n]\), respectively,
\[
(\mathbf{T}_1 \otimes_K \mathbf{T}_2)[i_1 j_1 + 1, \ldots, i_n j_n + j_n] = \mathbf{T}_1[i_1, \ldots, i_n]T_2[j_1, \ldots, j_n].
\]

For convenience, in Sections B.1-B.3 we will see factor vectors of \( \mathcal{J}_n \) as \( n \times n \) matrices, rather than vectors of length \( n^2 \), and hence denote them with capital letters \( \mathbf{U}, \mathbf{V}, \mathbf{W} \). If \( \{(\mathbf{U}_p, \mathbf{V}_p, \mathbf{W}_p)\}_{p=1}^R \) and \( \{(\mathbf{U}_q, \mathbf{V}_q, \mathbf{W}_q)\}_{q=1}^R \) are factorizations of \( \mathcal{J}_n \) then the following provides a rank \( R^2 \) factorization of \( \mathcal{J} \otimes_K \mathcal{J} \):
\[
\{(\mathbf{U}_p \otimes_K \mathbf{U}_q', \mathbf{V}_p \otimes_K \mathbf{V}_q', \mathbf{W}_p \otimes_K \mathbf{W}_q')\}_{p,q=1}^R.
\]

Finally, we denote as \( S_n \) the group of permutations of \( n \) elements.

### B.1 Equivalence of factorizations

Since the matrix multiplication tensors have a product structure (that is, \( \mathcal{J}_4 = \mathcal{J}_2 \otimes \mathcal{J}_2 = \mathcal{J}_2 \otimes_K \mathcal{J}_2 \)), one can obtain using Eq. 1 a rank 49 factorization of \( \mathcal{J}_4 \) from a rank 7 factorization (Strassen) of \( \mathcal{J}_2 \). Such product-structure factorizations are the only known schemes to decompose \( \mathcal{J}_n \) into 49 factors, and are referred to in the main paper as Strassen\footnote{Note that Winograd’s algorithm \cite{2} involves 48 multiplications, but it only applies to commutative rings and does not give bounds on the tensor rank. Winograd’s algorithm cannot be applied recursively on larger matrices.}^2

Our goal here is to show that the factorizations obtained using AlphaTensor are not equivalent to the known factorization of product structure, and that these factorizations are pairwise nonequivalent. To do so, we first define the notion of equivalence.

When seen as a trilinear map, the (transposed) matrix multiplication operation is \( (\mathbf{X}_1, \mathbf{X}_2, \mathbf{X}_3) \rightarrow \text{Trace}(\mathbf{X}_1 \mathbf{X}_2 \mathbf{X}_3) \). Using the invariance of trace to cyclic permutations, \( \text{Trace}(\mathbf{X}_1 \mathbf{X}_2 \mathbf{X}_3) = \text{Trace}(\mathbf{X}_2 \mathbf{X}_3 \mathbf{X}_1) = \text{Trace}(\mathbf{X}_3 \mathbf{X}_1 \mathbf{X}_2) \). Since transpose does not change the trace either, we also have
\[
\text{Trace}(\mathbf{X}_1 \mathbf{X}_2 \mathbf{X}_3) = \text{Trace}(\mathbf{X}_1^T \mathbf{X}_2^T \mathbf{X}_3^T) = \text{Trace}(\mathbf{X}_2^T \mathbf{X}_3^T \mathbf{X}_1^T) = \text{Trace}(\mathbf{X}_3^T \mathbf{X}_1^T \mathbf{X}_2^T).
\]

Similarly, for any invertible matrices \( \mathbf{A}, \mathbf{B}, \mathbf{C} \),
\[
\text{Trace}(\mathbf{X}_1 \mathbf{X}_2 \mathbf{X}_3) = \text{Trace}((\mathbf{A} \mathbf{X}_1 \mathbf{B}^{-1})(\mathbf{B} \mathbf{X}_2 \mathbf{C}^{-1})(\mathbf{C} \mathbf{X}_3 \mathbf{A}^{-1})).
\]

These invariances imply that, given a factorization \( \{(\mathbf{U}_r, \mathbf{V}_r, \mathbf{W}_r)\}_{r=1}^R \) of \( \mathcal{J}_n \), we can generate many equivalent factorizations of \( \mathcal{J}_n \). Such symmetries have been heavily studied for \( n = 2 \) and \( n = 3 \) \cite{3, 4, 5, 6, 7, 1}. We use the group of symmetries introduced in \cite{3}, which proved that Strassen’s algorithm for multiplying \( 2 \times 2 \) matrices is unique up to the actions of this symmetry group.

A permutation \( \pi \in S_3 \) acts on a rank one tensor \( \mathbf{U} \otimes \mathbf{V} \otimes \mathbf{W} \) by permuting the factors \( \mathbf{U}, \mathbf{V}, \mathbf{W} \) with an additional transposition when the signature of \( \pi \) is \(-1\). For example, for the cyclic permutation \((1, 2, 3)\), we have \((1, 2, 3) \cdot (\mathbf{U} \otimes \mathbf{V} \otimes \mathbf{W}) = \mathbf{V} \otimes \mathbf{W} \otimes \mathbf{U}\), and for the transposition \((1, 2)\), we have \((1, 2) \cdot (\mathbf{U} \otimes \mathbf{V} \otimes \mathbf{W}) = (\mathbf{V}^T \otimes \mathbf{U}^T \otimes \mathbf{W}^T)\). Given three invertible matrices \( \mathbf{A}, \mathbf{B}, \mathbf{C} \), their action on a rank one tensor is given by \((\mathbf{A} \otimes \mathbf{B} \otimes \mathbf{C})\cdot (\mathbf{U} \otimes \mathbf{V} \otimes \mathbf{W}) = (\mathbf{AUB}^{-1} \otimes \mathbf{BVC}^{-1} \otimes \mathbf{CWA}^{-1})\). The two actions are combined as follows: given \((\mathbf{A}, \mathbf{B}, \mathbf{C}, \pi)\), the permutation \( \pi \) is applied followed by the \((\mathbf{A}, \mathbf{B}, \mathbf{C})\) action. For example, if \( \pi = (1, 2, 3)\), then \((\mathbf{A}, \mathbf{B}, \mathbf{C}, \pi) : (\mathbf{U} \otimes \mathbf{V} \otimes \mathbf{W}) = (\mathbf{AVB}^{-1} \otimes \mathbf{BVC}^{-1} \otimes \mathbf{CWA}^{-1})\).

Two factorizations \( \{(\mathbf{U}_r, \mathbf{V}_r, \mathbf{W}_r)\}_{r=1}^R \) and \( \{(\mathbf{U}_r', \mathbf{V}_r', \mathbf{W}_r')\}_{r=1}^R \) are said to be equivalent if there exists \((\mathbf{A}, \mathbf{B}, \mathbf{C}, \pi)\) that maps the two sets of factors through the action described above: that is, there exists a permutation \( \sigma \in S_R \) such that for all \( r \in [R] \), we have
\[
\mathbf{U}_r \otimes \mathbf{V}_r \otimes \mathbf{W}_r = (\mathbf{A}, \mathbf{B}, \mathbf{C}, \pi) : (\mathbf{U}_{\sigma(r)}' \otimes \mathbf{V}_{\sigma(r)}' \otimes \mathbf{W}_{\sigma(r)}').
\]

We denote the equivalence as follows: \( \{(\mathbf{U}_r \otimes \mathbf{V}_r \otimes \mathbf{W}_r)\}_{r=1}^R \overset{\mathbf{A}, \mathbf{B}, \mathbf{C}, \pi}{\sim} \{(\mathbf{U}_r' \otimes \mathbf{V}_r' \otimes \mathbf{W}_r')\}_{r=1}^R \). For more details on symmetries of factorizations, see e.g., \cite{5}.

Using this notion of equivalence, \cite{3} showed that all rank 7 factorizations of \( \mathcal{J}_2 \) are equivalent. We show that this is not the case for \( \mathcal{J}_4 \), and we exhibit thousands of nonequivalent rank 49 factorizations of \( \mathcal{J}_4 \).

**Theorem B.1.** There exist at least 14,235 non-equivalent rank-49 decompositions of the matrix multiplication tensor \( \mathcal{J}_4 \). These decompositions are not of product structure, i.e., they are not equivalent to Strassen\footnote{Note that Winograd’s algorithm \cite{2} involves 48 multiplications, but it only applies to commutative rings and does not give bounds on the tensor rank. Winograd’s algorithm cannot be applied recursively on larger matrices.}^2.

The procedure for certifying non-equivalence of factorizations is provided in Section B.2.
B.2 Algorithm for certifying nonequivalence

We now explain the procedure we used to certify the nonequivalence of factorizations, and prove the result in Theorem B.1. To determine whether two factorizations are equivalent, we define invariants under the transformation introduced in Section B.1. The first invariant we use is the matrix rank invariant, given by

\[ R(\{U_r, V_r, W_r\}_{r=1}^R) = \{\text{rank}(U_r), \text{rank}(V_r), \text{rank}(W_r)\}_{r=1}^R, \]

where \(\{\cdot\}\) denotes an unordered tuple. \(R\) clearly satisfies the invariance under transformations in Section B.1. Using this invariant, we obtained 38 distinct factorizations with different invariants \(R\) (amongst more than 50,000 factorizations found by AlphaTensor). This invariant has been used extensively in prior work to check for equivalent factorizations (see e.g., [5]).

To further distinguish between non-equivalent factorizations, we developed a new invariant. This invariant is only applicable to a specific subset of factorizations, but on this subset it is much more granular than \(R\). More specifically, the new invariant is applicable to those factorizations that have exactly one factor such that the product of matrices is identity; i.e. without loss of generality \(U_1 V_1 W_1 = I\). (Note that this property is itself invariant under the transformations in Section B.1.) This property was satisfied by many but not all factorizations found by AlphaTensor. For factorizations satisfying this property we then define the invariant as follows:

\[ \mathcal{K}(\{U_r, V_r, W_r\}_{r=1}^R) = \{\text{CharPoly}(\Phi(U_r \otimes_K V_r \otimes_K W_r))\}_{r=1}^R, \]

where \(\Phi(U_r \otimes V_r \otimes W_r) = (I \otimes_K W_r^{-1} V_r^{-1} I) (U_r \otimes_K V_r \otimes_K W_r) (V_r \otimes_K W_r \otimes I) (I \otimes_K U_r \otimes W_r)\), and \text{CharPoly}\ denotes the characteristic polynomial \(\text{CharPoly}(A)(x) = \det(xI - A)\). Intuitively, \(\Phi\) is a canonicalization operator that applies the \((A, B, C) = (I, W_r^{-1} V_r^{-1}, I)\) action to each rank one tensor in the factorization, so that the first factor becomes \(U_1 \otimes V_1 \otimes W_1 := \Phi(U_1 \otimes V_1 \otimes W_1) = I \otimes I \otimes I\). The following theorem states that \(\mathcal{K}\) is an invariant:

**Theorem B.2.** If \(\{U_r, V_r, W_r\}_{r=1}^R\) and \(\{U'_r, V'_r, W'_r\}_{r=1}^R\) are equivalent and \(U'_1 V'_1 W'_1 = U_1 V_1 W_1 = I\), then \(\mathcal{K}(\{U_r, V_r, W_r\}_{r=1}^R) = \mathcal{K}(\{U'_r, V'_r, W'_r\}_{r=1}^R)\).

See Section I.1 for the proof. Amongst all the factorizations obtained from AlphaTensor where this invariant is applicable, there are 14,207 factorizations that have a different value of \(\mathcal{K}\). Together with 28 non-equivalent factorizations where this invariant is not applicable, these form a set of 14,235 non-equivalent rank-49 factorizations of \(\mathcal{T}_4\). See the Python notebook in the supplementary material for the actual computation.

We further check that the factorizations obtained using AlphaTensor are not of product structure. To do so, we rely on the following result, which shows that it is sufficient to check that \(\{U_r, V_r, W_r\}_{r=1}^R\) is equivalent to one factorization of product form to infer that it is nonequivalent to all factorizations of product form.

**Theorem B.3.** Let \(\{\{U_r, V_r, W_r\}_{r=1}^{49}\}\) be a rank 49 factorization of \(\mathcal{T}_4\). Let \(\{\{S_p^1, S_p^2, S_p^3\}_{p=1}^7\}\) be a rank 7 factorization of \(\mathcal{T}_2\) (i.e., a Strassen scheme for multiplying 2 \(\times 2\) matrices). The following two statements are equivalent:

1. \(\{\{U_r, V_r, W_r\}_{r=1}^{49}\}\) is equivalent to a factorization of product form \(\{\{X_p \otimes_K X_q', Y_p \otimes_K Y_q', Z_p \otimes_K Z_q'\}_{p,q}^7\}\).

2. There exist permutations \(\pi, \pi' \in S_3\) such that \(\{\{U_r, V_r, W_r\}_{r=1}^{49}\}\) is equivalent to

\[ \left\{ (T^\pi(S^\pi(1)) \otimes_K T^\pi(S^\pi(2)) \otimes_K T^\pi(S^\pi(3)) \otimes_K T^\pi(S^\pi(2)) \otimes_K T^\pi(S^\pi(3)) \otimes_K T^\pi(S^\pi(2)) \otimes_K T^\pi(S^\pi(3))) \right\}_{p,q}^7. \]

where \(T^\pi\) is the transpose operator when the signature of \(\pi\) is \(-1\), and identity otherwise.

See Section I.2 for the proof.

B.3 Diversity of factorizations

The multitude of nonequivalent factorizations discovered by AlphaTensor demonstrates that this space is richer than previously thought. Without further manipulations or optimizing for different objectives, the rank-49 factorizations of \(\mathcal{T}_4\) discovered by AlphaTensor have the following properties:
• **Entries** in \{-4, -3, -2, -1, 0, 1, 2\}. The reason we were able to discover factorizations with entries outside of the set \(F = \{-2, -1, 0, 1, 2\}\) that the network can predict is that after converting an \(F\)-valued factorization obtained in a different basis back into the canonical basis, its entries can end up outside of \(F\). The entries are still integral because we use unimodular basis change matrices.

• **Matrix ranks** of the factor vectors (when seen as \(4 \times 4\) matrices) in the factorizations form 38 different types [8, 5, 1, 9]. Represented as a three-variable polynomial, the type of Strassen\(^2\) is \(p(x, y, z) = x^4y^4z^4 + 12x^2y^2z^2 + 36xyz\) (there is 1 factor with all three vectors of matrix rank 4, 12 factors with all three vectors of matrix rank 2, and 36 factors with all three vectors of matrix rank 1), and AlphaTensor also discovers many factorizations of this type. However, AlphaTensor also discovers factorizations with 37 new types.

• **Sparsity.** The number of nonzeros in our discovered factorizations ranges from 455 to 636.

• **Cyclicity.** Around 0.2\% of the discovered factorizations are cyclic (closed under permuting \((U, V, W)\) with a cyclic element of \(S_3\)); the vast majority are noncyclic.

• **Numerical stability.** [10] identified two principal quantities that govern the numerical error bounds of matrix multiplication algorithms: a prefactor \(Q\), and a stability factor \(E\). Lower values correspond to tighter bounds. For the Strassen\(^2\) algorithm \(Q = 24\) and \(E = 144\), while for the discovered factorizations \(Q \in [23, 39]\) and \(E \in [136, 1046]\). In particular, AlphaTensor discovered an algorithm with \(Q = 23\) and \(E = 136\) without using a reward function that would optimize for numerical stability. Finally, the growth factor [11] of the factorizations is in the range from 220.14 to 547.26.

### C Structured matrix multiplication

We apply AlphaTensor to more general operations beyond matrix multiplication. Just like standard matrix multiplication, any bilinear operation can be represented by a tensor, and its low-rank decompositions correspond to efficient algorithms. Bilinear operations encompass a large set of computational problems: structured matrix multiplication (e.g., symmetric matrix multiplication), polynomial multiplication, or more customized bilinear operations commonly used in machine learning models, such as graph networks [12, 13]. Here, we explore two use-cases: circulant matrix-vector product (or equivalently, circular convolution) in finite fields, and skew-symmetric matrix-vector product.

For the former use-case, our goal is to demonstrate the generality and power of AlphaTensor by discovering the Fourier basis, a complex and foundational transformation in mathematics, in a finite field, where arithmetic rules are non-intuitive. For the latter, the optimal algorithm is unknown [14], and we use AlphaTensor to discover an algorithm that improves over the state-of-the-art algorithm. We employ a common methodology for both use-cases: using AlphaTensor, we compute low-rank decompositions for tensors corresponding to small instances; then, following a human inspection, we generalize these algorithms to arbitrary-sized matrices/vectors.\(^2\)

#### C.1 Circular matrix-vector product in finite fields

The first problem we consider is the product between a \(n \times n\) circulant matrix and a vector of length \(n\). This operation is equivalent to computing the circular convolution between two vectors of length \(n\): the first column of the matrix and the input vector. A naive implementation of this operation would have bilinear complexity \(O(n^2)\); in contrast, under certain conditions, there exists an optimal algorithm that achieves bilinear complexity \(O(n)\). This optimal algorithm consists of three steps: (i) obtaining the discrete Fourier transform (DFT) of each vector input (in a finite field, it is the cyclotomic DFT instead), (ii) multiplying (element-wise) the results, and (iii) obtaining the inverse DFT of the result. Here we aim at rediscovering this optimal algorithm using AlphaTensor.

The tensor \(T_{n}^{\text{circ}}\) that represents the circular matrix-vector product has size \(n \times n \times n\), and its entries are given by \(T_{n}^{\text{circ}}[i, j, k] = \delta\{k = (i + j) \mod n\}\) for \(0 \leq i, j, k < n\), where \(\delta\{\}\) is the indicator function (we use zero-based indexing here). The tensor \(T_{n}^{\text{circ}}\) has therefore \(n^n\) nonzero elements, but its rank is \(n\) (provided the \(n\)-length DFT exists in the corresponding ring \(\mathcal{E}\) of interest, which is always the case if \(\mathcal{E} = \mathbb{R}\)).

We focus on computations in finite fields. In particular, we set \(\mathcal{E}\) to be the finite field of order \(p\), where \(p\) is a prime power. This is challenging for two reasons. First, the action space becomes huge even for moderate values of \(n\) and \(p\),

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\(^2\)Unlike standard matrix multiplication, where decompositions for fixed size tensors yield algorithms for any size, this extra human step can be needed for other bilinear operations where recursion is not possible or is not efficient.
e.g., for $n = 8$ and $\rho = 17$ (which are values that we consider), the action space is of the order of $\rho^{3n} \approx 10^{29}$. Second, multiplications and additions are performed according to the arithmetic in the finite field, and therefore interpreting the factor and tensor entries as integers may be a poor choice (e.g., for $\rho = 17$, the values 1 and 16 are both 1 unit away from 0, since $0 + 1 = 1$ and $16 + 1 = 0$). To account for these challenges, we adapt AlphaTensor as described next.

**Methodology.** We now describe the modifications of AlphaTensor that we use for this case:

- **Vector-per-move.** In the standard TensorGame, each move (action) corresponds to a triplet $(u^{(t)}, v^{(t)}, w^{(t)})$. Here, we modify TensorGame so that the factor triplet is split into three separate moves. The first move of the game corresponds to $u^{(t)}$, the second one to $v^{(t)}$, the third one to $w^{(t)}$, and so on. Consequently, the state of the game $s$ is augmented with the vectors already written down for the next factor: for each $t \geq 0$,

$$s_{3t} = (S_t, ()),$$
$$s_{3t+1} = (S_t, (u^{(t+1)})),$$
$$s_{3t+2} = (S_t, (u^{(t+1)}, v^{(t+1)})).$$

The tensor $S_t$ in the state is only updated after every third move, when a $w^{(t)}$ vector is written down. The update is then analogous to the standard TensorGame. Thus, the main difference of this variant is that there are three times as many moves to reach the same residual tensor. While this has no impact on the score achieved in the game (as long as the maximum number of moves is increased threefold), there is a computational advantage: the move size is $3 \times$ smaller (which implies an exponential reduction in the size of the set of possible moves), even at the expense of $3 \times$ longer games.

- **Network input.** To help the network learn the arithmetic in finite fields, we pre-process the input as follows. For every input entry $x$, we compute all the products $\{x, 2x, \ldots, (\rho - 1)x\}$. We concatenate the $\rho - 1$ resulting entries, pre-process them using four additional dense blocks, and expose the result as an additional input to both the torso and the policy head.

- **Action canonicalization reward.** The action canonicalization is particularly important in finite fields, where a given rank-1 tensor can be expressed using $(\rho - 1)^2$ equivalent triplets $(\lambda_1 u, \lambda_2 v, \lambda_3 w)$ with $\lambda_1 \lambda_2 \lambda_1 = 1 \pmod{\rho}$; e.g., there are 256 equivalent triplets for $\rho = 17$. In a finite field we define the canonical form as having the first nonzero element of $u$ and the first nonzero element of $v$ equal to 1. Instead of imposing the canonicalization, we add a penalty to the reward whenever the agent outputs a factor triplet that is not in canonical form. Specifically, the penalty is $-0.5$ for each individual factor ($u$ or $v$) that is not in canonical form.

- **Permutation symmetry reward.** Two actions $(u, v, w)$ and $(u', v', w')$ can be applied in either order due to commutativity. Therefore, to reduce the number of equivalent factorizations, we use another type of canonicalization—defined over the whole factorization instead of over each individual factor. Specifically, we define the canonical representation of a factorization as the factorization in which the factors are sorted lexicographically. When the agent fails to output a factorization in its canonical form, we add a penalty reward equal to $-1/R_{\text{limit}}$ for each pair of consecutive actions that are not played in canonical order. This reward is added in addition to the factor canonicalization reward. All synthetic demonstrations are also converted to their canonical form (considering both factor canonicalization and permutation symmetry).

- **Synthetic demonstrations.** When generating demonstrations, we ensure that the factor entries take all the possible values in the finite field. We set $p_{\text{entry}}(0) = 0.5$ and $p_{\text{entry}}(v) = \frac{1}{\rho - 1}$ for $v = 1, \ldots, \rho - 1$.

- **Hyper-parameters.** We set $R_{\text{limit}} = 64$ and allow all elements of $\mathcal{E}$ as possible factor entries: $F = \{0, 1, \ldots, \rho - 1\}$. The policy head uses $N_{\text{steps}} = 2$, $N_{\text{logs}} = \rho^2$ (i.e., $289$ for $\rho = 17$), $N_{\text{layers}} = 4$, and at inference time (in MCTS) we sample $N_{\text{samples}} = 64$ actions. The value head is an implicit quantile network (IQN) head [15] with 8 samples, and it ignores the policy embedding input $z_1$. Signed permutations are not used; instead we inject more diversity by setting $N_{\text{cob}} = 300,000$.

**Results.** We apply AlphaTensor to the tensor $\Sigma_n^{\text{unc}}$, setting $\rho = 17$ and $n = 2, 4, 8$, in a single-target setting (i.e., a separate experiment for each $n$). Extended Data Table 2 shows the obtained solutions. Despite the difficulty of the task

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3In the finite field of order $\rho = 17$, the $n$-point discrete Fourier transform exists for vectors of the considered length ($n = 2, 4, 8$), as well as for the extrapolation for $n = 16$. We also ran experiments on a finite field with non-prime cardinality (with $\rho = 9$), also recovering the Fourier basis for $n = 2, 4, 8$. 

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and the enormous action space, AlphaTensor finds the optimal decomposition for the considered values of $n$. These decompositions correspond precisely to the Fourier basis, from which patterns can be detected through visual inspection — we color-coded such patterns for clarity in Extended Data Table 2. From these examples, one can generalize the solution to arbitrary $n$ and field by setting $u_k^{(r)} = v_k^{(r)} = z^{kr}$ and $w_k^{(r)} = z^{-kr} / n$, where $0 \leq k, r < n$ and $z$ is an $n$-th primitive root of unity$^4$ (see below for details on this step). That is, $U$ and $V$ are the (cyclo-tomic) DFT matrices, while $W$ corresponds to the inverse DFT. As the Fourier transform is known to be the optimal algorithm, this shows the capability of AlphaTensor to discover optimal algorithms in huge algorithm spaces.

**Generalizing the factorizations found by AlphaTensor.** Here we exemplify the process to generalize the factorizations found by AlphaTensor on small tensors on the finite field of order 17 to any arbitrarily sized tensor and finite field, which is the only step in the process that requires human intervention.

We start by showing an example for $n = 4$, but the procedure is analogous for the sizes $n = 2$ and $n = 8$. For $n = 4$, the raw factorization found by AlphaTensor is:

$$U = V = \begin{pmatrix} 1 & 1 & 1 & 1 \\ 16 & 13 & 4 & 1 \\ 1 & 16 & 16 & 1 \\ 16 & 4 & 13 & 1 \end{pmatrix}, \quad W = \begin{pmatrix} 13 & 13 & 13 & 13 \\ 4 & 1 & 16 & 13 \\ 13 & 4 & 4 & 13 \\ 4 & 16 & 1 & 13 \end{pmatrix},$$

where each column represents a factor, i.e., $U = V = [u^{(1)}, \ldots, u^{(n)}]$ and $W = [w^{(1)}, \ldots, w^{(n)}].$

First, we note that the matrices above can be made symmetric by permuting the factors (i.e., columns). This leads to the factorization$^5$

$$U' = V' = \begin{pmatrix} 1 & 1 & 1 & 1 \\ 1 & 4 & 16 & 13 \\ 1 & 16 & 1 & 16 \\ 1 & 13 & 16 & 4 \end{pmatrix}, \quad W' = 4^{-1} \begin{pmatrix} 1 & 1 & 1 & 1 \\ 1 & 13 & 16 & 4 \\ 1 & 16 & 1 & 16 \\ 1 & 4 & 16 & 13 \end{pmatrix},$$

where we have additionally re-written $W'$ by taking out the factor $4^{-1} = 13$ in order to make $w'^{(1)}$ similar to $u'^{(1)}$.

Second, we note that the entries in the matrices above involve only a subset of the elements in $\mathbb{F}$. In particular, they involve only the elements that are powers of 4 (since $1 = 4^0, 4 = 4^1, 16 = 4^2$, and $13 = 4^3$). Taking this into account, and applying the identity $4^a = 4^{a+4}$ (which holds in the finite field of order $\rho = 17$), we rewrite the factors in a form that is more convenient for generalization:

$$U' = V' = \begin{pmatrix} 4^0 & 4^0 & 4^0 & 4^0 \\ 4^0 & 4^1 & 4^2 & 4^3 \\ 4^0 & 4^2 & 4^4 & 4^6 \\ 4^0 & 4^3 & 4^6 & 4^9 \end{pmatrix}, \quad W' = 4^{-1} \begin{pmatrix} 4^0 & 4^0 & 4^0 & 4^0 \\ 4^0 & 4^{-1} & 4^{-2} & 4^{-3} \\ 4^0 & 4^{-2} & 4^{-4} & 4^{-6} \\ 4^0 & 4^{-3} & 4^{-6} & 4^{-9} \end{pmatrix},$$

which we can express in a more compact way as $u_k^{(r)} = v_k^{(r)} = 4^{kr}$ and $w_k^{(r)} = 4^{-kr} / 4$ for $0 \leq k, r < n$.

Following an analogous procedure for $n = 2$, we obtain $u_k^{(r)} = v_k^{(r)} = 16^{kr}$ and $w_k^{(r)} = 16^{-kr} / 2$, while for $n = 8$ we obtain $u_k^{(r)} = v_k^{(r)} = 2^{kr}$ and $w_k^{(r)} = 2^{-kr} / 8$.

Thus, we can extrapolate these results to find the general expression $u_k^{(r)} = v_k^{(r)} = z^{kr}$ and $w_k^{(r)} = z^{-kr} / n$, where $z$ is an $n$-th primitive root of unity in the finite field — for example, an extrapolation for $n = 16$ in the same finite field can be built using $z = 3$. Note that this generalization is valid not only for other values of $n$; it is also not limited to $\rho = 17$ — it is applicable to any finite field and any $n$ such that an $n$-th primitive root of unity exists in the field. Furthermore, the generalization is also valid beyond finite fields, as it also applies for complex-valued inputs by setting $z$ to an $n$-th primitive root of unity, i.e., $z = \exp \left\{ \frac{2\pi \sqrt{-1}}{n} \right\}$.

**C.2 Skew-symmetric matrix-vector product**

Here, we describe the second use-case of structured matrix multiplication. We use AlphaTensor to discover a new algorithm for computing the product between a $n \times n$ skew-symmetric matrix $A$ and a vector $b$ that outperforms the state-of-the-art approaches.

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$^4$An $n$-th primitive root of unity $z$ satisfies $z^n = 1$ and $z^k \neq 1$ for any $k \in \{1, \ldots, n-1\}$.

$^5$The choice of the permutation is not unique, but any such choice leads to an analogous final expression for the generalization.
The skew-symmetric matrix-vector product is a bilinear operation that can be represented by a tensor $\mathcal{T}_{n}^{\text{skew}}$ of size $\frac{n(n-1)}{2} \times n \times n$. To build this tensor, we start from the general matrix-vector multiplication tensor $\mathcal{T}_{n,n,1}$ of size $n^2 \times n \times n$. We build the tensor $\mathcal{T}_{n}^{\text{skew}}$ as follows: first we compute an auxiliary tensor $\mathcal{T}_{n,n,1}^{\text{aux}}[i,j,k] = \mathcal{T}_{n,n,1}[i,j,k] - \mathcal{T}_{n,n,1}[i,k,j]$, and then we remove all the slices of the auxiliary tensor that are linearly dependent, obtaining the $\frac{n(n-1)}{2} \times n \times n$ tensor $\mathcal{T}_{n}^{\text{skew}}$. An explicit algorithm to build this tensor is presented in Algorithm C.14.

Algorithm C.14: Builds tensor representing the skew-symmetric matrix-vector multiplication of size $n$.

**Input:** $n$ (the size of both the matrix and the vector).

1: $m = \frac{n(n-1)}{2}$
2: $\mathcal{T}_{n}^{\text{skew}} = 0$
3: $r = 1$
4: $c = 1$
5: for $i \in \{1, \ldots, m\}$ do
6: \hspace{1em} $c = c + 1$
7: \hspace{1em} if $(c - 1) \mod n = 0$ then
8: \hspace{2em} $r = r + 1$
9: \hspace{2em} $c = r + 1$
10: \hspace{1em} $\mathcal{T}_{n}^{\text{skew}}[i,r,c] = 1$
11: \hspace{1em} $\mathcal{T}_{n}^{\text{skew}}[i,c,r] = -1$
12: return $\mathcal{T}_{n}^{\text{skew}}$

**Results.** Figure 4a in the main paper shows the decompositions obtained by AlphaTensor. Similarly to the previous use-case of structured matrix multiplication (Appendix C.1), we detect a pattern for small sizes $n$ (color-coded in the illustration), which we generalize and prove for arbitrary $n$, yielding a general algorithm for the skew-symmetric matrix-vector product (Figure 4b in main paper).

**Theorem C.4.** The number of multiplications required to multiply a skew-symmetric matrix and a vector of dimension $n$ is at most $(n-1)(n+2)/2$.

**Proof.** See Section I.3.

This algorithm is strictly better in terms of bilinear complexity compared to previously known algorithms [14] — it uses $\sim \frac{1}{2}n^2$ multiplications instead of $\sim n^2$. We also note that this discovered algorithm is asymptotically optimal, as $\frac{1}{2}n^2$ matches the asymptotic lower bound obtained by counting the number of different elements in a skew-symmetric matrix. The discovered algorithm, which improves upon state-of-the-art methods, demonstrates the viability of AlphaTensor in discovering new and more efficient algorithms. We believe this methodology can be applied to other bilinear operations, and yield efficient algorithms taking into account the structure of the problem.

**Generalizing the factorizations found by AlphaTensor.** Unlike the circulant matrix-vector case, in skew-symmetric matrix-vector multiplication there exist many decompositions of the same rank, even when factoring out permutation and factor symmetries. AlphaTensor indeed found many such valid decompositions that are not just the ones presented in Figure 4a. Nevertheless, we noted that many of these decompositions presented a peculiar structure: for each factor, either $\mathbf{u}$ or $\mathbf{v}$ is a one-hot vector. Even more interestingly, when $\mathbf{u}$ is a one-hot vector whose nonzero entry corresponds to element $(i,j)$ from the matrix $\mathbf{A}$, then the corresponding vector $\mathbf{v}$ is non-zero only in the elements $i$ and $j$. Conversely, when $\mathbf{v}$ is a one-hot vector whose nonzero entry corresponds to the $i$-th element of the vector $\mathbf{b}$, then the corresponding $\mathbf{u}$ is non-zero only on the elements corresponding to $(j,i)$ of matrix $\mathbf{A}$ for any $j$ in $\{1, \ldots, n\}$ (i.e., the elements of the matrix that get multiplied by the $i$-th element of the vector $\mathbf{b}$). This insight led us to the hypothesis that we can always decompose the $n$-skew-symmetric matrix-vector multiplication tensor by using $(n+2)(n-1)/2$ terms of the special forms described above. This is the strategy followed in the proof in Section I.3, which shows that this is indeed the case.
D Rapid tailored algorithm discovery

D.1 Benchmarking details

We use AlphaTensor to find provably correct matrix multiplication algorithms which are tailored to optimize the running time in a particular setting, where the setting includes hardware (e.g., a V100 GPU or TPU), matrix size (e.g., multiplying a pair of $8,192 \times 8,192$ matrices), and the arithmetic precision (e.g., single precision arithmetic). In these experiments we benchmark algorithms discovered by AlphaTensor on the fly, and use this benchmarking result for the reward function. That is, we set $r'_t = r_t + \lambda b_t$, where $r_t$ is the rank reward described in the paper, $b_t$ is the benchmarking reward (nonzero only at the terminal state), and $\lambda$ is a user-specified coefficient.\(^6\)

After every game that ends with a correct factorization of the matrix multiplication tensor, an AlphaTensor actor sends a remote request to a benchmarking server equipped with the target hardware (e.g., a V100 GPU). The benchmarking server builds the matrix multiplication algorithm defined by the incoming factorization and then applies just-in-time (JIT) compilation using JAX\(^{[16]}\), a Python library for high-performance machine learning research. JAX JIT analyzes the given Python code and optimizes the code for the target hardware. For example, it can decide to fuse a matrix multiplication followed by a matrix addition into a single CUDA GEMM call. JAX also controls the order of execution of operations and can schedule multiple ops to run in parallel. In many cases it is possible to outperform JAX JIT by writing custom CUDA code, but JAX JIT proved to be good enough in our experiments, with the additional benefit of being fully automatic. This allowed us to automatically optimize and benchmark every matrix multiplication algorithm proposed by AlphaTensor.

![Figure D.1: Schematic illustration of the training pipeline when using benchmarking to define the reward function. Every actor has a dedicated benchmarking server, but also occasionally uses other benchmarking servers (indicated by dashed lines) to increase the precision for the most promising algorithms.](image)

To implement the benchmarking, we were guided by the information in \(^{[17]}\), as well as the Python timeit module documentation \(^{[18]}\). To reduce benchmarking noise, timeit employs three tricks: it runs the function being benchmarked many times (such that the overall running time of the loop is at least 0.2 seconds); it disables Python garbage collection for the duration of benchmarking; and it repeats the loop three times, reporting the minimal timing (to be more robust to spikes in the timing due to interruptions from the OS and other users of the machine).

We followed all these three practices with a single modification. When calling a JAX JIT function, it returns a future instead of the immediate result, scheduling the computation on the GPU. As a result, running the matrix multiplication function multiple times in a loop can potentially schedule the function multiple times and potentially run them in parallel, which can skew the benchmarking results. We therefore opted to compute a sequence of matrix multiplications that all depend on each other in the inner loop, namely six sequential matrix multiplications $(\mathbf{A}(\mathbf{A}(\mathbf{A}(\mathbf{A}(\mathbf{A}(\mathbf{A}\mathbf{B}))))))$ and used the block_until_ready() method on the final output. In this way, the matrix multiplications in the inner loop run sequentially, but the GPU is never blocked by waiting for the CPU to schedule the next multiplication, since

\(^6\)While the benchmarking reward might be enough in theory, it is useful for the learning dynamics to combine benchmarking with rank reward, to shape the reward towards more efficient algorithms.
scheduling the next multiplication by the CPU can be done in parallel with computing the current multiplication by the GPU.

When implementing this approach, we found that the variance of benchmarking was still large. For example, when using this approach to benchmark an algorithm discovered by AlphaTensor, the estimated speed up against the baseline Strassen\(^2\) algorithm varied from 4\% to 11.6\% (see Figure D.2). We identified the main reason of this noise to be the GPU frequency scaling (GPU driver can change the clock frequency on the fly). We implemented two additional techniques to improve the stability of the measurements.

First, additionally to measuring the running time of the algorithm of interest, we also measure the running time of the baseline Strassen\(^2\) algorithm and report the ratio between the two. The idea is that since the two measurements are done one after another, the clock frequency is likely to be similar when benchmarking the two algorithms. This makes our rewards comparable even for two factorizations benchmarked hours apart using very different clock frequencies, since each ratio represents the speedup with respect to the baseline computed with (almost) fixed frequency. We repeat this process of benchmarking the target algorithm and the baseline several times (4 in the experiments) and report the median ratio. As seen from numerical experiments, using this additional trick indeed significantly reduces the variance of the measurements (see Figure D.2).

Second, to be even more agnostic to the state of the benchmarking server, we benchmark the most promising algorithms on multiple benchmarking servers and report the median across those. Namely, for algorithms that are better than 75\% of the other algorithms seen so far we use the median ratio across 2 benchmarking servers, for algorithms that are better than 90\% of the algorithms seen so far we use the median across 5 servers, and for algorithms that are better than 95\% of the other algorithms we use the median across 17 servers.

When measuring the running time of an algorithm, we preload the input data (the two block matrices) into the GPU memory and do a series of 3 warm-up runs to make sure the GPU is ready to be used for benchmarking. We do not count the time to transfer the data to and from the GPU memory, as in most cases the GPU is used for more than one matrix multiplication operation in a row and the data transfer time gets amortized. We also do not count the time to split the input matrices into blocks and to construct the resulting matrix from the blocks as it is possible to implement any of the fast matrix multiplication algorithm in C CUDA using memory addressing which would make the block splitting and concatenating essentially free. See benchmarking pseudocode in Figure D.3.

![Figure D.2: Estimated speed up of a single algorithm with respect to the Strassen\(^2\) baseline. The histogram is computed by repeating the same benchmark 1,000 times on 100 different GPUs (100,000 benchmarkings in total). The two plots correspond to the two different benchmarking schemes discussed in the text: a “simple” scheme based on the Python best practices and “with interleaving baseline” that includes the additional trick discussed above to circumvent the frequency scaling issue. The “exact” speed up is computed as the median of all the speed ups estimated by both methods.]

In the AlphaTensor runs, we use Strassen\(^2\) as the baseline algorithm for the benchmarking. However, to report the results, our discovered algorithms are re-benchmarked against standard matrix multiplication, where we use a higher precision. Namely, we used the median ratio across 200 repeats. To further increase the accuracy, we fixed the GPU
```python
def benchmark_single_server(factorization, a, b):
    algorithm = algorithm_from_factors(factorization)
    algorithm = jax.jit(algorithm)
    baseline = jax.jit(algorithm_from_factors(strassen_factorization))

    # Warm-up
    for i in range(3):
        algorithm(a, b).block_until_ready()
        baseline(a, b).block_until_ready()
    gc.disable()  # Disable the garbage collector.
    ratios = []
    for i in range(4):
        start = time.time()
        c = b
        for j in range(6):
            c = algorithm(a, c)
            c.block_until_ready()
        end = time.time()
        alg_time = end - start
        start = time.time()
        c = b
        for j in range(6):
            c = baseline(a, c)
            c.block_until_ready()
        end = time.time()
        baseline_time = end - start
        ratios.append(baseline_time / alg_time)
    gc.enable()
    return np.median(ratios)
```

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**Figure D.3:** Python pseudocode for benchmarking a matrix multiplication algorithm on a single benchmarking server (as discussed in Appendix D.1, for the most promising factorizations we additionally compute the median ratio across multiple servers).

Clock frequency\(^7\) to the maximum possible value (1530 for a V100 GPU). For example, when repeatedly estimating the speed up of Strassen\(^2\) against the standard baseline using this approach, we got estimates from 4.15% to 4.3% with a standard deviation of 0.06 percent points.

For TPUs we follow the exact same benchmarking procedures, with only two exceptions: a) we don’t fix the clock frequency (because on TPUs clock is always constant); b) we use matrices of datatype `bfloat16` (in contrast to `float32` used for GPUs), as this is a more common setting among TPU users.

### D.2 Experimental setup

We adapt AlphaTensor to work with benchmarking reward as follows. The immediate reward is set to \(r'_t = r_t + \lambda b_t\), where \(r_t\) is the reward described in the paper, and \(b_t\) is the benchmarking reward, equal to zero in all intermediate states, and equal to

\[
b_t = \frac{\text{time taken to execute baseline algorithm}}{\text{time taken to execute found algorithm}} - \gamma,
\]

for a terminal state with \(S_t = 0\), where \(\gamma\) is a hyper-parameter. As the algorithms found initially are of high rank (and hence not efficient), we initially set \(\lambda = 0\), and activate the benchmarking reward only in late stages of the training (i.e., when AlphaTensor discovers sufficiently small ranks). In our experiments we used \(\lambda = 5.0\), \(\gamma = 0.8\) and Strassen\(^2\) as the baseline algorithm. For synthetic demonstrations, the benchmarking reward is set to 0, as such tensors do not correspond to a matrix multiplication algorithm. Moreover, we only benchmark games played in the canonical basis, as we did not find the use of other basis helpful.

In addition to the value head that predicts the rank of the tensor, we train an auxiliary value head and an auxiliary reward head. The auxiliary reward head is trained to predict \(b_t\), while the auxiliary value head is trained to predict the auxiliary return (future auxiliary reward) at any state. The value that is used in MCTS is a linear combination of the

---

\(^7\)We only fix the GPU clock frequency for the final reporting because our internal infrastructure makes it hard to do it for live experiments at scale.
main value and the auxiliary value, \( v = v_{\text{main}} + \lambda v_{\text{auxiliary}} \). We use the rewards predicted by the reward head in MCTS simulations, as benchmarking an algorithm to compute the true environment reward \( b_t \) is time consuming, and would significantly slow down MCTS. These auxiliary heads are only trained on selfplay games, i.e. we do not train them on demonstrations.

In addition to the usual inputs (described in Methods section) we provide an extra input to the neural network consisting of all previous actions (factors) played to reach the current state (tensor). Unlike the residual tensor that does not contain information about the matrix multiplication algorithm, the factors encode the algorithm and hence provide important information to the auxiliary heads.

### D.3 Additional results

We now show that by optimizing algorithms for larger sizes, one can obtain further speed-ups. Our experiments are performed on TPU v2. We note a substantial speed-up when optimizing the algorithm for the intended size. For example, when multiplying matrices of size 16,384, the algorithm optimized for 8,192 yields 11.2% speed-up compared to the standard algorithm, while the AlphaTensor-discovered algorithm optimized for matrices of size 16,384 achieves a 13.5% speed-up.

![Figure D.4: Speed-up of two algorithms found by AlphaTensor, relative to the standard (optimized) matrix multiplication algorithm on TPU v2. The first algorithm (shown in blue) is optimized for multiplying matrices of size 8,192, while the second (shown in purple) is optimized for multiplying matrices of size 16,384. The baseline Strassen\(^2\) algorithm is also shown. We report the median over 1,000 runs. The standard deviation over runs is < 0.4 percent points.](image_url)

**E Finding border rank with AlphaTensor**

*AlphaTensor* is a general method that supports any tensor decomposition environment and any reward function. One example is *AlphaTensor*’s ability to operate in finite fields, where the factor and tensor entries live in a finite set, and the multiplication and addition arithmetic obeys the rules in finite fields. Another example is *AlphaTensor*’s ability to find efficient algorithms tailored to specific hardware.
To demonstrate the ability of AlphaTensor to decompose tensors in different settings, we show a use-case that departs from the standard low-rank tensor decomposition. Specifically, we aim at finding the border rank of a tensor. The border rank of a tensor $\mathcal{T}$ is the minimum integer $R$ such that the decomposition $\sum_{\varepsilon = 1}^{R} \mathbf{u}^{(r)}(\varepsilon) \otimes \mathbf{v}^{(r)}(\varepsilon) \otimes \mathbf{w}^{(r)}(\varepsilon)$ converges to $\mathcal{T}$ as the scalar $\varepsilon \to 0$ for some factors $\{\mathbf{u}^{(r)}(\varepsilon), \mathbf{v}^{(r)}(\varepsilon), \mathbf{w}^{(r)}(\varepsilon)\}$ that depend on $\varepsilon$. We emphasize that the goal of this section is to showcase the flexibility of AlphaTensor, as opposed to discovering new results.

**Methodology.** To find the border rank of a tensor, we modify AlphaTensor and TensorGame as follows.

- **Ring.** We set the ring $\mathbb{E}$ to be the set of Laurent polynomials in $\varepsilon$. Thus, the factor entries are Laurent polynomials in the variable $\varepsilon$, which we constrain to be in the set $\mathbb{F} = \{0, \pm 1, \pm \varepsilon, \pm \varepsilon^2, \pm \varepsilon^4, \pm \varepsilon^6\}$. Since these actions do not make any progress towards finding a border rank decomposition. Moreover, for simplicity we also disallow actions that lead to a rank-one tensor containing any Laurent polynomial with an exponent of degree strictly greater than $-3$.

- **Disallowed actions.** We disallow actions (factor triplets) $\mathbf{u}^{(r)}(\varepsilon) \otimes \mathbf{v}^{(r)}(\varepsilon) \otimes \mathbf{w}^{(r)}(\varepsilon)$ that lead to either the all-zero tensor or a rank-one tensor whose all entries are $O(\varepsilon)$, since these actions do not make any progress towards finding a border rank decomposition. Moreover, for simplicity we also disallow actions that lead to a rank-one tensor containing any Laurent polynomial with any degree strictly greater than $-3$.

- **Game end and reward.** We terminate the game at step $R$ if either all the terms of the residual tensor $\mathcal{T} - \sum_{r=1}^{R} \mathbf{u}^{(r)}(\varepsilon) \otimes \mathbf{v}^{(r)}(\varepsilon) \otimes \mathbf{w}^{(r)}(\varepsilon)$ are $O(\varepsilon)$, or $R = R_{\text{limit}}$. The reward is $-1$ for each step taken during the game, plus an additional negative reward equal to the number of terms of the residual tensor that are not $O(\varepsilon)$.

- **Network input.** To form the input of the network at step $t$, we concatenate 4 tensors obtained from $\mathcal{S}_t$. These 4 tensors contain integer-valued entries instead of Laurent polynomials. To form them, we retain the coefficients of $\mathcal{S}_t$ corresponding to $\varepsilon^d$ for some fixed value of $d$. By letting the exponent $d$ take values in $\{-3, -2, -1, 0\}$, we obtain the 4 tensors that we concatenate together (the coefficients corresponding to positive degrees $d$ do not have any relevance when $\varepsilon \to 0$, so we ignore them).

- **Action canonicalization.** We canonicalize the factor triplets similarly as the standard AlphaTensor. We define the canonical form of $\mathbf{u}(\varepsilon)$ as the factor $\mathbf{u}'(\varepsilon) = \lambda \mathbf{u}(\varepsilon)$ (with $\lambda \in \{1, -1\}$) such that the Laurent polynomial corresponding to the first nonzero entry of $\mathbf{u}'(\varepsilon)$ has positive coefficient for the term with smallest degree (e.g., the first non-zero entry is $\varepsilon^{-1} - 1$ instead of $-\varepsilon^{-1} + 1$). Moreover, whenever all the entries of a factor triplet $(\mathbf{u}(\varepsilon), \mathbf{v}(\varepsilon), \mathbf{w}(\varepsilon))$ are polynomials with non-negative exponents of $\varepsilon$, we canonicalize the triplet by setting $\varepsilon = 0$.

- **Synthetic demonstrations.** To generate synthetic demonstrations, we follow the same approach as when optimizing the standard rank: we sample random factors $(\mathbf{u}(\varepsilon), \mathbf{v}(\varepsilon), \mathbf{w}(\varepsilon))$, and then create the tensor $\mathcal{T}(\varepsilon) = \sum_{r=1}^{R} \mathbf{u}^{(r)}(\varepsilon) \otimes \mathbf{v}^{(r)}(\varepsilon) \otimes \mathbf{w}^{(r)}(\varepsilon)$, so that the demonstration is $\{(\mathbf{u}^{(r)}(\varepsilon), \mathbf{v}^{(r)}(\varepsilon), \mathbf{w}^{(r)}(\varepsilon))\}^{R}_{r=1}$, $\mathcal{T}(\varepsilon))$. To generate each factor, we independently sample its entries. For each entry, we generate a Laurent polynomial with either one or two non-zero coefficients, with probabilities 0.7 and 0.3, respectively (each coefficient can be either $1$ or $-1$ with equal probability). We assign each coefficient to a term $\varepsilon^d$ of the polynomial, where $d$ itself is a random variable with $p(d = 0) = 0.7$, $p(d = 1) = p(d = -1) = 0.1$, and $p(d = 2) = p(d = -2) = 0.05$. We reject those factor triplets that do not conform valid actions.

- **Change of basis generation.** We generate change of basis similarly as the standard AlphaTensor. The change of basis matrices contain entries in $\{-1, 0, 1\}$, with probabilities $p_{\text{cob-entry}}(0) = 0.96$ and $p_{\text{cob-entry}}(-1) = p_{\text{cob-entry}}(1) = 0.02$.

- **Hyper-parameters.** We set $R_{\text{limit}} = 12$. The policy head uses $N_{\text{steps}} = 2$ and $N_{\text{logs}} = |F|^2 = 2,601$. We set $N_{\text{cob}} = 10,000$.

**Results.** We apply AlphaTensor to find the border rank decomposition of three tensors: the $2 \times 2 \times 2$ tensor $\mathcal{T}^{\text{small}}_1$ that is zero everywhere except for $\mathcal{T}^{\text{small}}_1[1, 1, 2] = \mathcal{T}^{\text{small}}_1[1, 2, 1] = \mathcal{T}^{\text{small}}_1[2, 1, 1] = 1$ (see [19]); the $4 \times 4 \times 4$ tensor $\mathcal{T}^{\text{lim}}$ obtained from $\mathcal{T}_2$ after replacing one unit entry with zero, as in [20]; and the $4 \times 6 \times 6$ matrix multiplication tensor $\mathcal{T}^{\text{2, 2, 3}}$. The rank of $\mathcal{T}^{\text{small}}_1$, $\mathcal{T}^{\text{lim}}$, and $\mathcal{T}^{\text{2, 2, 3}}$ is 3, 6, and 11, respectively, while the border rank is 2, 5, and 10 [19, 21, 22]. AlphaTensor successfully finds $R = 2$, $R = 5$, and $R = 10$ for each target respectively; in fact for each one it discovers multiple decompositions with $R$ factors. This demonstrates AlphaTensor’s ability to operate in a complex environment with more general game state and actions.
For completeness, we show next one of the border-rank-5 decompositions of $T_{\text{Bini}}$ found by AlphaTensor:

$$U(\varepsilon) = \begin{pmatrix} 1 & 1 & 1 & 1 & 1 - \varepsilon \\ 0 & 1 & 0 & 0 & -\varepsilon \\ 0 & 0 & -\varepsilon^2 & 0 & 0 \\ 0 & 0 & 0 & \varepsilon^2 & \varepsilon^2 \end{pmatrix}, \quad V(\varepsilon) = \begin{pmatrix} 1 & 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 0 & \varepsilon \\ -1 & \varepsilon^{-1} & 0 & \varepsilon^{-2} & \varepsilon^{-1} \\ 0 & 1 & 0 & 0 & -\varepsilon \end{pmatrix},$$

$$W(\varepsilon) = \begin{pmatrix} 1 - \varepsilon^{-2} & \varepsilon & 0 & 0 \\ 0 & -\varepsilon^{-2} & 1 & 0 \\ 1 & 0 & \varepsilon^{-1} & 0 & \varepsilon^{-1} \\ 0 & 0 & -1 & 0 & \varepsilon^{-1} \end{pmatrix},$$

where the factors are stacked vertically, e.g., $U(\varepsilon) = [u^{(1)}(\varepsilon), \ldots, u^{(R)}(\varepsilon)]$.

Similarly, we show next a border-rank-10 decomposition of $T_{2,2,3}$ found by AlphaTensor:

$$U(\varepsilon) = \begin{pmatrix} 1 & \varepsilon^{-2} + 1 & 0 & 1 & \varepsilon^{-2} + 1 & 0 & 0 & \varepsilon & 0 & \varepsilon^{-1} + 1 \\ 0 & 0 & 0 & -1 & 0 & 0 & \varepsilon & 0 & 1 & -1 \\ 0 & 1 & 1 & 0 & 0 & 1 & \varepsilon^{-1} & -1 & \varepsilon^{-2} + \varepsilon^{-1} & 0 \\ 0 & 0 & 0 & 0 & 1 & 1 & 0 & 0 & \varepsilon^{-1} & \varepsilon \end{pmatrix},$$

$$V(\varepsilon) = \begin{pmatrix} 1 & 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & -\varepsilon^{-2} & 1 & 0 & \varepsilon^2 & 0 & 0 & 1 & 0 & \varepsilon \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & \varepsilon^2 & \varepsilon & \varepsilon \\ 1 & 0 & 0 & \varepsilon^{-1} & 1 & 0 & 0 & 0 & \varepsilon^{-1} & \varepsilon^{-1} \\ 0 & 0 & -1 & 0 & 0 & \varepsilon^{-1} & \varepsilon^{-1} & 0 & 1 & 0 \\ 0 & 0 & 0 & 1 & 0 & -1 & 0 & 0 & -\varepsilon^{-2} & \varepsilon \end{pmatrix},$$

$$W(\varepsilon) = \begin{pmatrix} 1 & 0 & 0 & -\varepsilon & 0 & 0 & 0 & 0 & 0 & 0 \\ -\varepsilon^{-2} - 1 & 1 & 0 & 0 & 1 + \varepsilon^2 & 0 & 0 & 0 & \varepsilon^{-2} & 0 \\ 0 & \varepsilon^{-2} & 0 & 0 & 0 & 1 & \varepsilon^{-1} & 0 & 0 & 0 \\ 0 & 0 & 1 & 0 & 0 & \varepsilon & 0 & 0 & 0 & 0 \\ 1 & -\varepsilon^2 & 0 & -1 + \varepsilon^2 & -1 & 0 & 0 & 0 & 0 & 1 \\ 0 & 0 & 0 & 0 & \varepsilon^2 & -1 & -1 & 0 & 1 & -\varepsilon^2 \end{pmatrix}.$$

### F Ablations

| Ablation                        | Rank found |
|---------------------------------|------------|
| Without synthetic demonstrations| 64         |
| Without selfplay (supervised only) | 60        |
| Without change of basis         | 58         |
| Without signed permutations     | 53         |
| Without retraining on best games 10% of the time | 52         |
| Without QR head (using a categorical head) | 52         |
| AlphaTensor (no ablation)       | 49         |

**Table 1:** Results of repeating the experiment that discovered rank-49 factorizations of the general matrix multiplication tensor $\mathcal{T}_4$ (over standard arithmetic), each time disabling one of the components of AlphaTensor. In the experiment without selfplay we trained AlphaTensor on synthetic demonstrations only, and used the resulting neural network in MCTS seeking to decompose the target tensor $\mathcal{T}_4$.

Apart from the components ablated in Table 1, another important feature of AlphaTensor is the ability to train a single agent for decomposing multiple target tensors. When working over modular arithmetic $\mathbb{Z}_2$, an agent trained on all three matrix multiplication tensors $\mathcal{T}_{n,m,p}$ with $n, m, p \leq 5$ discovers a rank-47 factorization of $\mathcal{T}_4$, whereas an agent trained solely on $\mathcal{T}_4$ only discovers rank-49 factorizations. We performed an analysis on one of the experiments that discovered a rank-47 factorization of $\mathcal{T}_4$ and found that the agent is able to transfer knowledge between the three target tensors $\mathcal{T}_{3,3,4}, \mathcal{T}_{3,4,4},$ and $\mathcal{T}_4$. Specifically, a new agent trained from scratch but initialized with the discovered solutions to
both $\mathcal{F}_{3,3,4}$ and $\mathcal{F}_{3,4,4}$ in its demonstrations buffer is very quickly able to rediscover rank-47 factorizations of $\mathcal{F}_4$. This is not the case when the agent is initialized with solutions to $\mathcal{F}_{3,3,4}$ only, or with solutions to $\mathcal{F}_{3,4,4}$ only.

## G  Hyperparameters

| Hyper-parameter                             | Value                       |
|---------------------------------------------|-----------------------------|
| Move limit $R_{\text{limit}}$               | 125                         |
| Factor entries $F$ when seeking algorithms over arbitrary rings | $\{-2, -1, 0, 1, 2\}$        |
| Factor entries $F$ when seeking algorithms over $\mathbb{Z}_2$ | $\{0, 1\}$                |
| Number of simulations (before 50k steps)    | 200                         |
| Number of simulations (after 50k steps)     | 800                         |
| Initial value of $c(s)$ ($c_1$ in [23])     | 1.25                        |
| Scaling of $c(s)$ with parent visit count ($c_2$ in [23]) | 19,652                    |
| Maximum number of times a state is trained on | 1                          |
| Replay buffer size (selfplay games)         | 100,000                     |
| Synthetic demonstrations                     | 5,000,000                   |
| Best games replay buffer size               | 1,000                       |
| Training split (selfplay / demonstrations / best games) before 10k steps | 10% / 90% / 0%   |
| Training split (selfplay / demonstrations / best games) after 10k steps | 70% / 25% / 5%             |
| Batch size                                  | 2048                        |
| Gradient clipping by global norm            | 4.0                         |
| Optimizer                                   | AdamW [24]                  |
| Weight decay                                | $10^{-5}$                   |
| Initial learning rate                       | $10^{-4}$                   |
| Learning rate decay factor                  | 0.1                         |
| Learning rate decay steps                   | 500,000                     |

### Table 2: Common training hyper-parameters.

The hyper-parameters for specific sub-components are listed where those components are described: the configuration of the network architecture in Appendix A.1, the synthetic demonstration generation procedure in Appendix A.2, and the distribution and sampling of basis changes in Appendix A.3. Table 2 lists the remaining general hyper-parameters.

## H  Combining smaller factorizations into bigger ones

In this section, we describe and extend the divide-and-conquer technique [25, 26] that allows one to combine factorizations of smaller matrix multiplication tensors into factorizations of larger matrix multiplication tensors. For example, this technique allows us to obtain a rank-287 factorization of $\mathcal{F}_{5,8,10}$ (which improves over the previously known rank-291 [25]) by combining a known rank-33 factorization of $\mathcal{F}_{2,4,5}$ and a rank-47 factorization of $\mathcal{F}_{3,4,5}$ found by AlphaTensor. We refer to the supplementary data for all such decompositions.
H.1 Illustrative example

Let us describe the divide-and-conquer approach using $\mathcal{F}_9$ as an illustrative example. Specifically, let us find an algorithm for multiplying two $9 \times 9$ matrices $A$ and $B$,

$$
\begin{pmatrix}
(a_{11} & a_{12} & a_{13} & a_{14} & a_{15} & a_{16} & a_{17} & a_{18} & a_{19} \\
(a_{21} & a_{22} & a_{23} & a_{24} & a_{25} & a_{26} & a_{27} & a_{28} & a_{29} \\
(a_{31} & a_{32} & a_{33} & a_{34} & a_{35} & a_{36} & a_{37} & a_{38} & a_{39} \\
(a_{41} & a_{42} & a_{43} & a_{44} & a_{45} & a_{46} & a_{47} & a_{48} & a_{49} \\
(a_{51} & a_{52} & a_{53} & a_{54} & a_{55} & a_{56} & a_{57} & a_{58} & a_{59} \\
(a_{61} & a_{62} & a_{63} & a_{64} & a_{65} & a_{66} & a_{67} & a_{68} & a_{69} \\
(a_{71} & a_{72} & a_{73} & a_{74} & a_{75} & a_{76} & a_{77} & a_{78} & a_{79} \\
(a_{81} & a_{82} & a_{83} & a_{84} & a_{85} & a_{86} & a_{87} & a_{88} & a_{89} \\
(a_{91} & a_{92} & a_{93} & a_{94} & a_{95} & a_{96} & a_{97} & a_{98} & a_{99})
\end{pmatrix}
\begin{pmatrix}
(b_{11} & b_{12} & b_{13} & b_{14} & b_{15} & b_{16} & b_{17} & b_{18} & b_{19} \\
(b_{21} & b_{22} & b_{23} & b_{24} & b_{25} & b_{26} & b_{27} & b_{28} & b_{29} \\
(b_{31} & b_{32} & b_{33} & b_{34} & b_{35} & b_{36} & b_{37} & b_{38} & b_{39} \\
(b_{41} & b_{42} & b_{43} & b_{44} & b_{45} & b_{46} & b_{47} & b_{48} & b_{49} \\
(b_{51} & b_{52} & b_{53} & b_{54} & b_{55} & b_{56} & b_{57} & b_{58} & b_{59} \\
(b_{61} & b_{62} & b_{63} & b_{64} & b_{65} & b_{66} & b_{67} & b_{68} & b_{69} \\
(b_{71} & b_{72} & b_{73} & b_{74} & b_{75} & b_{76} & b_{77} & b_{78} & b_{79} \\
(b_{81} & b_{82} & b_{83} & b_{84} & b_{85} & b_{86} & b_{87} & b_{88} & b_{89} \\
(b_{91} & b_{92} & b_{93} & b_{94} & b_{95} & b_{96} & b_{97} & b_{98} & b_{99})
\end{pmatrix}
$$

To apply the divide-and-conquer approach, we need a high-level matrix multiplication algorithm, i.e., a factorization of some matrix multiplication tensor. The choice of the tensor and its factorization affects the resulting number of factors of the decomposition of $\mathcal{F}_9$. Hence, in practice, it is beneficial to consider all available choices (i.e., all available sizes and algorithms), and pick the one leading to the best result. To make the exposition concrete, let us consider $\mathcal{F}_{2,3,3}$. (This is a generalization of the scheme described in [25], which only considered factorizations of $\mathcal{F}_2$ as the high-level matrix multiplication algorithm.) Consider the following high-level algorithm representing a rank-15 decomposition of $\mathcal{F}_{2,3,3}$ (this particular decomposition is discovered by AlphaTensor):

\[
\begin{align*}
    h_1 &= A_{23}(B_{21} - B_{23} - B_{31}) \\
    h_2 &= (A_{22} + A_{23})(B_{21} - B_{23}) \\
    h_3 &= (A_{12} - A_{23})(B_{21} - B_{23} - B_{32}) \\
    h_4 &= (A_{12} + A_{22})(B_{12} + B_{21} - B_{22} - B_{23}) \\
    h_5 &= A_{12}(B_{22} + B_{23} - B_{32}) \\
    h_6 &= (A_{12} + A_{13})B_{32} \\
    h_7 &= (A_{12} + A_{21} + A_{22})(B_{12} + B_{23}) \\
    h_8 &= (A_{21} + A_{22})B_{23} \\
    h_9 &= (A_{11} + A_{12} + A_{21} + A_{22})B_{12} \\
    h_{10} &= (A_{11} + A_{21})(B_{11} - B_{13} + B_{31} - B_{32} - B_{33}) \\
    h_{11} &= (A_{11} - A_{13} - A_{23})(B_{11} - B_{13} - B_{33}) \\
    h_{12} &= (A_{11} - A_{13} + A_{21} - A_{23})(B_{11} - B_{13}) \\
    h_{13} &= (A_{11} - A_{13})B_{33} \\
    h_{14} &= A_{21}(B_{11} - B_{23}) \\
    h_{15} &= A_{11}(B_{12} + B_{13} + B_{33}) \\
    C_{11} &= h_{10} + h_{11} + h_{15} + h_{1} + h_{3} + h_{5} + h_{7} - h_{8} - h_{9} \\
    C_{21} &= h_{14} - h_{1} + h_{3} + h_{8} \\
    C_{12} &= h_{5} + h_{6} - h_{7} + h_{8} + h_{9} \\
    C_{22} &= h_{2} + h_{3} - h_{4} - h_{5} + h_{7} - h_{8} \\
    C_{13} &= -h_{13} + h_{15} + h_{7} - h_{8} - h_{9} \\
    C_{23} &= h_{11} - h_{12} + h_{13} + h_{14} + h_{8}
\end{align*}
\]

We can apply the high-level algorithm for multiplying block matrices,

\[
\begin{pmatrix}
C_{11} & C_{12} & C_{13} \\
C_{21} & C_{22} & C_{23}
\end{pmatrix} =
\begin{pmatrix}
A_{11} & A_{12} & A_{13} \\
A_{21} & A_{22} & A_{23}
\end{pmatrix}
\begin{pmatrix}
B_{11} & B_{12} & B_{13} \\
B_{21} & B_{22} & B_{23} \\
B_{31} & B_{32} & B_{33}
\end{pmatrix}
\]
Let us fill the block matrices with the elements of the original matrices \( A \) and \( B \) (by padding with zeroes to match the sizes\(^8\)). For example, one can use the following scheme:

\[
\begin{pmatrix}
  c_{11} & c_{12} & c_{13} \\
  c_{21} & c_{22} & c_{23} \\
  c_{31} & c_{32} & c_{33} \\
  0 & 0 & 0 \\
  0 & 0 & 0 \\
  0 & 0 & 0 \\
\end{pmatrix}
= \begin{pmatrix}
  a_{11} & a_{12} & a_{13} \\
  a_{21} & a_{22} & a_{23} \\
  a_{31} & a_{32} & a_{33} \\
  0 & 0 & 0 \\
  0 & 0 & 0 \\
  0 & 0 & 0 \\
\end{pmatrix}
\begin{pmatrix}
  b_{11} & b_{12} & b_{13} \\
  b_{21} & b_{22} & b_{23} \\
  b_{31} & b_{32} & b_{33} \\
  0 & 0 & 0 \\
  0 & 0 & 0 \\
  0 & 0 & 0 \\
\end{pmatrix}
\]

There are multiple ways in which the elements of \( A, B, \) and \( C \) can be placed into the block matrices above, depending on the specific structure of the zero padding. Again, this choice affects the resulting number of factors in the decomposition of \( T_9 \), as described below. In practice, we try all combinations and pick the best one.

We now analyze the steps of the high-level algorithm when applied to the blocks of the matrices in Eq. 3. Step 1, \( h_1 = A_{23} (B_{21} - B_{31}) \), involves multiplying a \( 6 \times 3 \) matrix by a \( 3 \times 3 \) matrix, and thus can be done by using the algorithm \( \mathcal{T}_{6,3,3} \) (which involves 40 multiplications). Steps 2, 3, and 4 are similar. However, step 5 is different: it involves \( A_{12} \), which is of size \( 6 \times 3 \); however half of its rows are zero, so we can use \( \mathcal{T}_3 \) (for which we have a rank-23 decomposition) for the computations in this step. We can also see that the sparsity pattern of the block matrix \( C \) also affects the resulting number of terms in the decomposition. For example, step 10 of the high-level algorithm only involves dense matrix multiplications and thus one might think that it can only be done via \( \mathcal{T}_{6,3,3} \). However, \( l_{110} \) is only used later for computing \( C_{111} \), whose last three rows are zero, so there is no need to compute the last three rows of \( h_{10} \), and thus it can be done via \( \mathcal{T}_3 \).

Exploiting the sparsity patterns in all 15 steps, we obtain that the final rank of the decomposition is 498 (this decomposition involves 6 factorizations of \( \mathcal{T}_3 \), and 9 factorizations of \( \mathcal{T}_{6,3,3} \)). We refer to the accompanying supplementary data for all the decompositions.

### H.2 General approach

For each target \( \mathcal{T}_{n,m,p} \) with \( n, m, p \in \{3, \ldots, 12\} \), we try all the factorizations discovered by AlphaTensor as high-level factorization, and all non-equivalent ways of placing the original matrix elements into the blocks of the high-level matrices (i.e., the sparsity patterns). Since the results depend on the particular high-level factorization, we are able to leverage the diversity of the decompositions found by AlphaTensor.

This generalized divide-and-conquer approach subsumes adding and multiplying factorizations. Indeed, by using the trivial decomposition of \( \mathcal{T}_{1,1,2} \) as the high-level algorithm, one can sum factorizations of \( \mathcal{T}_{n,m,p} \) and \( \mathcal{T}_{n,m,p'} \) into...\(^8\)To apply the high-level matrix multiplication algorithm to the block matrix, we need to pad every block in a matrix to have the same shape so that we can add blocks as specified by the high-level algorithm. Note that this padding is only done as an intermediate step for constructing the factorization; the resulting factorization of \( \mathcal{T}_{9,9,9} \) (and thus the algorithm for multiplying \( 9 \times 9 \) matrices) does not involve padding.
a factorization of $J_{n,m,p+p'}$. Similarly, by dividing the original matrices $A$, $B$ and $C$ into blocks without introducing any zero padding, one can replicate multiplication of factorizations. For all the results, we refer to Extended Data Table 1.

I Proofs

In this section, we prove the results in Theorem B.2 and Theorem B.3, which are needed to prove Theorem B.1. We also provide a proof for Theorem C.4.

I.1 Proof of Theorem B.2

Proof. By transitivity, applying $\Phi$ to two equivalent factorizations preserves their equivalence. Therefore, the two factorizations $\{(U_r, V_r, W_r)\}_{r=1}^{R}$ and $\{(U'_r, V'_r, W'_r)\}_{r=1}^{R}$ with $U_r = U, V_1 W_1, V_r = W_1^{-1} V_1^{-1} V_r W_1, W_r = W_1^{-1} W_r$, and $U'_r = U'_1 V'_1 W'_1, V'_r = W'_1^{-1} V'_1^{-1} V'_r W'_1, W'_r = W'_1^{-1} W'_r$ are also equivalent. Hence, there exist $(A, B, C, \pi)$ such that

$$
AT_r(\tilde{U}_r)B^{-1} \otimes_K BT_\pi(\tilde{V}_r)C^{-1} \otimes_K CT_\pi(\tilde{W}_r)A^{-1} = (A \otimes_K B \otimes_K C)(T_\pi(\tilde{U}_r) \otimes_K T_\pi(\tilde{V}_r) \otimes_K T_\pi(\tilde{W}_r))(B^{-1} \otimes_K C^{-1} \otimes_K A^{-1})
$$

where $T_\pi$ is the identity function when $\pi$ has signature 1, and the transposition function otherwise. By construction $(U_1, V_1, W_1) = (I, I, I)$, so $T_\pi(U_1) = T_\pi(V_1) = T_\pi(W_1) = I$, and for $r = 1$ Eq. 4 reduces to

$$
AB^{-1} \otimes_K BC^{-1} \otimes_K CA^{-1} = I \otimes_K I \otimes_K I.
$$

Inverting this equality, we obtain

$$
BA^{-1} \otimes_K CB^{-1} \otimes_K AC^{-1} = I \otimes_K I \otimes_K I.
$$

Multiplying Eqs. 4 and 5, we obtain:

$$
AT_\pi(\tilde{U}_r)A^{-1} \otimes_K BT_\pi(\tilde{V}_r)B^{-1} \otimes_K CT_\pi(\tilde{W}_r)C^{-1} = (A \otimes_K B \otimes_K C)(T_\pi(\tilde{U}_r) \otimes_K T_\pi(\tilde{V}_r) \otimes_K T_\pi(\tilde{W}_r))(A \otimes_K B \otimes_K C)^{-1}
$$

Using the invariance of the CharPoly function under similarities, we obtain

$$
\text{CharPoly}(T_\pi(\tilde{U}_r) \otimes_K T_\pi(\tilde{V}_r) \otimes_K T_\pi(\tilde{W}_r)) = \text{CharPoly}(\tilde{U}_r' \otimes_K \tilde{V}_r' \otimes_K \tilde{W}_r').
$$

The eigenvalues and therefore also the characteristic polynomial of a matrix are also invariant under transposition, so $\text{CharPoly}(T_\pi(\tilde{U}_r) \otimes_K T_\pi(\tilde{V}_r) \otimes_K T_\pi(\tilde{W}_r)) = \text{CharPoly}(\tilde{U}_r \otimes_K \tilde{V}_r \otimes_K \tilde{W}_r)$ and we conclude that

$$
\text{CharPoly}(\tilde{U}_r' \otimes_K \tilde{V}_r' \otimes_K \tilde{W}_r') = \text{CharPoly}(\tilde{U}_r' \otimes_K \tilde{V}_r' \otimes_K \tilde{W}_r').
$$

I.2 Proof of Theorem B.3

Before we prove Theorem B.3, we need the following two lemmas:

Lemma I.1. If $\{(X_p, Y_p, Z_p)\}_{p=1}^{R}$ and $\{(F^1_p, F^2_p, F^3_p)\}_{p=1}^{R}$, then

$$
\{(X_p \otimes_K X'_q, Y_p \otimes_K Y'_q, Z_p \otimes_K Z'_q)\}_{p,q=1}^{R} \sim \{(F^1_p \otimes_K F^1_q, F^2_p \otimes_K F^2_q, F^3_p \otimes_K F^3_q)\}_{p,q=1}^{R}
$$

A $\otimes A'$, $B \otimes B'$, $C \otimes C'$, $id$

$$
\left\{\begin{array}{l}
T_\pi(F^{(1)}_p) \otimes_K T_\pi(F^{(1)}_q), T_\pi(F^{(2)}_p) \otimes_K T_\pi(F^{(2)}_q), T_\pi(F^{(3)}_p) \otimes_K T_\pi(F^{(3)}_q)
\end{array}\right\}_{p,q=1}^{R}.
$$

Lemma I.2. If $\{(X_p, Y_p, Z_p)\}_{p=1}^{R}$ and $\{(F^1_p, F^2_p, F^3_p)\}_{p=1}^{R}$, then

$$
\{(X_p \otimes_K X'_q, Y_p \otimes_K Y'_q, Z_p \otimes_K Z'_q)\}_{p,q=1}^{R} \sim \{(F^1_p \otimes_K F^1_q, F^2_p \otimes_K F^2_q, F^3_p \otimes_K F^3_q)\}_{p,q=1}^{R}
$$

A $\otimes A'$, $B \otimes B'$, $C \otimes C'$, $id$

$$
\left\{\begin{array}{l}
T_\pi(F^{(1)}_p) \otimes_K T_\pi(F^{(1)}_q), T_\pi(F^{(2)}_p) \otimes_K T_\pi(F^{(2)}_q), T_\pi(F^{(3)}_p) \otimes_K T_\pi(F^{(3)}_q)
\end{array}\right\}_{p,q=1}^{R}.
$$

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Proof. By assumption we have
\[ X_p \otimes Y_p \otimes Z_p = (AT_\pi(F_p^{(1)})B^{-1}) \otimes (BT_\pi(F_p^{(2)})C^{-1}) \otimes (CT_\pi(F_p^{(3)})A^{-1}), \]
\[ X'_q \otimes Y'_q \otimes Z'_q = (A'T_\pi(F_q^{(1)})B^{-1}) \otimes (B'T_\pi(F_q^{(2)})C^{-1}) \otimes (C'T_\pi(F_q^{(3)})A^{-1}). \]

Multiplying these two identities, we obtain
\[
(X_p \otimes_K X'_q) \otimes (Y_p \otimes_K Y'_q) \otimes (Z_p \otimes_K Z'_q) = (X_p \otimes Y_p \otimes Z_p) \otimes_K (X'_q \otimes Y'_q \otimes Z'_q)
\]
\[
= \left( (AT_\pi(F_p^{(1)})B^{-1}) \otimes_K (A'T_\pi(F_q^{(1)})B^{-1}) \right)
\]
\[
\otimes \left( (BT_\pi(F_p^{(2)})C^{-1}) \otimes_K (B'T_\pi(F_q^{(2)})C^{-1}) \right)
\]
\[
\otimes \left( (CT_\pi(F_p^{(3)})A^{-1}) \otimes_K (C'T_\pi(F_q^{(3)})A^{-1}) \right)
\]
\[
= \left( [A \otimes_K A'] \left[ T_\pi(F_p^{(1)}) \otimes_K T_\pi(F_q^{(1)}) \right] [B \otimes_K B']^{-1} \right)
\]
\[
\otimes \left( [B \otimes_K B'] \left[ T_\pi(F_p^{(2)}) \otimes_K T_\pi(F_q^{(2)}) \right] [C \otimes_K C']^{-1} \right)
\]
\[
\otimes \left( [C \otimes_K C'] \left[ T_\pi(F_p^{(3)}) \otimes_K T_\pi(F_q^{(3)}) \right] [A \otimes_K A']^{-1} \right),
\]
where we used the identities \((AC) \otimes_K (BD) = (A \otimes_K B)(C \otimes_K D)\) and \((B \otimes_K B')^{-1} = (B^{-1} \otimes_K B'^{-1}).\]

Lemma 1.2. Let \(\mathcal{T}\) be a tensor with nonnegative entries and of rank \(R > 0\), and let \((X_p \otimes_K X'_q, Y_p \otimes_K Y'_q, Z_p \otimes_K Z'_q)\) be a factorization of \(\mathcal{T}^{\otimes 2}\). Then, there exist \(\alpha, \beta\) with \(\alpha \beta = 1\) such that both \((\alpha X_p, \alpha Y_p, \alpha Z_p)\) and \((\beta X'_q, \beta Y'_q, \beta Z'_q)\) are valid factorizations of \(\mathcal{T}\).

Proof. Since \((X_p \otimes_K X'_q, Y_p \otimes_K Y'_q, Z_p \otimes_K Z'_q)\) is a factorization of \(\mathcal{T}^{\otimes 2}\), we have:
\[
\mathcal{T}^{\otimes 2}[i_1 + n'_2, i_2 + n'_2, j_1 + n'_2, j_2 + n'_2, k_1 + n'_2, k_2 + n'_2]
\]
\[
= \sum_{p=1}^{R} \sum_{q=1}^{R} (X_p \otimes_K X'_q)[i_1 + n'_2, i_2 + n'_2][Y_p \otimes_K Y'_q][j_1 + n'_2, j_2 + n'_2][Z_p \otimes_K Z'_q][k_1 + n'_2, k_2 + n'_2]
\]
\[
= \left( \sum_{p=1}^{R} X_p[i_1, i_2]Y_p[j_1, j_2]Z_p[k_1, k_2] \right) \left( \sum_{q=1}^{R} X'_q[i'_1, i'_2]Y'_q[j'_1, j'_2]Z'_q[k'_1, k'_2] \right)
\]
Hence, we obtain
\[
\sum_{i'_1, i'_2, j'_1, j'_2, k'_1, k'_2} \mathcal{T}^{\otimes 2}[i_1 + n'_2, i_2 + n'_2, j_1 + n'_2, j_2 + n'_2, k_1 + n'_2, k_2 + n'_2]
\]
\[
= \mathcal{T}[i_1, i_2, j_1, j_2, k_1, k_2] \sum_{i'_1, i'_2, j'_1, j'_2, k'_1, k'_2} \mathcal{T}[i'_1, i'_2, j'_1, j'_2, k'_1, k'_2]
\]
\[
= \left( \sum_{p=1}^{R} X_p[i_1, i_2]Y_p[j_1, j_2]Z_p[k_1, k_2] \right) \left( \sum_{q=1}^{R} X'_q[i'_1, i'_2]Y'_q[j'_1, j'_2]Z'_q[k'_1, k'_2] \right),
\]
from which we deduce that
\[
\alpha^{-1} \mathcal{T}[i_1, i_2, j_1, j_2, k_1, k_2] = \sum_{p=1}^{R} X_p[i_1, i_2]Y_p[j_1, j_2]Z_p[k_1, k_2],
\]
where
\[
\alpha^{-1} = \frac{\sum_{i'_1, i'_2, j'_1, j'_2, k'_1, k'_2} \mathcal{T}[i'_1, i'_2, j'_1, j'_2, k'_1, k'_2]}{\sum_{i'_1, i'_2, j'_1, j'_2, k'_1, k'_2} \sum_{q=1}^{R} X'_q[i'_1, i'_2]Y'_q[j'_1, j'_2]Z'_q[k'_1, k'_2]}.
\]
Note that the denominator is nonzero, as otherwise the tensor $\mathcal{T}$ is equal to zero. Similarly, we have

$$\beta^{-1} \mathcal{T}[i', j', i_1', j_1', k_1', k_2'] = \sum_{q=1}^{R} X'_q[i_1', i_2'][Y'_q[j_1', j_2'][Z'_p[k_1', k_2'],
\beta^{-1} = \frac{\sum_{i_1,i_2,j_1,j_2,k_1,k_2} \mathcal{T}[i_1, i_2, j_1, j_2, k_1, k_2]}{\sum_{i_1,i_2,j_1,j_2,k_1,k_2} \sum_{p=1}^{R} X_p[i_1, i_2] Y_p[j_1, j_2] Z_p[k_1, k_2]}.
$$

Computing $\alpha^{-1} \beta^{-1}$, we obtain

$$\alpha^{-1} \beta^{-1} = \frac{\sum_{i_1,i_2,j_1,j_2,k_1,k_2} \sum_{q=1}^{R} X'_q[i_1', i_2'][Y'_q[j_1', j_2'][Z'_p[k_1', k_2']]}{\sum_{i_1,i_2,j_1,j_2,k_1,k_2} \sum_{p=1}^{R} X_p[i_1, i_2] Y_p[j_1, j_2] Z_p[k_1, k_2]} \cdot \frac{\sum_{i_1,i_2,j_1,j_2,k_1,k_2} \sum_{q=1}^{R} X'_q[i_1', i_2'][Y'_q[j_1', j_2'][Z'_p[k_1', k_2']]}{\sum_{i_1,i_2,j_1,j_2,k_1,k_2} \sum_{p=1}^{R} X_p[i_1, i_2] Y_p[j_1, j_2] Z_p[k_1, k_2]},
$$

which is equal to 1 by summing Eq. 8 over all $(i, j, k, i', j', k')$.

We now prove Theorem B.3.

**Proof.** We have $2 \implies 1$. We now prove that $1 \implies 2$. $(\{X_p \otimes_K X'_q, Y_p \otimes_K Y'_q, Z_p \otimes_K Z'_q\})_7^{p,q=1}$ is a factorization of $\mathcal{T}_4$ of rank 49. Using Lemma I.2, there exists $\alpha, \beta$ such that $(\{\alpha X_p, \alpha Y_p, \alpha Z_p\})_7^{p=1}$ and $(\{\beta X'_q, \beta Y'_q, \beta Z'_q\})_7^{q=1}$ are both valid factorizations of $\mathcal{T}_2$ of rank 7, where $\alpha \beta = 1$. We know that all rank 7 factorizations of $\mathcal{T}_2$ are equivalent [3]; hence, we have

$$\{(\alpha X_p, \alpha Y_p, \alpha Z_p)\}_7^{p=1} \sim \{(S^1_p, S^2_p, S^3_p)\}_7^{p=1},
\{(\beta X'_q, \beta Y'_q, \beta Z'_q)\}_7^{q=1} \sim \{(S^1_p, S^2_p, S^3_p)\}_7^{p=1},$$

where we recall that $(\{S^1_p, S^2_p, S^3_p\})_7^{p=1}$ is a Strassen factorization of $\mathcal{T}_2$. Using Lemma I.1, there exist permutations $\pi$ and $\pi'$ for which the following holds:

$$\{(X_p \otimes_K X'_q, Y_p \otimes_K Y'_q, Z_p \otimes_K Z'_q)\}_7^{p,q=1} \sim \left\{(T_{\pi}(S^1_p) \otimes_K T_{\pi'}(S^2_q), T_{\pi}(S^2_p) \otimes_K T_{\pi'}(S^3_q))\}_7^{p,q=1},
$$

where we have used the property that $\alpha \beta = 1$. By transitivity of the equivalence relation, we obtain that

$$\{(U_r, V_r, W_r)\}_7^{r=1} \sim \left\{(T_{\pi}(S^1_p) \otimes_K T_{\pi'}(S^2_q), T_{\pi}(S^2_p) \otimes_K T_{\pi'}(S^3_q))\}_7^{p,q=1},
$$

which concludes the proof. \qed

### I.3 Proof for skew-symmetric matrix-vector product

We now prove Theorem C.4. We start with a simple, but useful result:

**Lemma I.3.** Let $\Xi$ be any $n \times n$ matrix, $\Xi = [\xi_{r,c}]$. Then the following equality always holds

$$\sum_{r=1}^{n} \sum_{c=1}^{n} \xi_{r,c} = \sum_{c=1}^{n} \sum_{r=1}^{n} \xi_{r,c}.
$$

**Proof.** Both sides of the equation sum up the lower triangular part of the matrix $\{\Xi_{r,c} | 1 \leq c \leq r \leq n\}$. \qed

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1.3.1 Problem formulation and the results

Consider an \( n \times n \) antisymmetric matrix \( A = [a_{r}^{c}]_{r=1,...,n}^{c=1,...,n} \), \( a_{r}^{c} = -a_{c}^{r} \) for \( r = 1, \ldots, n \), \( c = 1, \ldots, n \). Let \( b \) be an \( n \)-dimensional vector \([b_{1}, \ldots, b_{n}]^{\top}\). We want to compute the following:

\[
y_{\bar{r}} := \sum_{c=1}^{n} b_{c} a_{\bar{r}}^{c} \quad \text{for } \bar{r} = 1, \ldots, n.
\]

What we aim to do in this proof is to re-express the previous expressions as a function of the following two kinds of terms:

\[
\omega_{\bar{r}} := a_{\bar{r}}^{\bar{c}} b_{\bar{c}} + a_{\bar{c}}^{\bar{r}} b_{\bar{r}} \quad \text{for } \bar{r} = 1, \ldots, n, \bar{c} = 1, \ldots, n,
\]

\[
\beta_{\bar{r}} := b_{\bar{r}} \sum_{r=1}^{n} a_{\bar{r}}^{r} \quad \text{for } \bar{r} = 1, \ldots, n,
\]

**Remark 1.** Each of the terms \( \omega_{\bar{r}} \) and \( \beta_{\bar{r}} \) requires precisely one multiplication to compute.

**Proof.** The term \( \beta_{\bar{r}} \) requires only one multiplication directly by the definition.

There holds \( \omega_{\bar{r}} := a_{\bar{r}}^{\bar{c}} b_{\bar{c}} + a_{\bar{c}}^{\bar{r}} b_{\bar{r}} = a_{\bar{r}}^{\bar{c}} (b_{\bar{c}} - b_{\bar{r}}) \) as \( a_{\bar{r}}^{\bar{c}} = -a_{\bar{c}}^{\bar{r}} \) for \( \bar{r} = 1, \ldots, n \), \( \bar{c} = 1, \ldots, n \), which completes the proof.

We start with a simple observation:

**Lemma I.4.** For all \( \bar{r}, \bar{c} \) there holds

\[
\omega_{\bar{r}} = \omega_{\bar{c}}, \quad \omega_{\bar{r}} = 0.
\]

This implies that there are only \( n(n-1)/2 \) different terms \( \omega_{\bar{r}} \), which together with the \( n \) terms in \( \beta_{\bar{r}} \) make \( n(n+1)/2 \) terms, each of those requiring only one multiplication.

**Lemma I.5.** There holds

\[
y_{\bar{r}} = \sum_{c=1}^{n} \omega_{\bar{c}}^{c} - \beta_{\bar{r}}
\]

**Proof.** By definition

\[
\sum_{c=1}^{n} \omega_{\bar{r}}^{c} = \sum_{c=1}^{n} a_{\bar{r}}^{c} b_{c} + a_{\bar{c}}^{\bar{r}} b_{\bar{r}} = \sum_{c=1}^{n} a_{\bar{r}}^{c} b_{c} + \sum_{c=1}^{n} a_{\bar{c}}^{\bar{r}} b_{\bar{r}} = y_{\bar{r}} + \beta_{\bar{r}}.
\]

The previous lemma allows us to prove that we need \( n(n+1)/2 \) scalar multiplications to multiply a skew-symmetric matrix and a vector. In the following we will go beyond this by leaving one of these terms out, in particular \( \omega_{n-1}^{n} \), leading to \( n(n+1)/2 - 1 = (n+2)(n-1)/2 \) required scalar multiplications.

**Corollary I.1.** There holds

\[
y_{\bar{r}} = \sum_{r=1}^{\bar{r}-1} \omega_{\bar{r}}^{r} + \sum_{c=\bar{r}+1}^{n} \omega_{\bar{r}}^{c} - \beta_{\bar{r}}
\]

Note that this corollary allows us to express \( y_{1}, \ldots, y_{n-2} \) in terms of \( \omega \) and \( \beta \) elements without using \( \omega_{n-1}^{n} \).

**Lemma I.6.** There holds

\[
\sum_{r=1}^{n} \sum_{c=1}^{n} \omega_{r}^{c} = \sum_{r=1}^{n} \sum_{c=r}^{n} \omega_{r}^{c} = \sum_{r=1}^{n} \beta_{r}.
\]
Proof. We use the identity $\omega^c_r = \omega^r_c$ to show the first equality

$$\sum_{r=1}^n \sum_{c=1}^r \omega^c_r = \sum_{r=1}^n \sum_{c=1}^r \omega^r_c = \sum_{c=1}^n \sum_{r=c}^n \omega^c_r = \sum_{r=1}^n \sum_{c=r}^n \omega^r_c,$$

by Lemma 1.3

Now it remains to prove that $\sum_{r=1}^n \sum_{c=1}^r \omega^c_r = \sum_{r=1}^n \beta_r$.

$$\sum_{r=1}^n \sum_{c=1}^r \omega^c_r = \sum_{r=1}^n \sum_{c=1}^r a^c_r b_c + a^r_r b_r = \sum_{r=1}^n \sum_{c=1}^r a^c_r b_c + \sum_{r=1}^n \sum_{c=1}^r a^r_r b_r = \sum_{c=1}^n \sum_{r=c}^n a^c_r b_c + \sum_{r=1}^n \sum_{c=r}^n a^r_r b_r.$$

But $a^r_r = 0$ for all $r = 1, \ldots, n$, due to antisymmetry, thus

$$\sum_{r=1}^n \sum_{c=r}^n \omega^c_r = \sum_{r=1}^n \sum_{c=1}^r a^c_r b_c = \sum_{c=1}^n b_c \sum_{r=1}^n a^r_r = \sum_{c=1}^n \beta_c.$$

\[ \square \]

Proposition 1. The following equalities hold

$$y_{\bar{r}} = - \sum_{r=1, r \neq \bar{r}}^{n} \sum_{c=r, c \neq \bar{r}}^{n} \omega^c_r + \sum_{r=1, r \neq \bar{r}}^{n} \beta_r, \quad (15)$$

$$y_{\bar{r}} = - \sum_{r=1, r \neq \bar{r}}^{n} \sum_{c=1, c \neq \bar{r}}^{n} \omega^c_r + \sum_{r=1, r \neq \bar{r}}^{n} \beta_r \quad (16)$$

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Using Proposition 1 (Eq. 15), we get

Proof. By Lemmas I.5 and I.6, there holds

\[ y_{\tilde{r}} = \sum_{c=1}^{n} \omega_{\tilde{r}}^c - \beta_{\tilde{r}} = \sum_{c=1}^{n} \omega_{\tilde{r}}^c - \beta_{\tilde{r}} - \sum_{r=1}^{n} \sum_{c=r}^{n} \omega_{\tilde{r}}^c + \sum_{r=1}^{n} \beta_{r} = \]

\[ = 0 \text{ by Lemma I.6} \]

\[ \sum_{c=1}^{n} \omega_{\tilde{r}}^c - \sum_{r=1}^{n} \sum_{c=r}^{n} \omega_{\tilde{r}}^c + \sum_{r=1}^{n} \beta_{r} = \]

\[ \sum_{r=1}^{\tilde{r}-1} \omega_{\tilde{r}}^r - \sum_{r=1}^{n} \sum_{c=r}^{n} \omega_{\tilde{r}}^c + \sum_{r=1}^{n} \beta_{r} = \]

\[ \sum_{r=1}^{\tilde{r}-1} \omega_{\tilde{r}}^r - \sum_{r=1}^{n} \sum_{c=r}^{n} \omega_{\tilde{r}}^c + \sum_{r=1}^{n} \beta_{r} = - \sum_{r=1}^{n} \sum_{c=1}^{\tilde{r}-1} \omega_{\tilde{r}}^c + \sum_{r=1}^{n} \beta_{r}. \]

Analogously

\[ y_{\tilde{r}} = \sum_{c=1}^{n} \omega_{\tilde{r}}^c - \beta_{\tilde{r}} = \sum_{c=1}^{n} \omega_{\tilde{r}}^c - \beta_{\tilde{r}} - \sum_{r=1}^{n} \sum_{c=1}^{r} \omega_{\tilde{r}}^c + \sum_{r=1}^{n} \beta_{r} = \]

\[ = 0 \text{ by Lemma I.6} \]

\[ \sum_{c=1}^{n} \omega_{\tilde{r}}^c - \sum_{r=1}^{n} \sum_{c=1}^{r} \omega_{\tilde{r}}^c + \sum_{r=1}^{n} \beta_{r} = \]

\[ \sum_{c=\tilde{r}+1}^{n} \omega_{\tilde{r}}^c - \sum_{r=1}^{\tilde{r}-1} \sum_{c=1}^{r} \omega_{\tilde{r}}^c + \sum_{r=1}^{n} \beta_{r} = - \sum_{r=1}^{n} \sum_{c=1}^{\tilde{r}+1} \omega_{\tilde{r}}^c + \sum_{r=1}^{n} \beta_{r}. \]

Corollary I.2. We have

\[ y_{n-1} = -\sum_{c=1}^{n-2} \omega_{n-1}^c - \sum_{c=1}^{n} \omega_{n-1}^c + \sum_{r=1}^{n} \beta_{r} \]

\[ y_{n} = -\sum_{r=1}^{n} \sum_{c=1}^{r} \omega_{n}^c + \sum_{r=1}^{n} \beta_{r} \]

Proof. Using Proposition 1 (Eq. 15), we get

\[ y_{n-1} = -\sum_{r=1}^{n} \sum_{c=1}^{n} \omega_{n-1}^c + \sum_{r=1}^{n} \beta_{r} = \]

\[ -\sum_{r=1}^{n} \sum_{c=1}^{n-1} \omega_{n-1}^c + \sum_{r=1}^{n} \beta_{r} - \sum_{r=1}^{n} \sum_{c=1}^{n-1} \omega_{n-1}^c + \sum_{r=1}^{n} \beta_{r}. \]

The second equality is a straightforward application of Proposition 1 (Eq. 16) with \( \tilde{r} = n \).

Theorem I.5. The operation of a multiplication of a \( n \times n \) antisymmetric matrix and a vector can be expressed using \( \frac{n(n+1)}{2} - 1 \) multiplications.

Proof. Recall (Remark 1), that each of the terms \( \omega_{\tilde{r}}^c \) and \( \beta_{\tilde{r}} \) can be computed using one multiplication. Thanks to Lemma I.4 we know that there are only \( \frac{n(n-1)}{2} \) distinct terms \( \omega_{\tilde{r}}^c \). In addition, the formulas require \( n \) values of \( \beta_{\tilde{r}} \), each of them requiring 1 multiplication.

Note that thanks to Corollary I.1 we can compute \( y_1 \ldots y_{n-2} \) using \( \frac{n(n-1)}{2} - 1 \) (instead of \( \frac{n(n-1)}{2} \)) terms \( \omega_{\tilde{r}}^c \), in particular not incorporating \( \omega_{n-1}^c \).

Finally, observe that the formulas in Corollary I.2 do not incorporate the term \( \omega_{n-1}^c \) either, which ends the proof.
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