Design and performance of the multi-PMT optical module for IceCube Upgrade

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The IceCube Upgrade is the first step towards the next-generation neutrino observatory at the South Pole, IceCube-Gen2, and will be installed in the central region of the existing array. The Upgrade will consist of 693 newly developed, densely spaced optical sensors and 50 standalone calibration devices, which will enhance IceCube’s capabilities both at low and high neutrino energies. Of the new sensors, 402 will be multi-PMT Digital Optical Modules (mDOMs). Consisting of 24 small photomultipliers arranged inside a pressure vessel, the mDOM features a large sensitive area distributed nearly homogeneously over the full solid angle. The use of multiple, individually read-out PMTs allows directional information to be obtained for the registered photons and enables the use of multiplicity triggering within a single module, e.g., for background suppression. The challenges driving the mDOM development included tight restrictions on module size, data-transfer rate, and power consumption as well as the harsh environment in the deep ice at the South Pole. In this contribution we present the final mDOM design that meets these challenges.

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1. Introduction

Located deep in the ancient glacial ice of Antarctica, IceCube [1] is the neutrino telescope with the largest instrumented volume worldwide. IceCube’s initial energy range, optimized for the investigation of the neutrino sky at the TeV to PeV energy scale and beyond, was extended down to $\sim 10$ GeV by DeepCore [2], enabling world-class measurements of neutrino oscillation parameters [3, 4]. The IceCube Upgrade [5] will further enhance IceCube’s capabilities through the installation of 693 new optical modules distributed along seven vertical strings, mainly located in the DeepCore region. The Upgrade will reduce IceCube’s energy threshold to a few GeV which will significantly enhance the precision of oscillation measurements. It will also provide a platform for improved calibration of the existing detector. The enhanced understanding of the optical properties of the deep ice will reduce the main systematic errors that contribute to the directional uncertainty of astrophysical neutrinos allowing us to re-analyze more than ten years of archival IceCube data.

The optical module is the basic building block of a large-volume neutrino telescope. In first-generation detectors it features a single large PMT measuring the amount of incoming photons (derived from the signal charge) as well as their arrival times. Novel optical sensors will play a key role in the expected performance enhancements of the IceCube Upgrade. A large fraction will be multi-PMT Digital Optical Modules (mDOMs) featuring 24 relatively small PMTs (see Sec. 2.1). This multi-PMT approach, introduced to deep-sea detectors by the KM3NeT Collaboration [6], results in attractive advantages with respect to the traditional single-PMT technique, including a homogeneous solid angle coverage and a larger sensitive photocathode area per module. Furthermore, the mDOM provides not only the number and arrival time of photons, but also directional information as well as the possibility of multiplicity triggering inside one module.

2. mDOM Design: Development and Status

The environmental conditions in the deep ice and the detector infrastructure at the South Pole pose unique challenges to optical module technology, such as a limited borehole diameter, pressure spikes during freeze-in and a tight power budget per module. The mDOM design was driven by these challenges. Figure 1 shows a picture of one of the first fully assembled mDOMs together with an exploded view highlighting different components. The mDOM features 24 three-inch class PMTs1 each equipped with its own active base that generates the high voltage in-situ. The PMT signals are routed to the mainboard where they are digitized, processed in the mainboard’s central FPGA and sent to the surface via the ICM (Ice Communication Module) which is the communication interface common to all devices in the ice. The PMTs as well as calibration devices are fixed in place with a 3D-printed support structure which is glued and optically coupled with silicone-based gel to the pressure housing made from borosilicate glass. In the following we give a more detailed account of the key components.

2.1 Photomultipliers

The PMT used in the mDOM is the R15458-02 model by Hamamatsu. Based on the R12199-01 HA MOD, it was further optimized for the tight spatial constraints inside this module, resulting in

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1The actual PMT diameter is 80 mm ($\sim 3.15$ inch).
2.2 Active PMT Bases

The PMT bases were custom designed for the mDOM PMT, featuring low power, off-the-shelf components, and dimensions to fit the very limited space in the center of the mDOM. Figure 2 shows the block diagram. The external connections are via a ribbon cable with low voltage power and a UART (Universal Asynchronous Receiver Transmitter) command interface, and a coax signal output cable connected directly to the anode.

To generate and regulate high voltage, a microcontroller on each base creates a rectangular waveform with adjustable frequency and duty cycle, switching between 0 and 3.3V at 100kHz–110kHz (typical). This waveform drives an LC resonator to output a sine wave with peak-to-peak amplitude 75V–150V, which drives a 13-stage Cockcroft-Walton multiplier. In contrast to previously reported PMT bases with resonant drive circuits (e.g., Ref. [9]), the resonator is direct coupled with no transformer. The inter-dynode voltages starting from the anode follow approximately the recommended ratios 1:1:1:1:1:1:1:1:1:3, ending with the cathode at negative high voltage.

The voltage is adjusted by changing the duty cycle of the switched rectangular drive waveform, with a fixed frequency below the resonance maximum. The control is optimized when the
frequency is chosen so \( V_{\text{max}} \) is 5\%–10\% above the desired \( V_{\text{out}} \) value. The microcontroller program accepts voltage setting commands via the UART interface, automatically determines an appropriate operation frequency and monitors performance. New versions of the firmware can be uploaded via the UART.

For regulation, a control loop is implemented in the microcontroller program, using the first stage (dynode 10, Dy10) voltage as the measured quantity and the duty cycle as the controlled quantity. Successive stage voltages are slightly smaller so the cathode is at \( \sim 0.1 \) times the Dy10 voltage. The loop dynamics include proportional and integral error terms, with measurements taken 10 times per second. The output time constant is about 1 s and stability is typically 0.03\% (RMS).

The power for the high voltage generator for an output of 1200 V is typically 5 mW. The power consumption is dominated by losses in the resonance inductor. The microcontroller operates from 1.8 V and adds another 2 mW.

The resonator drive circuit includes an impedance matching LC pair to minimize the switch current and corresponding high frequency transients (EMI). RC filters are used between Cockcroft-Walton outputs and the dynodes and cathode, with an additional filtering layer for dynodes near the anode output. The final ripple and switching noise are typically below 20 \( \mu \text{V}_{\text{pp}} \) as observed at the anode output (with 50 \( \Omega \) load).

The bases were manufactured under contract with Hamamatsu, and tested at the factory using equipment and procedures supplied by the IceCube collaboration. This testing included cycling between \(-40^\circ\text{C}\) and \(85^\circ\text{C}\) and a “burn-in” period at \(85^\circ\text{C}\). It also included subsequent measurement of all dynode output voltages for accuracy and stability. After soldering to PMTs, Hamamatsu tested the combined assemblies for correct signal output before shipping.

### 2.3 mDOM Mainboard

The main parts of the mDOM mainboard are two DC/DC converters, circuitry to monitor currents and voltages, 24 AFE (analog front-end) channels, a 2 Gbit DDR3 RAM based event buffer,
a powerful FPGA, a MCU (Microcontroller Unit), the ICM (IceCube Communication Module), a Xilinx CPLD, various sensors and the mDAB (mDOM Adapter Board). A simplified block diagram is depicted in Fig. 3. The overall power consumption measured at the input of the primary DC/DC is \( \sim 10 \text{ W} \).

Communication signals and DC power are provided to the mDOM by a single wire pair. A precision 20 MHz oscillator provides a system clock to both the MCU and the FPGA. Data exchange, mainly between the ICM and the MCU, takes place through a UART channel, running at 3 MBd. Various sensors, most of them located at the border of the PCB, can be read out using a I2C bus connection. The MCU initializes the AFE channels and sets and monitors the individual operation voltages of the PMTs. The MCU’s SPI bus is used to individually power-enable any of the AFE channels. The same bus also controls the mDAB board. The mDOM adapter board, equipped with a Xilinx CPLD, is primarily needed to accommodate the connectors of the three attached cameras, their illumination board and the two flasher LED chains.

The central task of the main FPGA is to control the 24 AFE channels. The ADC baselines and trigger thresholds are set by the MCU through the FPGA. Each of the 24 PMT channels provides its ADC and discriminator outputs to the mainboard FPGA. The 12 bit ADCs are operated at 120 MHz, and the discriminators are sampled at 960 MHz. The digitized waveforms along with the associated discriminator signals allow for precise leading-edge time extraction (resolution of about 1 ns). Custom firmware inspects each of the 24 datastreams for trigger conditions in real time. Waveform acquisition can be triggered either by the discriminator or by over-threshold ADC samples. Following a trigger, the FPGA writes the trigger sample along with a configurable number of pre- and post-samples into an internal buffer. Triggers in one channel do not interfere with triggering logic in any other channel. The waveform acquisition is deadtime-free provided the FPGA’s internal buffers do not overflow.

Data containing ADC waveforms and discriminator samples are transferred out of the waveform buffers and into DDR3 SDRAM by the FPGA. This process can occur at the same time as new waveforms are being written into the buffer. The mainboard has 2 gigabits of DDR3 memory, which is enough to buffer many seconds or even minutes of PMT data. This amount of data storage is
sufficient to deal with the trigger delay caused by the surface-based trigger processing.

The most challenging aspect of the AFE design was to accomplish low power consumption while maintaining enough bandwidth, sufficient dynamic range and good linearity. In contrast to traditional designs, a fully DC-coupled approach was used in order to achieve low noise in the neighborhood of electromagnetic interference. This avoids any droop effects (ADC-baseline variations) depending on the time between consecutive PMT pulses. The PMT signal, connected to the discriminator through a $49.9 \, \Omega$ resistor, is terminated at a 0.95 V DC level. Two precise, 16 bit DAC channels are used to adjust the discriminator threshold and the ADC baseline. For pulse shaping, two low pass filters are being used, one between the two amplifier stages and one in front of the ADC. The precision (0.1%) gain setting resistors are chosen to achieve a dynamic range of about 70 pe (photoelectron).

In order to characterize the behavior of the bare AFE channels, parameters such as the signal-to-noise-ratio (SNR), dynamic range, linearity as well as long-term stability of the discriminator threshold were investigated at low temperatures using a one-channel piggy-back board with the mainboard AFE design connected to a calibrated PMT, illuminated by tunable pulsed LED, together with a second reference PMT read-out by a digitizer.

The left plot of Fig. 4 shows example PMT pulses for 1 pe, 2 pe and 3 pe at $-22 \, ^\circ C$. The SNR was determined to be 60 (62) for $-22 \, ^\circ C$ ($-38 \, ^\circ C$), clearly exceeding the required SNR of at least 25. The AFE response must also be linear in amplitude to the incident light intensity within 10% in the dynamic range from 0.2 pe to 50 pe. Example PMT pulses for large charges can be seen in the right plot of Fig. 4 for $-22 \, ^\circ C$. The ADC dynamic range was found to extend from $\sim 0.1$ pe to $\sim 70$ pe, saturating for charges beyond independent of the temperature. At $-22 \, ^\circ C$ linearity on the 10% level is fulfilled throughout the dynamic range. Long-term (25 days) measurements of the discriminator threshold have also shown to remain stable, with a less than 0.05 mean pe drift per week.

The performance of the mDOM mainboard has been tested in a standalone setup, as well as in operation in a fully integrated mDOM (see Fig. 1). A basic functional check-out test of the mDOM mainboard consists of a check of the DDR3 memory, the uploading, flashing and read-back of the FPGA firmware, and the various sensors (pressure, temperature, magnetometer, etc.). In the case of an integrated mDOM, communication to the PMT bases is checked, alongside requirements such as the read-out of the per-PMT waveforms, PMT signal rates, or the distribution of the time between consecutive PMT signals. Finally, the calibration devices (see Sec. 2.4) are tested. The
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Figure 5: Single photo-electron charge histogram of an mDOM PMT as extracted by the mainboard MCU at $-35\,^\circ C$. A multi-component fit (orange) allows to deduce the mean number of photoelectrons per pulse as well as the gain of the PMT.

MCU software allows for fast ADC and discriminator scans, efficient block read-out of waveforms, and a charge stamp readout from in-board waveform integration. An example of such a charge distribution is shown in Fig. 5. Waveforms were acquired using the discriminator trigger at a 0.2 pe threshold, while the PMT was illuminated with SPE (single pe) level photons\footnote{On average $\ll 1$ photons per pulse reach the photocathode.} by the closest LED flasher in the mDOM.

2.4 Calibration Devices

For detector calibration, each mDOM is equipped with three cameras and four bright illumination LEDs (for details see \cite{10}), as well as ten flasher LEDs. The calibration devices are mounted on the inside of the internal support structure (see Sec. 2.5) behind glass windows, which separate them from the optical gel and ensure an optical transition between the device and air as necessary for correct operation. For the flasher LEDs and illumination LEDs, latching mechanisms are printed into the support structure, while the heavier cameras are attached to their printed support posts with sheet metal screws. The flasher LEDs of type Roithner XRL-400-50 emit light at a wavelength of $(405 \pm 10)$ nm at a maximum brightness of $10^9$ photons per pulse with a pulse width (FWHM) of 7.5 ns (shorter at lower intensities). Each LED is mounted on its own PCB and is driven by a Kapustinsky driver. The five flashers in an mDOM hemisphere (four located between the equatorial and polar PMTs, one in the pole region; see Fig. 1 right) are linked in a daisy chain and can be addressed individually\footnote{Failures on a PCB, especially the connectors, could result in loss of all subsequent flashers in the daisy chain.}.

2.5 Mechanical Structure

The mDOM features a borosilicate glass pressure housing designed and tested to withstand pressures up to 700 bar. It provides protection against the constant hydrostatic pressure in the deep-ice environment of $\sim 250$ bar as well as short-term pressure spikes during freeze-in after module deployment.

A silicone-based two-component gel provides optical contact between the PMTs and the pressure vessel as well as cushioning against vibrations for the internal components. The chosen gel retains elasticity and optical transparency for temperatures ranging from room temperature down to $-45\,^\circ C$, the latter corresponding to surface temperatures at South Pole during deployment. Components in direct contact with the gel were tested for gel-curing compatibility. In the mDOM
integration process the gel for each mDOM hemisphere is poured separately. Before potting, the gel components are thoroughly degassed at a few mbar. To minimize air entrapment during mixing and ensure a uniform mixing ratio and gel flow, a Meter Mix PAR 30 gear pump dispenser is used. Before curing, the gel is evacuated again to remove air inclusions in recesses or rough surfaces.

Inside the pressure vessel, the mDOM features an internal support structure to mount PMTs, calibration devices and readout-electronics. Its geometry features expansion joints to compensate for thermal shrinkage.

3. Status and Outlook

At the time of writing, the mDOM design is undergoing a Design Verification Test (DVT) campaign. In this procedure, ten fully functional mDOMs are being produced and, after an initial functional check-out (at room temperature as well as at \(-40\) °C), are being submitted to a series of tests followed by a final check-out to verify the continuous functionality of the module. The test program recreates the impact of critical situations that can potentially lead to module failure. It features a shock test (tipping over of module inside a transportation box), a vibration test (land and air transport), a harness load test (maximum load during the lowering of a string of modules into the borehole), a thermal shock test (submerging a cold module into the warm borehole water) as well as a high pressure cycle (pressure spikes during freeze-in). Each of the tests is run by at least one of the ten DVT modules, with some modules undergoing multiple tests. In addition, the functionality of all mDOM components is assessed at temperatures as low as \(-40\) °C over a short period of time for two modules and over a long period of time for three modules.

The DVT will also provide input for the optimization of the final module production process. Mass production of the mDOMs for IceCube Upgrade will be split between three sites. The support structures will be equipped with calibration devices in Münster. The final assembly will take place at DESY/Zeuthen and at Michigan State University. Deployment of the modules at the South Pole is foreseen for the Antarctic summer season of 2022/23.

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