ABSTRACT

Wang algebra was initiated by Ki-Tung Wang as a short-cut method for the analysis of electrical networks. It was later popularized by Duffin and has since found numerous applications in electrical engineering and graph theory. This is a semi-tutorial paper on Wang algebra, its history, and modern applications. We expand Duffin’s historic notes on Wang algebra to give a full account of Ki-Tung Wang’s life. A short proof of Wang algebra using group theory is presented. We exemplify the usefulness of Wang algebra in the design of T-coils. Bridged T-coils give a significant advantage in bandwidth, and were widely adopted in Tektronix oscilloscopes, but design details were guarded as a trade secret. The derivation presented in this paper, based on Wang algebra, is more general and simpler than those reported in literature. This novel derivation has not been shared with the public before.

INDEX TERMS

Bandwidth extension, bridged-T networks, matrix determinant, peaking, T-coil, Wang algebra, wideband amplifier.

I. INTRODUCTION

Wang algebra is a commutative algebra $\mathbb{W}$ with the properties

$$x + x = 0, \quad x^2 = 0 \quad (1)$$

for all $x \in \mathbb{W}$. It was proposed by Ki-Tung Wang in 1934 as a convenient rule to simplify the analysis of electrical networks [1]. In essence, it is a clever method to compute the determinant of a symmetric matrix in the context of solving a system of simultaneous linear equations. A usual approach in the pre-computer era would be to use Cramer’s rule, but calculating the determinants by hand is not only tedious, but also highly likely to run into mistakes, due to the many terms involved. In Wang algebra, a large number of terms vanish due to (1), thus greatly reducing the calculation complexity [2], [3], [4], [5], [6]. Wang algebra has been a useful method to design electronic circuits and modern integrated circuits (ICs), in particular T-coils and interconnects [7], [8]. Yet, the impact of Wang algebra goes much beyond electrical engineering. It has also found applications in networking and graph theory [9]. Historically, Wang algebra was studied in great detail by famous mathematicians Chow [4] and Duffin [10], [11].

Somewhat mysteriously, Duffin gave the following comment in [12]:

“K. T. Wang managed an electrical power plant in China, and in his spare time sought simple rules for solving the network equations... Wang could not write in English so...”

1. The works by Chow [4] and Duffin [10] themselves deserve special remarks. Wei-Liang Chow, who had studied algebraic geometry under the great algebraist van der Waerden, was running a business in Shanghai, China, to support his family. Shanghai was occupied by Japanese troops during World War II, and as a result he had lost his academic job in the National Central University. Chow could hardly do any research during this war period (1937-1945), and the study of Wang algebra [4] was an exception (and in fact his only work on an engineering problem). Amazingly, after almost a decade in business, he made a strong return to research and became a prominent algebraic geometer in the U.S. Chow ring, a fundamental concept in algebraic geometry, is named after him. Richard Duffin is well known for his contributions to electrical networks and geometric programming. Together with Albert Schaeffer, he introduced the concept of frames, which play an important role in signal processing. The Duffin-Schaeffer conjecture, concerning rational approximation of real numbers, is a famous conjecture in number theory that had stood open for nearly 80 years. It was finally proved by Dimitris Koukoulopoulos and James Maynard in 2019. Maynard won a Fields Medal partly because of this proof.
Ki-Tung Wang (1875-1948), shown in Fig. 1, was a Chinese mathematician, electrical engineer and philosopher. Believed to be the first Chinese scholar to publish a mathematical paper in an international journal, he is well known for his work on Wang algebra, as well as investigation on the relationship between sciences and Buddhism [13].

Ki-Tung Wang was born into a prominent family in Suzhou, Jiangsu Province in 1875. His ancestor Ao Wang (1450-1524) was ranked no. 3 in the Imperial Examination and later became a Grand Secretary of the Cabinet (equivalent to Prime Minister) of the Ming Dynasty. His father Song-Wei Wang (1849-1895) was also a Jinshi (Imperial Scholar), the highest degree of Imperial Examination in China.

In 1895, Ki-Tung Wang graduated from the Imperial Tungwen College2 (modern-day Peking University), then was hired as a mathematical lecturer there. He had already published several Chinese articles on mathematics, and investigated the relation between classical Chinese mathematics and modern mathematics [14].

In 1909, Ki-Tung Wang served as an administrator of Chinese students in Europe, then he did internships at the English Electrical Company and Siemens [13]. During this period, he published a paper on the differentiation of quaternionic functions3 in the Proceedings of the Royal Irish Academy [15], which is believed to be the first paper published by Chinese mathematicians in international journals [13], [16].

In 1912, Ki-Tung Wang was offered a job at the Ministry of Education, then in as part of the newly founded Republic of China [13].

In 1914, Ki-Tung Wang went to industry and became an electrical engineer at the Zhenjiang Power Plant, Jiangsu Province [13].

In 1928, Ki-Tung Wang was appointed Research Fellow4 at the National Research Institute of Engineering, Academia Sinica [13]. He proposed a new method to derive the impedance of electrical networks, which is sometimes advantageous to the traditional method [1], [17]. In 1950, Duffin and Bott recognized that his rules form an algebra and presented this method to the American Mathematical Society, under the title “The Wang algebra of networks” [10].5 It is unclear how Duffin learned the story that Wang could not write in English.6 This was unlikely to be true, since Ki-Tung Wang had learned English at the Imperial Tungwen College [18], worked in Britain, and published an English paper [15] before. Nevertheless, it is possible that Ki-Tung Wang needed some help on English writing, since it had been more than 20 years since the publication of [15].

Ki-Tung Wang was very interested in philosophy beyond the limits of modern sciences. He had several publications on the relationship between sciences and Buddhism, including a book Comparative Study of Buddhism and Sciences [19].

B. FROM OLD THEORY TO MODERN APPLICATIONS

Nearly 90 years have passed since Wang algebra was proposed. It has found numerous applications in areas ranging from graph theory to electrical and electronic engineering. This article serves as a survey of the history, theory and modern applications of Wang algebra.

As an example of applications, we will focus on T-coils, which have been used for many years for wideband amplifier

2. It literally means “Multilingual College”.

3. Quaternions are extension of complex numbers, with lots of engineering applications such as astronautics, robotics, computer visualization, animation, special effects in movies, navigation, etc. It is worth mentioning that differentiation of quaternionic functions is highly nontrivial—even more involved than the Cauchy-Riemann condition for complex functions.

4. Equivalent to Principal Scientist nowadays.

5. Duffin himself cited in [10] H. W. Becker’s 1948 unpublished notes on Wang algebra.

6. One possibility is that Duffin might have met Ki-Tung Wang’s daughter or son. Several children of Ki-Tung Wang earned Ph.D. degrees in the US; in particular, his son Shou-Jin Wang (1904-1984) obtained his Ph.D. in physics from Columbia University, worked at Peking University and retired from MIT Lincoln Lab (https://www.guokr.com/article/441034/). It is likely that Duffin had met Shou-Jin Wang.
designs with distributed amplification, peaking\(^7\) networks, termination networks, and transmission line equalization [7], [20], [21], [22], [23], [24], [25], [26], [27], [28], [29], [30], [31], [32], [33], [34]. In recent years, high-speed IC designs have added T-coils because of three useful properties: 1) constant input resistance (constant-R), 2) usually over double the bandwidth extension, and 3) a second order transfer function.

Tektronix pioneered the use of T-coils for bandwidth enhancement in 1960s. Bridged T-coils give a significant advantage in bandwidth over conventional RC design (more precisely 2\(\sqrt{2}\) times improvement). They were widely adopted in Tektronix oscilloscopes, but design details were guarded as a trade secret [20], [35], [36]. It was not until 1990 that former Tektronix engineer Feucht provided the T-coil design equations [26], but no derivation was given. Lee published a tedious derivation for the standard symmetric T-coil [20, Ch. 12].

In this paper, a general method is presented to design constant impedance bridged-T networks in terms of symbolic elements. General formulas for the element relationships are derived using Wang algebra. The constant impedance constraint is used by adding an element to balance a bridge circuit. This simplifies calculating the transfer function, and it is presented in a reduced form in terms of a Thévenin equivalent circuit. When applied to a constant input resistance (constant-R) network, such as a bridged T-coil that connects to a capacitor load, extended designs with optional resistors in series with inductors and resistors in parallel with capacitors exist for both symmetrical and asymmetrical configurations. The process is to substitute known element values in the symbolic formulas and to solve for the remaining elements. All transfer functions are second order, and it is convenient to parameterize the formula in terms of a bridging capacitor. Compared with an RC circuit, the bandwidth is usually more than doubled with selected peaking. Previously published and new configurations can be derived from these formulas.

C. ORGANIZATION
The rest of this paper is organized as follows. In Section II, we give an introduction to Wang algebra, as well as a one-page proof based on group theory. Section III is a survey of the applications of Wang algebra to electrical engineering and graph theory. Section IV-A is devoted to the design of T-coils using Wang algebra. Conclusions are given in Section V.

II. WANG ALGEBRA
In essence, Wang algebra gives a method to calculate the determinant of a symmetric matrix, which can be more convenient sometimes. A proof was already outlined in the original article [1]. Duffin’s proof [10] was based on Grassmann algebra, which is 10-pages long. Chow’s proof [4] based on matrix theory is also quite tricky. In the following, we will present a short proof using group theory, for completeness. Familiarity with group theory is assumed, in particular symmetric group \(S_n\) of order \(n\) [37]. Readers uninterested in the proof may simply skip it.

**Theorem 1 (Wang Algebra):** Let \(A = [a_{ij}]_{n \times n}\) be a symmetric matrix, i.e., \(a_{ij} = a_{ji}\), where \(1 \leq i, j \leq n\). Write the diagonal elements of \(A\) as

\[
a_{ii} = a_{ii}' = \sum_{j \neq i} a_{ij}.
\]

Then the determinant \(\text{det}(A)\) can be computed as

\[
\text{det}(A) = \prod_{i=1}^{n} \left( a_{ii}' - \sum_{j \neq i} a_{ij} \right)
\]

in Wang algebra \(\mathbb{W}\).

It should be pointed out that the product (2) must be computed symbolically, for it is certainly not true that \(\text{det}(A) = \prod_{i=1}^{n} a_{ii}\) numerically. In other words, we take \(a_{ii}', a_{ij} (j \neq i)\) as symbols \(\in \mathbb{W}\) in the computation of (2).

The key point here is that many terms disappear thanks to the rule of Wang algebra (1). Once the product (2) has been computed, numerical values \(a_{ij} \in \mathbb{C}\) can be substituted in to find the determinant. Readers are referred to Section III for examples.

**Proof:** We will prove Theorem 1 in two steps. We use a standard formula of the matrix determinant:

\[
\text{det}(A) = \sum_{\sigma \in S_n} \text{sgn}(\sigma) \prod_{i=1}^{n} a_{\sigma(i)}
\]

where \(\sigma\) is a permutation in the symmetric group \(S_n\), and \(\text{sgn}(\sigma) = \pm 1\) denotes the signature of \(\sigma\).

**Lemma 1:** Let \(A = [a_{ij}]_{n \times n}\) be a symmetric matrix. Then, except the term corresponding to the main diagonal, all terms of Eq. (3) contain either a square or a factor \(a_{ii}\).

**Proof:** Firstly, the factor 2 is due to the symmetry of the matrix: associated with any term \(\prod_{i=1}^{n} a_{\sigma(i)}\) in Eq. (3), there is another one

\[
\prod_{i=1}^{n} a_{\sigma(j)} = \prod_{j=1}^{n} a_{\sigma^{-1}(j)}.
\]

Since \(a_{\sigma(j)} = a_{\sigma^{-1}(j)}\), the two terms are equal. Moreover, \(\text{sgn}(\sigma) = \text{sgn}(\sigma^{-1})\). Thus we obtain the factor 2 if the two terms are distinct.

The argument above fails if and only if the two terms are in fact the same (e.g., the term \(a_{11}a_{22} \ldots a_{nn}\) corresponding to the main diagonal). This happens if and only if

\[
\sigma = \sigma^{-1},
\]

\(i.e., \sigma^2 = 1\). This implies \(\sigma\) has order 1 or 2.

The case of order 1 corresponds to the main diagonal.

The case of order 2 consists of one or more disjoint cycles of length 2, \(i.e.,\) transpositions, which looks like

\[\text{sgn}(\sigma) = \pm 1\].
\(\sigma = (i, j)(k, l)\) etc. In other words, such a product contains square(s) \(a_{ij}a_{ij} = a_{ij}^2, a_{ij}a_{lk} = a_{lk}^2\) etc.

**Lemma 2:** Let the diagonal elements of \(A\) be given by

\[
a_{ii} = a_{ii}' - \sum_{j \neq i} a_{ij}.
\]

Then any terms of (3) involving an off-diagonal element are cancelled out.

**Remark 1:** Lemma 2 is slightly more general than Wang algebra, since it does not assume that \(A\) is symmetric.

**Proof:** Without loss of generality, consider an off-diagonal element \(a_{ij}\) where \(i \neq j\). The sum of all terms involving \(a_{ij}\) is given by

\[
\sum_{\sigma: \sigma(i)=j} \text{sgn}(\sigma) a_{ij} \prod_{k=1, k \neq i}^{n} a_{k\sigma(k)}.
\]

We claim that all these terms will be cancelled out by those in the following sum

\[
\sum_{\sigma': \sigma(i)=j} \text{sgn}'(\sigma') a_{ij} \prod_{k=1, k \neq i}^{n} a_{k\sigma'(k)}.
\]

Since \(\sigma(i) = j\), indexes \(i, j\) must belong to a certain cycle \((i_1, i_2, \ldots, i_l)\) of length \(l > 1\). Let us extract from (7) those containing this cycle:

\[
\sum_{\sigma: (i_1, i_2, \ldots, i_l) @ \sigma} \prod_{k=1, k \neq i}^{n} a_{k\sigma(k)}.
\]

There exist corresponding terms in (8) with \(\sigma'\) fixing indexes \(i_1, i_2, \ldots, i_l\):

\[
\sum_{\sigma': (i_1, i_2, \ldots, i_l) = i_l} \prod_{k=1, k \neq i}^{n} a_{k\sigma'(k)}.
\]

Clearly, \(\sigma\) is a composition of \(\sigma'\) and \((i_1, i_2, \ldots, i_l)\).

Note that a negative counterpart of \(a_{k\sigma(k)}\) exists in \(a_{kk} = a_{kk}' = \sum_{m \neq k} a_{km}\), so the sign of those terms in (10) is

\[
\text{sgn}(\sigma')(1)^{l-1}.
\]

But the sign of those terms in is (9)

\[
\text{sgn}(\sigma) = \text{sgn}(\sigma')(1)^{l-1},
\]

they must have different signs, therefore being cancelled out completely.

The proof of Theorem 1 is completed by combining the two lemmas.

**Remark 2:** In [1], Ki-Tung Wang treated terms containing a square and terms containing factor 2 separately, which correspond to 2-cycles \((i_1, i_2)\) and longer cycles \((i_1, i_2, \ldots, i_l)\) in the above proof, respectively.

An astute reader may wonder if there is any gain to use Wang algebra to compute the matrix determinant. After all, applying the formula (3) crudely requires \(n \cdot n!\) multiplications, while computing (2) may require \(n^n\) multiplications in the worst case. By Stirling’s formula

\[n! \approx (n/e)^n,\]

the latter is asymptotically much greater. Nevertheless, there are some advantages of Wang algebra:

- The number of terms is greatly reduced by applying Wang’s rule (1), thus it can be more convenient for small dimensions \(n\);
- Even if there are faster numerical algorithms to compute matrix determinant, the symbolic formulas given by Wang algebra can lead to considerable insights.

We will demonstrate these advantages in the following sections.

### III. APPLICATIONS

#### A. APPLICATIONS TO ELECTRICAL NETWORKS/CIRCUITS

For simplicity, let us assume planar networks, i.e., networks which can be drawn on a plane such that no branches cross each other. An example of planar electrical network is shown in Fig. 2, with impedances \(a, b, c, d, e\). In fact, this is a model of the bridged T-coil which will be analyzed in detail in the following section. The problem is to determine the joint impedance \(Z\). Applying Kirchhoff’s voltage law to three loops shown, we obtain the following system of linear equations:

\[
\begin{pmatrix}
a + d & -a & -d \\
-a & a + b + c & -b \\
-d & -b & b + d + e
\end{pmatrix}
\begin{pmatrix}
I_2 \\
I_3
\end{pmatrix} = \begin{pmatrix}
V \\
0 \\
0
\end{pmatrix}.
\]

Note that the above matrix is always symmetric and exhibits the structure given in (6), by selecting the orientations of the loops properly. Denote by \(M_1\) the \(3 \times 3\) matrix on the left-hand side of (11), and by \(M\) the \(2 \times 2\) submatrix on

8. For example, the elimination method (aka Gauss elimination) can be used to compute matrix determinant with \(O(n^3)\) complexity. The elimination method was invented by ancient Chinese mathematicians to solve systems of linear equations in The Nine Chapters on the Mathematical Art (see [38] for an English translation), predating the work of Gauss by about 2000 years. The Nine Chapters on the Mathematical Art is a textbook taught in the National University during the Han Dynasty of China (202 BCE – 220 CE). Ki-Tung Wang was aware of the elimination method, since classical Chinese mathematics was taught at the Imperial Tungwen College [14], [18].
its bottom right. Cramer’s rule is a standard approach to computing the joint impedance $Z$:

$$
\frac{1}{Z} = \frac{I}{V} = \frac{\det(M)}{\det(M_1)}.
$$

(12)

To proceed, we firstly compute the numerator:

$$
\det(M) = \det\begin{pmatrix}
  a + b + c & -b \\
  -b & b + d + e
\end{pmatrix}
= ab + ad + ae + b^2 + bc + bd + be + cd + ce
= ab + ad + ae + bc + bd + be + cd + ce
$$

(13)

where $b^2 = 0$ in Wang algebra $\mathbb{W}$. Then we multiply it with $a + d$ to compute the denominator:

$$
\det(M_1) = (a + d)(ab + ad + ae + bc + bd + be + cd + ce)
= 2abd + abc + abe + acd + ace + ade + bcd + bde + cde
= abc + abe + acd + ace + ade + bcd + bde + cde.
$$

where $2abf = 0$ in Wang algebra $\mathbb{W}$. Many product terms are 0 and do not have to be listed. Note the saving it brings in this case: 18 initial terms in the denominator are reduced to 8 in the end.

For the reason to be clear in the next section, the determinant $\det(M)$ is called the mesh determinant of the electrical network.

We can see that another statement of Wang algebra for computing network determinants is:

**Corollary 1 (Wang’s Rule):** The determinant of a planar network does not contain any terms containing a square or a factor 2. Moreover, all its terms have coefficient $+1$.

Ki-Tung Wang initially considered planar networks [1]. In follow-up works by Chinese researchers [2], [3], [4], the restriction of planar networks was removed. Wang algebra $\mathbb{W}$ still holds for non-planar networks, but the rule stated above needs to be modified slightly. More precisely, a term coefficient may be an odd number; therefore, the coefficients are calculated modulo 2 as in Wang algebra $\mathbb{W}$.

Duffin proved that the so-called just discriminants, whose coefficients are all equal to $+1$ and which do not necessarily come from Kirchhoffian networks, can also be evaluated in Wang algebra [10]. For example, he showed that Wang algebra holds for a network lying on the surface of a torus. But Grassmann algebra needs to be used for non-just discriminants, in a similar way to Wang algebra for just discriminants.

**B. APPLICATIONS TO GRAPH THEORY**

Of course, electrical networks are examples of graphs. Since matrix theory and graph theory are closely related, it is not surprising that Wang algebra is also useful in graph theory. Specifically, it gives an algebraic method to enumerate the trees and cotrees of a graph. Readers are referred to [9] for an introduction to graph theory and its engineering applications.

A graph consists of a set of nodes together with a set of edges. The electrical network shown in Fig. 2 is an example of graph. It contains 4 nodes, as well as 5 edges labelled by $a$, $b$, $c$, $d$, and $e$.

A (spanning) tree of a graph is a set of edges which connect all nodes and which do not contain any loops. For example, edges $\{a, b, e\}$ form a tree in Fig. 2. The complement of a tree in a graph is called a cotree; in other words, it is a set of edges when removed leave no loops. For example, $\{c, d\}$ form a cotree, since it is the complement of $\{a, b, e\}$. Wang algebra gives a handy method to enumerate trees/cotrees.

1) ENUMERATING TREES/COTREES

For convenience, let the admittances be $A = 1/a$, $B = 1/b$, $C = 1/c$, $D = 1/d$, $E = 1/e$ in Fig. 2. By Kirchhoff’s current law, we have another set of equations for Fig. 2:

$$
\begin{pmatrix}
  A + C & -A & -C \\
  -A & A + B + D & -B \\
  -C & -B & B + C + E
\end{pmatrix}
\begin{pmatrix}
  V_1 \\
  V_2 \\
  V_3
\end{pmatrix}
= \begin{pmatrix}
  I \\
  0 \\
  0
\end{pmatrix}.
$$

(14)

The above matrix also exhibits the structure given in (6). Denote by $S$ the $3 \times 3$ matrix on the left-hand side of (14), and by $S_1$ the $2 \times 2$ submatrix on its bottom right. The determinant $\det(S)$ is called the node determinant of the electrical network.

Applying Cramer’s rule yields the joint impedance

$$
Z = \frac{V}{I} = \frac{\det(S_1)}{\det(S)}.
$$

(15)

Again, calculation is greatly simplified by using Wang algebra. The numerator can be calculated as

$$
\det(S_1) = (A + B + D)(B + C + E) = AB + AC + AE + BC + BE + BD + CD + DE.
$$

Multiplying it with $A + C$ yields the determinant

$$
\det(S) = (A + C) \det(S_1)
= ABCE + ABD + ACD + ADE
+ ACE + BCE + BCD + CDE.
$$

The saving is the same as before: 18 initial terms in the denominator are reduced to 8 in the end.

A tree, respectively cotree, product is the product of the edges of the tree, respectively cotree. It is known that the terms of node determinant are tree products [9, Th. 2.28], while the terms of mesh determinant are cotree products [9, Th. 2.29]. In fact, the 8 terms of node determinant $\det(S)$ are precisely the trees, while those of the mesh determinant (13) are the cotrees of the graph shown in Fig. 2.

The following relation holds between the mesh determinant and node determinant:

$$
\det(M(a, b, c, d, e)) = abced \cdot \det(S(A, B, C, D, E)).
$$

(16)

This agrees with the one-to-one correspondence between trees and cotrees.
2) COUNTING TREES/COTREES

Obviously, setting \( a = b = c = d = e = 1 \), we obtain the number of trees/cotrees of a graph:

\[
\det(M(1, 1, 1, 1, 1)) = \det(S(1, 1, 1, 1, 1)).
\]  

(17)

For example,

\[
S(1, 1, 1, 1, 1) = \begin{pmatrix}
2 & -1 & -1 \\
-1 & 3 & -1 \\
-1 & -1 & 3
\end{pmatrix}.
\]  

(18)

It is easy to check that \( \det(S(1, 1, 1, 1, 1)) = 8 \), implying there are 8 trees in the graph, as expected. In fact, \( \det(S(1, 1, 1, 1, 1)) \) is a cofactor of the Laplacian matrix of the graph, and this is a well-known method in graph theory to calculate the number of trees. This fact can be used to check if we have obtained the correct number of terms in network determinants.

IV. BRIDGED T-COILS

In this section, we demonstrate how Wang algebra can be used to greatly simplify the design of bridged T-coils. T-coils are an old technology, but featured with modern applications. Constant-R T-coils provide ideal load or termination. They are an old technology, but featured with modern applications. Constant-R T-coils provide ideal load or termination. They offer 2.73 improvement for acceptable 0.4% overshoot to ideal step input. Now T-coils are used in high-speed buffer design and electrostatic discharge (ESD) compensation with bandwidth improvement. A review of historical applications at Tektronix is given in Section IV-A.

For peaking applications, T-coils are designed to drive capacitive loads that model input transistor structures (or in a reciprocal manner, current source transistor drivers with output capacitance). T-coil extensions for loads with series or parallel resistor elements have been published [7], [25], [26], [27], [29], [30], [31]. Also, more detailed models of the physical structure reveal resistive losses in the branches [32], [33], [34]. With the formulas in this section, extensions for losses or resistive elements can be included in the design process. Extended T-coil configurations are shown in Section IV-B.

Several methods have been reported to derive the transfer function to a capacitor load [20], [21], [23], [24], [25], and this remains an algebraically tedious process, especially for the extended configurations. Most of these methods first derive fourth order equations. These are reduced to second order equations after identifying common factors. The calculations (not shown) are particularly tedious for the general configurations reported later [7].

For generality, a symbolic method using Wang algebra for simplicity is used in Section IV-C to derive the general constant input impedance (constant-Z) constraint relationship. A symbolic approach (based on an augmented Thévenin equivalent circuit) is used along with the constant-Z constraint to produce a transfer function that is already of reduced order (or easy to reduce).

Section IV-D gives two sets of T-coil formulas for the most general symmetrical and asymmetrical cases where the termination is \( R \). Simpler T-coils can be derived by removing some resistor terms. A design process is suggested based on selecting complex-pole angles.

A. T-COIL HISTORY AT TEKTRONIX AND AFTERWARDS

The application of T-coils in products is documented in personal narratives by Battjes [28] and Addis [27]. The first author adds T-coil derivations done while at Tektronix and after leaving Tektronix. Around 1948, William Hewlett had lunch in Oregon with Tektronix president Howard Vollum and a key engineer Logan Belleville. Hewlett penciled out the distributed amplifier circuit [22] on a paper napkin. Tektronix adopted distributed amplifiers or distributed deflection circuits in cathode ray tubes in oscilloscopes. The input for the Tektronix 519 oscilloscope (1 GHz) drove the distributed vertical deflection plates directly. Other products included the 517 (50 MHz with distributed amplifier), 545 main frame with plugins (30 MHz), 545A (30 MHz), and 585A (100 MHz with distributed deflection plates). T-coils for inter-stage peaking were used in Type K and L plugins and in the 3A6 plugin that connected directly to a cathode ray tube deflection structure. This is just a short list of some early implementations. More information is available by searching the product names. Note, some schematics assume, but do not show the bridging capacitor \( C_B \) (see Fig. 3).

The portable oscilloscopes, Tektronix 454 and 454A (150 MHz) used distributed deflection elements and an output amplifier with T-coil peaking. They also used T-coils for input delay line differential phase compensation. T-coils on individual circuit boards were fabricated for transistor interstage peaking. The transistor input was still approximated as capacitor. A leading edge (at that time) oscilloscope the Tektronix 7904 (500 MHz) had minimum VSWR T-coil that compensated for some additional parasitic degradations. The follow-on 7104 (1 GHz) oscilloscope used faster transistors, thin film conductors on substrates, and a transmission line package design.

The so-called “Ross T-coil” is noted in [27], [28] and was implemented by thin film deposition to peak and terminate
the inter-chip transmission lines. Other T-coils boosted the frequency response and minimized reflections in a 50Ω input for the 11A72 dual channel plug-in preamplifier hybrid.

Battjes [28] (a Stanford University graduate) joined Tektronix to design bipolar transistor amplifiers. He created and conducted internally the amplifier frequency and time response (AFTR) class for new engineers. The T-coil derivations were those of [22] or variations thereof. Rather than using a full bipolar transistor hybrid-pi input model used in Fig. 4(d) and Figs. 5(c,d); Battjes favored the simpler $R_S - C$ load model in Fig. 4(b) and Fig. 5(a). It offered better time-constant design intuition and the $R_P$ contribution was considered large and negligible. Besides, the full design equation mathematics shown in this paper were not known at that time.

Sitting next to Battjes, the first author became interested in working out the mathematics for the $R_S - C$ model. He applied Wang algebra, but never revealed the derivation details outside of Tektronix. Various forms of the result were later published as the “Ross T-coil” [27], [28].

The Wang algebra approach differed from earlier known derivations. The coupled inductor was split into three inductors, as shown in Fig. 3, to avoid any pre-defined coupling assumptions. A constant resistance constraint was applied initially to calculate some fixed element values (shown on and above the Gain line in Table 1). The equations were helpful in making later simplifications. The parameter of interest was selected as $C_B$ because it could be varied from 0 to nearly infinity. $C_B$ was easily related to the inductance $M$, the coupling coefficient $k$, and a second order equation damping variable $\delta$. This derivation did not require taking an absolute value of $k$, as was done in some other derivations. Nor was it limited to symmetrical T-coils. (For example, the T. T. True patent [31] for asymmetrical T-coils was known internally, but never used.)

After leaving Tektronix, the first author investigated deriving the equations for the full bipolar transistor hybrid-pi input model in Fig. 4(d) and Figs. 5(c,d), as discussed in this paper. As technology evolved, digital displays replaced distributed deflection structures. Also, hybrid integrated circuits and integrated circuits were replacing discrete transistor circuits. But as the references show, T-coils are still being implemented in integrated circuits and for ESD protection.

### B. CONSTANT-R BRIDGED T-COILS

The standard T-coil is shown in Fig. 3(a). The structure contains a bridging capacitor $C_B$ and is terminated by a resistor $R$. Of interest is the transfer function to a load capacitor $C$. The three-terminal coupled transformer in Fig. 1(a) can be modeled as shown in Fig. 3(b) by inductors $L_1$, $L_2$, and $L_3$ with well-documented conversions between the two forms. This allows for a wider range of solutions including...
cases (for some extended configuration values) that make $L_1$ negative. In the standard T-coil, $L_1 = L_2$.

Fig. 4 shows symmetrical constant-R configurations with Fig. 4(a), the standard configuration. Figs. 4(b)–4(d) add additional resistor combinations. These extensions add a bridging resistor $R_B$ to allow symmetry with $L_1 = L_2$ and $R_1 = R_2$. Symmetrical structures might be easier to fabricate.

Similar to Fig. 4, Fig. 5 shows asymmetrical constant-R T-coils configurations. Fig. 5(d) provides a general configuration with a selectable $R_1$ over a limited range as long as $R_2 \geq 0$. For example, resistors can be entered proportionally in both inductor branches for physical losses.

The first author derived the formulas in the late 1960s for the series configuration in Fig. 5(a) to include bipolar transistor base resistance. Equivalent formulas have been published in later works [26], [27], [28], [29], [30]. The constant-R component values for Fig. 5(b) were revealed in [31], without the associated transfer function. Later, the first author published equations for generalized configurations including both Fig. 4(d) and Fig. 5(d) [7]. The asymmetrical configuration did not need a bridging resistor.

C. CONSTANT-Z BRIDGED-T NETWORKS

1) DRIVING POINT IMPEDANCE

A general bridged-T network driving point impedance in Fig. 2 can be derived in symbolic form from loop or nodal equations. Wang algebra simplifies the process with the aforementioned rules. The resulting driving point admittance is set equal to the terminating admittance $1/e$:

$$\frac{1}{Z} = \frac{1}{e} = \frac{N}{D} = \frac{(a + b + c)(b + d + e)}{(a + d)N} = \frac{ab + ad + ae + bc + bd + be + cd + ce}{abc + abe + acd + ace + ade + bcd + bde + cde}. \quad (19)$$

In (19) the driving point admittance numerator $N$ is formed by adding the impedances of a set of independent loops except the input loop, and then by multiplying these sums with Wang algebra rules. The denominator $D$ is formed by multiplying $N$ and the input loop sums and applying Wang algebra rules.

With regular algebra, (19) is simplified to produce symbolic, constant-Z constraints for asymmetrical (20) and symmetrical (21) networks as arranged as

$$(a + b)(d - e^2/c) + ab + (a - b)e - e^2 = 0, \quad (20)$$

$$a = b, \quad 2a(d - e^2/c) + a^2 - e^2 = 0. \quad (21)$$

The component relationships are formed by equating the factors of each power of the Laplace variable $s$ to zero after inserting the actual impedances into (20) or (21). This process yields a set of independent constraints. In some cases the relationships show that the circuit is not suitable due to a mathematical contradiction or to non-realizable (negative) components.

2) TRANSFER FUNCTION

A method is presented for calculating the transfer function to node $V_d$ in Fig. 6(a), if the constant-Z constraint is satisfied. The configuration in Fig. 6(b) produces an equivalent result with impedance $f$ and $V_f = V(e + f)/e$. A Thévenin equivalent circuit can be derived at node $V_d$ as shown in Fig. 6. Without any loss in generality, the bridge in Fig. 7(a) can be balanced by choosing $f = e/(ab)$ to null out the $c$ term. The Thévenin impedance, $Z_{Th}^e$, becomes $(b + c)/(a + f)$. As shown in Fig. 7(b) this also produces a scaled-mirror symmetrical divider about $V_{Th}$ (which can be shown by splitting $c$ into two impedances, $ca/(a + b)$ and $cb/(a + b)$, and joining them at the $V_{Th}$ node. The values shown in Fig. 7(c) are:

$$V_{Th} = V, \quad Z_{Th} = b' + e' = (b + e)\frac{a}{a + b}. \quad (22)$$

This equivalent circuit applies for any constant-Z bridged-T network. For constant-R T-coils, the transfer function of interest is to the capacitor $C$ (usually part of a transistor model) within branch $d$. For symmetrical networks the Thévenin impedance reduces to one-half of the impedances of the $b + e$ branches. For asymmetrical networks, the Thévenin impedance also produces reduced order transfer.
functions where applicable (sometimes after applying some constant-Z identities). Fig. 7(a) suggests that poles associated with branch \( c \) do not appear in the transfer function. The conductance and capacitance elements within branches \( a + b \) in (22) can be cancelled, as shown later.

**D. GENERAL T-COIL EQUATIONS**

1) ROOT LOCUS

The Thévenin equivalent circuit approach produces second order transfer functions for all T-coils in Fig. 4 and Fig. 5 and can be parameterized in terms of the bridging capacitor \( C_B \) for the complete range of solutions. Also, \( G_P = 1/R_P \).

\[
\frac{V_C}{V} = \frac{1/(G_P + sC)}{Z_{th} + R_S + sL_3 + \frac{1}{(G_P + sC)}}
\]

(23a)

\[
\frac{V_C}{V} = \frac{1}{B_0 + B_1s + B_2s^2} = \frac{1}{B_0 + (D_0 + D_1C_B)s + D_2C_Bs^2}.
\]

(23b)

In Fig. 8, the complex poles of interest are shown for increasing \( C_B \) and lie on a circle with

\[
\text{center} = -\frac{B_0}{D_0}, \quad \text{radius} = \frac{B_0}{D_0} \sqrt{1 - \frac{D_0D_1}{B_0D_2}}.
\]

(24)

Fig. 7(b) shows the case where the circle defined by (24) is to the left of the origin because \( D_1 > 0 \) when \( R_P \) exists.

A design process is to solve for the poles of (23) and then find \( C_B \) for a specified pole-angle measured from the negative real axis. Both poles are found by solving quadratic equations. Elements and terms for symmetrical and asymmetrical transfer functions in (23) are given next. The \( L_3 \) value varies with \( C_B \) (and the pole-angle) as shown in (28) and (37) below. A final step is to calculate physical inductor values and a coupling coefficient.

2) SYMMETRICAL T-COILS

The T-coil equations for the general case in Fig. 4(d) are expressed using \( G_P = 1/R_P \) and \( G_B = 1/R_B \) to support omitted resistor simplifications with zero-valued \( G_P \) and/or \( G_B \) elements.

1) Constant-R elements:

\[
R_1 = R_2 = \frac{R^2G_P}{2} \quad (25)
\]

\[
G_B = \frac{R_S}{R^2 + G_P/4} \quad (26)
\]

\[
L_1 = L_2 = \frac{R^2C}{2} \quad (27)
\]

\[
L_3 = \frac{R^2C_B - L_1/2. \quad (28)}{L_3}
\]

2) Transfer function terms:

\[
B_0 = 1 + G_P\left(\frac{R}{2} + \frac{R^2G_P}{4} + R_S\right) \quad (29)
\]

\[
D_0 = \left(2R + R^2G_P + 4R_S\right)/C \quad (30)
\]

\[
D_1C_B = R^2G_P \quad (31)
\]

\[
D_2C_B = R^2CC_B \quad (32)
\]

3) ASYMMETRICAL T-COILS

The T-coil equations for the general case in Fig. 5(d) are presented.

1) Constant-R elements:

\[
R_2 = \frac{R^2G_P - R_1[1 + (R_S + R)G_P]}{1 + (R_S + R - 1)G_P} \quad (33a)
\]

\[
R_1 = \frac{R^2G_P - R_2[1 + (R_S - R)G_P]}{1 + (R_S + R + 2)G_P} \quad (33b)
\]

\[
R_1 + R_2 = \frac{(R - R_1)^2G_P}{1 + (R_S + R_1 - 2)G_P} \quad (33c)
\]

\[
R_1 + R_2 = \frac{(R + R_2)^2G_P}{1 + (R_S + R_2 + 2)G_P} \quad (33d)
\]

\[
L_1G_P = (L_1 + L_2)G_P = (R_1 + R_2)C \quad (34a)
\]

\[
L_T = \frac{(R - R_1)^2C}{1 + (R_S + R_1 - 2)G_P} \quad (34b)
\]

\[
L_T = \frac{(R + R_2)^2C}{1 + (R_S + R_2 + 2)G_P} \quad (34c)
\]

\[
L_1 = \frac{(R_1 - R)(R_1 + R_S - R)G_P}{1 + (R_S + R_1 - R)G_P} \quad (35)
\]

\[
L_2 = \frac{(R_2 + R)(R_2 + R_S + R)G_P}{1 + (R_2 + R_1 + R)G_P} \quad (36)
\]

\[
L_3 = R^2C_B - L_1L_2/(L_1 + L_2) \quad (37)
\]

2) Transfer function terms:

\[
B_0 = 1 + R_SG_P + R_1[1 + R + R_S + R_2G_P]R + R_2 \quad (38)
\]

\[
D_0 = L_2[R_1 + (R_1 + R_S - R)G_P](R - R_1)2 \quad (39)
\]

\[
D_1C_B = R^2G_P \quad (40)
\]

\[
D_2C_B = R^2CC_B \quad (41)
\]

The resistance constraint is implemented in (33a) after selecting \( R_1; (33b) \) is an equivalent arrangement. Summations (33c) and (33d) are derived from (33a) and (33b) respectively. The summations (33c) and (33d) are used in (34a) to derive (34b) and (34c) such that zero-valued elements (besides \( R \)) are allowed. These forms are useful to
TABLE 1. T-coil example for all configurations. \( R = 50 \Omega, \ C = 4 \text{ pF}, \ R_S = 10 \Omega, \ P_P = 500 \Omega, \) Pole Angle = \(30^\circ \ & 45^\circ \), \( R_1 \) is selected for Fig. 5(d).

| Load | Standard \( C \) | Symmetrical T-Coils | Asymmetrical T-Coils | General \( R_S - R_P \) C |
|------|-----------------|---------------------|---------------------|------------------------|
| Fig. | 2(a) | 2(b) | 2(c) | 2(d) | 3(a) | 3(b) | 3(c) | 3(d) |
| \( R_1 \) | 2.5 \( \Omega \) | 2.5 \( \Omega \) | | | 1.907 \( \Omega \) |
| \( R_2 \) | 2.5 \( \Omega \) | 2.5 \( \Omega \) | | | 5.556 \( \Omega \) | 5.435 \( \Omega \) | 3.100 \( \Omega \) |
| \( R_B \) | 250 \( \Omega \) | 2000 \( \Omega \) | 222.2 \( \Omega \) | | | | |
| \( L_1 \) | 5 nH | 5 nH | 5 nH | 5 nH | 4 nH | 5.409 nH | 4.257 nH | 3.888 nH |
| \( L_2 \) | 5 nH | 5 nH | 5 nH | 5 nH | 6 nH | 5.702 nH | 6.612 nH | 6.127 nH |
| \( \) Gain | 1 | 1 | 0.95 | 0.932 | 1 | 1 | 0.98 | 0.943 |
| \( C_B \) (30°) | 0.333 \( \text{ pF} \) | 0.653 \( \text{ pF} \) | 0.361 \( \text{ pF} \) | 0.685 \( \text{ pF} \) | 0.480 \( \text{ pF} \) | 0.364 \( \text{ pF} \) | 0.504 \( \text{ pF} \) | 0.490 \( \text{ pF} \) |
| \( L_3 \) | (-1.667 nH) | (-0.867 nH) | (-1.597 nH) | (-0.788 nH) | (-1.200 nH) | (-1.867 nH) | (-1.133 nH) | (-1.154 nH) |
| \( BW \) | 2.167 GHz | 1.548 GHz | 2.136 GHz | 1.566 GHz | 1.806 GHz | 2.075 GHz | 1.780 GHz | 1.841 GHz |
| \( BWER \) | 2.723 | 1.945 | 2.684 | 1.968 | 2.269 | 2.607 | 2.237 | 2.314 |
| \( C_B \) (45°) | 0.500 \( \text{ pF} \) | 0.980 \( \text{ pF} \) | 0.552 \( \text{ pF} \) | 1.053 \( \text{ pF} \) | 0.720 \( \text{ pF} \) | 0.556 \( \text{ pF} \) | 0.772 \( \text{ pF} \) | 0.750 \( \text{ pF} \) |
| \( L_3 \) | (-1.250 nH) | (-0.050 nH) | (-1.121 nH) | 0.132 nH | (-0.600 nH) | (-1.387 nH) | (-0.659 nH) | (-0.504 nH) |
| \( BW \) | 2.251 GHz | 1.608 GHz | 2.198 GHz | 1.606 GHz | 1.876 GHz | 2.135 GHz | 1.829 GHz | 1.893 GHz |
| \( BWER \) | 2.828 | 2.020 | 2.763 | 2.019 | 2.357 | 2.683 | 2.298 | 2.378 |

**E. EXAMPLE**

Table 1 shows a set of load values and the designs for all T-coils. Elements at and above the Gain line remain unchanged for each Fig. 4 or Fig. 5 configuration. Blank entries represent omitted elements for simpler structures. Element values (negative values in parenthesis) for stated pole angles are shown below single solid lines along with corresponding parameters include bandwidth (BW) and bandwidth extension ratio (BWER) relative to a \( 1 + RC_s \) single-pole bandwidth.

**V. CONCLUSION**

In this paper, we have presented a survey of Wang algebra and its applications to T-coils. A Thévenin equivalent circuit method simplifies transfer function extraction of constant-Z bridged-T networks. Because of a balanced-bridge process, the Thévenin equivalent circuit produces a final result with common poles and zeros already cancelled. Two sets of general constant-R bridged T-coil equations are derived, and a design process is suggested.

Even greater bandwidth extensions have been reported by trading off the constant-R property and controlling other parameters [39], [40]. These extensions are outside of the scope of this paper.

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**APPENDIX A**

**SYMMETRICAL CONSTANT-Z DERIVATIONS**

The symmetrical network equations (25)-(28) can be derived for the Fig. 4(d) elements with the substitutions in (42) entered in (21):

\[
\begin{align*}
  a &= b = sL_1 + R_1, \\
  c &= sC_B + G_B, \\
  d &= sL_3 + R_S + \frac{1}{(sC + G_P)}, \\
  e &= R.
\end{align*}
\]

This substitution expands to the product terms in (43):

\[
\begin{align*}
  s^3 \left( -2CC_BL_1R^2 + CL_1^2 + 2CL_1L_3 \right) + \\
  s^2 \left( -2CG_BL_1R^2 - 2CG_PL_1R^2 - 2CC_BR^2R + G_PL_1^2 + 2G_PL_1L_3 + 2CL_1R_1 + 2CL_3R_1 + 2CL_1R_3 \right) + \\
  s^1 \left( -2CG_PL_1R^2 - 2CBG_PL_1R^2 - 2CBG_PR^2R_1 - CR^2 + 2G_PL_1R_1 + 2G_PL_3R_1 + CR^2 \right)
\end{align*}
\]
One set of steps is outlined. Commercial or free symbolic mathematical tools can automate the reductions by doing substitutions and cancellations. SageMath (a free, open-source tool created by academia and available in the cloud or for downloading at www.sagemath.org) is used in these steps.

The \( s^3 \) multiplier terms are set to zero to produce (28). After substituting for \( L_3 \), the \( s^2 \) multiplier terms are simplified to yield a preliminary expression for \( R_1 \) that includes \( G_B \) and also to yield (26) and (27) directly from the simplified \( s^1 \) and \( s^0 \) multiplier terms that are set to zero. Finally, (26) is used to simplify the previous \( R_1 \) result to produce (25).

To form the transfer function as a function of \( C_B \), (27) is also used so that \( C_B \) can range from zero to infinity. This provides a complete solution space covering real and complex poles. Values of \( C_B \) (and \( L_3 \)) can be determined for complex poles and selected pole angles, as shown in Table 1.

**APPENDIX B**

**ASYMMETRICAL CONSTANT-Z DERIVATIONS**

The asymmetrical network equations (33)-(37) can be derived for the Fig. 5(d) elements with the substitutions in (44) entered in (20):

\[
a = sL_1 + R_1,
\]
\[
b = sL_2 + R_2,
\]
\[
c = sC_B,
\]
\[
d = sL_3 + R_S + \frac{1}{(sC + GP)},
\]
\[
e = R.  
\]

This substitution expands to the product terms in (45):

\[
s^3 \left( -CC_B L_1 R^2 - CC_B L_2 R^2 + CL_1 L_2 + CL_1 L_3 \right) + CL_2 L_3 \]
\[
s^2 \left( -CB_GP L_1 R^2 - CB_GP L_2 R^2 - CB_R^2 R_1 \right.
\]
\[
- CB_R^2 R_2 + GP L_1 L_2 + GP L_1 L_3 + GP L_2 L_3 + CL_1 R + CL_2 R + CL_1 R_1 + CL_3 R_1 + CL_1 R_2 + CL_1 R_3 + CL_1 R_5 + CL_2 R_5 \right.
\]
\[
+ s^1 \left( -CB_GP R^2 R_1 - CB_GP R^2 R_2 + GP L_3 R \right.
\]
\[
+ GP L_2 R + CR^2 + GP L_2 R_1 + GP L_3 R_1 + CRR_1
\]
\[
+ GP L_1 R_1 + GP L_3 R_2 - CRR_2
\]
\[
+ CR_1 R_2 + GP L_1 R_5 + GP L_2 R_5
\]
\[
+ CR_1 R_5 + CR_2 R_5 + L_1 + L_2 \right)
\]
\[
s^0 \left( -GP R^2 + GP RR_1 - GP RR_2 + GP R_1 R_2 \right.
\]
\[
+ GP R_1 R_5 + GP R_2 R_5 + R_1 + R_2 \right) = 0. \tag{45}
\]

One set of steps is briefly outlined. Some of the later steps are done manually. The \( s^3 \) multiplier terms are set to zero to produce (37). The \( s^0 \) multiplier terms are set to zero to produce (33a) and (33b). After replacing \( L_3 \) with (37), all terms with \( L_3 \) and \( C_B \) elements are eliminated in a modified expansion.

In this expansion, the \( s^2 \) multiplier terms yield (46) after they are set to zero and divided by \( C\):

\[
L_2^2 (R_S + R_1 - R) + 2L_4 L_2 R_S + L_1^2 (R_S + R_2 + R) = 0. \tag{46}
\]

The \( s^1 \) multipliers are simplified with two substitutions. The terms that are multiplied by \( GP \) are identical to the terms in the left side of (46) and are removed. The remaining terms that are multiplied by \( C(L_1 + L_2) \) are replaced because they are identical to the first six terms of the \( s^0 \) multiplier in (45) after these six terms are divided by \(-GP\). The mathematical process produces \((L_1 + L_2)(R_1 + R_2)/GP\), which is set equal to a remaining \((L_1 + L_2)^2\). Equation (34a) follows after dividing both sides by \((L_1 + L_2)\).

A quadratic equation formula is used to solve (46) for either \( L_2 \) in (47) or \( L_1 \) in (48) in terms of the other inductor. Each solution has the same six product terms under the radical as the first six \( s^0 \) multiplier terms divided by \(-GP\) in (45). These terms can be simplified as described above to form \(\sqrt{(R_1 + R_2)/GP}\).

\[
L_2 = L_T - L_1 = L_1 \frac{-R_S + \sqrt{(R_1 + R_2)/GP}}{R_S + R_1 - R}, \tag{47}
\]
\[
L_1 = L_T - L_2 = L_2 \frac{-R_S + \sqrt{(R_1 + R_2)/GP}}{R_S + R_2 + R}. \tag{48}
\]

After doing some mathematical simplification, (35) follows from (47), (33c), and (34b). Equation (36) follows from (48), (33d), and (34c).

Equations (35) and (36) are formed with positive radicals in (47) and (48). The negative radical solutions produce divide by zero cases if \( GP = 0 \).

As done in the symmetrical case, (37) is substituted to express the transfer denominator (38)-(41) as a function of \( C_B \).

**REFERENCES**

[1] K. T. Wang, “On a new method of analysis of electrical networks,” *Memor Nat. Res. Inst. Eng. Academia Sinica*, vol. 2, pp. 1–11, 1934.

[2] S.-L. Ting, “On the general properties of electrical network determinants,” *Chin. J. Phys.*, vol. 1, pp. 18–40, 1935.

[3] C.-T. Tsai, “Short cut methods for expanding the determinants involved in network problems,” *Chin. J. Phys.*, vol. 3, pp. 148–181, 1939.

[4] W.-L. Chow, “On electric networks,” *J. Chin. Math. Soc.*, vol. 2, pp. 321–339, 1940.

[5] Y.-H. Ku, “Résumé of Maxwell’s and Kirchhoff’s rules for network analysis,” *J. Franklin Inst.*, vol. 253, no. 3, pp. 211–224, 1952.

[6] S. Bellert, “Topological analysis and synthesis of linear systems,” *J. Franklin Inst.*, vol. 274, no. 6, pp. 425–443, 1962.

[7] B. Ross, “Generalization of T-coil equations,” in *Proc. 3rd Electrotech. Comput. Sci. Conf.*, Portorož, Slovenia, 1994, pp. 39–43.

[8] B. Ross, “Wang algebra and interconnects,” *Asian IBIS Summit*, Beijing, China, 2007. [Online]. Available: https://ibis.org/summits/sep07a/ross.pdf

[9] W.-K. Chen, *Graph Theory and Its Engineering Applications*. Singapore: World Sci., 1997.
[10] R. J. Duffin, “An analysis of the Wang algebra of networks,” Trans. Amer. Math. Soc., vol. 93, no. 1, pp. 114–131, 1959.
[11] R. Duffin and T. Morley, “Wang algebra and matroids,” IEEE Trans. Circuits Syst., vol. 25, no. 9, pp. 755–762, Sep. 1978.
[12] R. J. Duffin, “Some problems of mathematics and science,” Bull. Amer. Math. Soc., vol. 80, no. 6, pp. 1053–1070, 1974.
[13] J.-H. Guo, “K. T. Wang: First Chinese scholar to publish a mathematical paper in international journals.” Institute for the History of Natural Sciences, Chinese Academy of Sciences. 2015. [Online]. Available: http://www.ihns.cas.cn/kxcb_new/kpzw_new/201602/20160229_4538251.html
[14] J.-H. Guo, “From the nine chapters to ancient Chinese mathematics: An interpretation of Ki-Tung Wang’s letter to Yan Li,” J. Gaungxi Univ. Nationalities, vol. 21, no. 1, pp. 14–18, 2015.
[15] K. T. Wang, “The differentiation of quaternion functions,” Proc. Royal Irish Acad., vol. 29, pp. 73–80, Jul. 1912.
[16] J.-H. Guo, “K. T. Wang and the differentiation of Quaternion functions,” China Historical Mater. Sci. Technol., vol. 23, no. 1, pp. 65–70, 2002.
[17] J.-H. Guo, “Ki-Tung Wang’s new method for the analysis of electric network and its scientific influence,” China Historical Mater. Sci. Technol., vol. 24, no. 4, pp. 312–319, 2002.
[18] J.-H. Guo, “An inquiry of mathematics teaching in the Imperial Tungwen college,” Stud. Hist. Natural Sci., vol. 22, pp. 47–60, 2003.
[19] K. T. Wang, Comparative Study of Buddhism and Sciences. Shanghai, China: Shanghai Buddhism Press, 1933. [Online]. Available: http://www.nnychd.com/jsrw/wjt/8000.html
[20] T. H. Lee, Planar Microwave Engineering: A Practical Guide to Theory, Measurements, and Circuits. New York, NY, USA: Cambridge Univ. Press, 2004.
[21] J. Williams, Ed. Boston, MA, USA: Butterworth-Heinemann, 1991, vol. 38, no. 12, pp. 1781–1793, Aug. 2008.
[22] M. S. Galal and B. Razavi, “Bandwidth extension technique for CMOS technology,” IEEE J. Solid-State Circuits, vol. 38, no. 12, pp. 2334–2340, Dec. 2003.
[23] E. Pillai and J. Weiss, “Novel T-coil structure and implementation in a 6.4-Gbs CMOS receiver to meet return loss specifications,” in Proc. Electron. Components Tech. Conf., 2007, pp. 147–153.
[24] E. R. Pillai, L. L. Hsu, W. Sauter, and D. W. Storaska, “On-pad broadbanding matching network,” U.S. Patent 7 265 433 B2, Sep. 2007.
[25] B. Razavi, “The bridged T-coil [a circuit for all seasons],” IEEE Solid-State Circuits Mag., vol. 7, no. 4, pp. 9–13, Dec. 2015.
[26] P. Rako, “What’s all this T-coil stuff, anyhow?” Electronic Design. Apr. 2019. [Online]. Available: https://www.electrodesign.com/technologies/analog/article/21807815/whats-all-this-t-coil-stuff-anyhow
[27] G.-Z. Hu, Applied Modern Algebra. Beijing, China: Tsinghua Univ. Press, 1993.
[28] K. Shen, J. N. Crossley, A. W.-C. Lun, and H. Liu, The Nine Chapters on the Mathematical Art: Companion and Commentary. Oxford, U.K.: Oxford Univ. Press, 1999.
[29] S. Shekhar, J. Walling, and D. Allstot, “Bandwidth extension techniques for CMOS amplifiers,” IEEE J. Solid-State Circuits, vol. 41, no. 11, pp. 2424–2439, Nov. 2006.
[30] J. S. Walling, S. Shekhar, and D. J. Allstot, “Wideband CMOS amplifier design: Time-domain considerations,” IEEE Trans. Circuits Syst. I, Reg. Papers, vol. 55, no. 7, pp. 1781–1793, Aug. 2008.