PAST TTTC EVENTS

The 23rd IEEE International Symposium on Defect and Fault Tolerance in VLSI and Nanotechnology Systems (DFT’17)
23-25 October, 2017
Cambridge, UK
http://www.dfts.org/

DFT is an annual Symposium providing an open forum for presentations in the field of defect and fault tolerance in VLSI and nanotechnology systems inclusive of emerging technologies. One of the unique features of this symposium is to combine new academic research with state-of-the-art industrial data, necessary ingredients for significant advances in this field. All aspects of design, manufacturing, test, reliability, and availability that are affected by defects during manufacturing and by faults during system operation are of interest.

The IEEE International Test Conference (ITC 2017)
31 October - 2 November, 2017
Fort Worth, TX, USA
http://www.itctestweek.org/about-itc/

International Test Conference is the world’s premier venue dedicated to the electronic test of devices, boards and systems—covering the complete cycle from design verification, design-for-test, design-for-manufacturing, silicon debug, manufacturing test, system test, diagnosis, reliability and failure analysis, and back to process and design improvement. At ITC, design, test, and yield professionals can confront challenges faced by the industry, and learn how these challenges are being addressed by the combined efforts of academia, design tool and equipment suppliers, designers, and test engineers. ITC, the cornerstone of the Test Week event, offers a wide variety of technical activities targeted at test and design theoreticians and practitioners, including: formal paper sessions, tutorials, panel sessions, case studies, invited lectures, commercial exhibits and presentations, and a host of ancillary professional meetings.

The 26th IEEE Asian Test Symposium (ATS 2017)
27–30 November, 2017
Taipei, Taiwan
http://ares.ee.ncu.edu.tw/ats17/index.php

The Asian Test Symposium provides an international forum for engineers and researchers from all countries of the world, not just from Asia, to present and discuss various aspects of system, board and device testing with design, manufacturing and field considerations in mind.

UPCOMING TTTC EVENTS

The 21st Design, Automation, and Test in Europe (DATE) Conference
19–23 March, 2018
Dresden, Germany
https://www.date-conference.com/

The 21st DATE conference and exhibition is the main European event bringing together designers and design automation users, researchers and vendors, as well as specialists in the hardware and software design, test and manufacturing of electronic circuits and systems. DATE puts strong emphasis on both technology and systems, covering ICs/SoCs, reconfigurable hardware and embedded systems, and embedded software. The five-day event consists of a conference with plenary invited papers, regular papers, panels, hot-topic sessions, tutorials and workshops, two special focus days and a track for executives. The scientific conference is complemented by a
commercial exhibition showing the state-of-the-art in design and test tools, methodologies, IP and design services, reconfigurable and other hardware platforms, embedded software, and (industrial) design experiences from different application domains, e.g. automotive, wireless, telecom and multimedia applications. The organisation of user group meetings, fringe meetings, a university booth, a PhD forum, vendor presentations and social events offers a wide variety of extra opportunities to meet and exchange information on relevant issues for the design and test community. Special space will also be allocated for EU-funded projects to show their results.

**NEWSLETTER EDITOR’S INVITATION**

I would appreciate input and suggestions about the newsletter from the test community. Please forward your ideas, contributions, and information on awards, conferences, and workshops to Theocharis (Theo) Theocharides, Department of Electrical and Computer Engineering, University of Cyprus, 75 Kallipoleos Avenue, PO Boxes 20,537, Nicosia, 1678, CYPRUS; ttheocharides@ucy.ac.cy.

Theo Theocharides.
Editor, TTTC Newsletter

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CONTRIBUTIONS TO THIS NEWSLETTER: Send contributions to Theocharis (Theo) Theocharides, Department of Electrical and Computer Engineering, University of Cyprus, 75 Kallipoleos Avenue, PO Boxes 20,537, Nicosia, 1678, CYPRUS; ttheocharides@ucy.ac.cy. For more information, see the TTTC Web page: [http://tab.computer.org/tttc](http://tab.computer.org/tttc).