Behavioural types for non-uniform memory accesses

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Concurrent programs executing on NUMA architectures consist of concurrent entities (e.g. threads, actors) and data placed on different nodes. Execution of these concurrent entities often reads or updates states from remote nodes. The performance of such systems depends on the extent to which the concurrent entities can be executing in parallel, and on the amount of the remote reads and writes.

We consider an actor-based object oriented language, and propose a type system which expresses the topology of the program (the placement of the actors and data on the nodes), and an effect system which characterises remote reads and writes (in terms of which node reads/writes from which other nodes). We use a variant of ownership types for the topology, and a combination of behavioural and ownership types for the effect system.

1 Introduction

A prevalent paradigm in high performance machines is NUMA (non uniform memory access) systems, e.g., the AMD Bulldozer server[1]. NUMA systems have many nodes which contain processors and memory; Figure 1 shows the common NUMA structure. The nodes are connected with the other nodes through a system bus that allows processes running on a specific node to access the memory of the other nodes.

Memory access is either local, i.e. accessing memory in the local node, or remote, i.e. accessing memory of remote nodes. Remote accesses require requests to the system bus, and are thus more expensive than local accesses. Moreover, different remote accesses do not necessarily have the same cost (the time to obtain/write data in memory). Therefore, to characterize the communication (read/write) costs of a concurrent program, we need to know its topology (the placement of the actors and data on the nodes), and a characterisation of the reads and writes across nodes.

In this work we consider a concurrent language based on actors (or active objects) and objects[5], which we call $L_{numa}$, a language where, for the sake of simplicity, mutually recursive (synchronous and asynchronous) method invocations with communication are assumed to be not allowed and all the active objects must be created in the main class.

We develop a variant of ownership types[6] to express the location of actors and of data. In particular, we propose two levels of abstraction: classes have ownership (location) parameters, the main program defines the abstract locations and creates objects in these abstract locations; and at runtime the abstract locations are mapped to nodes (cf. Appendix C). We also propose a combination of behavioural and
ownership types to characterise the interactions (reads, writes and messages sent) among objects located in different nodes.

Ownership types [4, 6, 12] were first introduced to statically describe the heap topology. Here we introduce ownership-like annotations to describe the system topology, that is, its nodes and where threads are running and data is allocated. Behavioural types [2, 8, 9, 14, 18] are usually used to describe and statically, or dynamically, verify patterns of interaction between processes/threads/participants of concurrent and parallel computations. Here we present a type system that allows the programmer to specify the interactions among objects located in different nodes, and therefore we abstract the communication made through the system bus.

Outline. This paper is organised as follows: Section 2 introduces the syntax of $\mathcal{L}_{\text{numa}}$, Section 3 gives the operational semantics, Section 4 presents the typing rules, and Section 4 shows properties of $\mathcal{L}_{\text{numa}}$, and finally Section 6 concludes. Several definitions are given in the appendix.

2 Syntax

Figure 2 presents the syntax of $\mathcal{L}_{\text{numa}}$. A program consists of a set of class declarations representing actors and passive objects. The use of the keyword active in a class declaration indicates that the class represents actors. Passive objects are similar to ordinary Java objects while actors have all the properties of passive objects, and in addition also have their own execution thread and may send messages to other actors. As in actor-based languages, messages are stored in private queues. A more detailed definition can be found in [5].

$$
\begin{align*}
P & \in \text{Program} ::= \text{Cd}^* \\
\text{Cd} & \in \text{ClassDecl} ::= [\text{active}] \text{ class } C(\langle p \rangle^+) \text{ Fd Md} \\
\text{Fd} & \in \text{FieldDecl} ::= f : T \\
\text{Md} & \in \text{MethodDecl} ::= \text{def } m(x : T) : T \text{ as } b \{e\} \\
\text{ot} & \in \text{OwnershipType} ::= C(\langle l \rangle^+) \\
T & \in \text{Type} ::= \text{bool} | \text{nil} | \text{int} | \text{ot} \\
l & \in \text{Location} ::= p | L \\
\text{val} & \in \text{Value} ::= \text{null} | \text{true} | \text{false} \\
\text{var} & \in \text{Variable} ::= x | \text{this} \\
\end{align*}
$$

Figure 2: Syntax of classes and (behavioural) types. The boxed constructs are not user syntax. In the class declaration $\text{Cd}$ the use of $[\ ]$ means that the keyword active is optional.

Each class, active or passive, is annotated with a set of location parameters $p_1, \ldots, p_n$ where $p_1$ represents the place where the instance of the class is allocated and $p_2, \ldots, p_n$ locations that can be used in the types of the rest of the class. The location parameters of the main class, $L_1, \ldots, L_n$, are abstractions of the concrete nodes, and at runtime will be mapped to concrete node identifiers.

A class declaration might have field and method declarations. A field declaration consists of a field identifier and a type; a method declaration consists of a method identifier, one parameter (variable and type), return type, behavioural type and an expression (method body). $\mathcal{L}_{\text{numa}}$ has the types bool, nil, and an ownership type $C(\langle l_1, \ldots, l_n \rangle)$ which represents objects located in $l_1$ that may contain references to...
objects in locations $l_2, \ldots, l_n$. The syntax of expressions is similar to other OO programming languages; note only the asynchronous method call (message sending), $e!m(e)$.

The most interesting part of the syntax is our treatment of behavioural types. We have basic operations, $\pi$, which are reading from a remote node ($rd(l, l)$), writing to a remote node ($wrt(l, l)$), and message sending ($msg(l, l, m)$)—this has to be reflected in the behaviour, as it adds messages to queues in remote memory. For all of them the first location is where the expression is being executed and the second is the location where a read/write is made or a message sent. We also have types to describe conditional expressions, $\{b \text{ or } b\}$, (the two branches in the if-then-else expression imply two branches in the type), and for-loops, $\text{Loop}(n: b)$. A behavioural type, $b$, may be empty, $\epsilon$, meaning that there is no “communication” across different nodes, the sequence of operations, $bop.b$, and two types in parallel, $[b, b]$, introduced by message sendings.

### 3 Semantics

We now describe the dynamic semantics of $\mathcal{L}_{\text{numa}}$. Nodes, $N$, defined in Figure 3, aim to reflect NUMA nodes. Namely, a node in our formalism has an identifier, a heap with all the data allocated in it, and several execution threads $\text{EThread}$. An execution thread belongs to an actor, and has a stack and an expression being executed. A heap is a mapping from addresses to (passive and active) objects. An

\begin{align*}
N \in \text{Node} &= \text{NodeId} \times \text{Heap} \times \text{EThread} \\
\mathcal{T} \in \text{EThread} &= \text{Stack} \times \text{Expr} \\
h \in \text{Heap} &= \text{Addr} \rightarrow \text{Object} \\
\sigma \in \text{Stack} &= \text{Addr} \times \text{Frame} \\
\varphi \in \text{Frame} &= \text{var} \rightarrow \text{value} \\
Q \in \text{Queue} &= \bullet \mid \emptyset \mid m(v) :: Q \\
\mathcal{L} \in \text{LocsMap} &= \text{LocId} \rightarrow \text{NodeId} \\
\kappa \in \text{NodeId} &= \mathbb{N} \\
l \in \text{Location} &= \text{as before} \mid \kappa
\end{align*}

Figure 3: Dynamic Entities. We assume the existence of a map $\mathcal{L}$ that maps abstract locations (declared by the programmer in the main class) to NUMA node identifiers.

An object consists of a class identifier $C$, a sequence of node identifiers representing the actual location parameters, a mapping from field identifiers to their values, and a message queue, where the queue of a passive object is $\bullet$. An address, $\alpha$, consists of a node identifier, $\kappa \in \text{NodeId}$, and an offset, $n \in \mathbb{N}$.

In our system, a configuration $\hat{N}$ can be reduced to another configuration $\hat{N'}$ either without any communication or implying a remote access from one of the nodes to another node. In the first case, the rule $[\text{GsExec1}]$ should be applied, where only one node is involved in the reduction. In the second case the rule $[\text{GsExec2}]$ should be used, where two nodes are involved in the reduction, as shown in Figure 4.

In the same way, expression reduction may result in accessing remote memory or not; therefore we divide the operational semantics rules as follows:

1. Expressions that do not access memory or send messages. These are defined in Figure 5.
Figure 4: Global semantics

2. Expressions that result in accesses to memory. These are defined in Figure 5 and are further divided into:

(a) The access happens locally—only one node required.
(b) The access happens remotely—two different nodes required.

Figure 5 shows shows the rules for the point 1, where no accesses to memory, either in the same node or not, are made. Each rule takes a node identifier, its heap, a stack and an expression, and reduces to

\[
\text{[SiFTrue]} \quad \forall \kappa, h, \sigma, \text{if true then } e_1 \text{ else } e_2 \xrightarrow{\kappa} h, \sigma, e_1
\]

\[
\text{[SiFFalse]} \quad \forall \kappa, h, \sigma, \text{if false then } e_1 \text{ else } e_2 \xrightarrow{\kappa} h, \sigma, e_2
\]

\[
\text{[SLET]} \quad x \text{ fresh in } \phi \quad \phi' = \phi[x \rightarrow v] \xrightarrow{\kappa, h, \sigma, \phi, \text{let } x = v \text{ in } e} h, \sigma, \phi', e
\]

\[
\text{[SRET]} \quad \forall \kappa, h, \sigma, \phi, \text{return } v \xrightarrow{\kappa, h, \sigma, \phi, \text{return } v} h, \sigma, v
\]

\[
\text{[SVAR]} \quad \phi(x) = v \xrightarrow{\kappa, h, \sigma, \phi, x \rightarrow h, \sigma, \phi, v}
\]

\[
\text{[SFOR]} \quad x \text{ fresh in } \phi \quad \forall i \in (n_1 + 1) \ldots n_2 \text{ do } e \xrightarrow{\kappa, h, \sigma, \phi, \text{for } i \in n_1 \ldots n_2 \text{ do } e} h, \sigma, \phi[i \rightarrow n_1], e'
\]

\[
\text{[SCALL]} \quad \text{owners}(h, \alpha) = C(\overline{K}) \quad \phi = \alpha \cdot (\text{this } \mapsto \alpha, x \mapsto v) \xrightarrow{\kappa, h, \sigma, \alpha.m(v) \mapsto h, \sigma, \phi, \text{return } M(C, m)[K]} h, \sigma, \phi, v
\]

\[
\text{[SRECEIVE]} \quad \alpha \downarrow = \kappa \quad h(\alpha) = (C, \overline{K}, m(v) :: Q) \quad e = M(C, m)[\overline{K}] \xrightarrow{\kappa, h, \alpha \cdot 0, \text{null } \mapsto h[\alpha \mapsto Q], \alpha \cdot (\text{this } \mapsto \alpha, x \mapsto v), \text{return } e}
\]

\[
\text{[SCONTEXTNPE]} \quad \forall \kappa, h, \sigma, E[NPE] \xrightarrow{\kappa} h, \sigma, NPE
\]

\[
\text{[SNPE]} \quad \forall \kappa, h, \sigma, e_{npe} \xrightarrow{\kappa} h, \sigma, NPE
\]

where \( e_{npe} \) can be null.f, null.f = e, null.m(e), null!m(e), null[i], null[i] = e'

Figure 5: Semantic rules for expressions that do not perform remote operations. Null-pointer exceptions included.
a new heap, a new stack and a new expression. They have the form \( \kappa,h,\sigma,e \xrightarrow{\pi} h',\sigma',e' \). These rules show reduction without any communication among different nodes (they show reduction through \( \varepsilon \)). The intuition behind them is standard and similar can be found in the literature. Note only the rule for the message receiving, \([\text{SRECEIVE}]\), which takes an empty stack and a null expression, meaning that the expression of the thread being executed is fully reduced, and returns a new frame and expression after taking the next message in the queue to be processed. The expression returned is the body of the method asynchronously invoked, as there is a new frame with the values passed as arguments.

Figure 6 shows the semantic rules for the point 2. The rules on the left belong to 2(a); they have the same form of the rules introduced in Figure 5. The rules on the right belong to 2(b); they take two node identifiers, their heaps, a stack and an expression, and reduce to two new heaps, a new stack and a new expression. They have the form \( \kappa_1, h_1, \sigma, e \parallel \kappa_2, h_2 \xrightarrow{\pi} h'_1, \sigma', e' \parallel h'_2 \). In both cases they reduce through an operation described by \( \pi \)—the remote operation made or empty, \( \varepsilon \) (in the case of the absence of a remote operation). For instance, message sending in rule \([\text{SMsG}]\) adds a message to the queue of an

\[
\begin{align*}
\text{SMsG} & : \quad h' = h[(\kappa, n) \cdot m(v)] \\
\kappa, h, \sigma, \langle \kappa, n \rangle!m(v) & \xrightarrow{\varepsilon} h', \sigma, \text{null}
\end{align*}
\]

\[
\begin{align*}
\text{SMsG} & : \quad h' = h_2[(\kappa_1, n) \cdot m(v)] \\
\kappa_1, h_1, \sigma, \langle \kappa_2, n \rangle!m(v) & \parallel h_2 \xrightarrow{\varepsilon} h_1, \sigma, \text{null} \parallel h_2'
\end{align*}
\]

\[
\begin{align*}
\text{SFReadL} & : \quad \pi = \text{rd}(\kappa, \kappa_2) \quad v = h_2[(\kappa_2, n)](f) \\
\kappa_1, h_1, \sigma, \langle \kappa_2, n \rangle, f & \xrightarrow{\varepsilon} h_1, \sigma, v \parallel h_2
\end{align*}
\]

\[
\begin{align*}
\text{SFWriteL} & : \quad \pi = \text{wrt}(\kappa, \kappa_2) \quad h' = h_2[(\kappa_2, n), f \mapsto v] \\
\kappa_1, h_1, \sigma, \langle \kappa_2, n \rangle, f & = v \parallel h_2 \xrightarrow{\varepsilon} h_1, \sigma, v \parallel h_2'
\end{align*}
\]

\[
\begin{align*}
\text{SNewL} & : \quad \kappa = \mathcal{L}(L_1) \quad \langle \kappa, n \rangle \notin \text{dom}(h) \\
& \quad h' = h[(\kappa, n) \mapsto \text{initObj}(C(L))] \\
\kappa, h, \sigma, \text{new } C(L) & \xrightarrow{\varepsilon} h', \sigma, \langle \kappa, n \rangle
\end{align*}
\]

\[
\begin{align*}
\text{SNewR} & : \quad \kappa_2 = \mathcal{L}(L_1) \quad \langle \kappa_2, n \rangle \notin \text{dom}(h_2) \quad \pi = \text{wrt}(\kappa_1, \kappa_2) \\
& \quad h'_2 = h_2[(\kappa_2, n) \mapsto \text{initObj}(C(L))] \\
\kappa_1, h_1, \sigma, \text{new } C(L) & \parallel h_2 \xrightarrow{\varepsilon} h_1, \sigma, \langle \kappa_2, n \rangle \parallel h'_2
\end{align*}
\]

\[
\begin{align*}
\text{SContextL} & : \quad \kappa, h, \sigma, e \xrightarrow{\pi} h', \sigma', e' \\
\kappa, h, \sigma, E[e] & \xrightarrow{\varepsilon} h', \sigma', E[e']
\end{align*}
\]

\[
\begin{align*}
\text{SContextR} & : \quad \kappa_1, h_1, \sigma, e \parallel \kappa_2, h_2 \xrightarrow{\pi} h'_1, \sigma', e' \parallel h'_2 \\
\kappa_1, h_1, \sigma, E[e] & \parallel \kappa_2, h_2 \xrightarrow{\varepsilon} h'_1, \sigma', E[e'] \parallel h'_2
\end{align*}
\]

Figure 6: Set of semantic rules described in 2. The left rules show the reduction of expressions that execute locally (a) and the right rules, expressions that interact with remote objects (b).
4 Type Checking

Figure 7 shows the typing rules of $\mathcal{L}_{numa}$. They have the form $\Gamma \vdash e : T, b$ where an expression $e$ is verified against a sequence of typing contexts $\Gamma$ resulting in a type $T$ and an effect $b$. A typing context is a mapping from variables and addresses to types:

$$\Gamma \in \text{TypingContext} = (\text{var} \cup \text{Addr}) \rightarrow \text{Type}$$

The effect $b$ describes the behaviour of $e$, that is, the memory accesses and messages sent to remote locations. Effects are concatenated via the function $\circ$ as defined below.

$$e \circ b = b \quad \text{bop}.b_1 \circ b_2 = \text{bop}.(b_1 \circ b_2) \quad [b_1, b_2] \circ b_3 = [b_1 \circ b_3, b_2]$$

The type $T$ associated to an expression is found in a standard way: similar can be found in [4].

\[
\begin{array}{c}
\text{[T-VAR/ADDR]} \\
\Gamma, \Gamma' \vdash \text{var} \triangleright \Gamma'(\text{var}), e \\
\hline
\text{[T-TRUE/FALSE]} \\
\Gamma, \Gamma' \vdash \text{true} \triangleright \text{bool}, e \\
\hline
\text{[T-SKIP/NULL]} \\
\Gamma, \Gamma' \vdash \text{skip} \triangleright \text{nil}, e \\
\Gamma, \Gamma' \vdash \text{null} \triangleright \text{nil}, e \\
\hline
\text{[T-LET]} \\
\Gamma, \Gamma' \vdash e_1 \triangleright T_1, b_1 \quad x \notin \text{dom}(\Gamma) \\
\hline
\text{[T-COND]} \\
\Gamma \vdash \text{if } e_1 \text{ then } e_2 \text{ else } e_3 \triangleright T, b_1 \circ \{ b_2 \text{ or } b_3 \} \\
k > j \quad \Gamma = \Gamma', \Gamma'[i \mapsto \text{int}] \vdash e \triangleright T, b \\
\hline
\text{[T-RET]} \\
\Gamma \vdash e \triangleright T, b \\
\Gamma, \Gamma' \vdash \text{return } e \triangleright T, b \\
\hline
\text{isActive}(\text{this}) \implies \text{isMain}(\Gamma, \text{this}) \\
\text{[T-NEWO]} \\
\Gamma \vdash \text{new } o \triangleright o \triangleright \text{wrt}(\ell(\Gamma), l_1) \\
\hline
\text{[T-FWRITE]} \\
\Gamma \vdash e \cdot f = e' \triangleright T, b_1 \circ b_2 \triangleright \text{wrt}(\ell(\Gamma), l_1) \\
\hline
\text{[T-FREAD]} \\
\Gamma \vdash e \cdot f = e' \triangleright T, b_1 \circ b_2 \triangleright \text{wrt}(\ell(\Gamma), l_1) \\
\hline
\text{[T-CALL]} \\
\ell(\Gamma) = l_1 \quad \mathcal{M}(C, m)[l_1] = (T, T', e_3, b_3) \\
\hline
\text{[T-MESSAGE]} \\
\Gamma \vdash e_1 \triangleright C[l_1], b_1 \\
\Gamma \vdash e_2 \triangleright T', b_2 \\
\ell(\Gamma) = l_0 \quad \mathcal{M}(C, m)[l_0] = (\text{nil}, T', e_3, b) \\
\hline
\text{[T-MESSAGE]} \\
\Gamma \vdash e_1 \cdot m(e_2) \triangleright \text{nil}, b_1 \circ b_2 \circ \text{msg}(l_0, l_1, m).[\varepsilon, b] \\
\end{array}
\]

Figure 7: Typing rules

therefore we focus only in the behaviour produced. The rules for variables and values, [T-VAR/ADDR], [T-TRUE/FALSE], [T-SKIP/NULL] result in empty effects, $\varepsilon$, because they do not represent any communication. The typing rule [T-LET] results in the concatenation of the behaviour of both expressions. The resulting behaviour of the rule [T-COND] is the behaviour of the predicate concatenated with a choice type which describes the behaviour of both branches. The rule [T-FOR] returns a loop type $\text{Loop}(n : b)$, where $n$ is the number of iterations of the loop and $b$ is the behavioural type of its body.

The behaviour of the creation of an object, with [T-NEWO], is a write behaviour, from the location of this to the location of the new object, as new data is written to memory. The predicate $\text{isActive}(C)$ is true if the class of the object being created in annotated as active and the predicate $\text{isMain}(\Gamma, \text{this})$ is true if the class being verified is the main class. The field write is also represented by the write behaviour,
given that it changes data already in memory. Typing the expression \( e.f = e' \) with the rule \([\text{T-FWRITE}]\) returns the concatenation of the behaviour of \( e \), the behaviour of \( e' \) and the write from the location of this to the location of the object changed. Following the same idea, the field read, \( e.f \), is represented by the read behaviour and therefore the rule \([\text{T-FREAD}]\) gives the concatenation of the behaviour of \( e \) with a read type from the location of this and to the location of the object read. The typing rule, \([\text{T-CALL}]\), describes synchronous method invocation which is only allowed if the receiver is in the same location as the this object. Its behaviour is the behaviour of the receiver concatenated with the behaviour of the expression passed as argument and the behavioural type annotated in the body of the invoked method. The typing rule for the message send, \([\text{T-MESSAGE}]\), is similar. However, it is possible to send a message to a different location and moreover it introduces parallelism in our types: the receiving of the message should be executed in parallel with the continuation of the message sending—the resulting behaviour has the continuation type, which in this case is \( \varepsilon \), in parallel with the expression to be executed due the message received.

5 The global behaviour

We define a global behaviour, \( \Sigma \), as a sequence of behavioural types

\[
\Sigma \in \text{Behaviour}
\]

The behaviour of a node \( N \) describes the remote reads, writes and message sends to be executed by the node; it is obtained from the behaviour of the execution threads and message queues of all actors in that node. The global behaviour of a runtime configuration, \( \overline{N} \), describes the remote reads, writes and message sends to be executed by all nodes; it is the parallel combination of the behaviours of each the nodes \( N_i \). Both definitions, the behaviour of a node and the global behaviour of a configuration, are below.

Definition 1 (The global behaviour).

1. \( N_1, \ldots, N_n \uparrow \overline{b}_1, \ldots, \overline{b}_n \iff \forall i \in 1..n : N_i \uparrow \overline{b}_i \)
2. \( \kappa, h, (\sigma_1, e_1), \ldots, (\sigma_n, e_n) \uparrow b_1, \ldots, b_n \iff \forall i \in 1..n : h, \sigma_i, e_i \uparrow b_i \)
3. \( h, \sigma, e \uparrow \text{filter}(b \circ b_1 \circ \ldots \circ b_n) \iff \exists T : h, \sigma \vdash e \triangleright T, b \land (h(\sigma_{\downarrow 1}) = (C, \kappa^+, \ldots, m_1(v_1) :: \ldots :: m_n(v_n) :: \emptyset)) \land \forall j \in 1..n : \exists T : h, (\text{this} \mapsto \sigma_{\downarrow 1}, x \mapsto v_j) \vdash M(C, m_j)[\kappa^+] \triangleright T, b_j) \)

Using this notion of global behaviour, we implicitly assume a well-formed program and we state soundness of our typing, which says that if a well-formed configuration, \( \overline{N} \), with a global behaviour \( \Sigma \), reduces to another configuration \( \overline{N}' \) through a communication step \( \pi \) then the resulting configuration \( \overline{N}' \) will have behaviour \( \Sigma' \) which is a reduction of \( \Sigma \) through \( \pi \).

Theorem 1. If \( \vdash \overline{N} \land \overline{N} \uparrow \Sigma \land \overline{N} \triangleright \overline{N}' \text{ then } \exists \Sigma' : \overline{N}' \uparrow \Sigma' \land \Sigma \sqsubseteq_\pi \Sigma' \)

The definitions of well-formed configuration (including well-formed heap and well-formed stack) and (global) behaviour reduction are defined below:
Definition 2 (Well-formed (1) configuration, (2) node, (3) heap, (4) stack and (5) stack frame).

(1) $\vdash \overline{N}$ iff $\forall i, j : N_i \downarrow_1 = N_j \downarrow_1 \implies i = j \land \forall N' : \overline{N} \vdash N'$

(2) $\vdash \kappa, h, (\langle \sigma_1, e_1 \rangle, \ldots, \langle \sigma_n, e_n \rangle)$ iff

$\forall \alpha \in \text{dom}(h) : \alpha \downarrow_1 = \kappa \land h(\alpha) \downarrow_2 = \kappa, \land \overline{N} \vdash h$

$\land \forall i \in \{1..n\} : \text{heaps}(\overline{N}) \vdash \sigma_i \land \exists T_i, b_i : h, \sigma_i \vdash e_i \triangleright T_i, b_i$

(3) $\vdash h$ iff $\forall \alpha \in \text{dom}(h) : \text{heaps}(\overline{N}) \vdash \alpha : \text{owners}(h, \alpha)$

(4) $h \vdash \alpha : \varphi_1, \ldots, \varphi_n$ iff $\forall i \in \{1..n\} : h \vdash \varphi_i$

(5) $h \vdash (\text{this} \mapsto \alpha, x_1 \mapsto v_1, \ldots, x_n \mapsto v_n)$ iff $\{\alpha, v_1, \ldots, v_n\} \subseteq \{\text{true, false}\} \cup \text{dom}(h)$

Definition 3 (Global behaviour reduction).

$$\Sigma \subseteq \pi \Sigma' \iff \Sigma = \overline{b_1}, b, \overline{b_2} \land \Sigma' = \overline{b_1}', b', \overline{b_2}' \land \exists \pi \ b' \land \ (b = [b_1, b_2] \implies b' = b_1 \land \exists b_j \in \Sigma, b_j' \in \Sigma' : b_j' = b_j \circ b_2)$$

Definition 4 (Behaviour reduction).

$$\Sigma \subseteq \pi \Sigma' \iff \Sigma = \overline{b_1}, b, \overline{b_2} \land \Sigma' = \overline{b_1}', b', \overline{b_2}' \land \exists \pi \ b' \land \ (b = [b_1, b_2] \implies b' = b_1 \land \exists b_j \in \Sigma, b_j' \in \Sigma' : b_j' = b_j \circ b_2)$$

Theorem 1 is a corollary of Lemmas 1 and 2.

Lemma 1. If $\overline{N} \vdash h \land h \vdash \sigma \land \kappa, h, \sigma, e \overset{\tau}{\not\rightarrow} h', \sigma', e' \land h, \sigma \vdash e \triangleright T, b \land \neg(\sigma \downarrow_2 = \emptyset \land e = \text{null})$ then $\exists b' : h', \sigma' \vdash e' \triangleright T, b' \land \text{filter}(b) \subseteq \pi \text{filter}(b')$

Lemma 2. If $\overline{N} \vdash h_1 \land \overline{N} \vdash h_2 \land h_1 \cup h_2 \vdash \sigma \land \kappa_1, h_1, \sigma, e \| \kappa_2, h_2 \overset{\tau}{\not\rightarrow} h_1', \sigma', e' \| h_2' \land h_1 \cup h_2, \sigma \vdash e \triangleright T, b$ then $\exists b' : h_1', h_2', \sigma' \vdash e' \triangleright T, b' \land \text{filter}(b) \subseteq \pi \text{filter}(b')$

6 Final Remarks

Related Work. To the best of our knowledge there is no integration of behavioural types in the active/passive object paradigm, or any formalism that combines behavioural types with ownership types to describe memory accesses; however there are already a few programming languages that use session (behavioural) types in actor-based languages, namely: the integration of session types in a Featherweight Erlang introduced by Mostrous and Vasconcelos [10]; an implementation of multiparty session types in an actor library written in Python presented by Neykova and Yoshida [11]; and the behavioural type system for an actor calculus, proposed by Crafa [7].

With respect to programming languages with the notion of locations and proximity among processes and data, Rinard presented an extension of the programming language Jade (an implicitly parallel programming language designed to explore task-level concurrency [15]) that allows the execution of tasks close to the data that they will use [15]. The language has constructs to describe how the processes access to the data; this information is analysed and used to improve the communication. Given that it is more expensive to access data remotely than locally, the author introduces a locality optimization algorithm that schedules the execution of tasks on places (processors) close to the data. The programming language X10 [17], developed by IBM, also features a notion of locality/places. In X10, each object can be either
assigned to a place or distributed among different places. Chandra et al. [3] presented a new dependent type system for X10 that captures information about the locality of the resources in a partitioned heap for distributed data structures, called place types. It provides information not only about whether a reference is local or remote, but also if two remote references point to resources in the same place or not. Therefore, the compiler may use this information to decrease the runtime overhead.

**Conclusion.** This paper presents the formalisation of a small object-oriented programming language that amalgamates behavioural types with ownership types in order to describe remote memory accesses in NUMA systems. Ownership types play a role in the representation of the topology and behavioural types in the definition of reads, writes and messages sent to remote locations. This sequence of memory access operations are annotated in the method declarations as the ownership/location parameters are annotated in class declarations. This formalisation is just the first step towards a programming language that optimises performance by moving objects to nodes where they have a cheaper cost (the cost of interacting with other objects and of doing remote accesses).

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A Identifier Conventions and Semantics

Identifier conventions.

\[ n \in \mathbb{N} \quad C \in \text{ClassId} \quad m \in \text{MethId} \quad f \in \text{FieldId} \quad L \in \text{LocId} \quad p \in \text{OwnershipId} \quad x, i \in \text{varId} \]

B Auxiliary definitions, shorthands and lookup functions

**Definition 5** (Well-formed program and class).

\[ \vdash P \equiv \forall ([\text{active}] \text{ class } C(...)) \in P) : P \vdash C \]

\[ P \vdash C \equiv \begin{cases} \emptyset(C) = \{p_1, ..., p_n\} \land \forall m : M(C, m) = (T, x : T', e, b) \land (\text{this} \mapsto C(p_1, ..., p_n), x \mapsto T') \vdash e \mapsto T, b' \\ \implies b = \text{filter}(b') \end{cases} \]

Given that the effects returned during type checking do not exclude reads and writes happening in the same node, we apply a function \( \text{filter}(b) \) in order to exclude such annotations. The function is defined as follows.

\[ \text{filter}(\epsilon) = \epsilon \quad \text{filter}([b_1, b_2]) = \text{[filter}(b_1), \text{filter}(b_2)] \]
\[ \text{filter}(\pi.b) = \text{if source}(\pi) = \text{dest}(\pi) \text{ then } \epsilon \text{ else } \pi, \text{filter}(b) \]
\[ \text{filter}([b_1 \text{ or } b_2].b_3) = \text{if } \text{filter}(b_1) = \epsilon \land \text{filter}(b_2) = \epsilon \text{ then } \epsilon \text{ else } \{\text{filter}(b_1) \text{ or } \text{filter}(b_2)\}. \text{filter}(b_3) \]

\[ \text{filter}([\text{Loop}(n : b).b']) = \text{if } \text{filter}(b) = \epsilon \text{ then } \epsilon \text{ else } \text{Loop}(n : \text{filter}(b))). \text{filter}(b') \]

Note that if the expressions nested in for-loops or conditional expressions have behaviour \( \epsilon \), then the loop or choice types are not annotated.

**Definition 6** (Value agreement).

\[
\begin{align*}
\text{WFTRUE} & : h \vdash \text{true} : \text{bool} & \text{WFFALSE} & : h \vdash \text{false} : \text{bool} & \text{WFNULL} & : T = \text{nil} \lor \text{isValid}(T) & h \vdash \text{null} : T \\
\forall i : h \vdash v_i : \mathcal{F}(C, f_i)[\mathcal{K}] & \quad h \vdash \alpha : C(\mathcal{K}) & \forall i \in I : h \vdash v_i : \mathcal{F}(C, f_i)[\mathcal{K}] & \quad h \vdash \alpha : C(\mathcal{K}) \\
\forall i \in I : h \vdash v_i : \mathcal{F}(C, f_i)[\mathcal{K}] & \quad h \vdash \alpha : C(\mathcal{K}) & \quad h \vdash \alpha : C(\mathcal{K}) & \quad h \vdash \alpha : C(\mathcal{K}) & \quad h \vdash \alpha : C(\mathcal{K}) \\
\end{align*}
\]

For \( I \) some index set \quad \forall i \in I : h \vdash v_i : \mathcal{F}(C, f_i)[\mathcal{K}] & \quad h \vdash \alpha : (\text{this} \mapsto \alpha, x \mapsto v_i) \vdash v_i \mapsto M(C, m_i)_{2} [\mathcal{K}].b \]
Look up functions

Considering $P$, the globally accessible program, and the class declaration

\[
\text{class } C(p^+) \{ \overline{Fd} \ Md \} \in P
\]

we define:

\[
\emptyset(C) = \{ p^+ \}
\]

\[
\mathcal{F}(C, f) = T \text{ iff } f : T \in \overline{Fd}
\]

\[
\mathcal{F}_x(C) = \{ \overline{Fd} \}
\]

\[
\mathcal{N}(C, m) = (T, T', e, b) \text{ iff } m(x : T') : T \text{ in } b \{ e \} \in \overline{Md}
\]

\[
\mathcal{F}(C, f)[h_1, \ldots, h_n] = \mathcal{F}(C, f)[h_1/p_1, \ldots, h_n/p_n] \text{ where } \emptyset(C) = \{ p_1, \ldots, p_n \}
\]

Operations on the heap

\[
h[\alpha \mapsto o] = h' \text{ where } h'(\alpha) = o \land \forall \alpha_i \in \text{dom}(h) \setminus \{ \alpha \} : h(\alpha_i) = h'(\alpha_i)
\]

\[
h[\alpha, f \mapsto v] = h' \text{ where } h'(\alpha) = h(\alpha)[f \mapsto v] \land \forall \alpha_i \in \text{dom}(h) \setminus \{ \alpha \} : h(\alpha_i) = h'(\alpha_i)
\]

\[
h[\alpha :: m(v)] = h' \text{ where } h(\alpha) = o \land \alpha_4 \neq 1 \land h'[\alpha \mapsto (\alpha_1, \alpha_2, \alpha_3, m(v) :: \alpha_4)]
\]

\[
\land \forall \alpha_i \in \text{dom}(h) \setminus \{ \alpha \} : h(\alpha_i) = h'(\alpha_i)
\]

owners(h, \alpha) = C(X) \text{ where } h(\alpha)_1 = C \land h(\alpha)_2 = X

\[
h_1 \cup h_2 = h \text{ where } \forall \alpha \in \text{dom}(h) : h_1(\alpha) = h(\alpha) \lor h_2(\alpha) = h(\alpha)
\]

Operations on objects

\[
o(f) \equiv o_{\lambda_3}(f)
\]

\[
o[f \mapsto v] \equiv (o_{\lambda_1}, o_{\lambda_2}, (f \mapsto v, f_i \mapsto v_i), \lambda_{\lambda_4}) \text{ where } o_{\lambda_3} = f \mapsto _\lambda f \mapsto v_i
\]

\[
\text{initObj}(C(L_1, \ldots, L_m)) \equiv \begin{cases} (C, \kappa_1, \ldots, \kappa_m, (f_i \mapsto \text{init}(T_i))_{i \in 1..n}, \emptyset) & \text{isActive}(C) \\ (C, \kappa_1, \ldots, \kappa_m, (f_i \mapsto \text{init}(T_i))_{i \in 1..n}, \bullet) & \text{otherwise} \end{cases}
\]

\[
\text{where } \mathcal{F}_x(C) = \{ f_1 : T_1, \ldots, f_n : T_n \} \text{ and } \forall j \in \{1..m\} : \kappa_j = \mathcal{L}(L_j)
\]

Operations on types

\[
\text{init}(T) \equiv \text{if } T = \text{bool then } \text{false else } \text{null} \quad \ell(\Gamma) = l \text{ iff } \Gamma = \bot \land \Gamma(\text{this}) = C(l, \_)
\]

Other definitions

\[
e[C, \kappa_1, \ldots, \kappa_n] = e[\kappa_1/p_1, \ldots, \kappa_n/p_n] \text{ where } \emptyset(C) = \{ p_1, \ldots, p_n \}
\]

\[
\text{heaps}(N_1, \ldots, N_n) = h_1 \cup \ldots \cup h_n \text{ iff } \forall i \in \{1..n\} : N_i \_2 = h_i
\]

\[
h, \sigma \vdash e \triangleright T, b \text{ iff } \text{BuildContext}(h, \sigma) \vdash e \triangleright T, b
\]

\[
\text{typeOf}(h, v) \equiv \text{if } v = \text{true } \lor v = \text{false then } \text{bool else } \text{owners}(h, v)
\]

\[
\text{BuildContext}(h, \varphi_1) = \Gamma_n
\]

\[
T_{\text{this}} = \text{typeOf}(h, \alpha)
\]

\[
T_1 = \text{typeOf}(h, v_1)
\]

\[
T_n = \text{typeOf}(h, v_n)
\]

\[
\Gamma = (\text{this } \mapsto T_{\text{this}}, x_1 \mapsto T_1, \ldots, x_n \mapsto T_n)
\]

\[
\text{BuildContext}(h, \alpha \cdot \varphi_1 \ldots \varphi_n) \equiv \text{BuildContext}(h, \text{this } \mapsto \alpha, x_1 \mapsto v_1, \ldots, x_n \mapsto v_n) = \Gamma
\]
C  Topology Example

Consider the following code with three class declarations: an active class C, a passive D and the class Main. An active object, instance of C, has three fields pointing to three objects in different locations of type D. The class main creates three abstract (or symbolic) locations L1, L2, L3 and the body of the main method.

\[
\text{active class } C(p_1, p_2, p_3) \\
\text{d1: D(p1)} \\
\text{d2: D(p2)} \\
\text{d3: D(p3)} \\
\text{class D(p)} \\
\text{class Main(L1, L2, L3)} \\
\text{def main(): nil} \\
\text{as b write(L1, L2). write(L1, L3) {}} \\
\text{let x = new C(L1, L2, L3) in} \\
\text{let y = (x.d1 = new D(L1)) in} \\
\text{let z = (x.d2 = new D(L2)) in} \\
\text{x.d3 = new D(L3)} \\
\}
\]

The topology after execution of the main method is depicted in the following figure. In the abstract location L1 there is an instance of class C and an instance of class D. Abstract locations L2 and L3 have both an instance of class D. Although the programmer define 3 abstract locations, the machine might have a different number of nodes. For instance, in a system with two different nodes, we could have the mapping \((L_1 \mapsto \kappa_1, L_2 \mapsto \kappa_2, L_3 \mapsto \kappa_2)\) between abstract locations and node identifiers, which means that the objects in L1 are in the node \(\kappa_1\), and objects from L2 and L3 are in the same node, as depicted in Figure 9.

![Figure 8: The ownership topology after the execution of the expression in the method main.](image)

![Figure 9: NUMA system with two different nodes](image)