DAC: Data-free Automatic Acceleration of Convolutional Networks
Xin Li ‡, Shuai Zhang ‡, Bolan Jiang †, Yingyong Qi †, Mooi Choo Chuah ‡ and Ning Bi †
‡Qualcomm AI Research
†Department of Computer Science and Engineering, Lehigh University
xil915@lehigh.edu, shuazhan@qti.qualcomm.com, bjiang@qti.qualcomm.com
yingyong@qti.qualcomm.com, chuah@cse.lehigh.edu, nbi@qti.qualcomm.com

Abstract
Deploying a deep learning model on mobile/IoT devices is a challenging task. The difficulty lies in the trade-off between computation speed and accuracy. A complex deep learning model with high accuracy runs slowly on resource-limited devices, while a lightweight model that runs much faster loses accuracy. In this paper, we propose a novel decomposition method, namely DAC, that is capable of factorizing an ordinary convolutional layer into two layers with much fewer parameters. DAC computes the corresponding weights for the newly generated layers directly from the weights of the original convolutional layer. Thus, no training (or fine-tuning) or any data is needed. The experimental results show that DAC reduces a large number of floating-point operations (FLOPs) while maintaining high accuracy of a pre-trained model. If 2% accuracy drop is acceptable, DAC saves 53% FLOPs of VGG16 image classification model on ImageNet dataset, 29% FLOPs of SSD300 object detection model on PASCAL VOC2007 dataset, and 46% FLOPs of a multi-person pose estimation model on Microsoft COCO dataset. Compared to other existing decomposition methods, DAC achieves better performance.

1. Introduction
Deep learning techniques have been applied to many areas of artificial intelligence, which affects our daily lives. For example, smart surveillance video systems that can detect and identify suspects help law enforcement personnel to maintain a safer living environment. Self-driving cars liberate drivers from steering wheels so that they can do more meaningful things, e.g., read business news. As technology for high-performance mobile or edge computing devices continues to improve, more and more deep learning models are deployed on these devices, e.g., face recognition systems are used on cell phones to unlock screens, etc.

However, some of these AI tasks, e.g., voice recognition, requires internet access, which means the model is not entirely run on mobile/IoT devices. The major reason is that most of the deep learning models with high accuracy run too slowly on resource-limited devices. Many techniques to reduce the size of neural network models, e.g., model quantization of neural network models using fewer bits, have been proposed to facilitate their implementations on mobile chips [27, 25, 26, 24]. However, limited by current hardware structure and the tolerance for model accuracy drop, most of these quantization methods for real applications only focus on the 8-bit format. To further accelerate neural network models, it is more important to reduce computation complexity directly from the network architectures. Some research [7, 20, 31, 16, 12, 13] has been done to simplify these models before running them on mobile/IoT devices. Such research can be roughly categorized into two classes:

Designing new light-weight network architectures: MobileNet proposed by Howard et al. in [7, 20] is an excellent example. The model is based on a streamlined architecture that uses depthwise separable convolutions to build a light weight deep neural network. The model achieves good accuracy and runs fast on mobile devices. Similar with MobileNet, ShuffleNet [31, 17] is another type of light weight network architecture, based on depthwise separable layers for acceleration. However, these models require powerful servers and massive data to tune the weights. This is not a friendly solution to those who cannot access such resources.

Modifying an existing model to a slim version: Another solution is to produce a slimmer version of an existing model. Unfortunately, the training data in some cases is exclusively available to the original designer of a model, which prevents other researchers from re-training the model after modification. Besides, it is costly and time-consuming to train a model from scratch. Thus, compared to designing new models and training them from scratch, accelerating an existing model based on its pretrained weights is a better solution. Network pruning and parameter decomposition are two common methods for this purpose. Network pruning is a practical tool for speeding up existing deep neural networks [18]. He et al. propose a channel pruning method...
Parameter decomposition is another way to simplify an existing model. It is a layer-wise operation that decomposes a layer into one or multiple smaller layers, either having smaller kernel sizes or fewer channels. Although there will be more layers after being decomposed, the total number of weights and the computational complexity will be reduced. The decomposition methods only use the pretrained weights of a layer, with the fact that most neural network models have much redundant parameters and can be largely simplified with low rank constraints. In this paper, we propose a new parameters decomposition method which does not require access to data or retraining.

The contributions of this paper are:

1. We propose a novel decomposition method that replaces standard convolutional layers in a pre-trained model with separable layers to significantly reduce the number of FLOPs.
2. The newly generated model maintains high accuracy without using any data and training process.
3. The experimental results on three computer vision application scenarios show that DAC maintains high accuracies even when a vast amount of FLOPs is trimmed.

The rest of this paper is organized as follows. Some related works are summarized in section 2. In section 3, we describe the architecture of DAC and our factorization method. The experimental results are reported in section 4, followed by the conclusion in Section 5.

2. Related Work

Much work has been done to do parameter decomposition. In this section, we will discuss some prior work that decomposes convolutional layers. To simplify the description, we assume the weight of the convolutional layer that we are going to decompose has a size of \((n \times k_w \times k_h \times c)\), where \(n\) is the number of kernels, \(k_w\) and \(k_h\) are the spatial width and height of a kernel respectively, and \(c\) is the number of channels of the input feature map.

First, Jaderberg et al. [8] propose a spatial decomposition method. The method decomposes a convolutional layer with \((n \times k_w \times k_h \times c)\) kernel size into two layers. One has horizontal filters with \((c' \times k_w \times 1 \times c)\) kernel size and the other consists of vertical filters with \((n \times 1 \times k_h \times c')\) kernel size. In theory, this method indeed reduces parameters. However, running the decomposed model on a mobile device that has limited resources does not result in a significant speed up. This is due to the caching behavior of data. A feature map is horizontally (or vertically) loaded into a continuous block of memory. When we compute convolution using horizontal (vertical) filters, we access the memory sequentially. There is no impact on running time. However, if we compute the convolution using vertical (horizontal) filters, we cannot access memory sequentially any more which results in more cache misses and hence longer computation time.

Then, Zhang et al. describe a channel decomposition method in [32]. It decomposes a convolutional layer with \((n \times k_w \times k_h \times c)\) kernel size into a convolutional layer with fewer output channels and a pointwise convolutional layer. The newly generated convolutional layer has \((c' \times k_w \times k_h \times c)\) kernel size, and the pointwise convolutional layer has \((n \times 1 \times 1 \times c')\) kernel size. Notice that the first layer is also an ordinary convolutional layer, so it does not improve the situation fundamentally.

Direct tensor decomposition methods including CP decomposition [11] and Tucker decomposition [9] are also applied to accelerate networks. After these tensor decompositions, one convolution layer will be factorized into 3 or 4 small layers with a bottleneck structure, opposite with [20] architecture. One big disadvantage of these tensor decomposition methods is that the depth of network architecture is tripled (3x) compared to the original model, thus it increases the memory access cost (MAC) and largely offset the gains from the reduction of FLOPs, as claimed in [17].

There are also many network decomposition works using low rank constraints in training process or solving layer-wise regression problem with data samples [23, 2]. But all these methods require the access of sufficient data from training/test domain.

Our research focus is based on the real application scenario with limited access of data. In this paper, we propose a novel data-free convolutional layers decomposition method and compare its performance to two most related works [32, 8].

3. Proposed Solution

The intuition of our proposed scheme is that the depth-wise + pointwise combination runs efficiently on mobile devices has already been proven by MobileNet [7]. It will be useful if we can convert an ordinary convolutional layer into such a structure and compute their weights from the original layer directly. The feasibility of decomposing the weights of a convolutional layer has been mathematically proved by Zhang et al. [32].
3.1. Convolutional Layer Factorization

In this section, we propose a novel factorization method for convolutional layers. Figure 1 shows the details of our scheme. An ordinary convolutional layer with the shape of \((n \times k_w \times k_h \times c)\) is decomposed into two layers. One is a depthwise layer with the shape of \((rC \times k_w \times k_h \times 1)\), and the other is a pointwise layer with the shape of \((n \times 1 \times 1 \times rC)\), where \(rC = r \times c\) and \(r\) is a factor used to balance the trade-off between model compression ratio and accuracy drop. There is no bias in the depthwise layer, and the bias vector in the original layer is assigned to the pointwise layer.

Even though our scheme is inspired by MobileNet, it is worth highlighting the differences between MobileNet and DAC. DAC has no non-linear layers (batch normalization layers and activation layers) between the depthwise and the pointwise layers. The absence of non-linear layers makes DAC quantization friendly and hence suitable for further hardware acceleration, which Sheng et al. [21] have already experimentally verified.

3.2. Weights Decomposition

Once a convolutional layer is factorized, we want to compute weights for the newly generated layers (a depthwise and a pointwise layer) from the original weights directly. We assume \(T\) is the trained weights of the original convolutional layer, and its shape is \((n \times k_w \times k_h \times c)\). We denote \(Td \in D := \mathbb{R}^{rC\times k_w \times k_h \times 1}\) as the weights of the depthwise layer and \(Ts \in S := \mathbb{R}^{n \times 1 \times 1 \times rC}\) as the weights of the pointwise layer. Then, the objective function of factorizing a convolutional layer is:

\[
\min_{Td \in D, Ts \in S} \|T - Ts \ast Td\|_F^2,
\]

where operator \(\ast\) is the combination of convolution operations of the depthwise and the pointwise layer, and \(\|\|_F\) is the Frobenius norm for tensor/matrix. Thus

\[
\min_{Td \in D, Ts \in S} \|T - Ts \ast Td\|_F^2
= \min_{Td \in D, Ts \in S} \sum_{i=1}^C \|T_i - Ts_i \ast Td_i\|_F^2
= \sum_{i=1}^C \min_{Ts_i, Td_i} \|T_i - Ts_i \ast Td_i\|_F^2
= \sum_{i=1}^C \min_{S_i, D_i} \|M_i - S_iD_i\|_F^2.
\]

Here matrices \(M_i, S_i\) and \(D_i\) are transformed from tensors \(T_i, Ts_i\) and \(Td_i\) respectively.

According to the SVD theory, the solution of minimization problem \(\min_{S_i, D_i} \|M_i - S_iD_i\|_F^2\) is the singular matrices with rank \(r\), where the top \(r\) singular values can be merged into either \(S_i\) or \(D_i\). Also, Frobenius norm \(\|\|_F\) can be defined as \(\|\|_{2,2}\) induced by \(L_2\) vector norm, so the above DAC minimization objective function can be considered as

\[
\min_{Td \in D, Ts \in S} \sup_{\|F\|_2 \neq 0} \frac{\|T - Ts \ast Td\|_F}{\|F\|_2},
\]
where \( F \) is the input feature maps and \( \| F \|_2 \) is the vector \( L_2 \) norm. In this formula, it minimizes the output feature maps with approximation error measured in Euclidean space and the constraint of the decomposition ‘rank’ \( r \) (the factor used to balance the trade-off between model compression ratio and accuracy drop). The process of weights decomposition is described in Algorithm 1.

### Algorithm 1: DAC Weights Decomposition

**Input:** Weights of a convolutional layer: \( T \in \mathbb{R}^{n \times k_w \times k_h \times c} \);

Decomposition Rank: \( r \).

**Output:** Weights of the depthwise layer: \( Td \in \mathbb{R}^{r \times k_w \times k_h \times 1} \);

Weights of the pointwise layer: \( Ts \in \mathbb{R}^{n \times 1 \times r \times c} \).

begin
1. \( list_d \in \mathbb{R}^{r \times k_w \times k_h \times 1} \leftarrow \emptyset \)
2. \( list_s \in \mathbb{R}^{n \times 1 \times r \times c} \leftarrow \emptyset \)
3. for \( i \in c \) do
   4. \( T_i \leftarrow T_i \in \mathbb{R}^{n \times k_w \times k_h} \)
   5. \( M_i \leftarrow \text{Reshape}(T_i, (n, k_w \times k_h)) \in \mathbb{R}^{n \times k_w \times k_h} \)
   6. \( D_i, S_i \leftarrow \text{Decompose}(M_i, r) \)
   7. \( list_d[i] \leftarrow D_i \in \mathbb{R}^{r \times k_w \times k_h \times 1} \)
   8. \( list_s[i] \leftarrow S_i \in \mathbb{R}^{n \times 1 \times r \times c} \)
9. \( Td \leftarrow \text{Reshape}(list_d, (r \times c, k_w, k_h, 1)) \)
10. \( Ts \leftarrow \text{Reshape}(list_s, (n, 1, r \times c)) \)
end

function Decompose(M, r)

begin
11. \( U, \Sigma, V \leftarrow \text{SV.D(M)} \)
12. \( U_r \leftarrow U_r \in \mathbb{R}^{r \times r} \)
13. \( V_r \leftarrow V_r \in \mathbb{R}^{r \times k_w \times k_h} \)
14. \( \Sigma_r \leftarrow \text{Reshape}(V_r, (r, k_w, k_h, 1)) \)
15. \( D \leftarrow \text{Reshape}(U_r \Sigma_r \Sigma_r^T, (r \times c, r \times c)) \)
16. \( S \leftarrow U_r \Sigma_r \Sigma_r^T \)
17. \( S \leftarrow \text{Reshape}(S, (n, 1, 1, r)) \)
return \( D, S \)
end

### 3.3. Computation Reduction

We consider the original convolutional layer with \((n \times k_w \times k_h \times c)\) kernel size takes a \((W_f \times H_f \times c)\) feature map \(F\) as an input and produces a \((W_f \times H_f \times n)\) feature map \(G\), where \(W_f\) and \(H_f\) are the spatial width and height of the feature maps. Here, we assume the output feature map has the same spatial size as the input for simplification. Then, the computation cost of the convolutional layer is:

\[
W_f \times H_f \times c \times k_w \times k_h \times n.
\]

The computation cost depends on the number of input channels \(c\), the number of output channels \(n\), the kernel size \(k_w \times k_h\) and the input features map size \(W_f \times H_f\). After decomposition, the newly generated depthwise and pointwise layer in total have the cost of \(W_f \times H_f \times k_w \times k_h \times rC + W_f \times H_f \times rC \times n\), where \(rC = r \times c\) and the reduction in computation is

\[
\frac{W_f \times H_f \times k_w \times k_h \times rC + W_f \times H_f \times rC \times n}{W_f \times H_f \times c \times k_w \times k_h \times n} = \frac{r}{n} + \frac{r}{k_w k_h}.
\]

### 4. Experimental Results

To prove the universality of our proposed scheme, we apply DAC to three major application scenarios in the field of Computer Vision: (1) Image Classification, (2) Object Detection, and (3) Multi-person Pose Estimation. We implement our scheme using Python and Keras Library \[4\] with Tensorflow backend \[1\].

#### 4.1. Datasets

Four datasets are used in this paper:

- **CIFAR-10 dataset**: The CIFAR-10 dataset \[10\] consists of 50,000 training images and 10,000 test images in 10 categories. It is a small dataset, from which we can quickly get results after tuning parameters. Thus, we use it for ablation study to get some insights about DAC, e.g., the impacts of using different ranks or decomposing different layers.

- **ImageNet dataset**: The ImageNet dataset \[19\] has 50,000 ILSVRC validation images in 1,000 object categories. We use this ILSVRC validation subset to evaluate the performance of DAC in the task of image classification.

- **Pascal VOC2007 dataset**: For object detection task, Pascal VOC2007 dataset \[5\] is used. It consists of 4,952 testing images for object detection. The bounding box and label of each object from twenty target classes have been annotated. Each image has one or multiple objects.

- **Microsoft COCO dataset**: The Microsoft COCO dataset \[14\] is used to evaluate the performance of DAC in the task of multi-person pose estimation. We use the COCO 2017 keypoints subset which consists of 5,000 validation images and 40K testing images.

#### 4.2. Ablation Study

Here, we use a pre-trained CIFAR-VGG model \[1\], a simple Convolutional Neural Network, on the CIFAR-10 dataset as our original model. Figure 2 shows the architecture of the CIFAR-VGG. In total, the CIFAR-VGG model has 13 convolutional layers. The original model (trained on CIFAR-10 training subset) achieves 93.6% on CIFAR-10 testing subset.

![Figure 2. The architecture of the CIFAR-VGG.](https://example.com/fig2)

First, we decompose a single convolutional layer to explore the impact of decomposing different layers. Table 1 shows the details of testing accuracy when applying varying ranks (rank 1 to rank 5) decomposition on different layers of CIFAR-VGG model. Each time, we only modify one layer.

---

1[https://github.com/geifmany/cifar-vgg](https://github.com/geifmany/cifar-vgg)
All results are collected using decomposed weights directly (no access to data or any training process).

### Table 1. Testing Accuracy on CIFAR-10 dataset when decomposing different layers of CIFAR-VGG model using variant ranks.

| Original Model | Accuracy (%) |
|----------------|--------------|
| 93.6           |              |
| Decomposed Layer | Rank1 | Rank2 | Rank3 | Rank4 | Rank5 |
| conv2d_1        | 18.0        | 76.4  | 86.7  | 91.9  | 92.8  |
| conv2d_2        | 39.1        | 86.5  | 91.6  | 92.7  | 93.2  |
| conv2d_3        | 54.0        | 87.8  | 92.6  | 93.2  | 93.4  |
| conv2d_4        | 31.4        | 83.7  | 91.8  | 92.8  | 93.2  |
| conv2d_5        | 80.1        | 90.2  | 92.6  | 93.1  | 93.8  |
| conv2d_6        | 84.3        | 90.9  | 92.8  | 93.3  | 93.4  |
| conv2d_7        | 66.0        | 89.5  | 92.6  | 93.0  | 93.3  |
| conv2d_8        | 83.2        | 91.1  | 92.6  | 93.0  | 93.2  |
| conv2d_9        | 91.2        | 93.1  | 93.3  | 93.4  | 93.5  |
| conv2d_10       | 91.7        | 93.2  | 93.3  | 93.3  | 93.4  |
| conv2d_11       | 93.1        | 93.4  | 93.3  | 93.4  | 93.4  |
| conv2d_12       | 93.3        | 93.4  | 93.4  | 93.4  | 93.5  |
| conv2d_13       | 92.9        | 93.4  | 93.4  | 93.4  | 93.5  |

From Table 1, we gain two insights: (a) Decomposing first few layers of a model causes large drops in accuracy (75% drop when rank 1 decomposition is applied on layer conv2d_1), while decomposing last few layers has a smaller impact on the accuracy (less than 1% drop when rank 1 decomposition is applied on layer conv2d_13). (b) Decomposing a layer using a larger rank helps to maintain the accuracy. This can be observed by comparing different columns in the same row. These two insights are consistent with our intuition. (a) Decomposing a layer generates tiny errors. If such errors occur at the beginning of a model, the errors will accumulate to bigger errors at the final prediction. (b) Compared to smaller ranks, larger ranks generate more parameters in the depthwise layers. Thus, the newly generated layers have more possibility of replicating the performance of the original layer.

Next, we explore the performance of DAC when multiple convolutional layers are decomposed. We decompose the model with two opposite directions: (1) from the last layer to the first one, and (2) from the first layer to the last one. To simply the experiment, we use the same rank to decompose all chosen layers. The experimental results are reported in Figure 3. First, one can quickly notice that most decomposition cases (solid points) achieve high accuracies (higher than 91.6% or 2% drop). Second, after saving 42% FLOPs, DAC still achieves 92.7% accuracy (drops less than 1%). Both of these prove that our proposed DAC has the capability of maintaining accuracy when the number of FLOPs is substantially reduced.

Besides, in Figure 3, red-star points (Rank 5) achieve high accuracies. If we compare the solid (open) red-star marks to other solid (open) marks, we can notice that the above insights also hold in the case of decomposing multiple convolutional layers. Ten (eight) out of twelve Rank 5 decomposition cases (solid red-star spots) drop accuracy by less than 2% (1%). The worst solid red-star case that achieves 91.2% (accuracy drops 2.4%) is caused by the decomposition of the first layers of the model (first insight discussed above). It is worth highlighting that these decomposed models that maintain high accuracies are generated by DAC without accessing data or training process.

### 4.3. Image Classification

For the task of image classification, we use the VGG16 model proposed by Simonyan et al. in [22]. It includes 12 (3x3) convolutional layers. We downloaded a model trained on ImageNet dataset. All convolutional layers but ii (3x3) convolutional layers. We downloaded a model pre-trained on ImageNet dataset. All convolutional layers but ii (3x3) convolutional layers. We downloaded a model trained on ImageNet dataset. All convolutional layers but ii (3x3) convolutional layers. We downloaded a model pre-trained on ImageNet dataset. All convolutional layers but ii (3x3) convolutional layers. We downloaded a model pre-trained on ImageNet dataset. All convolutional layers but ii (3x3) convolutional layers. We downloaded a model.

Here we compare our approach with two schemes, namely, the Filter Reconstruction Optimization proposed by Jaderberg et al. in [8] (Spatial Decomp. in Table 2) and the Channel Decomposition method proposed by Zhang et al. in [32] (Channel Decomp. in Table 2). Spatial Decomposition is the one that does not need data and training like DAC as we discussed in Section 2. Although the Channel Decomposition requires some data, we can still use the method as a filter reconstruction without accessing any data and training process. We implemented these two algorithms ourselves. For fair comparison, we choose appropriate parameters for Channel Decomposition and Spatial Decomposition, so that all schemes save roughly same amount of weights.

---

Note: The table and graph in the image are not fully transcribed here due to the limitations of text-based representation. The table data and the graph's accuracy and saved FLOPs ratio (%) are included in the description above.
FLOPs. Given a rank $r$ of DAC, the number of filters $c'_s$ in the first newly generated layer in Channel Decomposition can be computed using:

$$c'_s = r \cdot \frac{c(n + k_h k_w)}{c k_h k_w + n}$$  \hspace{1cm} (2)$$

and for Spatial Decomposition, the number of filters $c'_s$ in the first newly generated layer is

$$c'_s = r \cdot \frac{c(n + k_h k_w)}{c k_h w + n k_h}$$  \hspace{1cm} (3)$$

where $n$ is the number of kernels in original convolutional layer, $k_w$ and $k_h$ are the spatial width and height of a kernel respectively, and $c$ is the number of channels of the input feature map.

Table 2 shows the accuracy of the model (after saving 40%, 50%, and 60% FLOPs respectively) on ImageNet validation set. First, DAC maintains high accuracy on both Top-1 and Top-5 accuracy even when a significant amount of FLOPs are reduced. Second, compared to the Channel Decomposition and Spatial Decomposition, DAC performs much better. Especially when we saved 60% FLOPs, DAC achieves 41.4% higher accuracy than Channel Decomposition and 6.7% higher accuracy than Spatial Decomposition.

4.4. Multi-person Pose Estimation

For the task of multi-person pose estimation, we use the scheme extracted by Cao et al. [3]. Figure 4 is the architecture extracted from their paper. After generating the feature map $F$ by a convolutional network (initialized by the first 10 layers of VGG-19 [22] and fine-tuned), the model is split into two branches: the top branch predicts the confidence maps, and the bottom branch predicts the affinity fields.

Figure 4. The model architecture figure extracted from [3].

We download an implementation of Cao’s model \(^{1}\) that was pre-trained on Microsoft COCO dataset as our original model. It achieves 57.9% average precision (AP) on the validation subset of 2017 COCO keypoints challenge. This model consists of six stages, which means $t \in \{2, 3, 4, 5, 6\}$ in Figure 4. Thus, the first stage (Stage 1) has 6 convolutional layers (3x3 kernel size), and each of the following stage (Stage 2 to Stage 6) includes 10 convolutional layers (7x7 kernel size). Based on the above two insights, we decompose the model from the bottom to the top with variant ranks (from Rank20 to Rank3). Because the full rank of a (3x3) convolutional kernel (in Stage 1) is 9, so we set the maximum rank used to decompose these (3x3) convolutional layers equals to 5 for a large compression ratio.

Table 3 shows the accuracy of the model (after saving 40%, 50%, and 60% FLOPs respectively) on COCO 2017 keypoint challenge. The parameters of Channel Decomposition and Spatial Decomposition are computed using Equation 2 and 3 correspondingly. Compared to Channel and Spatial Decomposition, DAC achieves higher accuracy even when a significant amount of FLOPs is reduced. After saving 60% FLOPs, Channel Decomposition cannot correctly detect any person’s pose, while DAC can still achieve 7.1% higher accuracy than Spatial Decomposition.

Figure 6 shows the visualized multi-person pose estima-

\(^{1}\) https://github.com/anatolix/keras_Realtime_Multi-Person_Pose_Estimation

\(\text{Table 3. Results on the COCO 2017 keypoint challenge} \)

| Method              | Saved 40% | Saved 50% | Saved 60% |
|---------------------|-----------|-----------|-----------|
| Openpose [3] (Original) | 57.9 |           |           |
| Channel Decomp. [32] | 25.9 | 5.0 | 0 |
| Spatial Decomp. [8]  | 55.9 | 54.4 | 45.4 |
| DAC (Ours)           | 56.7 | 55.6 | 52.5 |
tion results on COCO dataset. It shows that after being decomposed using DAC, the model still works pretty well. There are only small changes observed. For example, the decomposed model misses a leg of a person in the first example (the second person on the right side) and the third sample (the second person on the left side). Please refer to our Appendix for more visualized results.

4.5. Object Detection

Next, we evaluate the performance of DAC in the task of object detection using the Single Shot MultiBox Detector (SSD) model proposed by Liu et al. [15]. Figure 7 shows the framework of the SSD.

Figure 7. SSD architecture extracted from [15]

We use a model \(^{10}\) pre-trained on Pascal VOC2007 and VOC2012 trainval subset. The model uses VGG-16 [22] as its base net that has (300x300) input size. Ten extra convolutional layers are added to the VGG-16 model to provide extra information. In total, 18 (3x3) convolutional layers and 5 (1x1) convolutional layers are used to generate multi-scale feature maps for detection, and 12 (3x3) convolutional layers are used to produce a fixed set of detection predictions. This model achieves 76.5% on VOC2007 testing set.

There is no benefit in decomposing a convolutional layer with (1x1) kernel size, so we only decompose those layers with (3x3) kernel size. Furthermore, considering that decomposing first layers causes large drops of accuracy, we do not decompose the first convolutional layer of the model. To simplify the description, we denote 18 layers (the first layer, conv1_1, is not decomposed) that generate multi-scale feature maps by “Feature Convolutional Layers (FL)” and 12 layers that produce detection predictions by “Detector Convolutional Layers (DL)”.

We demonstrate the experimental results in Figure 8. First, one can see that if 2% mAP drop is acceptable, DAC saves up to 29% FLOPs. Second, decreasing the decomposition rank results in a drop of mAP, which is also observed in the previous experiment. Third, compared to “DL”, “FL” achieves a bigger FLOPs saved ratio. This is because that there are fewer layers in “DL” and each layer in “DL” has fewer channels than layers in “FL”. In addition, for this model, the maximum decomposition rank is 9 so when the decomposition rank is set to 9, the number of parameters increases after decomposition. This is because that all layers we decompose in this model have (3x3) kernel size whose full rank is 9. The newly generated depthwise layer with Rank9 has the same number of parameters as the decomposed layer, while an extra pointwise layer that has \( rC \times N \times 1 \times 1 \) parameters is added.

Table 5 shows the comparison of the detection accuracy on PASCAL VOC2007 Dataset. One can see that DAC

\(^{10}\) https://github.com/pierluigiferrari/ssd_keras
Figure 9. Visualized results on PASCAL VOC2007 dataset. The first row shows the results generated using the original weights, while the second row shows the results created using a model that saves 40% FLOPs. Red dashed rectangles are ground truths. The first two samples are examples that the model works well after being decomposed, the third sample shows an example that DAC helps improve the performance, while the following three samples are different kinds of errors caused by decomposition.

Table 4. Detection results on PASCAL VOC2007 testing set. All results expected original are collected using the SSD300 model decomposed with Rank6. “DL” indicates only Detector Convolutional Layers are decomposed and “FL” indicates only Feature Convolutional Layers are decomposed.

| Model                  | mAP(%) | detection results on PASCAL VOC2007 Dataset. | Mean Average Precision (%) |
|------------------------|--------|---------------------------------------------|-----------------------------|
| SSD [15] (Original)    | 76.5   |                                             | 76.5                        |
| (DL) Channel [32]      | 76.1   | 78.6 83.9 75.3 67.8 48.5 86.7 84.1 87.7 58.1 79.3 75.0 85.9 87.5 82.6 77.5 51.2 77.1 79.5 78.2 76.3 |
| (DL) Spatial [8]       | 76.3   | 78.4 82.9 74.5 68.3 47.8 86.7 84.4 88.4 58.0 79.4 74.9 85.6 86.5 83.1 77.3 50.7 77.3 79.0 87.5 76.1 |
| (FL) Channel [32]      | 62.2   | 70.4 69.7 63.8 52.9 38.3 75.1 79.8 72.8 42.2 73.2 38.0 65.7 76.3 69.6 64.0 38.8 66.6 33.4 75.6 57.3 |
| (FL) Spatial [8]       | 63.2   | 73.7 69.7 64.6 52.0 39.0 75.6 79.9 77.6 42.6 73.2 39.5 70.7 76.3 73.1 65.5 37.9 67.0 53.0 77.9 56.1 |
| (FL) DAC (Ours)        | 75.3   | 78.2 83.0 73.0 67.1 44.3 86.3 83.3 87.7 56.6 78.5 75.2 84.2 85.9 82.8 75.8 48.8 75.3 78.6 86.4 75.6 |
| (DL+FL) Channel [32]  | 62.2   | 70.6 69.4 64.1 51.1 36.1 75.7 79.8 72.8 43.0 72.9 39.9 86.4 74.5 70.2 63.7 38.5 65.9 35.1 75.4 58.7 |
| (DL+FL) Spatial [8]   | 63.1   | 73.8 70.3 64.1 50.8 37.8 75.3 79.8 76.9 43.0 74.3 39.8 69.7 75.8 70.9 64.5 38.6 68.9 54.1 77.9 56.2 |
| (DL+FL) DAC (Ours)     | 74.8   | 76.4 81.1 73.1 66.0 44.6 85.9 83.1 88.1 56.5 76.8 74.0 84.3 86.1 83.1 75.5 47.7 74.1 77.5 86.1 75.7 |

Table 5. Object detection results on PASCAL VOC2007 Dataset.

| Model                  | Mean Average Precision (%) |
|------------------------|-----------------------------|
| SSD [15] (Original)    | 76.5                        |
| Channel Decomp. [32]   | 62.2 60.0 52.4              |
| Spatial Decomp. [8]    | 63.1 62.2 60.6              |
| DAC (Ours)             | 74.8 71.4 60.8              |

achieves higher accuracy than other schemes. In Table 4, we list the details of the detection results on PASCAL VOC2007 testing set. Comparing the results of DAC to the original model, one can see that decomposing the model using DAC does not impact the performance of the model too much, for all categories. The change of the accuracy happens on each category within a small range.

Figure 9 shows the visualized object detection results on PASCAL VOC2007 testing set. From the first two samples, one can see that after being decomposed, the model can still correctly detect objects. The locations and sizes of the detected bounding boxes have small changes. The third sample is an example that the original model does not detect an object (the bottle) that is successfully detected by our decomposed model. The fourth sample shows an extra false positive example (an unexpected potted-plant is detected), the fifth sample is a missing example (miss the car on the right), and the last sample is an example that the detected label changed (from bird to dog). Please refer to our Appendix for more visualized results.

5. Conclusion

In this paper, we propose a novel decomposition method, namely DAC. Given a pre-trained model, DAC is able to factorize an ordinary convolutional layer into two layers with much fewer parameters and computes its weights by decomposing the original weights directly. Thus, no training (or fine-tuning) or any data is needed. The experimental results on three computer vision tasks show that DAC reduces a large number of FLOPs while maintaining high accuracy of a pre-trained model.

We plan to evaluate the performance of DAC for deep learning models in other fields, e.g., voice recognition, language translation, etc. We also want to explore the possibility of adapting DAC on other types of layers, e.g. 3D convolutional layer, compared with other tensor decomposition formats [9, 11]. Another research direction is to combine low rank constraints with weight decomposition. These constraints could be convex regularizations like nuclear norm and Frobenius norm, or non-convex quasi-norms like Schatten p and TS1 [29, 28, 30].

6. Acknowledgements

We would love to express our appreciation to Jacob Nelson for his useful discussions.
References

[1] M. Abadi, A. Agarwal, P. Barham, E. Brevdo, Z. Chen, C. Citro, G. S. Corrado, A. Davis, J. Dean, M. Devin, et al. Tensorflow: Large-scale machine learning on heterogeneous distributed systems. arXiv preprint arXiv:1605.04467, 2016.

[2] J. M. Alvarez and M. Salzmann. Compression-aware training of deep networks. In Advances in Neural Information Processing Systems, pages 856–867. 2017.

[3] Z. Cao, T. Simon, S.-E. Wei, and Y. Sheikh. Realtime multi-person 2d pose estimation using part affinity fields. In The IEEE Conference on Computer Vision and Pattern Recognition (CVPR), 2017.

[4] F. Chollet et al. Keras. https://github.com/fchollet/keras, 2015.

[5] M. Everingham, L. Van Gool, C. K. I. Williams, J. Winn, and A. Zisserman. The PASCAL Visual Object Classes Challenge 2007 (VOC2007) Results. http://www.pascal-network.org/challenges/VOC/voc2007/workshop/index.html.

[6] Y. He, X. Zhang, and J. Sun. Channel pruning for accelerating very deep neural networks. 2(6), 2017.

[7] A. G. Howard, M. Zhu, B. Chen, D. Kalenichenko, W. Wang, T. Weyand, M. Andreetto, and H. Adam. Mobilenets: Efficient convolutional neural networks for mobile vision applications. arXiv preprint arXiv:1704.04861, 2017.

[8] M. Jaderberg, A. Vedaldi, and A. Zisserman. Speeding up convolutional neural networks with low rank expansions. arXiv preprint arXiv:1405.3866, 2014.

[9] Y.-D. Kim, E. Park, S. Yoo, T. Choi, L. Yang, and D. Shin. Compression of deep convolutional neural networks for fast and low power mobile applications. arXiv preprint arXiv:1511.06530, 2015.

[10] A. Krizhevsky and G. Hinton. Learning multiple layers of features from tiny images. Technical report, Citesee, 2009.

[11] V. Lebedev, Y. Ganin, M. Rakubha, I. Oseledec, and V. Lemptipsky. Speeding-up convolutional neural networks using fine-tuned cp-decomposition. arXiv preprint arXiv:1412.6553, 2014.

[12] X. Li and M. C. Chuah. Sbgar: Semantics based group activity recognition. In Proceedings of the IEEE Conference on Computer Vision and Pattern Recognition, 2017.

[13] X. Li and M. C. Chuah. Rehar: Robust and efficient human activity recognition. In Applications of Computer Vision (WACV), 2018 IEEE Winter Conference on. IEEE, 2018.

[14] T.-Y. Lin, M. Maire, S. Belongie, J. Hays, P. Perona, D. Ramanan, P. Dollár, and C. L. Zitnick. Microsoft coco: Common objects in context. In European conference on computer vision, pages 740–755. Springer, 2014.

[15] W. Liu, D. Anguelov, D. Erhan, C. Szegedy, S. Reed, C.-Y. Fu, and A. C. Berg. Ssd: Single shot multibox detector. In European conference on computer vision, pages 21–37. Springer, 2016.

[16] C. Louizos, M. Welling, and D. P. Kingma. Learning sparse neural networks through l0 regularization. arXiv preprint arXiv:1712.01312, 2017.

[17] N. Ma, X. Zhang, H.-T. Zheng, and J. Sun. Shufflenet v2: Practical guidelines for efficient cnn architecture design. In Proceedings of the European Conference on Computer Vision (ECCV), pages 116–131, 2018.

[18] P. Molchanov, S. Tyree, T. Karras, T. Aila, and J. Kautz. Pruning convolutional neural networks for resource efficient inference. arXiv preprint arXiv:1611.06440, 2016.

[19] O. Russakovsky, J. Deng, H. Su, J. Krause, S. Satheesh, S. Ma, Z. Huang, A. Karpathy, A. Khosla, M. Bernstein, et al. Imagenet large scale visual recognition challenge. International Journal of Computer Vision, 2015.

[20] M. Sandler, A. Howard, M. Zhu, A. Zhmoginov, and L.-C. Chen. Mobilenetv2: Inverted residuals and linear bottlenecks. pages 4510–4520, 2018.

[21] T. Sheng, C. Feng, S. Zhuo, X. Zhang, L. Shen, and M. Aleksic. A quantization-friendly separable convolution for mobile networks. arXiv preprint arXiv:1803.08607, 2018.

[22] K. Simonyan and A. Zisserman. Very deep convolutional networks for large-scale image recognition. ICLR, 2015.

[23] W. Wen, C. Xu, C. Wu, Y. Wang, Y. Chen, and H. Li. Coordinating filters for faster deep neural networks. In The IEEE International Conference on Computer Vision (ICCV), 2017.

[24] Y. Xu, Y. Wang, A. Zhou, W. Lin, and H. Xiong. Deep neural network compression with single and multiple level quantization. arXiv preprint arXiv:1803.03289, 2018.

[25] P. Yin, S. Zhang, J. Lyu, S. Osher, Y. Qi, and J. Xin. Binaryrelax: A relaxation approach for training deep neural networks with quantized weights. arXiv preprint arXiv:1801.06313, 2018.

[26] P. Yin, S. Zhang, J. Lyu, S. Osher, Y. Qi, and J. Xin. Blended coarse gradient descent for full quantization of deep neural networks. arXiv preprint arXiv:1808.05240, 2018.

[27] P. Yin, S. Zhang, Y. Qi, and J. Xin. Quantization and training of low bit-width convolutional neural networks for object detection. Journal of Computational Mathematics, 37(3):349–360, 2019.

[28] S. Zhang and J. Xin. Minimization of transformed l1 penalty: Closed form representation and iterative thresholding algorithms. Communications in Mathematical Sciences, 15(2):511–537, 2017.

[29] S. Zhang and J. Xin. Minimization of transformed l1 penalty: theory, difference of convex function algorithm, and robust application in compressed sensing. Mathematical Programming, 2018.

[30] S. Zhang, P. Yin, and J. Xin. Transformed schatten-1 iterative thresholding algorithms for low rank matrix completion. Communications in Mathematical Sciences, 15(3):839–862, 2017.

[31] X. Zhang, X. Zhou, M. Lin, and J. Sun. Shufflenet: An extremely efficient convolutional neural network for mobile devices, 2017.

[32] X. Zhang, J. Zou, K. He, and J. Sun. Accelerating very deep convolutional networks for classification and detection. IEEE transactions on pattern analysis and machine intelligence, 38(10):1943–1955, 2016.