Classical Simulation of Variational Quantum Classifiers using Tensor Rings

Dheeraj Peddireddy, Vipul Bansal, and Vaneet Aggarwal

Abstract

In recent times, Variational Quantum Circuits (VQC) have been widely adopted to different tasks in machine learning such as Combinatorial Optimization and Supervised Learning. With the growing interest, it is pertinent to study the boundaries of the classical simulation of VQCs to effectively benchmark the algorithms. Classically simulating VQCs can also provide the quantum algorithms with a better initialization reducing the amount of quantum resources needed to train the algorithm. This manuscript proposes an algorithm that compresses the quantum state within a circuit using a tensor ring representation which allows for the implementation of VQC based algorithms on a classical simulator at a fraction of the usual storage and computational complexity. Using the tensor ring approximation of the input quantum state, we propose a method that applies the parametrized unitary operations while retaining the low-rank structure of the tensor ring corresponding to the transformed quantum state, providing an exponential improvement of storage and computational time in the number of qubits and layers. This approximation is used to implement the tensor ring VQC for the task of supervised learning on Iris and MNIST datasets to demonstrate the comparable performance as that of the implementations from classical simulator using Matrix Product States.

Index Terms

Tensor Networks, Supervised Learning, Variational Quantum Circuits

I. INTRODUCTION

Quantum computing has been demonstrably proven to be superior than classical computing when solving problems such as searching unstructured databases [1] or factorization of large numbers [2]. Due to the rising interest in the computational capabilities of noisy intermediate scale quantum (NISQ) computers and the availability of experimentation platforms, there has been a rapid growth in the development of circuit based algorithms. VQCs or Parametrized Quantum circuits are one such class of the circuit based algorithms that has been studied extensively [3], specifically in context of their capabilities in solving various combinatorial optimization problems and intrinsic energy problems of molecules which were either intractable or computationally expensive using classical devices. These studies have also been extended to use VQCs as a replacement for artificial neural networks (ANN) in discriminative and generative tasks. Despite its infancy, there are numerous works exploring the algorithmic aspects and applications of VQCs. Several works have developed Quantum Approximate Optimization Algorithm and its generalizations to train parametrized quantum circuits using classical optimizers [4], [5], [6]. VQCs in the context of supervised learning, have first introduced in Mitarai et al. [7] which theoretically and numerically proved that quantum circuits can approximate non-linear functions similar to neural networks. Circuit based algorithms have been developed to emulate several methods from the classical machine learning literature such as Long Short-Term Memory [8] and Convolutional Networks [9], [10]. It has also been shown that that the VQCs have a better expressive power compared to neural networks [11].

Despite several studies indicating the promise of quantum superiority in machine learning, a key challenge in the current era of quantum computing is the limited quantum resources which in turn limits the number of qubits and the circuit depth. The other challenge is the inherent tendency of quantum devices towards decoherence, random gate errors and measurement errors which might severely limit the training of the variational circuits on quantum simulators. A possible solution to address these issues is to implement the training classically before deploying them on quantum simulators in order to avoid the compounding...
effects of decoherence throughout the numerous iterations usually required for learning. However, the simulation of quantum computations on a classical device grows exponentially harder in both the number of qubits and the circuit depth or the number of layers. Therefore it is pertinent to study the scope of classical simulation of quantum systems in the realm of learning not only to assist in the training, but also to validate the results from the quantum devices. Moreover, the much sought after quantum advantage is based on the assumption that there exists some tasks which can be solved much easily using the quantum devices over classical devices, so it is imperative to study the boundaries of classical simulation. One of the well studied approaches to compress the information in the quantum state is using the matrix product states (MPS) \[12\]. A recent work from Zhou et al.\[13\] has constructed a truncation technique that simulates a real quantum device by inducing a noise into the MPS approximation of a quantum state. This work extends upon the previous literature, by considering a noisy tensor ring approximation to train VQCs for supervised learning over popular datasets.

We note that the MPS (tensor train or TT) has been used extensively for scalable simulation of quantum circuits. However, such a decomposition suffers from the following limitations: (i) TT model requires rank-1 constraints to the border factors i.e., TT ranks are typically small for near-border factors and large for the middle factors, and (ii) the multiplications of the TT factors are not permutation invariant. In order to alleviate these issues, researchers have started to use cyclic MPS or tensor ring (TR) representation in the classical machine learning \[14\], \[15\], \[16\]. TR decomposition removes the unit rank constraints for the boundary tensor factors and utilizes a trace operation in the decomposition. The multilinear products between factors also have no strict ordering and the factors can be circularly shifted \[14\] allowing for all ranks to be the same. TR representation has been shown to significantly outperform TT representation for data completion \[14\], compression of classical neural networks \[15\], etc. These properties allow for a better approximation of circular entanglement in quantum circuits motivating the use of TR representation for compressing the quantum state in the VQC in this paper. This manuscript proposes a classical algorithm, Tensor Ring Variational Quantum Circuit (TRVQC) which represents the quantum state using a low-rank tensor ring and the transformations corresponding to the single and two-qubit gates in the VQC are applied to the TR while retaining its structure. In a naive Tensor Network approximation, two-qubit tranformations do not preserve the low rank of a tensor ring which TRVQCs address by approximating the resultant high rank tensor using truncated singular value decomposition.

To the best of authors’ knowledge, this is the first work on using scalable TR based VQCs for classification. We note that the approximation of two-qubit gates using singular value decomposition brings non-linearity in the operations, and is inherently similar to adding the ReLU or soft-max operations in classical neural networks. We believe that this non-linearity further helps in the selection of the learnable parameters thus helping achieve improved performance. Furthermore, VQCs have been
Fig. 2: Illustration of tensor ring decomposition of a rank-d tensor

popularized for their robustness to noise making them well-suited for NISQ era computers indicating that TRVQCs can share a similar robustness to the noise added through truncation.

The rest of the manuscript is organized as follows: Section II discusses the related literature corresponding to the approximation methods and their utility in QML. Section III-A discusses the workings and general architecture of VQCs. Section III-B introduces the mathematical notion of the TR approximation of a quantum state and the transformation of the TR with respect to the single and two qubit rotations. Section III-C describes the supervised learning algorithm that utilizes the TR approximation and transformation techniques from Section III-B. Section IV demonstrates the results from the experiments comparing the proposed architecture with a Matrix Product State simulator accompanied by a discussion on the storage and computational complexity. Finally, Section V concludes the manuscript by briefly summarizing the results from the work and discussing limitations and future directions of research.

II. RELATED WORK

Over the years, MPS and its generalizations have been used extensively in the classical simulation of quantum circuits [17], [18] owing to their capability of accurately capturing low to moderate entanglements in many-qubit quantum states. Several approximate techniques have been developed using the MPS parametrization such as Density Matrix Renormalization Group (DMRG) [19]. Projected entangled pair states (PEPS) [20] and multi-scale entanglement renormalization ansatz (MERA) [21]. Qiskit, a popular quantum programming platform builds upon the work proposed in Vidal et al. [17] to implement quantum circuits by simulating the quantum state using Matrix Product States. However using this method, tensor size can grow exponentially when there are a large number of two qubit gates in the circuit. To address this issue, Zhou et al. [13] proposed a truncation based on the singular values of the entangled tensors, which effectively represent the decoherence which limits the amount of entanglement that can be built into a quantum state in real quantum devices. This work was demonstrated to have a linear complexity in worst case when evaluating expected values from a quantum circuit at a small cost to accuracy. However, this technique was not evaluated on VQCs which are robust to the noise.

In the context of machine learning, MPS along with Tree Tensor Network approximation of quantum circuits have been successfully demonstrated in performing binary classification [22]. PEPS-based [23] and MPS-based [24] frameworks have been used for the task of image classification which utilizes the automatic gradients from the open source libraries. Although promising in cases of low entanglement, these works still suffer from the same problem as in Vidal et al. [17]. A quantum-inspired framework is proposed in Stoudenmire et al. [25] which uses tensor networks to represent the weight matrix in classical supervised learning models and train the tensor network directly. This work uses a singular value truncation method similar to the one in Zhou et al. [13] but the network architectures were developed independent of VQCs and do not involve quantum gates which are a crucial aspect of VQC-based learning algorithms. To address this shortcoming, we propose a method that utilizes a similar truncation method to retain the low-rank tensor ring as the unitary transformations from the VQC are applied to the TR representation of the quantum state.
III. Methodology

A. Variational Quantum Circuits

VQCs are quantum circuits with unitary rotation gates parametrized by trainable parameters. Several quantum algorithms have been developed to train the aforementioned VQCs for tasks like binary optimization, approximation, and classification. These variational quantum algorithms are usually quantum-classical hybrid algorithms where the loss function is evaluated using unitary transformations on quantum simulators and the parameters of the circuit are trained using classical optimization algorithms. VQCs can be used in similar applications as that of neural networks which primarily include function approximation. Quantum circuits use two qubit gates (usually controlled rotations) to encode entanglement into the quantum state which acts analogous to the activation functions in neural networks. Note that the quantum circuits (excluding measurement) are linear reversible transformations and multitude of classical machine learning literature has established the significance of non-linearity in function approximation. This will be one of the key changes in our approximation compared to the previous studies, which will bring in non-linearity into the approximation.

We describe the workings of a general VQC along with a pictorial representation of an example in Figure 1. Initially, the classical data is encoded into the qubits using rotations parametrized by the input data. The prepared qubit state is then transformed through a series of unitary rotation parametrized by trainable weights and entanglements. A subset of the transformed qubits are then measured to obtain the output in the form of expected value. The expected values are decoded into the appropriate class labels. A loss is computed akin to the neural networks using the decoded output and the true labels from the dataset. The free parameters are then updated using classical optimization methods like gradient descent, Adam, BFGS, etc. The trained circuit can then be used to predict the labels of the test data.

B. Tensor Ring Parametrization of VQC

Quantum circuits become too difficult to simulate on classical computers for larger number of qubits since the storage and computation is exponential in the number of qubits. Tensor networks [26] can be thought of as a graphical representation of tensors, where each node in the graph is a tensor with a finite rank (a rank-0 tensor is a scalar, rank-1 tensor is a vector, rank-2 tensor is a matrix etc.). A tensor ring is a tensor network which aims to represent a higher order tensor by a sequence of 3rd order tensors that are multiplied circularly as shown in Figure 2. A higher order tensor $\tau$ of rank $d$ is decomposed into $d$ tensors denoted by $M^{(k)}$ each of rank 3 spanned by the indices $\{n_k, r_k, r_{k+1}\}$ where $r_k$ and $r_{k+1}$ represent the bond dimension (or elements of the rank vector).
It has been shown that the tensor rings are a better and more effective compression algorithm than tensor trains (or MPS format) \[14, 15\], which motivates our choice. TT representation has lower border ranks and larger interior ranks which limit an efficient representation, while all ranks could be made similar in tensor ring format and thus decreased ranks helps achieve better representation at the same number of parameters. We also note that the many ansatz or architectures of VQCs have an efficient representation, while all ranks could be made similar in tensor ring format and thus decreased ranks helps achieve format) \[14\], \[15\], which motivates our choice. TT representation has lower border ranks and larger interior ranks which limit assume the training algorithm, large, the tensor ring would be able to capture the exact quantum state information at a computational cost. When designing qubit. A one qubit rotation (represented by a unitary matrix $U$) on $n^{th}$ qubit is given by the following equation:

$$M'(n)_{\mu'_{n-1} \mu_n}^{i_n} = \sum_{i_n} U_{i_n, i_n} M(n)_{\mu'_{n-1} \mu_n}^{i_n}$$

In order to perform a two qubit gate transformation on qubits $n$ and $n + 1$, we first transform the tensor ring into an orthogonal form centered around the qubits of interest. A series of operations are performed followed by a singular value decomposition as shown below. The two tensors at $n$ and $n + 1$ are first contracted along the shared bond index which is given by:

$$T_{\mu_{n-1} \mu_{n+1}}^{i_n i_{n+1}} = \sum_{\mu_n} M(n)_{\mu_{n-1} \mu_n}^{i_n} M(n + 1)_{\mu_n \mu_{n+1}}^{i_{n+1}}$$

Fig. 4: Circuit architecture for the Iris Classification problem
The two qubit gate \((U, \text{ reshaped into } U_{i_n' i_{n+1}' i_n i_{n+1}})\) is then applied on the two qubit tensor computed in the previous equation:

\[
(T')_{i_n' i_{n+1}' i_n i_{n+1}} = \sum_{i_n i_{n+1}} U_{i_n' i_{n+1}' i_n i_{n+1}} T_{i_n i_n i_{n+1}}
\]  

(4)

Finally, we reshape the tensor \(T'\) into a matrix of shape \((i_n' \times \mu_{n-1}) \times (i_{n+1}' \times \mu_{n+1})\), and perform singular value decomposition of the matrix:

\[
(T')_{i_n' i_{n+1}' i_n i_{n+1}} = \sum_{\mu_n} X_{i_n \mu_{n-1}} S_{\mu_n} Y_{i_{n+1} \mu_{n+1}}
\]  

(5)

where \(X\) and \(Y\) matrices are composed of the orthogonal vectors and \(S_{\mu_n}\) contains the singular values of the matrix \(T'\). The matrix has \(2\chi\) singular values irrespective of the two qubit gate structure where \(\chi\) denotes the bond dimension of the tensor ring. We then truncate the \(S_{\mu_n}\) matrix to keep only the \(\chi\) largest singular values and the resulting matrix is denoted by \(S'_{\mu_n}\). \(X\) and \(Y\) are truncated to only keep the orthogonal vectors corresponding to the \(\chi\) largest singular values. The new tensors corresponding the two qubits are given by:

\[
M'(n)_{i_n \mu_{n-1} \mu_n} = X_{i_n \mu_{n-1} \mu_n} S'_{\mu_n}
\]  

(6)

\[
M'(n + 1)_{i_n \mu_n \mu_{n+1}} = Y_{i_{n+1} \mu_n \mu_{n+1}}
\]  

(7)

Without the approximation, the operation of 2-qubit gate will be exponential in the number of qubits, the above operations are \(O(1)\) in the number of qubits, and thus helps scalability of the approach. The single qubit and two qubit transformations are demonstrated in Figure 3.

Algorithm 1 Training TRVQC on a single batch

**Input:** Input data , ground truth, VQC ansatz (U)

**Parameters:** Weight vector (\(\theta\)) corresponding to the variational layers, Optimization hyperparameters

**Output:** Updated weight vector

1: for each sample in batch do
2: Compute the tensor ring decomposition corresponding to the initial state \(|0\rangle^\otimes N\) which is a series of \(\chi \times \chi \times 2\) tensors with only the element at index (1,1,1) as 1 and rest of the elements as zeros
3: for each gate \(U\) in \(\mathbb{U}\) do
4: if \(U \in \mathbb{C}^{2 \times 2}\) then
5: Transform the TR following the procedure from the equation 2
6: else if \(U \in \mathbb{C}^{4 \times 4}\) then
7: Transform the TR following the procedure from equations 3,7
8: end if
9: end for
10: Contract the transformed tensor ring to obtain the full transformed quantum state
11: Choose bitstrings corresponding to the measurements to assign to the classification labels
12: Compute sigmoid probabilities of each label from the amplitudes of the corresponding bitstrings
13: Compute cross-entropy loss using ground truth and the probabilities
14: end for
15: Aggregate loss over batch size
16: Compute the gradients of the cross-entropy loss with respect to weights of the variational layers
17: Update the weights using classical gradient-based optimization methods

C. Training

We first explain the overall VQC architecture followed by the training procedure. We prepare the initial \(N\) qubit state as \(|0\rangle^\otimes N\), which is then converted to a TR representation as \(M(i)\) which is a \(\chi \times \chi \times 2\) tensor with only the element indexed
TABLE I: Test accuracy from training VQCs using the proposed tensor ring method (denoted by TR-VQC) benchmarked against naive Matrix Product State approximation (denoted by MPS-VQC) with varying number of qubits and circuit ansatz on Iris and reduced MNIST datasets.

| Dataset  | #qubits | TR-VQC | MPS-VQC |
|----------|---------|--------|---------|
|          | 1 layer | 2 layer | 3 layer | 1 layer | 2 layer | 3 layer |
| Iris     | 4       | 67.37  | 80.53  | 82.63   | 73.16   | 75.79  | 83.68 |
| reduced  | 4       | 81.67  | 83.73  | 77.97   | 79.66   | 81.02  | 77.63 |
| MNIST    | 8       | 75.93  | 81.69  | 80.02   | 57.96   | 65.42  | 67.11 |

To illustrate the capabilities of the proposed method for simulating VQCs, we perform experiments using various circuits with different number of qubits and layers on two datasets; Iris and a subset of MNIST containing classes 3 and 7. We also compare these results against the same circuits implemented on Qiskit’s classical simulator using full Matrix Product States. As discussed before, Qiskit’s simulator uses the work from Vidal et al. [17] which can grow exponentially in cases of high entanglement i.e. large number of two-qubit gates which is increasingly possible with more number of qubits and layers. Tensor ring rank $\chi$ is set to 8 for all experiments unless otherwise specified. It is to be noted that optimizing the circuit ansatze for performance as discussed in Du et al. [29] is left out of the scope of the simulations. All the results are averaged over 5 runs of training with different initialization.

### A. Iris Classification

The Iris data set includes four features and three labels. The dataset is split in a ratio of 3:1 for training/testing data. The circuit ansatz chosen for the problem is illustrated in Figure 4. The features are normalized before being encoded into the qubits. The initially prepared 4-qubit state of $|0000\rangle$ is decomposed into a tensor ring as described before. The features $\psi$ extracted from the data are encoded onto the qubits using a layer of $R_x$ gates. This is followed by a sequence of CNOT gates entangling all the consecutive qubits. After this, a sequence of single-qubit gates, i.e., $R_x$, $R_y$, and $R_z$, are applied to each qubit with trainable parameters. Here, $R_x(\cdot)$, $R_y(\cdot)$, and $R_z(\cdot)$ are rotation gates about the x, y, and z axes respectively on the Bloch sphere. The CNOT gate is represented by a connection between two qubit “wires” as shown in Figure 4. The circular end of
the connection represents the target qubit and the dot end represent the control qubit. Out of the $2^4$ possible measurements obtained from the VQC, we select amplitudes of 3 measurements (bitstrings) for representing the 3 classes of the dataset. We further apply softmax function to selected measures to convert the expected values to class probabilities. Then, we utilize a cross entropy loss and back-propagate to obtain gradients for each trainable parameter. In this experiment, we use an Adam optimizer with a learning rate of 0.01 for 50 epochs for a batch size of 4. The simulations are repeated with three circuits containing 1, 2 and 3 parametrized layers. The results of the experiments along with the results from the Qiskit simulations are listed in Table I.

B. Binary Classification: MNIST

We perform a binary classification on MNIST dataset with classes 3 and 7 (among the ten classes from 0 to 9). We choose 230 samples with labels 3 and 7 (referred to as reduced MNIST in this manuscript) to be split into training and test datasets to facilitate faster training. We have also chosen a smaller dataset for effective comparison against Qiskit’s algorithm which can prove harder to train over larger datasets. MNIST dataset consists of images of size $28 \times 28$, which is reduced to a smaller feature vector using Principal Component Analysis (PCA). We generate a feature space of dimension 4 and 8 for multiple experiments. The circuit structure uses the same layer as the experiment in IRIS dataset, however multiple experiments are conducted by repeating these layers multiple times as seen in Table I. The initially prepared qubit state $|0000\rangle$ for a 4 dimension feature space is decomposed into a tensor ring as described before. We select the final measurements $|0000\rangle$ and $|1111\rangle$ as output values for 2 classes (The same method, scaled appropriately is used to encode outputs for experiments with larger number of qubits). We further apply softmax function to selected measurements to obtain class probability and compute the cross-entropy loss and backpropagate to obtain gradients. The VQC is trained with an Adam optimizer with a learning rate of 0.01 and a batch size of 4 for 50 epochs. The accuracy from the simulations are reported in the Table I.

C. Discussion

The overall results are summarized in Table I. We found 4 and 8 to be the optimal number of qubits for performance for the given datasets. Increasing further to 16 qubits led to a drastic reduction in the accuracy for both the proposed method and the benchmark possibly due to overparametrization. In most cases, the tensor ring parametrized VQC performs comparably or outperforms the implementation of MPS-VQC on Qiskit with full quantum state information owing to the cyclic structure and the additional non-linearity induced by the truncated singular value decomposition over the two qubit gate transformations. In order to observe the effect of tensor ring rank on the performance of VQCs, a series of experiments using various ranks are conducted with 4 qubit and 2 layer circuit architectures with reduced MNIST data whose results are illustrated in Figure 5a. It is to be noted that the accuracy does not change significantly with an increasing TR rank indicating that a considerable

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**Fig. 5:** (a) Performance of Tensor Ring VQC with respect to varying rank for a 4 qubits, 2 layer circuit on reduced MNIST data (b) Average iteration times with varying number of qubits and circuit depth with reduced MNIST data
compression can be achieved using smaller ranks without losing out on the performance. We also plot the average iteration times (an iteration involves a forward pass and parameter optimization for a given batch size) with respect to the number of qubits and circuit layers in to demonstrate the linear complexity of the TRVQCs.

We note that we never store the full quantum state, unlike in standard quantum circuit. So, at each step of the operation, we only store the $N$ tensors, giving an overall storage complexity of $O(N\chi^2)$, where the tensor-ring rank $\chi$ is a hyper-parameter. For computational complexity, each computation of single or two-qubit gate in the tensor ring VQC is $O(1)$, and thus the overall complexity of the forward pass is $O(Nd)$, or the size of the circuit/number of gates. Similarly, each backpropagation round is the same complexity. Thus, each iteration of training has complexity $O(Nd)$ making the overall approach scalable in the number of qubits. In order to evaluate the final amplitudes which we will use to map to class probabilities, we contract the transformed tensor ring to obtain the final quantum state. It is to be noted that although the computational complexity of tensor ring contractions is $O(N\chi^3)$, the storage complexity would explode to $O(2^N)$ upon contraction since it results in the full quantum state. Although not utilized in this work, more efficient methods can be used to contract the tensor network as discussed in Ferris et al. which uses Monte Carlo simulations to compute the expectation, reducing the computational complexity to $O(N\chi^2)$.

V. Conclusion

In this work, we propose a new technique to train Variational Quantum Circuits for supervised learning on classical simulators using an approximate tensor ring representation of the quantum state. The computational time of the circuit evaluation grows linearly in the number of qubits, $N$, and the depth of the circuit, $d$, with the proposed method, as compared to the exponential growth in the simulation of ideal quantum circuits. We train the Tensor Ring Variational Quantum Circuits using the proposed approach on Iris and a subset of MNIST dataset. The performance is significantly better than the implementation of VQCs on classical simulators using full state MPS approximation in case of MNIST data. This demonstrates that the proposed architecture using TR representation could possibly outperform methods that don’t use approximation. Given the linear complexity as demonstrated by the iteration times from the simulations, scalability in number of qubits and layers can be achieved more efficiently using the proposed approximation which helps in larger scale applications along with a potential improvement in performance. It is also to be noted that the proposed approach can be used to train VQCs on classical devices before implementing on quantum simulators potentially reducing or eliminating any training on the quantum simulators.

We note that the quantum circuit operations (except measurement) are linear in the Hilbert space, while the added approximation of two-qubit gates bring non-linearity in the circuit, similar to Rectified Linear Unit (ReLU) or soft-max in the classical neural networks. More rigorous work is required to identify theoretical guarantees for improved performance with the addition of non-linearity in VQCs. One of the major challenges in training large scale quantum circuits is the barren plateau effect which demands initialization exploration to achieve comparable results. Barren plateaus also limit the performance of the algorithms implemented on tensor network based classical simulators, including the proposed method wanting for methods that address the issue. As for future work along this direction, building up on the limitations discussed, barren plateau mitigation methods specific to the tensor ring approximations must be studied for scalability. Another potential application of the proposed work would be as an initialization technique for QML algorithms before being implemented on quantum processors. It would also be interesting to see the performance of proposed approach replacing neural networks in tasks like Reinforcement Learning or Generative Adversarial Networks, etc.

APPENDIX

The matrix representation of some of the commonly used gates in the manuscript are listed below:

$$R_x(\theta) = \begin{bmatrix} \cos(\theta/2) & -\sin(\theta/2) \\ -\sin(\theta/2) & \cos(\theta/2) \end{bmatrix}$$

$$R_y(\theta) = \begin{bmatrix} \cos(\theta/2) & -\sin(\theta/2) \\ \sin(\theta/2) & \cos(\theta/2) \end{bmatrix}$$
\[ R_z(\theta) = \begin{bmatrix} e^{-i\theta/2} & 0 \\ 0 & e^{i\theta/2} \end{bmatrix} \]

\[ H = \frac{1}{\sqrt{2}} \begin{bmatrix} 1 & 1 \\ 1 & -1 \end{bmatrix} \]

\[ CNOT = \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 \\ 0 & 0 & 1 & 0 \end{bmatrix} \]

\[ R(\alpha,\beta,\gamma) = \begin{bmatrix} \cos(\alpha/2) & -e^{i\gamma}\sin(\alpha/2) \\ e^{i\beta}\sin(\alpha/2) & e^{i\beta+i\gamma}\cos(\alpha/2) \end{bmatrix} \]

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