Non-Linear Inductors Characterization in Real Operating Conditions for Power Density Optimization in SMPS

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Abstract
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Abstract: The exploitation of power inductors outside their linear region in switching converters can be achieved by raising the current until a decreasing of the inductance can be noticed. It allows using a smaller magnetic core increasing the power density of the converter. On the other hand, a detailed description of the magnetization curve including the temperature is required. Since this information is often not included in the inductor’s datasheets, this paper shows how to identify the behavior of an inductor when it is operated up to saturation and its temperature rises. In order to characterize the inductor in real operating conditions, a dedicated measurement rig has been developed. It consists of a switching converter that encompasses the
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**Keywords:** nonlinear magnetic, inductance measurement, power inductor, saturation magnetization, DC-DC power converter, power density.

1. Introduction

Inductors are crucial components of switch-mode power supply (SMPS) circuits where they are employed to store and release energy according to the operation of power switches. Currently, the inductor is often the most cumbersome component in an SMPS. In the next future, the continuous increase in switching frequencies will allow the integration of magnetic components with embedded windings directly in the PCB, a practical and cost-effective solution for a wide range of applications [1]. In order to increase the power density and reduce the weight of modern SMPSs, the inductors can be operated up to the so-called “practical limit” (defined also “linearity limit” or “saturation”) of power inductors [2]. This operation, where the differential inductance is reduced to half of its maximum value, is gaining attention. The behavior of inductors approaching saturation has already been of interest for low-frequency applications [3,4] due to its influence on the current in the circuit where it is employed. Also, in higher frequency applications concerning SMPS, the inductor can be advantageously exploited outside of the linear zone to reduce its cost and weight [2,5,6].

Among available magnetic materials for the inductor core, ferrite exhibits low losses and relatively high saturation induction; for this reason, it reveals suitable for power electronics applications [7]. In SMPS the inductor is operated with a DC current offset and AC components imposed by the converter operation. Moreover, the inductor’s non-linear operation modifies the triangular-shaped waveform of its current [8], raising its peak value and leading to a situation that can affect the stability of the converter. In fact, outside the linear zone, the current peak’s growth raises the losses, increasing the temperature. In general, the rise of the temperature influences the inductance, lowering its value. It can bring to a runaway situation requiring a more detailed thermal analysis [9–11].

This paper focuses the inductor’s behavior up to saturation, including the role of the temperature, considering the differential inductance reduced to half of its maximum value [2]. Recently, some papers analyzed SMPS inductors operated outside the linear zone. In [8,12–16], the time-domain waveforms were derived and compared to the experimental ones, with a good agreement, in different steady-state conditions using different models and approaches. Issues related to modeling the temperature effects have been assessed in [5], where current profiles for different temperatures of the inductor are obtained in steady-state. In [17] the thermal transient was analyzed, showing that, despite a lowering of the value of inductance due to the increase of temperature, small power inductors operating under partial saturation are not subjected to thermal drift. A model to reproduce thermal transient in SMPS simulations showing different current waveforms during the thermal transient is proposed in [10]; this approach adopts a behavioral model based on electrical parameters where the temperature is obtained as output. It should be noted that [17] derives the inductor temperature starting from the inductor total power dissipation and the inductor thermal resistance; even if the former can be calculated, the latter may depend on non-predictable conditions such as circuit layout, air flow and dust due to ageing. Moreover, it considers a fixed frequency control (FFC) for the converter. In this paper, we will discuss how the converter can be preserved by a variable frequency control (VFC).

All the papers mentioned above agree to adopt basic low power DC/DC converters (buck or boost) as case studies to test their methods since results can be re-assessed for few kW output power DC/DC converters. However, we show that, as the power is augmented, increased losses will contribute to lower the output voltage requiring a feedback correction. The voltage feedback can lead to overtake the current limit. Indeed, we aim to show the inductor operation in two different situations starting from the temperature measured
on the inductor. Even if it requires an additional measurement, i.e., the inductor temperature, it allows monitoring the proper and safe operation (sustainable saturation operation) in real operating conditions. Differently from the linear operation, the temperature of the inductor strongly influences the overall stresses. Besides, the effect of the feedback is discussed.

The inductor exploitation outside its linear zone requires a detailed characterization that often goes beyond the basic information supplied in the manufacturers’ datasheets; in fact, saturation is mostly defined as the point in which the inductance is reduced by 10% only, and the value of such a current is given at environment temperature [10]. However, the temperature is subjected to a relevant increase especially exploiting saturation. Besides, it is influenced by external factors like PCB layout, the proximity of the power switch heatsink, and air-cooling that are difficult to take into account. For this reason, we propose to measure the temperature at a suitable point of the inductor, retrieving information based on this value. A dedicated measurement system has been set-up for this purpose by the authors.

Measurement systems to retrieve the inductance value of inductors with ferrite core have been proposed in [18] and recently in [19]. However, those two papers deal with inductors under low power sinusoidal solicitations; therefore, the solutions are of limited interest for power electronics applications where inductors are simultaneously interested by a DC offset (average output current) and an AC signal (current ripple). Since the AC signal has a high-frequency content and must be amplified to a suitable level before the acquisition, an RF amplifier is often needed. Even if a lock-in amplifier can improve the noise-to-signal ratio [20], this approach generally leads to an expensive system. A measurement system addressing real operating conditions has recently been proposed in [21], where a large signal characterization is performed by using two suitable AC and DC generators to supply the inductor. An automated measurement system for core loss characterization is proposed in [22], where the attention is focused on transformers and inductors for SMPS. The heat generation and thermal model issues are discussed in [23] and [24], considering the inductor operated in the linear zone. A commercial system has been developed for testing inductors in quasi-real operating conditions exploiting a DC-bias current generator and a triangular current ripple generator placed in parallel with the inductor under test [25]. In the literature, the proposed approaches try to supply the inductor under test by emulating the operating conditions. The same approach has been followed by [26] adopting an SMPS to improve the method of [27] based on a power amplifier for modeling core losses. It has to be underlined that all papers considering the temperature of the inductor, to characterize its behavior, do not analyze the distribution of the temperature inside the core.

Based on these considerations, this paper analyzes the results obtained with a switching converter, driven by an automated measurement system that performs the test of power inductors in real operating conditions. The feasibility of such a system was previously examined in [28], where the circuit of the SMPS was controlled by a LabVIEW virtual instrument in order to vary the load. In this paper, a new complete version of the circuit is presented including the following improvements: a) the temperature is taken into account, which allows characterizing the inductor at constant temperature; b) the parasitic resistance of the winding is preliminarily estimated, and the related drop voltage is considered; c) the measurements are repeated to assess the mean value and the standard deviation of the results, giving an estimation of the error; d) a new inductor current sensor has been adopted to avoid any influence in the measurement; e) the PCB layout has been optimized to minimize parasitic inductances and consequent voltage ringing during commutation. The proposed system gives the core saturation curve versus inductance to be retrieved, as well as the magnetization curve profile, showing the inductance variation with the measured temperature. As an example of application, the characterization of two commercial inductors is proposed here, including them inside the SMPS circuit, retrieving the inductance vs. current curves at different temperatures. Differently from [18], the proposed approach can be generalized for power inductors; it is cheaper compared to [22], which uses a temperature chamber, and a commercial system such as [25] and can be built for laboratory purposes. The inductor thermal behavior and modeling are relevant to the DC/DC converter and EMI filter design [23,24,29–31]. Our contribution addresses this issue since the characterization always takes into account the temperature measured on the core. Finally, the knowledge on the inductor can be exploited in converters with different layouts since the measured temperature will represent the real operating conditions.
The paper is organized as follows. After the introduction, the main features of soft ferrites are presented in section 2. Section 3 describes the DC/DC converter and the measurement system, while section 4 contains a description of the virtual instrument developed to characterize the inductor and acquire the current profiles. Section 5 analyzes the error due to the method; two inductors model considering the temperature are presented in section 6, and finally, experimental results showing how the current profiles are influenced by temperature are given in section 7.

2. Magnetic Materials for Power Electronics

Among different magnetic materials used in power electronics, ferrites are common due to their low losses in a wide frequency range and high specific resistivity. Besides, the magnetic properties are isotropic, the Curie temperature is around 700°C, several fabrication techniques can be used and different shapes can be obtained. The saturation induction ranges from 0.25T to 0.45T (lower than iron and cobalt alloys) and the initial relative permeability ranges from 1,000 up to 20,000 for some new materials [7]. A higher cross-sectional area allows the use of ferrite for high power inductors to prevent saturation, increasing the weight and the cost of the component. The bulk resistivity is always high; it permits to distinguish the behavior of MnZn (0.1-10 Ω·m) and NiZn (10^4-10^6 Ω·m), depending on temperature and frequency. Such high resistivity is advantageous in keeping low the core losses even at higher frequencies where eddy current losses are dominant: the losses are inversely proportional to the resistivity of the core material and increase approximately with the square of the frequency. Moreover, the parasitic capacitance short-circuits the crystal boundaries at high frequencies, further decreasing resistivity [7]. It should be remarked that the ferrites mentioned above are “non-microwave ferrites”, meaning that they can be used only up to some hundreds of kHz. The magnetization curve of ferrites depends both on physical properties and on the manufacturing process. For this reason, the B-H loops at several frequencies and temperatures need to be measured directly on samples. For ferromagnetic materials, it is convenient to adopt the differential inductance, \( \frac{d\Psi}{di} \), defined as the incremental ratio on the plane \( \Psi \) vs. \( i \) (Figure 1), where \( \Psi \) is the total flux linkage, \( i \) is the magnetizing current, \( \Phi \) is the flux linkage per turn and \( N \) is the number of turns [2].

\[
L_{\text{dif}} = \frac{d\Psi}{di} = N \frac{d\Phi}{di} \quad (1)
\]

Figure 1. Example of a magnetization curve for a ferrite core
The practical limit in power electronics (the so-called linearity limit or saturation point) is defined as the point where the differential inductance is reduced, due to saturation, to half of its maximum value \[7\]. This definition sometimes differs from the value adopted by manufacturers, who consider the point at which the inductance is reduced by 10% only. It can be noted that the differential inductance coincides with the general inductance definition \( L = \frac{\Psi}{i} \) inside the unsaturated region. When the flux density exceeds saturation, due to reduction of inductance, a higher current variation is needed to reach the same flux variation as in the linear region. The effect of heating can be appreciated by comparing the hysteresis curves taken at different temperatures. In general, heating flattens the hysteresis curve, lowering the value at which saturation occurs [32]; such a lowering, due to the higher temperature, corresponds to a decrease of permeability and, consequently, to a lower inductance value. The abrupt fall of the inductance, caused by a similar variation of the permeability, can be avoided by introducing small air-gaps inside the magnetic path; it results in the “shearing” of the hysteresis loop [32]. Consequently, the permeability remains constant in a wide range, making the coil characteristics less dependent upon the initial permeability of the core. On the other hand, the lower inductance needs more turns with consequent more copper losses; thus, a trade-off is necessary [32]. The use of soft saturation ferrites is an alternative to materials with air-gap core.

3. The DC/DC converter and the measurement system

The circuit in which the inductors are tested is a buck converter working in continuous conduction mode (CCM). It is realized on a dedicated PCB. The converter switch is a power MOSFET IRFP150 with an IR2101 driver. The freewheeling diode is a MURB820PBF. The current through the inductor is measured by a Hall-effect current sensor ACS730 by Allegro.

The variable load is devised to obtain a constant current on a resistive load. A reference voltage, given by the output of a Digital to Analog Converter (DAC) MCP4822, controls an LM741 OP-AMP, which, together with an IRFP150 MOSFET and three 1 power resistors in parallel, connected between the source and ground, implement a current sink circuit. The MOSFET drain is connected to the buck converter’s output, thus acting as a constant current resistive load. Due to power dissipation, the MOSFET is mounted on a suitable heatsink.

The Arduino Uno board, connected to a notebook via USB, varies the DAC output voltage, the frequency and duty cycle of PWM signal, according to the data received from LabVIEW; moreover, it acquires the temperature of the inductor by an LM35DZ sensor stuck onto the core through a thermal pad. This information is also useful to keep the inductor temperature constant during measurements. It should be noted that the temperature of the inductor on the surface of the core can be considered uniform for the geometry, size and type of the inductors under study [9].

The test rig comprises a DC linear power supply (12V, 10A), a digital oscilloscope Siglent SDS 1104X-E, a personal computer and the PCB with the DC/DC converter including the IUT. For the measurements shown in this paper, the following parameters have been set: duty cycle of the DC/DC converter fixed at 50%, switching frequency set automatically by the system to limit the current ripple of the inductor below 0.4A (more details will be given in the next paragraphs), warm-up current equal to the saturation current that, at the lower temperature step, gives the 50% of the rated inductance value. Figure 2a shows all the parts described before: the circuit PCB, the control PC, the benchtop power supply and the oscilloscope with its three probes to measure the current and the voltage across the inductor.

As already anticipated, the measurement system is based on a DC/DC buck converter, where the inductor under test (IUT) is a part of the circuit. The variable load allows varying the DC component of the inductor current during measurements. The tests are performed on the same inductor with different DC operating point to obtain the inductance \( L_{adj} \) versus the inductor current \( i \) at different temperature points. In this way, the inductor is tested in real operating conditions.

The PWM square wave, controlling the switching MOSFET of the DC/DC converter, is generated by the Arduino Uno board with programmable frequency and duty cycle. Changing these parameters makes it possible to vary the amplitude of the current ripple in the IUT around the DC offset. The current signal, the
input and output voltages of the DC/DC converter are acquired by the oscilloscope that sends data to the virtual instrument developed with National Instruments LabVIEW. Once the signals have been acquired, the following formula derives the differential inductance value, where \( i \) and \( V_L \) are the current through the inductor and its voltage, respectively.

\[
L_{\text{dif}} = \frac{V_L}{\frac{di}{dt}} \quad (2)
\]

The current time derivative \( (\frac{di}{dt} ) \) is calculated during the conduction time \( (T_{\text{on}}) \) when the inductor is subjected to a constant voltage and the current grows like a ramp. Only about 90% of the ramp is considered for calculating the current slope, allowing a good estimation and excluding the oscillations during commutation; then, the current is sampled and its slope is retrieved based on linear regression. The current is sampled at 500Ms/s. The electric schematic of the buck converter is shown in Figure 2b.

Figure 2. (a) Measurement setup. (b) Simplified circuit schematic with the black components assembled on a PCB. (c) Virtual instrument interface.

4. The virtual instrument

The virtual instrument allows the management of the converter and the measurement system; besides, it arranges acquired data to be presented in graphic form. The main panel is shown in Figure 2c. The instrument manages the converter parameters and acquires the measurements from the oscilloscope and the temperature sensor. Moreover, it can: (i) calculate the parasitic resistance of inductor; (ii) plot the value of inductance \( L \) versus the inductor current \( i \) for given temperature; (iii) visualize the curves of inductance; (iv) store all the measurements data on the computer memory for off-line post-elaboration. Several virtual potentiometers and switches are available, through which the user can set the following parameters before starting the measurement: a) the nominal value of inductance; b) the maximum DC current to reach; c) the duty cycle of the converter; d) the voltage of the external power supply; e) the temperature at which the measurement must be performed; f) the current to heat the inductor when necessary. Besides that, it is possible to set the switching frequency of the DC/DC converter manually. Alternatively, it can be automatically chosen by the program according to the pre-set parameters.

After the communications with the oscilloscope and the Arduino board are initialized, the measure can start. Firstly, the parasitic resistance of the inductor is calculated; then, the temperature is brought to the set value by powering the DC/DC converter until the desired temperature is reached. Based on the geometrical characteristics of the core and on the material, its temperature can be assumed constant; for this reason, a thermochamber is not required [9]. The inductance evaluation is repeated for increasing DC current until the maximum pre-set current is reached; for each measurement, the core temperature is checked and eventually
Algorithm 1: Inductance measurement.

repeat if Inductor_Temp < Set_Temp //Temperature check
Converter-ON //Turn on the converter
Output_Load-Warm-up //Set a current to warm-up the inductor
end if
if Inductor_Temp > Set_Temp //Temperature check
Converter-OFF //Turn off the converter to cool down the inductor
end if
if Inductor_Temp == Set_Temp //Temperature check
Converter-ON //Turn on the converter
Voltage Acquisition //Acquire voltage waveform
Current Acquisition //Acquire current waveform
Inductance Calculation //Compute \( L_{\text{dif}} \) via (2)
Output_Load-Out //Reduce load and increase current
end if
until Inductor_Current < MaxCurrent //Stop measurement at maximum current
Converter-OFF //Turn off the converter

5. Error analysis

According to (2), inductance is calculated by dividing the voltage applied to the inductor (constant during \( T_{\text{on}} \) ) by the current slope. For the inductance calculation, the measurement of the current and its time derivative is repeated several times; the mean value of the calculated inductance is assumed as the true value, whereas the standard deviation of the results gives the error. Twelve acquisitions revealed, most of the time, as an acceptable trade-off, allowing the error to settle without overheating the inductor or excessively increasing acquisition time. By applying the error propagation on (2) and considering the voltage \( V_L \) as not affected by any error, the following expression is obtained:

\[
\Delta L_{\text{dif}} = \frac{\partial}{\partial (\Delta (Di))} \left( \frac{V_L}{Di} \right) \Delta (Di) = -\frac{V_L}{(Di)^2} \Delta (Di) \quad (3)
\]

where for the sake of clarity \( Di \) indicates the time derivative of the current in the denominator of (2). It can be noted that the error \( \Delta L_{\text{dif}} \) on the value of \( L_{\text{dif}} \) decreases when the derivative value increases. It means that the inductance measurement improves outside the linear region, where the current slope increases.

6. Results on inductors characterization

Two commercial inductors have been tested inside the developed buck converter; for each of them, the following information is retrieved: a) inductance versus temperature, b) saturation current versus temperature, c) magnetization curve. Then, the characterization of the core is compared with the data given by the manufacturer.

6.1. Inductor 1

The inductor 1 under test is the Coilcraft DO5010H-104 [33]. For this inductor, the datasheet shows an inductance of 100 \( \mu \text{H} \pm 20\% \) with a saturation current, giving 10\% inductance drop, of 3.0 A. The inductance curve measured at 25\°C is shown in Figure 3a, where the red circles are the mean inductance value for each current and the error bands represent the 95\% confidence interval. It can be noted that the value of inductance in the linear region lies within the tolerance interval given by the manufacturer. The manufacturer defines the saturation current as “the DC current at which the inductance drops 10\% from its value without current”. From this definition and considering a measured mean value of the nominal inductance at 25 \°C of 90 \( \mu \text{H} \), a value of about 3.2 A (at 81 \( \mu \text{H} \)) has been derived from the graph. Nevertheless, measurements were performed exceeding the 50\% inductance drop according to the literature [7] and coherently with the exploitation range for power applications. The corresponding current has been found as 4.0 A at 25 \°C. In all measurements, the error remained inside the tolerance exhibited by the manufacturer and, as expected, it reduces for higher currents.

These measurements have been repeated at temperature steps of 20 \°C up to 105 \°C. The corresponding curves are reported in Figure 3b. It can be noted that the saturation current decreases as the temperature increases, meaning that the higher the temperature, the greater the variations in current with the same voltage applied. Besides, the slope of the inductance plot in the saturation region slightly increases with temperature. Based on the inductance curve, the magnetic flux versus current can be retrieved by:

\[
\Psi = \int L_{\text{dif}} \text{di} \quad (4)
\]
This curve has the same shape as the magnetization curve since the flux is proportional to the induction $B$ and the current to the magnetic field $H$. Hence, it can be used to verify the extension of the linear zone with temperature. The flux curve calculations at $25\, ^\circ C$ and $105\, ^\circ C$ are shown in Figure 3c. These curves show that, at low temperatures, the linear zone has a broader range, while, for higher temperatures, the “knee” has reduced to about 2.3A. It represents the boundary of the linear zone.

Figure 3. Inductor 1 characteristics as a function of current and temperature: (a) inductance vs. current at $T_{\text{core}}=25\, ^\circ C$ with error band representing the 95% confidence interval; (b) inductance vs. current, (c) flux curve vs. current.

6.2. Inductor 2

The inductor 2 under test is the Coilcraft DO5010H-334 [33]. This inductor belongs to the same series as the previous one and shares the same core. The datasheet gives an inductance of $330\, \mu H \pm 20\%$ and defines the saturation current based on a drop of 10% as 1.9 A at $25\, ^\circ C$. This value is confirmed experimentally in Figure 4a considering the plot at $25\, ^\circ C$, while the corresponding flux curve is shown in Figure 4b. The characterization is extended to $105\, ^\circ C$ and to 2.2 A (current giving a drop of 50% of the rated value at $25\, ^\circ C$).

Figure 4. Inductor 2 characteristics as a function of temperature: (a) inductance vs. current, (b) flux curve vs. current.

6.3 Core characterization and comparison with data given by the manufacturer

The two considered inductors exploit the same core. Based on the obtained results, the core can be characterized by the saturation current versus inductance, provided at different temperatures. The manufacturer delivers this information only at $25\, ^\circ C$. [33]. It should be remarked that the manufacturer defines the saturation current as “DC current at which the inductance drops 10% from its value without the current”. It differs from the definition proposed by literature that considers a drop of 50%; for the sake of comparison, the values based on obtained results are calculated adopting the manufacturer’s definition.

The saturation current of an inductor ($I_{\text{sat}}$), as defined in [34], can be expressed as a function of the saturation flux density ($B_{\text{sat}}$) with $l_m$ the mean magnetic path length, $N$ the number of turns and $\mu_{\text{sat}}$ the permeability of the core at saturation, which is equal to the permeability of the core $\mu$ at zero current multiplied by a proportionality factor $C$ (0.5 for the literature standard, 0.9 for the manufacturer’s standard):\

$$I_{\text{sat}} = \frac{B_{\text{sat}} \cdot l_m}{\mu_{\text{sat}} \cdot N} = \frac{B_{\text{sat}} \cdot l_m}{\mu \cdot N} \quad (5)$$

The nominal inductance $L_{\text{nom}}$ is then calculated considering $A_c$, the area of the core section:

$$L_{\text{nom}} = \frac{\mu \cdot N^2}{l_m \cdot A} \quad (6)$$

A new expression linking the saturation current to the nominal inductance can be obtained combining (5,
6) and through a proportionality factor $K$ containing the geometrical and physical properties of the core:

$$I_{\text{sat}}(L_{\text{nom}}) = \frac{B_{\text{sat}}}{C} \sqrt{\frac{\mu A_c}{L_{\text{nom}}}} = \frac{K}{\sqrt{L_{\text{nom}}}} \quad (7)$$

From the experimental data regarding the inductor 1 at 25 °C, the proportionality factor $K$ can be evaluated by equation (7) multiplying both members by $\sqrt{L_{\text{nom}}}$, resulting in a value of $30.375 \times 10^{-3}$ (A/μH$^{0.5}$). Using this value, the saturation current as function of the nominal inductance, can be retrieved. This method is valid for all the inductors of the same series, sharing the same ferrite core. Such a comparison is highlighted in Figure 5a, showing an excellent fit with the datasheet information at 25°degC; the same result is obtained with the data retrieved from inductor 2, confirming the correctness of this approach.

Furthermore, the saturation current for inductor 1 ($I_{\text{sat-ind1}}$) depends from the temperature ($T_{\text{core}}$ considered in degC) and it can be interpolated by a straight line obtaining (8):

$$I_{\text{sat-ind1}}(T_{\text{core}}) = -0.0126 T_{\text{core}} + 3.5168 \quad (8)$$

By multiplying (8) by the square root of the nominal inductance (a mean measured value of 90 μH for the inductor 1), it is possible to retrieve the expression of the proportionality factor $K$ as a function of temperature; it should be noted that $K$ depends on the geometrical and physical properties of the core and is also temperature-dependent.

$$K(T_{\text{core}}) = (-0.1195 T_{\text{core}} + 33.363) \times 10^{-3} \quad (9)$$

Figure 5b shows the comparison between the data obtained from (7) with the expression of $K(T_{\text{core}})$ extrapolated from the measurement of inductors 1 and 2. Both expressions of $K$ result in similar values of the saturation current for the whole $L_{\text{nom}}$ range; nonetheless, a smaller range of inductance is shown in Fig 5b for the sake of tidiness. The quantitative data shown in Figure 5b are listed in Table 1.

**Table 1.** This Saturation currents at different temperatures with K derived from inductor 1 (100 μH) or from inductor 2 (330 μH).

| $L_{\text{nom}}$ (μH) | Saturation current @25 °C | Saturation current @60 °C | Saturation current @95 °C |
|-----------------------|---------------------------|--------------------------|--------------------------|
| 100                   | 3.038                     | 2.619                    | 2.151                    |
| 150                   | 2.480                     | 2.139                    | 1.797                    |
| 220                   | 2.048                     | 1.766                    | 1.484                    |
| 330                   | 1.672                     | 1.442                    | 1.212                    |
| 470                   | 1.401                     | 1.208                    | 1.015                    |

$$K_{\text{ind2}}(T_{\text{core}}) = (-0.1271 T_{\text{core}} + 33.585) \times 10^{-3}$$

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Figure 5. Simulations of the saturation current vs. inductance characteristic of the inductor family. The $K$ factor of (7) has been extrapolated from the experimental data of inductor 1 and 2: (a) Comparison of the simulation calculated with $K$ extrapolated from inductor 1 and inductor 2 with the information given by the datasheet at $T_{\text{core}}=25^\circ\text{C}$. The abscissa is on a logarithmic scale for better visualization. (b) Comparison of the simulated saturation currents obtained at different temperatures with $K$ extrapolated from inductor 1 and from inductor 2.

Since the expression of $K$ as the function of the temperature is known, it is possible to extend the manufacturer’s information, including the temperature behavior, to the whole family of inductors sharing the same core. Such dependence is presented in a 3D surface in Figure 6a, whereas the projection for a given nominal inductance (defined at 25°C) is presented in Figure 6b and at constant core temperature in Figure 6c. As previously remarked, $I_{\text{sat}}$ has a linear dependence from the temperature (Figure 6b). Moreover, at 105°C, the saturation current is 36% lower than its corresponding value at 25°C; this property is valid for all inductors sharing the same core. Such a drop cannot be ignored during the SMPS design process, especially when the inductor is exploited in partial saturation. Such analysis gives a complete characterization of the Coilcraft DO5010H-104 inductor family since each inductor of this series has the same ferrite core. This analysis can be extended to other inductors and is independent of the information available in the datasheet. It is worth noting that, although several inductors of the DO5010H-104 series cannot be measured by the system presented here due to the limited range of current provided by the power supply, however it is possible to extrapolate the characteristics of saturation current vs. core temperature through (7) for the whole family once that the proportionality factor $K$ is retrieved from the measurement of one suitable inductor of the same series.

Finally, this approach is helpful for retrieving information regarding the saturation and temperature behavior of power inductors in the non-linear region. The additional information can help the designers to adopt a cost-effective design of SMPS exploiting non-linear inductors, allowing them to address the adverse effects of saturation.

Figure 6. Saturation current behavior for the inductor family simulated from (7) and (9): (a) saturation current ($I_{\text{sat}}$) vs. nominal inductance ($L_{\text{nom}}$) vs. core temperature ($T_{\text{core}}$), (b) $I_{\text{sat}}$ vs $T_{\text{core}}$ for a given $L_{\text{nom}}$, (c) $I_{\text{sat}}$ vs. $L_{\text{nom}}$ at constant $T_{\text{core}}$. The inductance axis is on a logarithmic scale for better visualization.

7. Experimental analysis of the temperature effect

In order to assess the temperature impact on the inductor behavior, the two inductors previously characterized have been further investigated in real operating conditions. Some current profiles are shown to highlight how the temperature of the inductor influences the current shape and, consequently, the performance of the converter. The tests have been conceived based on the models of the two inductors that have been previously retrieved. The knowledge of the model is crucial since it allows to forecast the maximum current imposed by the lowering of the inductance.

For each inductor the test was performed starting from environmental temperature, with a fixed duty cycle and load. The mean current through inductor 1 is equal to 3.2 A, whereas the mean current through inductor 2 is equal to 1.7 A. In both cases we exceeded the manufacturer’s limit. The switching frequency has been chosen to reach a current near the saturation value (inductance reduced to half of its maximum value), obtained by the model in correspondence of 95°C. The oscilloscope outputs, shown in Figure 7, contain the diode voltage (blue trace), output voltage (green trace) and the inductor current AC component (red trace).
Three pictures corresponding to the core temperature at environment temperature (25 °C), 55 °C, and 95 °C are given for each inductor. The corresponding measurement values are listed in Table 2.

**Table 2**. Experimental measurement for different inductors and temperature conditions.

| Temperature (°C) | Output voltage (V) | Inductor current ripple (A) |
|------------------|--------------------|----------------------------|
| Inductor 1 ($L_{nom}=100$ μH) | 25 | 5.76 | 1.01 |
| | 55 | 5.65 | 1.65 |
| | 95 | 5.55 | 2.54 |
| Inductor 2 ($L_{nom}=330$ μH) | 25 | 4.16 | 0.71 |
| | 55 | 4.01 | 1.13 |
| | 95 | 3.84 | 1.74 |

7.1. Test on inductor 1

As concerns the inductor 1, the switching frequency is set to 38 kHz. Figure 7a shows the waveforms at the environmental temperature, where it can be noted that the current exhibits a triangular waveform corresponding to a linear behavior. The maximum value of the current in this operating condition can be measured by the oscilloscope where only the AC component is shown (the reference level corresponds to the mean current, i.e., 3.2 A), which is equal to 3.7 A. During operation, the temperature rises due to the losses in the magnetic core; figures 7b and 7c show that the current ripple in the inductor increases, as well as the maximum current. The current ripple measured at 95 °C is about 250% higher than the ripple at environmental temperature and the maximum current reaches 4.7 A (without considering the parasitic oscillations that can be filtered out). Moreover, a lowering of the output voltage can be appreciated; it is due to the increase of Joule drops; in particular, the output voltage from 5.76 V measured at 25 °C falls to 5.55 V at 95 °C. Since the converter is operated in an open-loop, there is no automatic increase of the duty cycle to compensate the voltage drop; however, considering that the voltage has decreased by 3.6 %, roughly an equal rise of the duty cycle should be expected; this corresponds to increase the conduction time of the power switch from 15 μs to 15.5 μs. In this case, the estimated peak of the current increases up to 4.9 A due to the nonlinearity.

7.2. Test on inductor 2

As concerns the inductor 2, the switching frequency is set to 18 kHz. Figure 7d shows the waveforms at environmental temperature. Also in this case, it can be noted that the current shows a triangular waveform corresponding to a linear behavior. In this case, the mean current through the inductor is equal to 1.7 A and the maximum current is 2.1 A. During operation, the temperature rises due to the losses in the magnetic core, and the current ripple in the inductor increases as well as the maximum current as shown in Figure 7e and Figure 7f. The current ripple measured at 95 °C is about 245% higher than the ripple at environmental temperature and the peak of the current reaches 2.7 A. The output voltage decreases from 4.16 V to 3.84 V corresponding to a lowering of 7.7%. To compensate it by feedback, the duty cycle has to be raised; it implies increasing the conduction time of the power switch from 25.5 μs to 27.7 μs. In this case, the estimated maximum value of the current reaches 2.46 A.

In any case, the effect of the temperature starting from a linear operation leads the inductors to overtake the threshold current corresponding to saturation. Besides, the feedback further worsens the overall stress of the circuit since it tries to compensate, by increasing the duty cycle, the voltage drop. This phenomenon becomes more relevant when the power increases as the voltage drop increases as well. The overtaking of the saturation limit current is due to the FFC operation. Differently, by adopting VFC, it is possible to impose the duty-cycle $D$ by increasing the switching frequency $F_s$, with a $T_{on}$ such that the current limit is
not exceeded. In fact, considering that \( D = \frac{T_{on}}{T} = \frac{T_{on}}{T}F_s \), it is possible to increase the duty cycle just by increasing the switching frequency, i.e., reducing \( T_{off} \).

\[
\text{(a) (b) (c)} \hspace{1cm} \text{(d) (e) (f)}
\]

**Figure 7.** Experimental measurement of the diode voltage (blue), output voltage (green) and inductor current in AC coupling (red) for different inductors and temperature conditions. First row depicts measurement conducted on the inductor 1 \((L_{nom} = 100 \mu H)\) for \( F_s = 38 kHz \) with a mean inductor current of 3.2 A at: (a) 25°C, (b) 55°C, (c) 95°C. Second row depicts measurement conducted on the inductor 2 \((L_{nom} = 330 \mu H)\) for \( F_s = 18 kHz \) with a mean inductor current of 1.7 A at: (d) 25°C, (e) 55°C, (f) 95°C.

Figure 8 shows the curves of the temperature versus time. We stopped the tests when the temperature reached 95 °C to avoid damaging inductor windings; indeed, the insulation is made of polymers such as polyurethane, polyamide and polyester. These materials usually degrade when the temperature exceeds 80-150 °C [35] with consequent failure of the insulating film covering the copper and short circuits among wires. Furthermore the manufacturer guarantees a constant \( I_{RMS} \) current up to 85°C [33]. Experimental data have been fitted to retrieve the exponential curve describing the temperature transient. It can be noted that inductor 1 reaches the maximum temperature before inductor 2, as expected since a greater current flows through it. The curve corresponding to inductor 2 tends to stabilize around the maximum temperature, whereas the curve of inductor 1 would rise to about 120 °C. It should be underlined that these transients describe the rise of the temperature starting from environmental temperature and a linear operation; the losses drive the inductor in non-linear zone, further raising the maximum peak of the current and lowering the output voltage.

The obtained results have general validity and can be applied to any inductor belonging to a DC/DC converter; they show that the temperature can drive an inductor outside the linear zone and that in the neighborhood of saturation with FFC a slight increase of the duty cycle causes a dramatic increase of the current peak.
A full characterization of power inductors is essential to exploit the saturation region; it must encompass the magnetization curve and the temperature as parameter since it plays a crucial role when it is operated in saturation. The behavior of two different inductors wound on the same core and placed in a buck converter has been verified when the saturation is exploited showing how the current profiles are varied.

The inductance characterization is based on current derivative and voltage across the inductor; some cautions ensure that the error is minimized within tolerance. Moreover, the inductor under test is characterized in real operating conditions since it is part of a switching converter, ensuring high precision measurement. The temperature of the inductor is measured at a suitable point of the core.

The measurement system gives a complete characterization of the inductance versus temperature, up to the saturation current defined as the current at which the inductance value drops at half its maximum. The measurement error is lower than the tolerance given by the manufacturer. The magnetization curve can also be retrieved, and the core saturation current versus inductance can be calculated. Experimental tests have been conceived to show how the temperature influences the current profile and, consequently, both the maximum current through the same inductor and the output voltage of the converter. The feedback of the converter can further increase the overall stress and this is emphasized by increasing the rated power.

Finally, we demonstrated that it is possible to extrapolate the characteristics of saturation current vs. core temperature for a whole family of inductors sharing the same core once that the proportionality factor $K$ is retrieved from the measurement of one suitable inductor of the same series. This approach is thus helpful for retrieving information regarding the saturation and temperature behavior of power inductors in the non-linear region, allowing the designers to address the adverse effects of saturation. Based on the detailed characterization of the power inductor, it is possible to extend its current range and to adopt, during the design, a smaller core compared to the core of an inductor in linear operation; in this way, the power density of the converter can be increased.

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References

1. Yan, Z.; Qimi, C.; Junbo, Z. Predicting core losses under the DC bias based on the separation model. IEEE J. Emerg. Sel. Top. Power Electron. 2017, 5, 833–840, doi:10.1109/JESTPE.2017.2664882.

2. Perdigão, M.S.; Trovão, J.P.F.; Alonso, J.M.; Saraiva, E.S. Large-Signal Characterization of Power Inductors in EV Bidirectional DC-DC Converters Focused on Core Size Optimization. IEEE Trans. Ind. Electron. 2015, 62, 3042–3051, doi:10.1109/TIE.2015.2402632.

3. Sima, W.; Peng, D.; Yang, M.; Liu, Y.; Duan, P.; Deng, J.; Qian, H. Low-frequency model for single-phase transformers based on the three-component Preisach model considering deep saturation. Int. J. Electr. Power Energy Syst. 2019, 110, 107–117, doi:10.1016/j.ijepes.2019.02.050.

4. Khalilifar, M.; Shahrtash, S.M.; Fateminia, M.; Ranjbar, A.M. Reactor modelling with saturation characteristic and inter-phase magnetic coupling. Int. J. Electr. Power Energy Syst. 2020, 123, 106228, doi:10.1016/j.ijepes.2020.106228.

5. Di Capua, G.; Femia, N. A novel method to predict the real operation of ferrite inductors with moderate saturation in switching power supply applications. IEEE Trans. Power Electron. 2016, 31, 2456–2464, doi:10.1109/TPEL.2015.2438952.
6. Di Capua, G.; Femia, N.; Stoyka, K. Switching Power Supplies with Ferrite Inductors in Sustainable Saturation Operation. *Int. J. Electr. Power Energy Syst.* 2017, 93, 494–505, doi:10.1016/j.ijepes.2017.06.003.

7. Valchev, V.C.; Van den Bossche, A. *Inductors and Transformers for Power Electronics*; 1st ed.; CRC Press: Boca Raton, FL, USA, 2018; ISBN 9781315221014.

8. Beraki, M.W.; Trovao, J.P.F.; Perdigao, M.S.; Dubois, M.R. Variable Inductor Based Bidirectional DC-DC Converter for Electric Vehicles. *IEEE Trans. Veh. Technol.* 2017, 66, 8764–8772, doi:10.1109/TVT.2017.2710262.

9. Vitale, G.; Lullo, G.; Scirè, D. Thermal stability of a DC/DC converter with inductor in partial saturation. *IEEE Trans. Ind. Electron.* 2020, doi:10.1109/TIE.2020.3014580.

10. Bizzarri, F.; Lodi, M.; Oliveri, A.; Brambilla, A.; Storace, M. A nonlinear inductance model able to reproduce thermal transient in SMPS simulations. In Proceedings of the Proceedings - IEEE International Symposium on Circuits and Systems; IEEE: Sapporo, Japan, 2019; Vol. 2019-May, pp. 1–5.

11. Oliveri, A.; Di Capua, G.; Stoyka, K.; Lodi, M.; Storace, M.; Femia, N. A power-loss-dependent inductance model for ferrite-core power inductors in switch-mode power supplies. *IEEE Trans. Circuits Syst. I Regul. Pap.* 2019, 66, 2394–2402, doi:10.1109/TCSI.2018.2889856.

12. Oliveri, A.; Lodi, M.; Storace, M. Accurate Modeling of Inductors Working in Nonlinear Region in Switch-Mode Power Supplies with Different Load Currents. In Proceedings of the SMACD 2018 - 15th International Conference on Synthesis, Modeling, Analysis and Simulation Methods and Applications to Circuit Design; 2018; pp. 233–236.

13. Burrascano, P.; Di Capua, G.; Laureti, S.; Ricci, M. Neural models of ferrite inductors non-linear behavior. In Proceedings of the Proceedings - IEEE International Symposium on Circuits and Systems; 2019; Vol. 2019-May.

14. Scire, D.; Lullo, G.; Vitale, G. Design and Modeling of an Interleaving Boost Converter with Quasi-Saturated Inductors for Electric Vehicles. In Proceedings of the 2020 AEIT International Conference of Electrical and Electronic Technologies for Automotive (AEIT AUTOMOTIVE); IEEE: Torino, Italy, 2020; pp. 1–6.

15. Migoni, G.; Romero, M.E.; Bergero, F.; Kofman, E. A Mixed Modeling Approach for Efficient Simulation of PWM Switching Mode Power Supplies. *IEEE Trans. Power Electron.* 2019, 34, 9758–9767, doi:10.1109/TPEL.2019.2891174.

16. Stoyka, K.; Femia, N.; Di Capua, G. Power Inductors Behavioral Modeling Revisited. *IEEE Trans. Circuits Syst. I Regul. Pap.* 2020, 67, 5636–5649, doi:10.1109/TCSI.2020.3002615.

17. Femia, N.; Di Capua, G.; Stoyka, K. Saturation and thermal stability of power inductors. In Proceedings of the ICECS 2020 - 27th IEEE International Conference on Electronics, Circuits and Systems, Proceedings; IEEE, 2020; pp. 1–4.

18. Uno, M. A Direct-Reading Instrument for Measurements of Inductance of Ferrite-Core Coils. *IEEE Trans. Instrum. Meas.* 1982, IM–31, 139–140, doi:10.1109/TIM.1982.6312540.

19. Waltrip, B.; Seifert, F. A programmable capacitor for inductance measurements. *IEEE Trans. Instrum. Meas.* 2017, 66, 1572–1578, doi:10.1109/TIM.2017.2659818.

20. Lu, J.; Pan, D.A.; Yang, B.; Qiao, L. Wideband magnetoelectric measurement system with the application of a virtual multi-channel lock-in amplifier. *Meas. Sci. Technol.* 2008, 19, doi:10.1088/0957-0233/19/4/045702.

21. Fernandez, C.; Pavlovic, Z.; Kulkarni, S.; McCloskey, P.; O’Mathuna, C. Novel High-Frequency Electrical Characterization Technique for Magnetic Passive Devices. *IEEE J. Emerg. Sel. Top. Power Electron.* 2018, 6, 621–628, doi:10.1109/JESTPE.2018.2798919.
22. Batista, A.J.; Fagundes, J.C.S.; Viarouge, P. An Automated Measurement System for Core Loss Characterization. *IEEE Trans. Instrum. Meas.* **1999**, *48*, 663–667, doi:10.1109/19.769682.

23. Cheng, T.; Lu, D.D.C.; Siwakoti, Y. Electro-Thermal Modeling of a Boost Converter Considering Device Self-heating. *2019 IEEE 4th Int. Futur. Energy Electron. Conf. IFEEC 2019* **2019**, doi:10.1109/IFEEC47410.2019.9014948.

24. Górecki, K.; Detka, K. Application of Average Electrothermal Models in the SPICE-Aided Analysis of Boost Converters. *IEEE Trans. Ind. Electron.* **2019**, *66*, 2746–2755, doi:10.1109/TIE.2018.2847694.

25. Thone, M.; Wens, J. *Bodo’s Power Systems*. 2015, pp. 52–54.

26. Lee, F.C.; Wang, S.; Li, Q. Next Generation of Power Supplies-Design for Manufacturability. *IEEE J. Emerg. Sel. Top. Power Electron.* **2020**, *6777*, 1–1, doi:10.1109/jestpe.2020.3002857.

27. Matsumori, H.; Shimizu, T.; Wang, X.; Blaabjerg, F. A practical core loss model for filter inductors of power electronic converters. *IEEE J. Emerg. Sel. Top. Power Electron.* **2018**, *6*, 29–39, doi:10.1109/JESTPE.2017.2761127.

28. Lullo, G.; Rosato, S.; Scirè, D.; Vitale, G. Characterization of Non-linear Inductors Including Thermal Effects for Power Applications. *Renew. Energy Power Qual. J.* **2018**, *1*, 728–734, doi:10.24084/repqj16.446.

29. Saeed, S.; Georgious, R.; Garcia, J. Modeling of magnetic elements including losses-application to variable inductor. *Energies* **2020**, *13*, 1750–1755, doi:10.3390/en13081865.

30. Liu, Y.; Yin, S.; Pan, X.; Wang, H.; Wang, G.; Peng, J. Effects of nonlinearity in input filter on the dynamic behavior of an interleaved boost PFC converter. *Energies* **2017**, *10*, 1530, doi:10.3390/en10101530.

31. Gorecki, K.; Detka, K. Influence of Power Losses in the Inductor Core on Characteristics of Selected DC-DC Converters. *Energies* **2019**, *12*, 1991, doi:10.3390/en12101991.

32. McLyman, C.W.T. *Transformer and Inductor Design Handbook*; 4th ed.; CRC Press: Boca Raton, FL, USA, 2017; ISBN 9781315217666.

33. Coilcraft Inc. SMT Power Inductors - DO5010H Series Available online: [https://www.coilcraft.com/en-us/products/power/unshielded-inductors/ferrite-drum-surface-mount/do/do5010h/](https://www.coilcraft.com/en-us/products/power/unshielded-inductors/ferrite-drum-surface-mount/do/do5010h/) (accessed on Sep 1, 2020).

34. Erickson, R.W.; Maksimović, D. *Fundamentals of Power Electronics*; Springer US: Boston, MA, 2001; ISBN 978-1-4757-0559-1.

35. Barili, A.; Brambilla, A.; Cottafava, G.; Dallago, E. A Simulation Model for the Saturable Reactor. *IEEE Trans. Ind. Electron.* **1988**, *35*, 301–306, doi:10.1109/41.192663.