Single shot measurement of a silicon single electron transistor

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Abstract

We have fabricated a custom cryogenic Complementary Metal-Oxide-Semiconductor (CMOS) integrated circuit that has a lower measurement bandwidth compared with conventional room temperature electronics. This allowed implementing single shot operations and observe the real-time evolution of the current of a phosphorous-doped silicon single electron transistor that was irradiated with a microwave pulse. Relaxation times up to 90 μs are observed, suggesting the presence of well isolated electron excitations within the device. It is expected that these are associated with long decoherence time and the device may be suitable for quantum information processing.

The requirement for faster measurement usually results from the need to shorten the timescale of complex experiments or to access dissipative information in time-dependent measurements. However, the measurement bandwidth is often limited by the low pass filter effect resulting from the load capacitance due to the connecting cables and measurement circuit and the feedback resistor of the amplifier. This problem is acute for quantum devices operating in the pA range and especially in quantum information processing for which fast access to qubit states is essential.

In solid-state quantum computation, single electron transistors (SET) [1] are now widely employed as a qubit readout device [2]. However, the bandwidth of the current measurement of the SET is often smaller than the decoherence rate of the system [2]. This is due to its tunnel barrier being much larger than the quantum resistance $h/e^2$ and due to large parasitic resistances [1], but also by the necessity to operate the SET at low temperature. Thus, a direct measurement of the qubit state or its evolution in real time is not possible and it is usually monitored indirectly by measuring the time averaged SET current over a large number of identical qubit operations, each operation including initialization, manipulation and measurement stages [3]. Systematic changes in qubit operation, usually in the manipulation stage, are then used to characterize the behaviour of the qubit. Alternative methods based on radio frequency techniques [4], offer a much higher bandwidth, but can lead to unwanted changes in the quantum mechanical state.

The problem of such fast current measurements is addressed in this letter through the use of a custom CMOS integrated circuit based on charge integration that operates at 4.2 K (LTCMOS) and in close proximity to a silicon SET. This arrangement enabled an efficient noise gain suppression by using shorter cabling between the measurement circuit and the device and a significant improvement in measurement speed ($\sim 10^4$ times faster [5]) compared to conventional measurements using room temperature source measure units. Quantum states in the SET were mapped by continuous microwave spectroscopy and the real time evolution of a particularly long lived state was observed in a single shot measurement.

The SETs are fabricated from a silicon-on-insulator (SOI) substrate with a 30 nm-thick silicon layer, doped with phosphorus at a density of $10^{20}$ cm$^{-3}$. High resolution electron beam lithography and reactive ion etching were used to pattern a single dot of diameter $\sim 60$ nm. The 30-nm width constrictions that separate the dot from the connecting source and drain leads are depleted of electrons and act as tunnel barriers. Thermal oxidation is then used to reduce the dot size and tunnel barrier widths as well as for surface passivation, resulting in a significant reduction in random telegraph switching. The devices are controlled by an in-plane gate that is formed from the same SOI layer (Fig. 1a and b). Devices were mounted in 20 pin leadless header packages at the end of the custom probe. The microwave signal was coupled to the SET by using an open ended semi-rigid waveguide that terminates $\sim 0.5$ mm away from the silicon surface (Fig. 1c). A LTCMOS circuit was incorporated into the probe and was used both to provide the voltages for the source, drain and gate leads and to measure the SET current. All lines were filtered by single stage low-pass filter with a cut-off of about 2 MHz to suppress electrical noise from the CMOS circuit. Both the device and the LTCMOS were kept at 4.2 K by immersing the probe into liquid helium.

The SET response to continuous microwave (CW) excitation consists of a large number of resonances that are detectable when the microwave power at the source $P$ is greater than about $-20$ dBm (Fig. 2). These resonances exhibit a wide range of amplitudes and quality factors $Q$ with the largest amplitudes corresponding to the lowest $Q$ values. Some low $Q$ resonances, typically

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However, the presence of resonances with large $Q$ values suggests a mechanism that is well isolated from the electromagnetic environment such as tunneling or trap-mediated transport. Such processes are indeed favoured by the presence of phosphorous donors in the SET island and tunneling barriers and by a significant number of localised states especially on the sidewalls of the SET.\[10\] This effect is enhanced by the large surface state density found on the non-(100) surfaces forming the sidewalls. Also, the resonance shapes are found to be Lorentzian with a width giving an energy scale two or three orders of magnitude lower than $k_B T$. This is inconsistent with phonon-mediated hopping. However, according to Landauer theory, the conductance of a single channel wire with a barrier is given by the transmission probability [11], and, for a sufficiently high barrier relatively to the Fermi energy, this probability is energy-independent, giving a temperature independent width in the resonance spectrum.\[12\] The previous observation also implies a weak coupling to the phonon bath and non-adiabatic processes (e.g. the timescale for the transport mechanism is shorter than the thermalization time). In this case, the resonance widths $\Delta \nu_0$ are related to the energy loss mechanism in the system and $Q = \pi \nu_0 / \Delta \nu_0 \sim \pi T_0 / \nu_0$, where $T_0$ is the energy relaxation time of the system and $\nu_0$ the resonance frequency. Of the plausible transport mechanisms, trap-assisted tunneling in the tunnel barrier or potential shifts due to reversible dipoles \[14\] in the SET island are most likely to be present as we did not observe any photon-assisted tunneling effects in the gate dependence of the source-drain current.\[15\] Electron hops most likely occur between localised states near the SET island. This situation and the dependence of $\nu_0$ with gate voltage has been previously discussed.\[7\] The electron displacement modifies the electrostatic environment inside the SET island and leads to a change in the potential close to the tunnel barrier. This changes the magnitude of the barrier and is responsible for the increase or decrease in the measured current depending on the location of the active traps. Localised states in the direct transport pathway would lead to low $Q$ resonances with large amplitude due to the short residence time of the electron, whereas remote traps could be responsible for the high $Q$ but low amplitude resonances.

The quality factor for the high $Q$ peaks suggests that the lifetime for these excitations can be as long as 100 $\mu$s. This allows the study of the real time evolution of the source-drain current after the application of a short microwave pulse, known as a single shot measurement $[16]$. To this purpose, a well-isolated resonance on a level back of 718 GHz was selected from a CW measurement at a power of +9 dBm, with a charge integration time of 200 $\mu$s and averaging the results over 200 measurements (Fig. 2). This results in a signal-to-noise ratio of about 100:1. The width of the chosen resonance was about 207 kHz, for which the corresponding relaxation time is $T_0 \sim 5 \mu$s. A single shot measurement was then performed by reducing the charge integration

![FIG. 1: (a) Scanning electron microscopy image of the SET. (b) Cross-section and plane view of the device with the sidewall depletion (grey), the source and drain potential barriers (dashed) and localising centres (black dots). (c) Source-drain current $I_{SD}$ versus gate voltage $V_g$ for source-drain biases $V_{SD}$ from -2 mV (bottom) to 2 mV (top). The arrow indicates the SET working point.](image1.png)

![FIG. 2: (a) $I_{SD}$ dependence on microwave frequency $\nu$, measured relatively to its value for $P = -20$ dBm. (b) High $Q$ resonance used for the single shot measurement ($Q \sim 41,000$). The Lorentzian fit is shown by the dotted line.](image2.png)
time to 20 µs and using a microwave pulse of duration of 2 µs. The microwave power was increased to about +25 dBm using a power amplifier to compensate for the decrease in the signal amplitude. These conditions also greatly reduced the signal to noise ratio compared to the CW measurement, so that the averaging had to be increased to 1000 measurements per point. The delay in the trigger signal causes the microwave pulse to arrive at \( t \sim 0.8 \) ms (Fig. 3). The signal measured in single shot mode contains some very large damped oscillations, which do not depend on the nature of the microwave pulse. They are even present in the absence of the microwave pulse and are measurement artefacts due to cable capacitance charging effects. By tuning the microwave frequency to on or off resonance values the effects of these background oscillations may be removed. The difference between these is an exponential decay with a time constant \( \tau \sim 90 \) µs. This value is more than one order of magnitude larger than that found from the CW linewidth measurement. As the time-averaged power in the pulsed measurement is a factor of 1000 less than that used in the CW measurement, this difference in relaxation time may result from microwave-induced electron heating in the CW measurement. The lifetime \( \tau \) corresponds to the time for the microwave-excited electron to recombine with its original ionised trap. Its large value suggests glassy behaviour, a characteristic manifestation of Coulomb interaction in systems with localized states. The disorder is due to the random placement of phosphorous donors and the glassy dynamics would result from the competition between the disorder and the Coulomb interaction.

In conclusion, we have been able to observe the real-time decay of a quantum state by performing a single shot measurement in a single electron transistor and using a low temperature custom CMOS measurement circuit. The microwave excitation induces electron hops between localised states within the SET island. The long relaxation time observed suggests the presence of coherent transport in the structure, making the present system usable quantum information technology.

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