Nanoparticle floating-gate transistor memory based on solution-processed ambipolar organic semiconductor

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Abstract. Organic thin-film transistor memory based on nano-floating-gate nonvolatile memory was demonstrated by a simple method. The gold nanoparticle that fabricated by thermally evaporated acted as the floating gate. Spin coated PMMA film acted as the tunneling layer. A solution-processed ambipolar semiconductor acted as the active layer. Because of the existence of both hole and electron carriers in bipolar semiconductor materials, it is more conducive to the editing and erasing of memories under positive and negative pressure. The memory based on metal nanoparticles and organic bipolar semiconductor shows good read-write function.

1 Introduction

Memory is one of the main applications of organic thin film transistors. In the 1970s, Frohman-Bentchkowsky et al. prepared floating gate transistors, which realized the injection and storage of charges in the floating gate, demonstrating the feasibility of using this structure as a memory.\cite{1} In the following decades, transistor memory has made great progress and gradually moved towards practical applications. With the research and development of organic semiconductors, the first organic transistor memory appeared in 1986. It was prepared by Tsumura et al. with polythiophene as the active layer. \cite{2} Since then, this field has made considerable progress. In 2009, Sekitani et al. reported the first flexible memory organic thin film transistor memory, and the write voltage was lower than 10 V.\cite{3} The device is prepared on a polyethylene-2,6-naphthalate (PEN) substrate and adopts a floating gate structure with aluminum oxide as the insulating layer, and 20 nm thick aluminum film as the floating gate layer. When a certain voltage is applied, the charge will be transferred to the floating gate layer by means of thermal excitation and quantum tunneling to achieve storage.

Generally, organic thin film transistor memories are divided into the following three categories according to different floating gate structures: ferroelectric polymer transistor memories, electret polymer transistor memories, and nanoparticle floating gate transistor memories. The storage medium of the ferroelectric polymer transistor memory is a ferroelectric polymer. The working principle is that when the gate voltage changes, the polarity of the ferroelectric polymer will change, and then the current can reflect the two stable states of on and off to storage information. The electret polymer transistor memory uses the defects of the electret polymer itself and other properties. Under a certain gate voltage, the charge of the organic semiconductor is injected into the insulating layer and maintains a stable state, and the electret that retains the charge is polymerized with the non-injected electret. The memory will reflect different currents under the same voltage to realize information storage. The working principle of the nanoparticle floating gate transistor memory is similar to electret polymer transistor memory, the difference is that the floating gate nanoparticles replace the electret to store charge.

In recent years, the research of transistor memory based on nanoparticle floating gate structure has made important progress.\cite{4-6} Metal nanoparticles, such as aluminum, copper, silver, and gold, are often used as nano-floating-gates. Kung-Hwa Wei et al. have done a systematic study on it, and the study shows that the size of the hole and electron injection barrier is closely related to the work function of the metal electrode.\cite{7} Among many metal materials, gold is very suitable as a storage medium in terms of chemical stability and metal work function.

Compared with inorganic transistor memory, due to the characteristics of organic materials, organic transistor memory has many advantages: lightness, flexibility, solution processing, and suitability for large-area preparation. Therefore, organic transistor memory has a broader application prospect. For example, organic transistor memory can be fabricated on a flexible substrate to achieve flexible storage.\cite{8-11} Using solution processing or even inkjet printing processes, organic transistor memory can be fabricated in a large area, greatly reducing costs. However, organic transistor memory still has some problems, such as poor stability, high operating voltage, unsatisfactory retention, long programming time, and further clarification of the...
working mechanism. There are many problems that need to be resolved.

In this paper, an organic transistor memory based on a nanoparticle suspended gate structure is prepared. Gold nanoparticles are used as a floating gate, polymethyl methacrylate (PMMA) is used as a tunnel insulating layer, and a bipolar organic semiconductor material is used as an active layer to prepare a nanoparticle suspension gate. In organic transistor memory, due to the presence of both holes and electron carriers in bipolar materials, the injection of different types of carriers under different voltages is realized, and the storage window is increased.

![Fig. 1. Diagrammatic sketch of nano floating gate organic transistor](image)

2 Experimental Details

Substrate cleaning: Put the glass substrate in acetone, isopropanol, lotion, deionized water, and isopropanol in the order of ultrasonic cleaning for 10 minutes, and put it in an oven at 80 °C for drying.

Preparation of gate electrode and gate insulating layer: Sputter Al-Nd alloy film, and anodize to form AlOx:Nd gate insulating layer.

Preparation of gold particle nano-floating grid: the above-mentioned substrate is turned upside down on the metal mask, placed in a vacuum evaporation equipment, and then vacuumed to below 3 × 10⁻⁴ Pa, then 0.1 Å·s⁻¹ The evaporation rate is 1 nm gold as the nano floating gate.

Preparation of the tunnel insulation layer: Take the above-mentioned substrate out of the equipment, spin-coating PMMA chlorobenzene solution, concentration 30 mg/mL, rotating speed 5000 rpm, spin coating 30 s, and then continue annealing in a glove box at 200 °C for 30 min, and it can get 100 nm thick PMMA film.

Preparation of the active layer: Prepare a PDPP-V solution with a solvent of dichlorobenzene and a concentration of 0.5 mg/mL. Conduct spin-coating on the above substrate at 2000 rpm in a nitrogen glove box. And then continue annealing in a glove box at 100 °C for 30 min, and finally a 30 nm thick film is formed.

Preparation of source and drain electrodes: Place the above-mentioned substrate upside down on the metal mask, put it into the vacuum evaporation equipment, evacuate to below 3 × 10⁻⁴ Pa, and then use 0.1~0.3 Å·s⁻¹ The evaporation rate is 45 nm gold as the source and drain electrodes, and the aspect ratio is 500 μm: 70 μm.

Device electrical performance test: It is tested by the semiconductor parameter tester Agilent 4155C at room temperature in the air.

3 Results and Discussion

As shown in our earlier work, FBT-Th₄ (1,4) is an organic semiconductor with high hole mobility.[12] Fig. 2 shows the OTFT memory with FBT-Th₄ (1,4) as the active layer. It can be seen that after writing to a certain negative pressure, the holes in the channel will tunnel through the tunnel insulation and enter the floating gate of metal nanoparticles, thus generating an internal bond electric field opposite to the gate voltage direction, resulting in a negative drift of the threshold voltage. Unfortunately, when the positive gate voltage is applied, the transfer curve does not deviate to the positive gate voltage direction, indicating that the holes stored in the floating gate cannot be erased by positive gate voltage. This is also a problem encountered by many unipolar memories, some of which are erased by laser irradiation.

![Fig. 2. Transfer curves of the organic memory devices with unipolar semiconductor according to the different programming/erasing biases at drain bias of -30 V.](image)

PDPP-V is a bipolar organic semiconductor material. We have adopted a top-gate low-contact structure to obtain the best performance of the device. The device structure diagram, molecular formula and corresponding transfer characteristic curve are shown in Fig. 3. The hole mobility of the device is 0.15 cm² V⁻¹ s⁻¹, and the electron mobility is 0.08 cm² V⁻¹ s⁻¹. The IDVG curve shows that PDPP-V exhibits obvious bipolar characteristics.
characteristic curve of the memory in Fig. 4, we select \( V_G = 80 \) V after erasing, and the \( I_D \) under the state of \( V_D = -30 \) V and \( V_G = 0 \) V is on, and be tested every 20 s after erasing. After selecting \( V_G = -80 \) V for programming, the \( I_D \) under \( V_D = -30 \) V, \( V_G = 0 \) V is off state, and the test is performed every 20 s after programming. The results are as shown in the Fig. 5, the device can still maintain a switching ratio of more than 100 after 1000 s.

Fig. 4. Transfer curves of the organic ambipolar memory devices according to the different programming/erasing biases at drain bias of \(-30\) V.

Fig. 5 is the retention characteristic curve of the device. According to the programming transfer characteristic curve of the memory in Fig. 4, we select \( V_G = 80 \) V after erasing, and the \( I_D \) under the state of \( V_D = -30 \) V and \( V_G = 0 \) V is on, and be tested every 20 s after erasing. After selecting \( V_G = -80 \) V for programming, the \( I_D \) under \( V_D = -30 \) V, \( V_G = 0 \) V is off state, and the test is performed every 20 s after programming. The results are as shown in the Fig. 5, the device can still maintain a switching ratio of more than 100 after 1000 s.

Fig. 5. The retention characteristic curve of the nanoparticle floating-gate ambipolar transistor memory.

Fig. 6 shows the programming/reading/erasing (P/R/E/R) endurance of the nanoparticle floating-gate ambipolar transistor memory, at \( V_D \) of \(-30\) V with \( V_G = -80 \) V/ 0 V/ 80 V/ 0 V respectively. The bipolar organic memory has obvious nonvolatile storage function, as shown in Fig. 6. This work provides a simple method for fabrication of nonvolatile memory.

Fig. 6 shows the retention characteristic curve of the nanoparticle floating-gate ambipolar transistor memory.

**4 Conclusion**

In this paper, the nonvolatile memory was prepared by evaporating Au nanoparticles as floating gate, PMMA as tunneling insulator, and PDPP-V as active layer.
Because bipolar organic semiconductors have both holes and electron carriers, the programming and erasing operations can be realized by gate voltage, and the multi-level memory can be realized under different gate voltage programming. Although the performance of the device is not outstanding, the whole process is simple, and the programming and erasing operation can be realized only by adjusting the gate voltage, which reflects the application potential of organic thin film transistor in organic memory.

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