Paper

Driven by complementary operation of SiC-MOSFET and SiC-JFET within isolated flyback converter circuit

Nobuo Satoh1a), Takaaki Hayashi1, Tatsuki Ohsato1,2, Hiroshi Arai1, and Yasuyuki Nishida1

1 Department of Electrical, Electronics and Computer Engineering, Chiba Institute of Technology
2-17-1 Tsudanuma, Narashino Chiba 275-0016, Japan
2 Department of Information and Image Sciences, Chiba University
1-33 Yayoi-cho, Inage-ku, Chiba 263-8522, Japan

a) satoh.nobuo@it-chiba.ac.jp

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Abstract: Flyback converters are used in DC–DC conversion with electric isolation between the input and the output. In flyback converter circuits, a silicon carbide (SiC) metal-oxide-semiconductor field-effect transistor (MOSFET) and a silicon carbide junction gate field-effect transistor (JFET) are adopted as the switching and rectifying devices, respectively. SiC devices were controlled due to one signal, and a switching frequency of 1 MHz was achieved. The voltage and current on the input and output sides in the flyback converter were observed as part of the circuit operation analysis. The power conversion efficiency was evaluated through waveform observation.

Key Words: flyback converter, megahertz-frequency switching, SiC power devices

1. Introduction

Multistep output DC circuits can be used as general-purpose low-power switching supplies [1]. In DC power supplies, a high-speed switching operation can decrease the amount of energy created in each cycle, which promotes the downsizing of the passive circuit elements such as inductors and capacitors [2]. Further, parasitic impedances in a circuit limit the operating frequency during high-speed switching. Switching losses occur because of a surge in voltage, the tail current, and ringing phenomena, which originate in both the circuit and the parasitic components of the active device [3–5]. The circuit and the device can be damaged by the losses generated due to Joule heating. One of the goals of our study is to suppress power-conversion losses. Since it is difficult to examine parasitic impedance in advance, circuit analysis is performed based on the experimental results in a manufactured circuit.

In this study, we use a flyback converter with a snubber circuit [6] operating at switching frequencies
of 100 kHz and 1 MHz, respectively. The circuit configuration of the flyback converter [7, 8] ensures that electric insulation is employed for most low-power switching supplies by using a high-frequency transformer, such as a choke coil, and a gate drive that insulates the control circuit and the principal power conversion circuit. In the circuit, two types of active devices are examined: trench-type metal-oxide-semiconductor field-effect transistor made of silicon carbide (SiC) as a switching power device (SiC-MOSFET) [9], and a junction-type field-effect transistor made of SiC (SiC-JFET) [10] as a rectifying power device. We investigate the transient behaviors of the converter for switching operations by utilizing each of the SiC power devices. Based on the expected experimental results, the design of the converter at high-power output and high-speed switching will then be examined.

2. Experimental setup

2.1 Flyback converter circuit

The flyback converter under investigation is shown in Fig. 1. The specified output voltage is 15 V at operating frequencies of 100 kHz and 1 MHz. This circuit consists of two features. Firstly, this circuit was significantly advantageous for miniaturizing the power supply circuit with high-speed switching by using the SiC power devices. Secondly, this flyback converter is that it employs complementary devices, such as normally-off MOSFET and normally-on JFET. These devices are used mutually to achieve the switching operation of the transformer responsible for energy storage, and the rectification operation responsible for energy release. The active device responsible for switching and rectifying functions are described in the 2.2 section.

Fig. 1. (a) Schematic circuit diagram of flyback converter using isolated dual-gate driver and transformer. (b) Photograph of the flyback converter.

The electric power supply \(E\) is connected to the primary side of the transformer, which corresponds to the power input side. Noise generation is suppressed by connecting a constant voltage source (GP0110-3; Takasago ltd.) as the series regulation type. To manage the attainment voltage, an aluminum electrolytic capacitor \(C_1\) with a charge capacity of 1500 \(\mu\)F (withstand voltage: 250 V; Nippon Chemi-Con Corp.) was connected to avoid voltage instability at the primary side. The resistance and capacitor \((RC)\) snubber circuit is connected with the power-MOSFET in parallel, and it was inserted to suppress the surge voltage. The circuit parameters of the \(RC\) snubber circuit were \(R_{sn} = 220 \Omega, C_{sn} = 2 \times 2 \text{nF}\), as shown in Fig. 1.

The secondary side of the transformer is connected to a load at the power output side. For high-speed switching operations, a laminated ceramic capacitor is selected as the smoothing capacitor \(C_2\) on the output side. An electrolytic capacitor is preferred as it is more robust against damage caused by heat, smaller spatial volume, high permissible ripple electric current, and the small equivalent series resistance (ESR). Based on the switching frequency \(f_{sw}\) and the accepted ripple rate, five 10 \(\mu\)F (UMK316BBJ106KL-T, withstand voltage: 50 V; Taiyo Yuden co., ltd.) capacitors are connected in parallel.
2.2 Active devices

In the primary side of this flyback converter, it was the MOSFET device that was responsible for the switching function. The SiC-MOSFET (SCT3030AL, voltage: 650 V, current: 70 A, ROHM co., ltd.) is a power MOSFET made of SiC semiconductor with a trench-wall construction for reducing resistance in the parasitic JFET region [11]. It is expected that the SiC power semiconductor will perform under a high withstand voltage and large current as used in the switching device [12].

In the secondary side of this converter, it was the JFET device that was responsible for the rectifying function. The SiC-JFET (UJN1205K, voltage: 1200 V, current: 38 A, United Silicon Carbide Inc.) was adopted as the rectifying device. The normally-on JFET receives the same signal for switching control on the primary side. It is presented in Fig. 2, the static characteristics of two types of active devices which were actually used.

![Figure 2](image-url)  
**Fig. 2.** Static characteristics of two types of active devices. (a) The SiC-MOSFET used as the switching device, and (b) The SiC-JFET used as the rectifying device.

2.3 Transformer

The secondary coil of the transformer is used as a choking coil in the flyback converter. There is an air-gap within the pulse transformer-core to prevent a decrease in electric current due to magnetic saturation [1]. In the case of the magnetic core, such as the air-gap, the coupling coefficient was inevitably less than 100%. Further, energy losses caused by leakage flux from the air-gap decrease the power conversion efficiency. As for the problem of latent heat during miniaturization, the area surrounding the air-gap can easily reach high temperatures. Therefore, it is necessary to consider problems associated with heat radiation.

We adopted a new magnetic material “Liqualloy,” which was developed by Alps electric co., ltd. Since this magnetic material has good magnetic saturation characteristics, there is no need for an air gap. Thus, the losses associated with the air gap do not occur. We selected a toroidal core (29.5-mm outer diameter, 12.6-mm inner diameter, 14.0-mm height, 80.1-mm magnetic path length, 82.4-mm² sectional area) without an air gap. We employed a polyester-enamelled copper wire (PEW) with a 0.5-mm diameter for the primary and secondary windings. Each winding exhibits 40 turns, and both have a self-inductance of 150 µH for an operating frequency of 1 MHz.

Figure 3 shows the magnetic-flux-density (B) and magnetic-field-strength (H) curve characteristic of the magnetic properties of two types of transformer core material and corresponding to operating frequencies. Figure 3(a) is the curve of the ferrite-core measured at 10 kHz as the reference, and the hysteresis characteristic is confirmed. It showed magnetic saturation even at frequencies higher than 10 kHz, and the hysteresis loss also increased. We pointed out that the self-inductance of the
Fig. 3. The $B-H$ curve characteristic of the magnetic properties of two types of transformer core material and corresponding to operating frequencies. Ferrite core (reference material) at 10 kHz, (b) Liqualloy core at 10 kHz, (c) Liqualloy core at 100 kHz, and (d) Liqualloy core at 1 MHz.

transformer which adopted the ferrite-core was also designed at 150 $\mu$H. On the other hand, Figs. 3(b)–(d) are curves when the liqualloy-core is adopted for the transformer. The liqualloy has linearity up to the frequency band of at least 1 MHz, and the saturation characteristic has not been measured.

2.4 Isolated gate driver

Isolated gate drivers must guarantee the prevention of fluctuation and conduction-noise without special setups. We experimentally compared the frequency characteristics of several types of isolated gate drivers with Si-8235 (Silicon Lab.), which was adopted as an isolated gate driver in the study. We developed a gate driver circuit for which the circuit pattern is specifically designed to minimize parasitic impedance. The switching trigger signal is transmitted to the isolated gate driver through the pulse generator (81110A; Agilent). On the primary side, to ensure a fast switching operation using SiC-MOSFET as the normally-off type, the gate-source voltage ($V_{gs-M}$) were set at the pulse-shaped waveform with binary level at 0 V (off-state) and 12 V (on-state).

Conversely, on the secondary side, to achieve the fast rectifying operation by the SiC-JFET as the normally-on type, the gate-source voltage ($V_{gs-J}$) were set at the pulse-shaped waveform with binary level at 0 V (on-state) and $-12$ V (off-state). There is a feature that is control by the negative potential is achieved by making the gate potential equal to the ground potential, and then varying the source potential.

2.5 Measurement for evaluation

We simultaneously observed the flyback converter using an oscilloscope (DLM4038; Yokogawa Electric Corp.) and two current probes, (701918; Yokogawa Electric Corp.) while analyzing the transient behavior. The following state variables were measured: the gate signal ($v_g$), gate-source voltage ($V_{gs-M}$), drain-source voltage ($V_{ds}$) of the SiC-MOSFET, gate-source voltage ($V_{gs-J}$) of the SiC-JFET, input voltage ($V_{in}$), current of the primary side ($i_1$), output voltage ($V_{out}$), and current of the secondary side ($i_2$). In addition, the calculated value ($p_{MOS}$) was obtained by multiplying the
drain-source voltage ($v_{ds}$) with the current of the primary side ($i_1$) associated with the loss incurred by the SiC-MOSFET.

The current probe delay was corrected to evaluate the high-speed switching operation. Without this correction, an error remains in the calculation of switching losses via the detection-delay of the current probe, because the observed waveforms have a short period owing to the fast-switching operation. It is necessary to correct the delay of the measured data caused by the current probe. The delay time between the voltage probe and the current probe measured 10 ns, based on results of the preliminary experiment in which the response was confirmed between the voltage waveform and the current waveform. Then, considering all experimental results, there are the temporal waveforms corrected by the delay of the current probe.

3. Results and discussions

We designed the flyback converter with snubber circuit for operating frequencies of 100 kHz and 1 MHz, respectively. The output supplies a direct voltage of 15 V, and the duty ratio was fixed as 50.0%.

Figure 4 shows the waveforms of the voltage and the current at the 100 kHz switching operation. First, it was confirmed that the gate-source voltages of both the SiC-MOSFET ($v_{gs-M}$) and the SiC-JFET ($v_{gs-J}$) were simultaneously applied according to the one-gate-signal design concept. Second, it was found that an accumulation of energy from the linearly rising current waveform on the primary side ($i_1$), and energy emission from the linearly descending current waveform on the secondary side ($i_2$), were correctly alternately performed. Finally, the electric power conversion efficiency had accomplished 91.9%. The total value of switching losses and conduction losses of the SiC-MOSFET was calculated to be 89.0 mW.

Figure 5 shows the waveforms of voltage and current at 1 MHz switching operation. Similarly, it was confirmed that the gate-source voltage ($v_{gs-M}$) of the SiC-MOSFET and the gate-source voltage ($v_{gs-J}$) of the SiC-JFET were simultaneously applied according to one-gate-signal in accordance with the design concept. Here, we compared the values of the input parasitic capacitances of the SiC-MOSFET (approximately 1500 pF) and the SiC-JFET (approximately 1000 pF), respectively. The

Fig. 4. Oscillograms of $V_{gs}$, $V_{ds}$, $V_{in}$, $V_{out}$, $I_1$, and $I_2$ at $f_{sw} = 100$ kHz in 15 V output voltage.
rising waveform of the SiC-JFET with a small input parasitic capacitance value was steep. The influence becomes apparent at the time when fast-driven at approximately 1 MHz switching operation.

Second, although the output voltage at the set value of 15 V is stably output, the vibration phenomenon of the frequency component of approximately 3.2 MHz is confirmed in both the current waveforms on the primary side ($i_1$) and the secondary side ($i_2$). The electric power conversion efficiency accomplished 61.6%. The total value of switching losses and conduction losses of the SiC-MOSFET was calculated to be 134 mW.

Here, we clarified how the surge voltage and the tail current are decided. There are two factors: the parasitic capacitance of the power MOSFET and the parasitic inductance of the circuit pattern in the flyback converter. The parasitic capacitance of the SiC-MOSFET and SiC-JFET are separated into input capacitance ($C_{rss}$), feedback capacitance ($C_{rss}$), and output capacitance ($C_{oss}$). Each value is known to have junction capacitances within the range of a few nanofarads [11]. We have designed this circuit such that the hard-wiring length of the circuit can be shortened as much as possible. The parasitic inductance of the circuit-pattern in the gate driver was measured within several hundred nanohenries. The resonant frequency of the flyback converter was calculated as several megahertz from these parasitic components [14].

It is also known that power loss at high-speed switching operation limits the use of magnetic components [15]. As a future task, the limits of switching-speed in our flyback converter circuit should consider not only the parasitic capacitances of the active devices, but also the magnetic saturation of the magnetic core material of the transformer.

As future tasks, since the increase in losses in high-frequency operation induces thermal destruction of the devices, it needs to establish thermal design considering the placement of circuit elements. Furthermore, we aim to achieve 50 W class output corresponding to 10 times of this study.

4. Conclusions
We fabricated a flyback converter with an output voltage of 15 V under the $f_{sw} = 1$ MHz operation. The circuit was operated while ensuring the electrical insulation by the transformer without air-gap and the isolated-gate-driver. We confirmed experimentally the reliable complementary operation of
the circuit using the SiC-MOSFET as the switching device and the SiC-JFET as the rectifying device. The efficiency of this power conversion was calculated to be 61.6% at 1 MHz operation by waveform observation.

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