Research Article

Dynamic Behaviors Analysis of a Novel Fractional-Order Chua’s Memristive Circuit

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1. Introduction

Memristor is a nonlinear circuit element that describes the relationship between charge and magnetic flux. Its theoretical concept was first proposed by Professor Chua in 1971 [1]. It has electrical characteristics that cannot be achieved by any combination of three basic elements: resistance, capacitance, and inductance. In 2008, HP Labs proved the physical existence of memristor for the first time, responding to Professor Cai’s speculation [2]. Since then, the related theoretical research of memristor has entered the practical stage, which has attracted strong attention from researchers [3–6].

The memristor is a nonlinear device, and it is easy to produce abundant chaotic phenomena when combined with other oscillating circuits. As early as 2008, Itoh and Chua replaced Chua’s diode with a memristor based on the known Chua’s chaotic circuit and obtained a memristive chaotic system [7]. In 2010, Bao Bocheng and others replaced the diode in the Chua’s oscillator with a memristor and a negative conductance. By setting different parameters and initial conditions, they showed different dynamic behaviors [8]. Later, Pham proposed a memristive hyperchaotic system and found that the system has hidden attractors [9]. This hyperchaotic system based on memristor has no equilibrium point, but it can still exhibit chaotic phenomena. Wang et al. [10] proposed a new type of memristor-based Wien-bridge oscillator and studied its parameter-independent dynamical behaviors. A hyperchaotic memristive circuit based on the Wien-bridge oscillator structure is proposed in [11]. Chang et al. [12] proposes and analyzes a memristor with coexistent shrinkage hysteresis loop and twin local activity domains and applies it to the classic Chua’s circuit to replace the diode. The complex dynamics of the system are analyzed using compound coexistence bifurcation diagram, Lyapunov exponent spectrum, and phase diagram. In recent years, various novel memristive chaotic circuits have been proposed and analyzed, such as a three-order Wien-bridge chaotic circuit without inductor [13],
a memristive chaotic oscillator with controllable amplitude and frequency [14], and a physical SBT memristor-based chaotic circuit [15]. In addition, researchers have also conducted in-depth studies on generalized memristors, such as diode bridge-cascaded RLC memristors and a generalized memristive simulator composed of a first-order RC [16, 17].

As we all know, almost all electronic devices are not ideal devices. Therefore, there will be deviations when using an ideal model to analyze actual electronic components. With the development of the theory of fractional calculus, Carlson and others proposed the concept of fractional-order capacitance [18]. Nowadays, a large number of studies have shown that fractional-order modeling of the system makes the description of its electrical characteristics more accurate. According to the theory of fractional calculus, a more accurate model can be obtained by extending the memristive chaotic circuit to the fractional order. This makes the frequency and electrical characteristics of the fractional-order memristive chaotic system more valuable for research [19]. Ding et al. [20] proposed a fractional-order memristive Chua’s circuit with time delay, which is composed of a passive flux-controlled memristor and a negative conductance in parallel and then completes the circuit simulation of the fractional-order inductor and capacitor and then analyzes the electrical characteristics of the fractional-order memristive chaotic circuit based on the fractional-order memristor [24–28].

The rest of the paper is organized as follows. Section 2 briefly introduces the fundamental of fractional calculus and constructs a fractional-order generalized memristor composed of a diode bridge and a second-order RLC filter and then proposes a fractional-order memristive chaotic circuit. In Section 3, by studying the stability of the equilibrium points and the influence of system parameters on the bifurcation, the dynamic behavior of the proposed fractional memristive chaotic circuit is theoretically analyzed and numerically simulated. Section 4 realizes the construction of the equivalent circuit of the fractional-order inductor and capacitor and then completes the circuit simulation of the fractional-order memristive chaotic circuit in PSpice. The last section is the conclusion.

2. Fractional Models of Generalized Memristor and Chua’s Memristive Circuit

2.1. Fractional Calculus Theory. The theory of fractional calculus has been studied for more than 300 years. The operator \( {}^aD^\alpha_t \) of fractional calculus is defined as follows:

\[
{}^aD^\alpha_t a\alpha = \begin{cases} 
\frac{d^\alpha}{dt^\alpha}, & \alpha > 0, \\
1, & \alpha = 0, \\
-1 \int_a t (dt)^{-\alpha}, & \alpha < 0,
\end{cases}
\]

where \( \alpha \) is limited to real numbers, \( t \) is the independent variable, and \( a \) is the lower boundary of the variable [29].

The definition proposed by Caputo is more convenient for initial conditions and closer to practical applications, so this paper uses Caputo definition. The fractional Caputo derivative of the function \( f(t) \) is defined as follows:

\[
{}^C{}^aD^\alpha_t f(t) = \frac{1}{\Gamma(n - \alpha)} \int_a^t \frac{f^{(n)}(\tau)}{(t - \tau)^{\alpha+1-n}} d\tau, \quad (0 \leq n - 1 < \alpha \leq n).
\]

In the formula, \( \Gamma(\cdot) \) is the Gamma function and \( n \in N \) is an integer.

Under the natural condition of the function \( f(t) \), for \( \alpha \to n \), the Caputo definition becomes the normal \( n \)-order derivative of the function \( f(t) \). The Laplace transform of Caputo fractional derivative is directly calculated by the following formula:

\[
L \left[ {}^C{}^aD^\alpha_t f(t) \right] = s^\alpha F(s) - \sum_{k=0}^{n-1} \frac{\alpha^k}{\Gamma(n-k)} f^{(k)}(0), \quad (n - 1 < \alpha \leq n).
\]

2.2. The Fractional-Order Generalized Memristor. Corinto and Ascoli [30] proposed a full-wave rectifier with a second-order RLC filter with memristive characteristics. On this basis, this paper proposes a new second-order generalized memristor composed of a diode bridge and a parallel RLC filter, and its circuit model is shown in Figure 1.

The mathematical model of the second-order RLC generalized memristor is as follows:

\[
i_{in} = f(v_c, v_m) = 2 I_s e^{-\rho v_m} \sinh(\rho v_m),
\]

\[
\frac{dx}{dt} = F(x, v_m) = \left[ \frac{2 I_s (e^{-\rho v_m} \cosh(\rho v_m) - 1) - i_L}{C} \right] \left[ \frac{v_c - R i_L}{L} \right],
\]

where \( \rho = 1/(2 n V_T) \), and \( I_s, n, \) and \( V_T \) are the diode reverse saturation current, emissivity, and thermal voltage, respectively. In addition, \( v_{in} \) is the excitation voltage of the generalized memristor, \( i_{in} \) is the input current, and \( x = [v_c, i_L]^T \). The internal function expression of the generalized memristor is as follows:

\[
G_M = \frac{i_{in}}{v_{in}} = \frac{2 I_s e^{-\rho v_m} \sinh(\rho v_m)}{v_{in}}.
\]
According to the theory of fractional calculus, this paper expands the integer-order capacitor and inductor in the generalized memristor to the fractional order to describe its electrical characteristics. The fractional-order circuit model is shown in Figure 2, where \( q \) is the order. Therefore, the unified mathematical model of the fractional-order generalized memristor is as follows:

\[
\frac{d}{dt^q}v_{c} = F(x, v_{in}) = \begin{cases} 
\frac{2I_s(e^{-\rho v_{c}} \cosh(\rho v_{in}) - 1)}{L^q} - i_L, \\
v_{c} - R_i L \\
\frac{v_{in} - R_i L}{C^q} \\
i_{in} = f(v_{c}, v_{in}) = 2I_s e^{-\rho v_{c}} \sinh(\rho v_{in}), \\
G_M = \frac{i_{in}}{v_{in}} = \frac{2I_s e^{-\rho v_{c}} \sinh(\rho v_{in})}{v_{in}}.
\end{cases}
\]

(6)

The component parameters of the novel fractional-order RLC generalized memristor are as follows: \( C = 4 \, \text{nF}, \) \( L = 230 \, \text{mH}, \) and \( R = 800 \, \Omega; \) the diode parameters are as follows: \( I_s = 2.682 \, \text{nA}, \) \( n = 1.836, \) and \( I_{T} = 25 \, \text{mV}. \) The Oustaloup method was used to obtain the approximate transfer function of the fractional-order integral operator [31]. In the frequency domain, the transfer function of a fractional integral operator with order \( q \) can be expressed as \( F(s) = 1/s^q. \) According to the Oustaloup approximation technique, transfer functions of different orders at the operating frequency can be obtained. Table 1 gives the approximate transfer functions of the fractional-order integrator \( F(s) \) with orders of 0.87, 0.9, 0.95, and 0.97, respectively.

The fractional-order capacitor \( C^q \) is realized by the equivalent unit circuit in Figure 3. The fractional-order inductance \( L^q \) is realized by the equivalent circuit in Figure 4.

In the complex frequency domain, the equivalent circuit expression for realizing the fractional-order capacitor \( C^q \) is equation (7). Tables 2 and 3, respectively, give the calculated values of resistance and capacitance in the equivalent cell circuit under different orders. The calculated values of resistance and inductance in the equivalent circuit of fractional inductor \( L^q \) under different orders are calculated by formula (8), which are given in Tables 4 and 5, respectively.

\[
F(s) = \frac{1}{Cs^q} = R_{in} + \sum_{k=1}^{n} \left( \frac{1/C_k}{s + (1/R_k C_k)} \right),
\]

(7)

\[
F(s) = \frac{1}{L^{1-q}} = \sum_{k=1}^{n} \frac{1}{L_k s + R_k} + \frac{1}{R_{in}}.
\]

(8)

On the basis of the aforementioned theory, in order to verify whether the proposed novel fractional-order RLC generalized memristor possesses the three essential characteristics of memristor [32], numerical simulation is performed using MATLAB/Simulink software. The input excitation source is \( u_{in} = V_{in} \sin(2\pi f t) V \), where \( f \) is the frequency of the input voltage and \( V_{in} \) is the amplitude of the input voltage. When \( V_{in} = 4 \, \text{V} \) and the order \( q \) are, respectively, 0.87, 0.9, 0.95, and 0.97, the \( i_{in} - u_{in} \) curves under different frequencies are shown in Figure 5. It can be seen from Figure 5 that the trajectory in the \( i_{in} - u_{in} \) plane shrinks at the origin, and the area of the tight hysteresis loop decreases monotonously as the excitation frequency increases. In addition, when the frequency of the input voltage tends to infinity, the tight hysteresis loop shrinks to a nonlinear single-valued function, which proves that the proposed fractional-order generalized memristor satisfies the basic characteristics of the memristor.

In addition, as the input voltage amplitude increases, the area of the tight hysteresis loop will increase, but the shape remains unchanged, as shown in Figure 6(a). In order to analyze the influence of the order on the characteristics of the memristor model, fix \( V_{in} = 4 \, \text{V}, f = 1 \, \text{kHz}, \) and \( q \) to be 0.87, 0.9, 0.95, and 0.97, respectively, and obtain the volt-ampere characteristic curves under different orders, as shown in Figure 6(b). It is found that the area of the tight hysteresis loop of the fractional-order generalized memristor decreases monotonously with the increase of the order under the same amplitude and frequency.

### 2.3. The Fractional-Order Memristive Chaotic Circuit

Yang et al. [33] proposed a chaotic circuit composed of capacitance, inductance, resistance, negative conductance, and nonlinear passive memristor. After realizing the fractional-order generalized memristor, a fractional-order memristive chaotic circuit is established using the proposed
3. Dynamic Behavior Analysis of Fractional-Order Generalized Memristive Chaotic System

3.1. Stability Analysis of the Equilibrium Point. The dynamic behavior of the proposed fractional-order memristive chaotic circuit is analyzed by calculating the equilibrium points and eigenvalues of the Jacobian matrix. The equilibrium points of the chaotic circuit can be obtained by solving the following equation:

\[
\begin{align*}
0 &= v_{c1} - \frac{v_{c2}}{C_1^q} - \frac{i_1}{C_1^q}, \\
0 &= \frac{v_{c2}(R_o G - 1)}{R_o C_2^q} + \frac{v_{c1}}{R_o C_2^q} - 2I_s e^{-\rho v_c} \sinh(\rho v_{c2}) \\
0 &= \frac{v_{c1}}{L_1^q}, \\
0 &= \frac{2I_s(e^{-\rho v_c} \cosh(\rho v_{c2}) - 1) - i_L}{C_1^q}, \\
0 &= \frac{v_c - R_i}{L_1^q}.
\end{align*}
\]  

(10)

Obviously, \( Q_1(0, 0, 0, 0, 0) \) is an equilibrium point in the fractional-order RLC memristive chaotic circuit. This paper uses MATLAB-based graphical analysis methods to obtain other equilibrium points [34]. Simplify formula (10) to get

\[
\begin{align*}
0 &= v_{c1} - \frac{v_{c2}}{C_1^q} - \frac{i_1}{C_1^q}, \\
0 &= \frac{v_{c2}R_o G - 1}{R_o C_2^q} + \frac{v_{c1}}{R_o C_2^q} - 2I_s e^{-\rho v_c} \sinh(\rho v_{c2}), \\
0 &= \frac{2I_s e^{-\rho v_c} \cosh(\rho v_{c2}) - 1 - i_L}{C_1^q}, \\
0 &= \frac{v_c - R_i}{L_1^q}.
\end{align*}
\]  

(11)
| \( q \)  | \( F(s) \)                                                                 |
|------|--------------------------------------------------------------------------|
| 0.87 | \((8.128e-07)^7 + 6.086e^5 + 4.512e05s^2 + 3.345e08s^4 + 2.48e09s^6 + 1.838e08s^7 + 1.363e05s^8 + 1.363e05s^9 + 1.838e08s^{10} + 2.48e09s^{11} + 3.345e08s^{12} + 4.512e05s^{13} + 6.086e^5 + 8.128e-07)\) |
| 0.9  | \((5.012e-07)^7 + 4.021e^5 + 3.195e05s^2 + 2.538e08s^4 + 2.016e09s^6 + 1.601e08s^7 + 1.272e05s^8 + 1.272e05s^9 + 1.601e08s^{10} + 2.016e09s^{11} + 2.538e08s^{12} + 3.195e05s^{13} + 4.021e^5 + 5.012e-07)\) |
| 0.95 | \((2.239e-07)^7 + 2.015e^5 + 1.796e05s^2 + 1.601e08s^4 + 1.427e09s^6 + 1.272e08s^7 + 1.133e05s^8 + 1.133e05s^9 + 1.427e09s^{10} + 1.601e08s^{11} + 1.272e08s^{12} + 1.133e05s^{13} + 2.015e^5 + 2.239e-07)\) |
| 0.97 | \((1.622e-07)^7 + 1.529e^5 + 1.427e05s^2 + 1.332e08s^4 + 1.243e09s^6 + 1.16e08s^7 + 1.082e05s^8 + 1.082e05s^9 + 1.16e08s^{10} + 1.243e09s^{11} + 1.332e08s^{12} + 1.427e05s^{13} + 1.529e^5 + 1.622e-07)\) |
Figure 3: The equivalent unit circuit of the fractional capacitor $C_q$.

Figure 4: The equivalent unit circuit of the fractional inductor $L_q$.

Table 2: The calculated value of the resistance in the equivalent unit circuit of the fractional-order capacitor $C_q$.

| $q$  | $R_m$ (Ω) | $R_1$ (Ω) | $R_2$ (Ω) | $R_3$ (Ω) | $R_4$ (Ω) | $R_5$ (Ω) | $R_6$ (Ω) | $R_7$ (Ω) |
|-----|-----------|-----------|-----------|-----------|-----------|-----------|-----------|-----------|
| 0.87| 203       | 4,961     | 2.75e5    | 1.51e7    | 8.32e8    | 4.57e10   | 1.10e13   | 6.63e6    |
| 0.9 | 125       | 2,882     | 1.84e5    | 1.16e7    | 7.31e8    | 4.61e10   | 1.56e13   | 2.47e7    |
| 0.95| 56        | 905       | 7.26e4    | 5.77e6    | 4.58e8    | 3.64e10   | 2.78e13   | 9.03e7    |
| 0.97| 40.5      | 451       | 3.97e4    | 3.46e6    | 3.01e8    | 2.62e10   | 3.5e13    | 1.37e8    |

Table 3: The calculated value of the capacitance in the equivalent unit circuit of the fractional-order capacitor $C_q$.

| $q$  | $C_1$ (µF) | $C_2$ (µF) | $C_3$ (µF) | $C_4$ (µF) | $C_5$ (µF) | $C_6$ (µF) | $C_7$ (µF) |
|-----|------------|------------|------------|------------|------------|------------|------------|
| 0.87| 1.49e-3    | 2.69e-3    | 4.9e-3     | 8.91e-3    | 1.62e-2    | 1.33e-2    | 2.21e4     |
| 0.9 | 2.76e-3    | 4.33e-3    | 6.85e-3    | 1.09e-2    | 1.72e-2    | 1.01e-2    | 6.36e3     |
| 0.95| 9.85e-3    | 1.23e-2    | 1.55e-2    | 1.95e-2    | 2.45e-2    | 6.34e-3    | 1.96e3     |
| 0.97| 2.07e-2    | 2.35e-2    | 2.7e-2     | 3.1e-2     | 3.56e-2    | 5.27e-3    | 1.35e3     |

Table 4: The calculated value of the resistance in equivalent cell circuit of fractional-order inductor $L_q$.

| $q$  | $R_m$ (Ω) | $R_1$ (Ω) | $R_2$ (Ω) | $R_3$ (Ω) | $R_4$ (Ω) | $R_5$ (Ω) | $R_6$ (Ω) | $R_7$ (Ω) |
|-----|-----------|-----------|-----------|-----------|-----------|-----------|-----------|-----------|
| 0.87| 2.83e5    | 1.16e4    | 2.09e2    | 3.8       | 6.91e-2   | 1.26e-3   | 5.21e-6   | 8.67      |
| 0.9 | 4.59e5    | 2.06e4    | 3.13e2    | 4.96      | 7.86e-2   | 1.25e-3   | 3.68e-6   | 2.32      |
| 0.95| 1.03e6    | 0.637     | 2.07e-6   | 1.58e-3   | 1.26e-1   | 9.97      | 792       | 6.36e4    |
| 0.97| 1.42e6    | 1.27e5    | 1.45e3    | 1.66e1    | 1.91e-1   | 2.19e-3   | 1.64e-6   | 4.19e-1   |

Table 5: The calculated value of inductance in equivalent cell circuit of fractional-order inductor $L_q$.

| $q$  | $L_1$ (H) | $L_2$ (H) | $L_3$ (H) | $L_4$ (H) | $L_5$ (H) | $L_6$ (H) | $L_7$ (H) |
|-----|-----------|-----------|-----------|-----------|-----------|-----------|-----------|
| 0.87| 8.59e-2   | 1.548e-1  | 2.82e-1   | 5.13e-1   | 9.33e-1   | 7.65e-1   | 1.27e6    |
| 0.9 | 1.58e-1   | 2.49e-1   | 3.94e-1   | 6.25e-1   | 9.9e-1    | 5.79e-1   | 3.65e5    |
| 0.95| 1.12e5    | 0.365     | 1.41      | 1.12      | 0.889     | 0.706     | 0.566     |
| 0.97| 1.19      | 1.35      | 1.55      | 1.7       | 2.05      | 3.03e-1   | 7.74e1    |
Figure 5: The tight hysteresis loop of the fractional generalized memristor under different orders. (a) $q = 0.87$, (b) $q = 0.9$, (c) $q = 0.95$, and (d) $q = 0.97$.

Figure 6: The tight hysteresis loop of the new fractional-order RLC generalized memristor. (a) Pinched hysteresis loop when $f = 1 \text{kHz}$, $q = 0.95$, and $V_m$ varies; (b) pinched hysteresis loop when $f = 1 \text{kHz}$, $V_m = 4 \text{V}$, and $q$ varies.
Using MATLAB to plot the functions of $v_{c2}$ and $v_c$, the coordinates of the intersection of the two functions are obtained, as shown in Figure 9. Therefore, the other two equilibrium points are $Q_{2,3} = (0, \pm 1.07478, \pm 0.000597, 0.09556, 0.000119)$.

The Jacobian matrix of formula (9) is as follows:

$$J = \begin{bmatrix}
-a & 0 & 0 & -a & 0 \\
dp & f & -ap & d & -dp + bc & de -cv & \cosh(cv_{c2}) \\
g & 0 & 0 & 0 & 0 \\
0 & bcme -cv & \cosh(cv_{c2}) & 0 & 0 \\
0 & 0 & 0 & 0 & y & -zy \\
\end{bmatrix}, \quad (12)$$

where $a = 1/Cq_1$, $b = 2L\rho$, $c = \rho$, $d = 1/Cq_2$, $f = G$, $g = 1/Lq_1$, $m = 1/Cq_1$, $p = 1/R\rho$, $y = 1/L$, and $z = R$.

The chaotic system can be obtained by bringing the equilibrium point into the Jacobian matrix:

$$\lambda_1(Q_1) = 16676.86,$$
$$\lambda_2(Q_1) = -5556.065 + 18427.691i,$$
$$\lambda_3(Q_1) = -5556.065 - 18427.691i,$$
$$\lambda_4(Q_1) = -1746.434 + 32923.507i,$$
$$\lambda_5(Q_1) = -1746.434 - 32923.507i,$$
$$\lambda_1(Q_2) = -184240.9,$$
$$\lambda_2(Q_2) = 37723.12,$$
$$\lambda_3(Q_2) = -32043.28,$$
$$\lambda_4(Q_2) = -3704.807 + 19661.39i,$$
$$\lambda_5(Q_2) = -3704.807 - 19661.39i,$$
$$\lambda_1(Q_3) = -385909.9,$$
$$\lambda_2(Q_3) = 211961.5,$$
$$\lambda_3(Q_3) = -3352.592 + 23299.84i,$$
$$\lambda_4(Q_3) = -3352.592 - 23299.84i,$$
$$\lambda_5(Q_3) = -5317.097.$$

From the above eigenvalues, it can be seen that all three equilibrium points have a positive real root, so the three equilibrium points are the unstable saddle points of the system.

3.2. Bifurcation Behaviors of Fractional-Order Memristive Chaotic System. In order to study the dynamic influence of the capacitance $C^i$ value on the fractional memristive chaotic system, the initial value of the chaotic system is set to $(0V, 0.01V, 0A, 0V, 0A)$, and the bifurcation diagram of the fractional-order memristive chaotic system can be obtained, as shown in Figure 10. It can be seen from Figure 10 that the dynamic behaviors of the fractional-order memristive chaotic system have three states: period, bifurcation, and chaos, and a narrow period window appears in the chaotic state. When $C^i < 0.8 nF$, the fractional-order system is in a single cycle state. When the value of $C^i$ increases, the system enters complex nonlinear dynamic behaviors, such as bifurcation and chaos. When $0.8 nF < C^i < 0.98 nF$, the fractional-order system is in cycle 2 state. When $1.55 nF < C^i < 1.65 nF$, the system is in a narrow periodic window in the chaotic region. Figure 11 shows...
the phase diagrams of the fractional-order memristive chaotic circuit with different values of the capacitor $C^L$. The results of the phase diagrams are consistent with the dynamic behaviors described in the bifurcation diagram. As shown in Figure 11(a), the fractional-order memristive chaotic system is in a periodic state when $C^L = 0.76 \text{nF}$. As can be seen in Figure 11(b), the system is in a bifurcated state, and the cycle 2 state appears when $C^L = 0.9 \text{nF}$. When $C^L = 1.6 \text{nF}$, the system is in a narrow periodic state in the chaotic region as shown in Figure 11(c). When $C^L = 4 \text{nF}$, the system is in a chaotic state, as shown in Figure 11(d).

4. Circuit Simulation Experiment of the Fractional-Order Memristive Chaotic System

The same equivalent circuits are used to realize the capacitors $C^L_1$ and $C^L_2$ and inductor $L^q$ in the fractional-order memristive chaotic circuit. In the whole system, the value of order $q$ is 0.95. Tables 6 and 7 show the equivalent resistance, capacitance, and inductance parameters of the fractional-order capacitors $C^L_1$ and $C^L_2$ and the fractional-order inductor $L^q$. The simulation diagram of the fractional-order Chua’s memristive circuit is shown in
Figure 9: Plotted functions and their intersection points.

Figure 10: The bifurcation diagram of the fractional-order memristive chaotic circuit with capacitor $C_q$ as a parameter.

Figure 11: Continued.
Figure 12: The diode in the chaotic circuit is set with the parameters of 1N4148, and the values of $R_o$, $R$, and $L^q$ remain unchanged.

Figure 13 shows the phase diagrams of the fractional-order memristive chaotic circuit in the chaotic state when $C^f = 4 \, \text{nF}$. The results verify the correctness of the numerical
Table 6: The equivalent resistance parameters of fractional-order capacitors $C_1^q$ and $C_2^q$ and inductor $L_1^q$ when $q = 0.95$.

| $L_1^q$ ($\Omega$) | $R_1$ ($\Omega$) | $R_2$ ($\Omega$) | $R_3$ ($\Omega$) | $R_4$ ($\Omega$) | $R_5$ ($\Omega$) | $R_6$ ($\Omega$) | $R_7$ ($\Omega$) |
|------------------|------------------|------------------|------------------|------------------|------------------|------------------|------------------|
| 8.04e4           | 4.970            | 6.2              | 0.78             | 9.83e-3          | 1.24e-4          | 1.62e-7          | 0.0499           |
| 2.24             | 36.2             | 2.9e3            | 2.31e5           | 1.83e7           | 1.46e9           | 1.11e12          | 3.61e6           |
| 2.24e1           | 3.62e2           | 2.9e4            | 2.31e6           | 1.83e8           | 1.46e10          | 1.11e13          | 3.61e7           |

Table 7: The equivalent capacitance and inductance parameters of fractional-order capacitors $C_1^q$ and $C_2^q$ and inductor $L_1^q$ when $q = 0.95$.

| $L_1^q$ ($H$) | $L_2$ ($H$) | $L_3$ ($H$) | $L_4$ ($H$) | $L_5$ ($H$) | $L_6$ ($H$) | $L_7$ ($H$) |
|---------------|-------------|-------------|-------------|-------------|-------------|-------------|
| 4.43e-2       | 5.53e-2     | 6.96e-2     | 0.0876      | 0.11        | 0.0285      | 8.8e3       |
| $C_1$ ($\mu F$) | $C_2$ ($\mu F$) | $C_3$ ($\mu F$) | $C_4$ ($\mu F$) | $C_5$ ($\mu F$) | $C_6$ ($\mu F$) | $C_7$ ($\mu F$) |
| 2.46e-1       | 3.07e-1     | 3.86e-1     | 4.86e-1     | 6.12e-1     | 1.59e-1     | 4.89e4      |
| 2.46e-2       | 3.07e-2     | 3.86e-2     | 4.86e-2     | 6.12e-2     | 1.59e-2     | 4.89e3      |

Figure 12: The equivalent circuit of the fractional-order Chua’s memristive chaotic system.
5. Conclusions

This paper proposes a fractional-order generalized memristor composed of a fractional-order capacitor, a fractional-order inductor, a resistor, and diodes and analyzes and verifies the memristive characteristics. Then Chua’s chaotic circuit is extended to the fractional order and combined with the fractional-order generalized memristor to form a fractional-order Chua’s memristive chaotic circuit. The numerical simulation and circuit simulation of the fractional-order Chua’s memristive chaotic circuit show that the three equilibrium points of the proposed fractional-order chaotic system are the unstable saddle points of the system, and the value of the capacitance $C_q$ has an effect on the dynamic behaviors of the fractional-order chaotic system significantly. As the value of capacitance $C_q$ increases, the fractional-order Chua’s memristive chaotic circuit enters a bifurcation from a periodic state and finally enters chaos state. A narrow period window was found in the chaotic state. The phase diagrams in the corresponding state are given in the paper, which are consistent with the dynamic characteristics described in the bifurcation diagram. Finally, the equivalent circuit of the fractional-order generalized memristive chaotic circuit is built in PSpice, and the results obtained by circuit simulation are consistent with the theoretical analysis and numerical simulations.

Data Availability

The data used to support the findings of this study are available from the corresponding author upon request.

Figure 13: Chaotic phase diagrams in circuit simulation based on PSpice software. (a) Phase trajectory in $v_{c2} - v_{c1}$ plane; (b) phase trajectory in $v_{c1} - i_1$ plane; (c) phase trajectory in $v_{c2} - i_1$ plane; (d) phase trajectory in $v_{c2} - i_M$ plane; (e) phase trajectory in $v_{c1} - i_M$ plane; (f) phase trajectory in $i_1 - i_M$ plane.
Conflicts of Interest

The authors declare that there are no conflicts of interest regarding the publication of this study.

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