Solder joint reliability based on creep strain energy density for SAC305 and doped SAC solders

Ramiro Sebastian Vargas Cruz\(^1\)*, and Viktor Gonda\(^2\)

\(^1\)\'{O}buda University, Doctoral School on Materials Sciences, Bécsiút 96b, 1034 Budapest, Hungary
\(^2\)\'{O}buda University, Bánki Donát Faculty of Mechanical and Safety Engineering, Népszínház u. 8, 1081 Budapest, Hungary

Abstract. Parallel to the development of new lead-free solders, electronic packaging has gone through a considerable evolution. A redistribution of layers allows the increase of functionality by increasing the number of inputs/outputs in the package while reducing the size. The reliability of the package is strongly influenced by the reliability of the interconnects. During production and service life, there are thermal processes involved that may lead to thermal fatigue. In this work, a two-dimensional finite element model of a Fan-Out Wafer Level Packaging (FO-WLP) was built, and simulations of thermal test cycles were carried out varying the solder interconnect material: SAC305, SACQ, SACR, or InnoLot. A thermal oscillating load from \(-40^\circ\text{C}\) to \(125^\circ\text{C}\) was applied to the packaging for three hours. State of the art concerning solder joint reliability models based on creep behavior reveals the benefit of using energy-based parameters, as cycles to failure are inversely proportional to the average creep strain energy density. Based on the average creep strain energy density simulation results, the reliability of the package with different solders was compared. The qualitative results suggest that SACQ has a significant advantage in the operational lifetime compared to SACR, InnoLot, and SAC305.

1 Introduction

Recent regulations on Waste from Electrical and Electronic Equipment (WEEE) and Restrictions of Hazardous Substances (RoHS) have limited the usage of Pb (lead) in electronic applications \([1], [2]\). One of the critical lead-contained components in the electronics industry had been the soldering material; the eutectic Sn-Pb (tin-lead) solder had to be replaced by lead-free soldering materials. Typically, solder joints are subjected to temperatures around 80\% of their melting temperature during their service life \([3]\). A significant change in the composition of the soldering material translates to an increase in the melting temperature that should not compromise the electronic device performance and reliability. One of the first alloy compositions that replaced lead-contained solders was the

*Corresponding author: ramiro.vargas@stud.uni-obuda.hu

© The Authors, published by EDP Sciences. This is an open access article distributed under the terms of the Creative Commons Attribution License 4.0 (http://creativecommons.org/licenses/by/4.0/).
near eutectic Sn-Ag-Cu (tin-silver-coper), widely known as SAC solder[4]. Varying the composition of the alloys, different SAC solders have been commercialized (e.g., SAC305, SAC405, SAC105)[5]. In the automotive industry, thermal requirements have been defined for Integrated Circuits (ICs) that undergo harsh working environments where temperature loads may reach over 150°C [6]. Consequently, new solder materials, called doped-SAC or SAC-X, were developed[7].

Currently, electronic equipment reliability is linked to performance and security [8]. Therefore, it is essential to understand reliability tests and mathematical models that can effectively predict the lifetime of materials. Regarding thermal cycling reliability tests, recent studies have proved the accuracy of Finite Element Analysis (FEA) on ICs simulations with lifetime prediction errors of less than 20% [9]. Lead-free solder joint reliability is of great interest to accurately predict the service life of an electronic device.

In this work, a two-dimensional finite element model of a Fan-Out Wafer Level Packaging (FO-WLP) was built, and simulations of thermal test cycles were carried out varying the solder interconnect material: SAC305, SACQ, SACR, or InnoLot. State of the art concerning solder joint reliability models based on creep behavior reveals the benefit of using energy-based parameters. Based on the average creep strain energy density simulation results, the reliability of the package with different solders was compared. The qualitative results suggest that SACQ has a significant advantage in the operational lifetime compared to SACR, InnoLot, and SAC305.

2 Reliability and material models for solder joints

Reliability of ICs subjected to cycling thermal loads has been a core topic of research, with thermal fatigue analysis by predictions of the number of cycles to failure. Initially, Distance to Neutral Point (DNP) was identified as a key parameter; hence the farthest solder joint from the neutral point of the IC possesses a risk due to the highest thermo-mechanical loads and a geometry that limits the size of the packaging and number of I/Os[10]. However, the accuracy of the approach has been intensely discussed because electronic packaging layouts have been improved. Lau[11] reported that the DNP approach presents some limitations. For instance, DNP failure prediction has been proved to be valid for packaging without underfill; however, it has been incorrectly applied where underfills are used[12].

The state of the art of reliability models used for solder joint reliability is summarized in Table 1. The number of cycles before failure is related either to strain or energy type of parameters. Li et al.[13] successfully conducted a simulation to evaluate the thermal fatigue life of a SAC solder material. Creep strain and creep strain energy density were used for the lifetime prediction. From the results, creep strain energy density using hyperbolic-sine model approached more accurately to experimental data.

According to Norris et al.[14], constant n was observed to be 2 for most of metals. Zubelewicz et al.[15] stressed that Coffin-Manson law does not fit experimental data for high strain rates. Recently, Clech[16] summarized researches that compare the Coffin-Manson model and critical Distance to Neutral Point (DNP).

Pang et al. [17] presented research where tables for n, n’, C and A are listed. Additionally, they included a new approach that includes frequency dependency.

Syed[18] proposed employing the Monkman-Grant model to predict the number of cycles to failure based on accumulated creep strain and creep strain energy density per cycle. Using the Power-law and Hyperbolic-sine creep model, his founding demonstrates that both accumulated creep and energy density can effectively predict the lifetime of solder stacks.
Although the Darveaux model was not available in commercial simulation programs, his approach was later corroborated to be more accurate than the Coffin-Manson model by Ramachandran et al. [19].

Table 1. Overview of solder joint reliability models.

| Model                              | Variable’s description                                      |
|------------------------------------|-------------------------------------------------------------|
| Coffin-Manson [20]                | \( N_f (-) \), Cycles to failure.                           |
| \( N_f (\Delta e_p)^n = C \)      | \( n (-) \), Empirical constant.                            |
|                                    | \( \Delta e_p (-) \), Inelastic strain range.               |
|                                    | \( C (-) \), Proportionality factor / Fatigue ductility coefficient. |
| Morrow [21]                       | \( n' (-) \), Fatigue exponent.                             |
| \( N_f n' W_p = A \)              | \( A (\text{MJ/m}^3) \), Material ductility coefficient.    |
|                                    | \( W_p (\text{MPa}) \), Inelastic strain energy density.    |
| Monkman-Grant [22]                | \( t_r (s) \), time to rupture.                             |
| \( t_r = \frac{\varepsilon_f}{\dot{\varepsilon}_{cr}} \) | \( \varepsilon_f (-) \), creep ductility.                    |
|                                    | \( \dot{\varepsilon}_{cr} (s^{-1}) \), creep strain rate.  |
| Darveaux et al. [23]              |                                                             |
| **Crack Initiation**              | \( N_0 = K_1 \Delta W_{ave}^{K_2} \)                       |
| **Crack Growth**                  | \( \frac{da}{dN} = K_3 \Delta W_{ave}^{K_4} \)            |
| **Energy Density Model**          | \( N_f = N_0 + \frac{a}{(\frac{da}{dn})} \)               |
|                                    | \( N_0 (-) \), Number of cycles before crack initiation.    |
|                                    | \( \Delta W_{ave} (\text{MPa}) \), Average strain energy density. |
|                                    | \( \frac{da}{dN} (\text{m/cycle}) \), Crack growth rate per cycle. |
|                                    | \( a (\text{m}) \), Critical length exposed to crack propagation. |
|                                    | \( K_{1-4} \), Constants based on the element size of the solder. |

For creep material models, either hyperbolic-sine law or Anand's type of models are often used. A more detailed list of creep-based material characterization experiments is summarized in Table 2. The authors who used the hyperbolic-sine model stressed the lack of articles reporting Anand's constants.

Table 2. Employed creep and reliability models in highlighted references.

| Author                  | Creep Model | Reliability Model |
|-------------------------|-------------|------------------|
|                         | Hyperbolic Sine | Anand | Morrow | Darveaux | Coffin Manson |
| Lui et al. [10]         | \( \times \) | \( \times \) | \( \times \) | \( \times \) |
| Che [24]                | \( \times \) | \( \times \) | \( \times \) | \( \times \) |
| Chen [25]               | \( \times \) | \( \times \) | \( \times \) | \( \times \) |
| Guven [26]              | \( \times \) | \( \times \) | \( \times \) | \( \times \) |
| Lin [27]                | \( \times \) | \( \times \) | \( \times \) | \( \times \) |
| Che and Pang [28]       | \( \times \) | \( \times \) | \( \times \) | \( \times \) |
| Rahangadale et al. [29] | \( \times \) | \( \times \) | \( \times \) | \( \times \) |

3 Simulation Methods

Finite Element Analysis (FEA) has been successfully applied for package reliability analysis [30]. According to Gokhale et al. [30], Computer-Aided Engineering (CAE) can predict failures with a 90% accuracy.

An Integrated CircuitS-PBGA-N167 from the Texas Instrument [31] in an advanced Fan-Out Wafer Level Packaging was used as the base for the 2-D finite element model. Half of the package was modeled. Boundary conditions on the left edge were applied for symmetry with constrained displacements in the x-direction. A vertical constrain was
placed to the left bottom corner concerning the y-direction displacement to ensure a unique mathematical solution (see Fig. 1). The IC comprises nine different materials detailed in Fig. 1. Corresponding material properties are listed in Table 3 for most constituents, except for the solders, which are shown in Table 4.

![Fig. 1. 2D Model (Half cross-section of the chip).](image)

**Table 3.** Material parameters [25].

| Material                | Young’s Modulus (GPa) | Poisson’s Ratio | CTE (ppm/°C) | Tg(°C) |
|-------------------------|-----------------------|----------------|--------------|--------|
| Si (chip)               | 131                   | 0.3            | 2.8          |        |
| EMC                     | T <Tg: 18.5; T >Tg: 1.2 | 0.3            | T <Tg: 9; T >Tg: 18 | 163    |
| Dielectrics             | T <Tg: 0.92; T >Tg: 0.1 | 0.3            | T <Tg: 80; T >Tg: 227 | 205    |
| Cu                      | 117                   | 0.34           | 17           |        |
| Underfill               | T <Tg: 3.8; T >Tg: 0.125 | 0.3            | T <Tg: 44; T >Tg: 119 | 141    |
| Solder Mask             | T <Tg: 2.4; T >Tg: 0.23 | 0.3            | T <Tg: 60; T >Tg: 161 | 100    |
| PCB (Printed Circuit Board) | For x,y: 25; For z: 11 | For xy 0.11 for xz, yz 0.39 | For x, y: 15 For z: 46 |        |

1Coefficient of Thermal Expansion, 2Glass Transition Temperature

**Table 4.** Material parameters for the Anand creep model.

| Description | SACQ[32] | SACR[33] | InnoLot[33] | SAC305[34] |
|-------------|----------|----------|-------------|------------|
| $s_n$ (MPa) | 0.405    | 34.72    | 32.42       | 21         |
| $A$ (s$^{-1}$) | 2.45·10$^8$ | 1000    | 25 000      | 3 501      |
| $\xi$       | 0.068    | 6        | 7           | 4          |
| m           | 0.36     | 0.15     | 0.35        | 0.25       |
| $h_n$ (MPa) | 3521.56  | 145 640  | 88 875      | 180 000    |
| $\delta$ (MPa) | 0.638    | 71.71    | 56.76       | 30.2       |
| n           | 0.0056   | 0.001    | 0.0097      | 0.01       |
| a           | 1.243    | 1.55     | 1.45        | 1.78       |
| $Q$(J·mol$^{-1}$) | 112 313.8 | 92 290.52 | 89 313.9 | 77 490.78 |

For solder materials, four types were selected in this study: SACQ, SACR, InnoLot, and SAC305. The chemical compositions of the solder alloys are shown in Table 5 for comparison. The visco-plastic mechanical model was employed for the solder behavior in the form of Anand's creep model[35]. Anand properties for each used solder are detailed in Table 4.

**Table 5.** Chemical composition (wt%) of some selected SAC solders.

| Solder        | Sn   | Ag  | Cu  | Bi  | Ni  | Sb  |
|---------------|------|-----|-----|-----|-----|-----|
| SAC-R[33]     | 96.62| 0.00| 0.92| 2.46| 0.00| 0.00|
| SAC-Q[33]     | 92.77| 3.41| 0.52| 3.30| 0.00| 0.00|
| InnoLot[33]   | 90.95| 3.80| 0.70| 3.00| 0.15| 1.40|
| SAC305[36]    | 95 – 96| 3.8 – 4.2| 0.3 – 0.7| 3.00| 0.00| 0.00|
An oscillating thermal load from –40°C to 125°C with three cycles was used, with a ramp-up time of 15 minutes, a dwell time of 15 minutes, a cooling time of 15 minutes, and another dwell time of 15 minutes, altogether 3 hours.

![Fig. 2. Thermal Load.](image)

Multicriteria time stepping was selected for the load cases (3 load cases of 3600s each) with fine criteria conditions to assure a minimum of 100 steps per thermal cycle.

### 4 Results

Creep strain energy density (CSED) data was collected from the four simulation replicates. The bottom corner locations of each solder ball displayed a high CSED for all the scenarios. The location with the most critical value of CSED (see Fig. 3) was situated in the bottom right corner of the outer solder ball (the most distant point from the line of symmetry).

As the multi-criteria was utilized for time-stepping, each CSED vs. time curve presented a different number of points: SAC305, 1808 points; SACQ, 4135 points; InnoLot, 1669 points; and SACR, 579 points. The number of points was directly proportional to the simulation time that varied from nearly one hour to over five hours.

![Fig. 3. Creep Strain Energy Density – Contour Band at the critical location (SAC305).](image)

The CSED vs. time curves from the critical location (Fig. 3) of each replicate were summarized in Fig 4. It is worth noting that SACQ presents the lowest CSED accumulation among the different soldering materials and the highest expected resolution due to a large number of steps. Additionally, InnoLot and SAC305 curves display a similar behavior during the first two thermal cycles. Nevertheless, the approximation of the average of
CSED only considers the CSED subtraction between the third and second thermal cycle (calculated in Table 6).

![Creep Strain Energy Density Comparison](image)

**Fig. 4. Creep Strain Energy Density Comparison.**

The aim of the comparison addresses creep-based reliability. From Table 1, Morrow's model was selected following the authors' trend in Table 2. Morrows's model requires the average creep strain energy density, $\Delta W_{av}$, as a key parameter of the number of cycles to failure calculation. According to Che and Pang[28], three thermal cycles are enough to obtain a stable plastic work density that approximates the average creep strain energy density. It can be computed by taking the end of the third and second thermal cycle as detail in Eq. (1).

**Calculation of average creep strain energy density ($\Delta W_{av}$) [28]:**

$$\Delta W_{av} = W_{3^{rd}_TC} - W_{2^{nd}_TC}$$

Where: $\Delta W_{av}$, average creep strain energy density; $W_{3^{rd}_TC}$ and $W_{2^{nd}_TC}$ Creep strain energy density at the end of the third and second thermal cycle (TC), respectively.

The computed approximation of average CSED shows that SACQ presents the lowest value, whereas SAC305 displays the highest value (see Table 6). Based on Morrow's reliability model presented in Table 1, the number of cycles to failure is inversely proportional to the average of CSED. Therefore, SACQ would resist the most extended working lifetime, followed by SACR, InnoLot, and SAC305. These results agree with a similar experimental comparison performed by Wei et al. [37] where SAC305 endured the least number of cycles to failure than SACQ, SACR, and InnoLot, under stress-controlled conditions.

**Table 6. Approximation of the Average of Creep Strain Energy Density (MPa).**

| CSED  | SAC305 | SACQ  | InnoLot | SACR  |
|-------|--------|-------|---------|-------|
| Cycle 1 | 0.339020 | 0.102072 | 0.337066 | 0.479284 |
| Cycle 2 | 0.617905 | 0.266476 | 0.653259 | 0.722654 |
| Cycle 3 | 0.883068 | 0.429206 | 0.908353 | 0.924602 |
| $\Delta W_{av}$ | 0.265163 | 0.162730 | 0.255094 | 0.201948 |

**5 Conclusions**

Reliability of an advanced electronic package is strongly influenced by the reliability of the interconnects. In this work, a two-dimensional finite element model of a Fan-Out Wafer
Level Packaging was built, and simulations of thermal test cycles were carried out varying the solder interconnect material: SAC305, SACQ, SACR, or InnoLot.

State of the art concerning solder joint reliability models based on creep behavior reveals the benefit of using energy-based parameters, as cycles to failure are inversely proportional to the average creep strain energy density.

Based on the average creep strain energy density simulations results, the reliability of the package with different solders was compared, and the following conclusions are drawn:

1. The critical location regarding CSED is commonly situated at the most outer solder ball. This conclusion strengthens the Distance to Neutral Point theory.
2. Doped SAC solders are more advantageous than SAC305 solder in terms of a working lifetime, as the simulation results agreed with similar experimental comparisons.
3. The qualitative results suggest that SACQ has a significant advantage in the operational lifetime compared to SACR, InnoLot, and SAC305.

References

1. R. Chandrappa, D. B. Das, Waste from Electrical and Electronic Equipment, 197–216 (2012)
2. EUROPEAN PARLIAMENT; THE COUNCIL OF THE EUROPEAN UNION, Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment, Official Journal of the European Union (2003)
3. T. R. Bieler, T. K. Lee, Lead-Free Solder, Reference Module in Materials Science and Materials Engineering (2017)
4. K. Suganuma, Curr. Opin. Solid State Mater. Sci., 5, 55–64 (2001)
5. H. Ma, J. C. Suhling, J. Mater. Sci., 44, 1141–1158 (2009)
6. R. W. Johnson, J. L. Evans, P. Jacobsen, J. R. R. Thompson, M. Christopher, IEEE Trans. Electron. Packag. Manuf., 27, 164–176 (2004)
7. I.E. Anderson, J Mater Sci: Mater Electron, 18, 55–76 (2007)
8. K. Seshan, Reliability Issues, Handbook of Thin Film Deposition, Elsevier, 43–62 (2018)
9. H. Zhang, F. Che, T. Lin, W. Zhao, Stress and reliability analysis for interconnects, Modeling, Analysis, Design, and Tests for Electronics Packaging beyond Moore, Elsevier, 131–244 (2020)
10. T. C. Lui, B. N. Muthuraman, THERMINIC 2016, Reliability assessment of wafer level chip scale package (WLCSP) based on distance-to-neutral point (DNP), (Budapest, Hungary, 2016)
11. J. H. Lau, Solder. Surf. Mt. Technol., 9 (2), 58–60 (1997)
12. W. Dauksher, W. S. Burton, Microelectron. Reliab., 43 (12), 2011–2020 (2003)
13. X. Li, Z. Wang, J. Mater. Process. Technol., 183 (1), 6–12 (2007)
14. K. C. Norris, A. H. Landzberg, IBM J. Res. Dev., 13 (3), 266–271 (1969)
15. A. Zubelewicz, R. Berrice, L. M. Keer, M. E. Fine, Am. Soc. Mech. Eng., 111, 179–182 (1988)
16. J. Clech, SMTA International 2016, The Combined Effect of Assembly Pitch and Distance to Neutral Point on Solder Joint Thermal Cycling Life, (Rosemont, USA, 2016)
17. J. H. L. Pang, B. S. Xiong, T. H. Low, Thin Solid Films, 462–463 (SPEC. ISS.), 408–412 (2004)
18. A. Syed, ECTC 2004, Accumulated creep strain and energy density based thermal fatigue life prediction models for SnAgCu solder joints, (Las Vegas, USA, 2004)
19. V. Ramachandran, K. C. Wu, K. N. Chiang, J. Mech., 34 (5), 637–643 (2018)
20. S. S. Manson, Thermal Stress and low-cycle fatigue (1966)
21. J. Morrow, Cyclic Plastic Strain Energy and Fatigue of Metals, Internal Friction, Damping, and Cyclic Plasticity, ASTM International, 45–87 (1965)
22. F. C. Monkman, N. J. Grant, American society of testing materials, Proceedings, 56, 593–620 (1956)
23. R. Darveaux, K. Banerji, A. Mawer, G. Dody, J. Lau, Reliability of plastic ball grid array assembly, Ball grid array technology, 13, 379–442 (1995)
24. F. X. Che, EPTC 2016, Study on board level solder joint reliability for extreme large fan-out WLP under temperature cycling (Singapore, 2016)
25. Z. Chen et al., EPTC 2018, Solder Joint Reliability Simulation of Fan-out Wafer Level Package (FOWLP) Considering Viscoelastic Material Properties (Singapore, 2018)
26. I. Guven, V. Kradinov, J. L. Tor, E. Madenci, J. Electron. Packag. Trans. ASME, 126 (3), 398–405 (2004)
27. W. Lin, Q. Pham, B. Baloglu, M. Johnson, ECTC 2017, SACQ Solder Board Level Reliability Evaluation and Life Prediction Model for Wafer Level Packages (Orlando, USA, 2017)
28. F. X. Che, J. H. L. Pang, EPTC 2004, Thermal fatigue reliability analysis for PBGA with Sn-3.8Ag-0.7Cu solder joints (Singapore, 2004)
29. U. Rahangdale et al., ITERM2017, Mechanical characterization of RCC and FR4 laminated PCBs and assessment of their board level reliability, (Orlando, USA, 2017)
30. N. S. Gokhale, S. Deshpande, S. Bedekar, A. Thite, Practical finite element analysis (Finito to infinite, Maharashtra, 2008)
31. Texas Instruments Incorporated, MicroStar BGA, Packaging Reference Guide (2000)
32. T. C. Lui, IWIPP2017, Lifetime prediction of viscoplastic lead-free solder: A new solder material, SACQ (Delft, Netherlands, 2017)
33. S. Ahmed, M. Basit, J. C. Suhling, P. Lall, ASME 2015 IEPTCE, Characterization of Doped SAC Solder Materials and Determination of Anand Parameters (San Francisco, USA, 2015)
34. M. Basit, M. Motalab, J. C. Suhling, P. Lall, ASME 2015 IEPTCE, Viscoelastic Constitutive Model for Lead-Free Solder Including Effects of Silver Content, Solidification Profile, and Severe Aging (San Francisco, USA, 2015)
35. L. Anand, J. Eng. Mater. Technol., 104 (1), 12 (1982)
36. K. Seelig, D. Suraski, [Online] Available: https://aimsolder.com [Accessed 20 April 2021]
37. X. Wei, S. Su, S. Hamasha, H. Ali, J. Suhling, P. Lall, ITERM2020, Fatigue Performance of Doped SAC Solder Joints in BGA Assembly (Orlando, USA, 2020)