Parametric Verification: An Introduction*

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Abstract. This paper constitutes a short introduction to parametric verification of concurrent systems. It originates from two 1-day tutorial sessions held at the Petri nets conferences in Toruń (2016) and Zaragoza (2017). A video of the presentation is available at youtube.com/playlist?list=PL9SULkoGjbeq8cdQVqFpUz7HYqD1fbEig, consisting of 14 short sequences. The paper presents not only the basic formal concepts tackled in the video version, but also an extensive literature to provide the reader with further references covering the area.

We first introduce motivation behind parametric verification in general, and then focus on different models and approaches, for verifying several kinds of systems. They include Parametric Timed Automata, for modelling real-time systems, where the timing constraints are not necessarily known a priori. Similarly, Parametric Interval Markov Chains allow for modelling systems where probabilities of events occurrences are intervals with parametric bounds. Parametric Petri Nets allow for compact representation of systems, and cope with different types of parameters. Finally, Action Synthesis aims at enabling or disabling actions in a concurrent system to guarantee some of its properties. Some tools implementing these approaches were used during hands-on sessions at the tutorial. The corresponding practicals are freely available on the Web.

1 Introduction to parametric verification

We first introduce the motivation for performing parametric verification. Several formal models can be considered, depending on the characteristics of the system and its properties the designer wants to address. We will also discuss the problems of interest in such a framework. At the end of this section, we will give pointers to some additional and complementary material.

* This is the author version of the manuscript of the same name published in the Transactions on Petri Nets and Other Models of Concurrency (ToPNoC). The published version is available at springer.com. This work is partially supported by the ANR national research program PACS (ANR-14-CE28-0002).
The reader is assumed to have basic knowledge of Petri nets and/or automata and their associated verification techniques, since they constitute the basis of the formal models we address in a parametric setting.

1.1 Why parameters and of what kind?

Parameters provide several facilities for easily modelling complex systems:

1. **Dimensioning**: systems often exhibit components that occur as *multiple copies* of the same structure or similar ones. For example, a wireless sensor network is composed of a certain number of identical sensors. At the design phase, the exact number of sensors might be unknown and would therefore be a parameter.

2. **Choice of actions**: different actions in the system might be possible in a given state. The designer having several possibilities in mind would model them and analyse the behaviour of the system with these actions enabled or disabled. In this case, the enabledness of each individual action is considered as a parameter.

3. **Design choices**: different system characteristics can be taken into account at the design phase so as to be evaluated. For example, when designing an electronic system, one might have the choice between different components, available of course at different prices, each providing its specific characteristics, such as a better or worse response time. In such a case, the designer may want to construct a single model, and use these timing characteristics as a parameter so as to evaluate which is the best possible choice according to his/her needs.

To handle the cases described, different kinds of parameters are used. They will influence the type of a formal model and verification techniques chosen. In the three previous cases, parameters would be:

1. **Instances numbering** allow for counting identical components in the system.
2. **Controllable actions** can be enabled or disabled, as opposed to the other ones which are always available for the system.
3. **Time or probabilities** provide means to handle different characteristics of the actual system components.

Therefore, many kinds of parameters can be considered according to the problem at hand.

1.2 Modelling languages

Among the popular traditional languages for modelling concurrent systems are automata and Petri nets, and their numerous extensions.

Unfortunately, these modelling languages are not completely suited to handle the systems of our interest. Indeed, numbering instances of components is easily achieved with high-level Petri nets such as Coloured Petri Nets (CPNs). In CPNs,
the Petri net is enriched with data carried by tokens and modified when firing transitions. These data can very well be a numbering of an instance. Nevertheless, the number of instances is fixed a priori, as opposed to a parameter. Therefore, when the designer wants to analyse several configurations of the system, first the model is built for a given number of instances, then analysed, the number is changed, the model analysed again, and so on.

In Petri nets or automata, there is no specific handling of controllable actions. Hence, to test several options in the design, each of them must be modelled and analysed individually.

Finally, timed versions of Petri nets and automata are widely used, but suffer from the same defaults: values must be known in advance.

Hence, with traditional modelling languages, values are to be set before the analysis is performed, and the process must be repeated for all possible values. It is thus a tedious process which boils down to testing all values one by one, taking a huge amount of time.

1.3 Problems of interest in a parametric setting

The major objective for introducing parameters is to circumvent this repetition of analysis, by introducing parameters in the models. Furthermore, the analysis techniques are suited to find constraints on parameters such that desired properties are satisfied (e.g. the property is satisfied for all values of the parameter $p$ between 1 and 10) or even synthesize the set of all such parameters valuations.

The first advantage of this approach is that answering these questions provides all possible values in a single analysis step. Second, the set of parameters obtained can be infinite (e.g. $p > 5$), a result that cannot in general be obtained with an enumerative approach.

1.4 Sources and references

Several sources of information are available to the reader, that provide additional details on the theoretical background, further examples, etc. Among these:

- the video of the tutorial presentation: youtube.com/playlist?list=PL9SOLKoGjbeqNcdQVqFpUz7HYqD1fbF1g;
- the slides of the tutorial: imitator.fr/tutorials/PN17/;
- the exercises with IMITATOR (imitator.fr/tutorials/PN17/) and ROMÉO (romeo.rts-software.org/doc/tutorial.html);
- the extensive literature that is referenced.

Outline. In Section 2, we first consider parameters as unknown constants in timed automata, i.e. Parametric Timed Automata [AHV93]. We review decidability results, and report on decidable subclasses. Then, in Section 3, we consider parameters as unknown probabilities in Parametric Interval Markov Chains [DLP16]. Section 4 deals with parameters that are unknown numbers of tokens in Petri nets, yielding Parametric Time Petri Nets [Dav+15; Dav+17]. As
a last formalism, we also consider in Section 5 the synthesis of actions (seen as Boolean parameters) [KMP15; KP15]. Finally we review some verification tools in Section 6.

2 Parametric Timed Automata

Classical qualitative model checking, implemented in powerful tools used successfully in industry, falls short when quantitative aspects of systems such as time, energy, probabilities, etc., are to be verified. Timed automata [AD94] allow for modeling and verifying time critical concurrent systems. This seminal work [AD94] received the CAV conference award in 2008, and since then numerous works have extended the formalism of timed automata.

However, despite of some success, (timed) model checking can be seen as slightly disappointing. There are two main reason of that:

1. the binary response to properties satisfaction may not be informative enough, and
2. the insufficient abstraction to cater for tuning and scalability of systems.

Adding parameters offers a higher level of abstraction by allowing unknown constants in a model. Parameters can be used to model unknown timing constants of timed systems. This approach has the following advantages:

– it becomes possible to verify a system at an earlier design stage, when not all timing constants are known with full certainty.
– it allows designers to cope with uncertainty even at runtime: some timings constants (e.g. periods of a real-time system) may be known up to a given precision only (e.g. given with an interval of confidence), and parameters can model this imprecision.

Parametric timed automata [AHV93] are an extension of timed automata where timing constants can become unknown, i.e. parameters. They represent a particularly expressive formalism: in fact, its expressiveness is Turing-complete [ALR16] and all non-trivial problems related to parametric timed automata are undecidable. For example, the mere existence of a parameter valuation for which there exists a run reaching some location is undecidable (see e.g. [And19b] for a survey).

Parametric timed automata suffer from negative decidability results, but they still remain a quite powerful formalism. They can be used to address robustness (in the sense of possibly infinitesimal variations of timing constants [BMS13]), to model and verify systems with uncertain constants, and to synthesize suitable (possibly unknown) valuations so that the system meets its specification.

In addition, several recent decidability results for subclasses of parametric timed automata (e.g. [BL09; BO14; JLR15; Ben+15; AL17]) has made this formalism more promising, while new algorithmic and heuristic techniques (e.g. [KP12; JLR15; And+15; Aşt+16; Li+17; And+19]) has made the parametric...
verification for some classes of problems more scalable and complete, or more often terminating.

Verification with parametric timed automata has had important outcomes in various areas, with verification of case studies such as the root contention protocol [Hun+02], Philip’s bounded retransmission protocol [Hun+02], a 4-phase handshake protocol [KP12], the alternating bit protocol [JLR15], an asynchronous circuit commercialised by ST-Microelectronics [Che+09], (non-preemptive) schedulability problems [JLR15], a distributed prospective architecture for the flight control system of the next generation of spacecrafts designed at ASTRIUM Space Transportation [Fri+12], and even analysis of music scores [FJ13].

In this section, we recall the syntax and semantics of parametric timed automata (Section 2.2) and their subclasses (Section 2.3). We introduce theoretical problems of interest (Section 2.4), and review decidability results (Section 2.5).

2.1 Basic notions

Let \( \mathbb{N} \), \( \mathbb{Z} \), \( \mathbb{Q}_+ \), and \( \mathbb{R}_+ \) denote the sets of non-negative integers, integers, non-negative rational numbers, and non-negative real numbers, respectively.

We first define the notions necessary to deal with clocks. We begin with clock valuations.

**Definition 1 (clock valuation).** Let \( X = \{x_1, \ldots, x_H\} \) be a finite set of clocks, i.e. real-valued variables that evolve at the same rate.

A clock valuation is a function \( w : X \to \mathbb{R}_+ \).

We identify a clock valuation \( w \) with the point \( (w(x_1), \ldots, w(x_H)) \).

The following notations allow for specifying null clocks, and adding simultaneously the same delay to all clocks.

**Notation 1 (clock operations)**

- We write \( X = 0 \) for \( \bigwedge_{1 \leq i \leq H} x_i = 0 \).
- We also use a special zero-clock \( x_0 \), always equal to 0 (as in e.g. [Hun+02]).
- Given \( d \in \mathbb{R}_+ \), \( w + d \) denotes the valuation such that \( (w + d)(x) = w(x) + d \), for all \( x \in X \).
- Given \( R \subseteq X \), we define the reset of a valuation \( w \), denoted by \( [w]_R \), as follows: \( [w]_R(x) = 0 \) if \( x \in R \), and \( [w]_R(x) = w(x) \) otherwise.

The systems considered comprise a priori unknown timing constants that are thus parameters to be synthesized according to the targeted property.

**Definition 2 (timing parameter valuation).** Let \( P = \{p_1, \ldots, p_M\} \) be a set of timing parameters, i.e. unknown timing constants.

A timing parameter valuation \( v \) is a function \( v : P \to \mathbb{Q}_+ \).

We identify a valuation \( v \) with the point \( (v(p_1), \ldots, v(p_M)) \).

Clocks and parameters are used together and thus can be combined.
Notation 2 (clocks and parameters valuations combined) Given a timing parameter valuation $v$ and a clock valuation $w$, we denote by $w|v$ the valuation over $X \cup P$ such that for all clocks $x$, $w|v(x) = w(x)$ and for all timing parameters $p$, $w|v(p) = v(p)$.

The expressions on clocks can concern clock themselves, but also involve parameters and constant delays.

Notation 3 (linear terms)

– In the following, let $lt$ denote a linear term over $X \cup P$ of the form $\sum_{1 \leq i \leq H} \gamma_i x_i + \sum_{1 \leq j \leq M} \beta_j p_j + d$, with $x_i \in X$, $p_j \in P$, and $\gamma_i, \beta_j, d \in \mathbb{Z}$.

– Let $plt$ denote a parametric linear term over $P$, that is a linear term without clocks (i.e., $\gamma_i = 0$ for all $i$).

The synthesis of parameters leads to expressing constraints on their values in order to guarantee that the model satisfies the expected properties.

Definition 3 (constraints on clocks and timing parameters). A constraint $C$ over $X \cup P$ is defined by the following grammar:

$$\phi ::= \phi \land \phi \mid \neg \phi \mid lt \gg 0,$$

where $\gg \in \{<, \leq, \geq, >\}$, $lt$ is a linear term.

Definition 4 (constraint satisfaction). A valuation $w|v$ satisfies a constraint $C$, denoted $w|v \models C$, if the expression obtained by replacing in $C$ each timing parameter by its valuation as in $v$ evaluates to true.

Zones allow for defining convex sets of clocks and timing parameters values.

Definition 5 (zones and parametric guards). A zone $C$ is a constraint such that each of its linear conjuncts can be written in the form $x_i - x_j \gg plt$, where $x_i, x_j \in X \cup \{x_0\}$. A parametric guard $g$ is a zone such that each of its linear conjuncts can be written in the form $x_i \gg plt$.

Definition 6 (satisfiability). Given a zone $C$, $w|v \models C$ indicates that valuating each clock variable $x$ with $w(x)$ and each timing parameter $p$ with $v(p)$ within $C$, evaluates to true. Zone $C$ is satisfiable if $\exists w, v$ s.t. $w|v \models C$.

Time elapsing can be obtained by adding a new variable to all clocks, ensuring that this variable is non-negative, and eliminating it (see, e.g. [And+09]).

Definition 7 (time elapsing of a zone). The time elapsing of a zone $C$, denoted by $C'$, is the constraint over $X \cup P$ obtained from $C$ by delaying all clocks by any arbitrary amount of time.

That is,

$$w'|v \models C' \iff \exists w : X \rightarrow \mathbb{R}_+, \exists d \in \mathbb{R}_+ \text{ s.t. } w'|v \models C \land w' = w + d.$$
2.2 Syntax and semantics

Syntax

Definition 8 (parametric timed automaton [AHV93]). A parametric timed automaton (PTA) is a tuple $A = (\Sigma, L, l_0, F, X, P, I, E)$, where:

1. $\Sigma$ is a finite set of actions,
2. $L$ is a finite set of locations,
3. $l_0 \in L$ is the initial location,
4. $F \subseteq L$ is a set of final or accepting locations,
5. $X$ is a finite set of clocks,
6. $P$ is a finite set of parameters,
7. $I$ is the invariant, assigning to every $l \in L$ a parametric guard $I(l)$,
8. $E$ is a finite set of edges $e = (l, g, \sigma, R, l')$ where $l, l' \in L$ are the source and target locations, $\sigma \in \Sigma$, $R \subseteq X$ is a set of clocks to be reset, and $g$ is a parametric guard called the transition guard.

Given a parameter valuation $v$, we denote by $v(A)$ the non-parametric timed automaton where all occurrences of each parameter $p_i$ have been replaced by $v(p_i)$. If $v(A)$ is such that all constants in guards and resets are integers, then $v(A)$ is a timed automaton [AD94]. In the following, we refer to any structure $v(A)$ as a timed automaton, by assuming a rescaling of the constants: by multiplying all constants in $v(A)$ by the least common multiple of their denominators, we obtain an equivalent (integer-valued) timed automaton.

Example 1. Consider the coffee machine in Fig. 1, modelled using a PTA with 4 locations, 2 clocks ($x_1$ and $x_2$) and 3 parameters ($p_1, p_2, p_3$). Invariants are boxed. The only accepting location (with a double border) is done. Observe that all guards and invariants are simple constraints.

The machine can initially be idle for an arbitrarily long time. Then, whenever the user presses the (unique) button (action press), the PTA enters location

![Diagram of a coffee machine modeled using a PTA](image)

Fig. 1: A coffee machine modeled using a PTA
add sugar, resetting both clocks. The machine can remain in this location as long as the invariant \((x_2 \leq p_2)\) is satisfied; there, the user can add a dose of sugar by pressing the button (action \textit{press}), provided the guard \((x_1 \geq p_1)\) is satisfied, which resets \(x_1\). That is, the user cannot press twice the button (and hence add two doses of sugar) within a time less than \(p_1\). Then, \(p_2\) time units after the machine left the idle mode, a cup is delivered (action \textit{cup}), and the coffee is being prepared; eventually, \(p_2\) time units after the machine left the idle mode, the coffee (action \textit{coffee}) is delivered. Then, after 10 time units, the machine returns to the idle mode—unless a user again requests a coffee by pressing the button.

### Concrete Semantics

**Definition 9 (Concrete semantics of a TA).** Given a PTA \(A = (\Sigma, L, l_0, F, X, P, I, E)\), and a parameter valuation \(v\), the concrete semantics of \(v(A)\) is given by the timed transition system \((S, s_0, \to)\), with

- \(S = \{ (l, w) \in L \times \mathbb{R}_+^I \mid w|v = I(l) \}\),
- \(s_0 = (l_0, 0)\), and
- \(\to\) consists of the discrete and (continuous) delay transition relations:
  - discrete transitions: \((l, w) \xrightarrow{e} (l', w')\), if \((l, w), (l', w') \in S\), there exists \(e = (l, g, \sigma, R, l') \in E\), \(w' = [w]_R\), and \(w|v = g\).
  - delay transitions: \((l, w) \xrightarrow{d} (l, w + d)\), with \(d \in \mathbb{R}_+\), if \(\forall d' \in [0, d], (l, w + d') \in S\).

Moreover we write \((l, w) \xrightarrow{\delta} (l', w')\) for a combination of a delay and discrete transition where \(((l, w), e, (l', w')) \in \to\) if \(\exists d, w'' : (l, w) \xrightarrow{d} (l, w'') \xrightarrow{\delta} (l', w')\).

Given a TA \(v(A)\) with concrete semantics \((S, s_0, \to)\), we refer to the states of \(S\) as the \textit{concrete states} of \(v(A)\). A (concrete) \textit{run} of \(v(A)\) is a possibly infinite alternating sequence of concrete states of \(v(A)\) and edges starting from the initial concrete state \(s_0\) of the form \(s_0 \xrightarrow{e_0} s_1 \xrightarrow{e_1} \cdots \xrightarrow{e_{m-1}} s_m \xrightarrow{e_m} \cdots\), such that for all \(i = 0, 1, \ldots, e_i \in E\), and \((s_i, e_i, s_{i+1}) \in \to\). Given a state \(s = (l, w)\), we say that \(s\) is reachable (or that \(v(A)\) reaches \(s\)) if \(s\) belongs to a run of \(v(A)\). By extension, we say that \(l\) is reachable in \(v(A)\), if there exists a state \((l, w)\) that is reachable. By extension, given a set of locations \(T \subseteq L\) (\(T\) stands for “target”), we say that \(T\) is reachable in \(v(A)\), if there exists a location \(l \in T\) that is reachable in \(v(A)\). Given a set of locations \(T \subseteq L\), we say that a run \textit{stays} in \(T\) if all of its states \((l, w)\) are such that \(l \in T\).

A \textit{maximal run} is a run that is either infinite (\textit{i.e.} contains an infinite number of discrete transitions), or that cannot be extended by a discrete transition. A maximal run is \textit{deadlocked} if it is finite, \textit{i.e.} contains a finite number of discrete transitions. By extension, we say that a TA is deadlocked if it contains at least one deadlocked run.

**Example 2.** Consider again the PTA modeling a coffee machine in Fig. 1. Let \(v\) be the parameter valuation such that \(v(p_1) = 1\), \(v(p_2) = 5\) and \(v(p_3) = 8\).
Given a clock valuation \( w \), we denote it by \((w(x_1), w(x_2))\). For example, \((0, 4.2)\) denotes that \(w(x_1) = 0\) and \(w(x_2) = 4.2\).

The following sequence is a concrete run of \(v(A)\).

\[
(\text{idle}, (0, 0)) \xrightarrow{\text{press}} (\text{add sugar}, (0, 0)) \xrightarrow{\text{press}} (\text{add sugar}, (0, 1.78)) \xrightarrow{\text{press}} (\text{add sugar}, (0, 4.2)) \xrightarrow{\text{cup}} (\text{preparing coffee}, (0, 8, 5)) \xrightarrow{\text{coffee}} (\text{done}, (0, 8)) \xrightarrow{\text{press}} (\text{add sugar}, (0, 0))
\]

As an abuse of notation, we write above each arrow the action name (instead of the edge), as edges are unnamed in Fig. 1.

This concrete run is not maximal (it could be extended).

**Language of timed automata** Let \((l_0, w_0) \xrightarrow{e_0} (l_1, w_1) \xrightarrow{e_1} \cdots \xrightarrow{e_{m-1}} (l_m, w_m) \xrightarrow{e_m} \cdots\) be a (finite or infinite) run of a TA \(v(A)\). The associated untimed word is \(\sigma_0 \sigma_1 \cdots \sigma_m \cdots\), where \(\sigma_i\) is the action of edge \(e_i\), for all \(i \geq 0\); the associated trace is \(l_0 \sigma_0 l_1 \sigma_1 l_2 \cdots \sigma_m l_{m+1} \cdots\).

Given a run \((l_0, w_0) \xrightarrow{e_0} (l_1, w_1) \xrightarrow{e_1} \cdots \xrightarrow{e_{m-1}} (l_m, w_m)\), we say that this run is accepting if \(l_m \in F\).

We define the untimed language as the set of all untimed words associated with accepting runs of a TA.

**Definition 10 (untimed language of a TA).** Given a PTA \(A = (\Sigma, L, l_0, F, X, P, I, E)\), and a parameter valuation \(v\), the untimed language of \(v(A)\), denoted by \(UL(v(A))\), is the set of untimed words associated with all accepting runs of \(v(A)\).

We define the trace set as the set of traces associated with the accepting runs.

**Definition 11 (trace set of a TA).** Given a PTA \(A = (\Sigma, L, l_0, F, X, P, I, E)\), and a parameter valuation \(v\), the trace set of \(v(A)\) is the set of traces associated with all accepting runs of \(v(A)\).

**Example 3.** Consider again the PTA \(A\) modeling a coffee machine in Fig. 1.

Let \(v\) be the parameter valuation such that \(v(p_1) = 1\), \(v(p_2) = 5\) and \(v(p_3) = 8\).

The untimed language of \(v(A)\) can be described as follows:

\[
\text{press}^{[1-6]} \text{ cup coffee (idle? press}^{[1-6]} \text{ cup coffee)*}
\]

where \(\sigma^{[a, b]}\), \(\sigma^?\), \(\sigma^*\) denote between \(a\) and \(b\) occurrences, zero or one occurrence, and zero or more occurrence(s) of \(\sigma\), respectively.

The trace set of \(v(A)\) can be described as follows:

\[
\text{idle (press add sugar)}^{[1-6]} \text{ cup preparing coffee coffee done}
\]

\[\left((\text{idle idle})^? \ (\text{press add sugar})^{[1-6]} \text{ cup preparing coffee coffee done})^*\]

\[\text{This is a non-standard definition of traces (compared to e.g. [Gla90]), but we keep this term as it is used in e.g. [And+09; AM15].}\]
Symbolic semantics Let us now recall the symbolic semantics of PTAs (see e.g. [Hun+02; And+09; JLR15]).

Definition 12 (Symbolic state). A symbolic state is a pair \((l, C)\) where \(l \in L\) is a location, and \(C\) its associated parametric zone.

Definition 13 (Symbolic semantics). Given a PTA \(A = (\Sigma, L, l_0, F, X, P, I, E)\), the symbolic semantics of \(A\) is the labelled transition system called the parametric zone graph \(PZG = (E, S, s_0, \Rightarrow)\), with

\[
\begin{align*}
S &= \{(l, C) \mid C \subseteq I(l)\}, \\
s_0 &= (l_0, (\lambda 1 \leq i \leq H x_i = 0)^x \wedge I(l_0)), \text{ and} \\
\{(l, C), e, (l', C')\} &\in \Rightarrow \text{ if } e = (l, g, \sigma, R, l', l) \text{ and} \\
C' &= ([(C \wedge g)]_R \wedge I(l'))^x \wedge I(l')
\end{align*}
\]

with \(C'\) satisfiable.

That is, in the parametric zone graph, nodes are symbolic states, and arcs are labelled by edges of the original PTA.

If \(\{(l, C), e, (l', C')\} \in \Rightarrow\), we write \(\text{Succ}(s, e) = (l', C')\).

A graphical illustration of the computation of \(\text{Succ}\) is given in Fig. 2. Starting from the parametric zone \(C\), it is intersected with guard \(g\), leading to the parametric values that allow for taking the transition. Then the necessary clocks are reset. However, for the transition to be taken, the new values thus obtained must satisfy the invariant of the target location, \(I(l')\). After this, when in \(l'\), time can elapse as long as the invariant still holds, leading to the new zone \(C'\).

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Fig. 2: Computing the successor of a symbolic state

A symbolic run of a PTA is an alternating sequence of symbolic states and edges starting from the initial symbolic state, of the form \(s_0 \xRightarrow{e_0} s_1 \xRightarrow{e_1} \cdots \xRightarrow{e_{m-1}} s_m\),

\[\text{This figure comes from [AS13], itself coming from an adaptation of a figure by Ulrich Kühne.}\]
such that for all \( i = 0, \ldots, m - 1, e_i \in E, s_i, s_{i+1} \in S \) and \((s_i, e, s_{i+1}) \in \Rightarrow\). Given a symbolic state \( s \), we say that \( s \) is reachable if \( s \) belongs to a symbolic run of \( A \). In the following, we simply refer to symbolic states belonging to a run of \( A \) as symbolic states of \( A \).

**Example 4.** Consider again the coffee machine example in Fig. 1. A (non-maximal) symbolic run is as follows:

\[(\text{idle}, x_1 = x_2 \land x_1 \geq 0) \Rightarrow (\text{add sugar}, x_1 = x_2 \land 0 \leq x_2 \leq p_2)\]

\[\Rightarrow (\text{add sugar}, p_1 \leq x_2 - x_1 \leq p_2 \land 0 \leq x_2 \leq p_2)\]

\[\Rightarrow (\text{add sugar, } 2 \times p_1 \leq x_2 - x_1 \leq p_2 \land 0 \leq x_2 \leq p_2)\]

\[\Rightarrow (\text{prepare coffee, } 2 \times p_1 \leq x_2 - x_1 \leq p_2 \land p_2 \leq x_2 \leq p_3)\]

\[\Rightarrow (\text{cup, } 0 \leq x_1 \leq 10 \land x_2 - x_1 = p_3 \land 2 \times p_1 \leq p_2 \leq p_3)\]

\[\Rightarrow (\text{coffee, } 2 \times p_1 \leq x_2 - x_1 \leq p_2 \land 2 \times p_1 \leq p_2 \leq p_3)\]

(For sake of readability, we use action names instead of edges along the transitions.)

The parametric zone graph of this example is infinite.

Given a concrete (respectively symbolic) run \((l_0, 0) \Rightarrow (l_1, w_1) \Rightarrow \cdots \Rightarrow (l_m, w_m)\) (respectively \((l_0, C_0) \Rightarrow (l_1, C_1) \Rightarrow \cdots \Rightarrow (l_m, C_m)\)), we define the corresponding discrete sequence as \(l_0 \Rightarrow l_1 \Rightarrow \cdots \Rightarrow l_m\). Two runs (concrete or symbolic) are said to be **equivalent** if their associated discrete sequences are equal.

Two important results (see e.g. [Hun+02]) relate the concrete and the symbolic semantics, and are recalled below using our syntax. They provide a sort of equivalence between symbolic parametric zones and concrete runs. That is, they guarantee that the zones correspond to feasible runs (correctness) and that each run is represented by a zone (completeness).

**Lemma 1** ([Hun+02, Proposition 3.17]). For each parameter valuation \( v \) and clock valuation \( w \), if there is a symbolic run in \( A \) reaching state \((l, C)\), with \( w \models v(C) \), then there is an equivalent concrete run in \( v(A) \) reaching state \((l, w)\).

**Lemma 2** ([Hun+02, Proposition 3.18]). For each parameter valuation \( v \) and clock valuation \( w \), if there is a concrete run in \( v(A) \) reaching state \((l, w)\), then there is an equivalent symbolic run in \( A \) reaching a state \((l, C)\) such that \( w \models v(C) \).

### 2.3 Subclasses of PTAs

Lower-bound/upper-bound parametric timed automata (L/U-PTAs), proposed in [Hun+02], restrict the use of parameters in the model.

**Definition 14 (L/U-PTA).** An L/U-PTA is a PTA where the set of parameters is partitioned into lower-bound parameters and upper-bound parameters, where an upper-bound (resp. lower-bound) parameter \( p_i \) is such that, for every
guard or invariant constraint $x \triangleright \sum_{1 \leq j \leq M} \beta_j p_j + d$, we have: $\beta_j > 0$ implies $\triangleright \in \{\leq, <\}$ (resp. $\triangleright \in \{\geq, >\}$), and $\beta_j < 0$ implies $\triangleright \in \{\geq, >\}$ (resp. $\triangleright \in \{\leq, <\}$).

In [BL09], two additional subclasses are introduced: L-PTAs (resp. U-PTAs) are PTAs with only lower-bound (resp. upper-bound) parameters.

L/U-PTAs enjoy a well-known monotonicity property [Hun+02]: increasing upper-bound parameters or decreasing lower-bound parameters can only add behaviours.

**Example 5.** Consider again the coffee machine in Fig. 1, modelled using a PTA $A$. This PTA is not an L/U-PTA; indeed, in the guard $x_2 = p_2$ (resp. $x_2 = p_3$), $p_2$ (resp. $p_3$) is compared with clocks both as a lower-bound and as an upper-bound. (Recall that $=$ stands for $\leq$ and $\geq$.)

However, if one replaces $x_2 = p_2$ with $x_2 \leq p_2$ and $x_2 = p_3$ with $x_2 \leq p_3$, then $A$ becomes an L/U-PTA with lower-bound parameter $p_1$ and upper-bound parameters $\{p_2, p_3\}$. Note that equalities are not forbidden in L/U-PTAs (e.g. $x_1 = 10$), but only equalities involving parameters are.

Several case studies fit into the class of L/U-PTAs: the root contention protocol, the bounded retransmission protocol and the Fischer mutual exclusion protocol are all modelled with L/U-PTAs in [Hun+02]; in [Hun+02; KP12], both the Fischer mutual exclusion protocol and a producer-consumer are verified using L/U-PTAs. Interestingly, the two case studies of the seminal paper on PTAs [AHV93] (viz. a toy train gate controller model and a model of Fischer mutual exclusion protocol) are also L/U-PTAs, although the concept of L/U-PTAs had not yet been proposed at that time. In addition, most models of asynchronous circuits with bi-bounded delays (i.e. where each delay between the change of an input signal and the change of the corresponding output is a parametric interval) can be modelled using L/U-PTAs.

We will also consider bounded PTAs, i.e. PTAs with a bounded parameter domain that assigns to each parameter an infimum and a supremum, both integers.

**Definition 15 (bounded PTA).** A bounded PTA is $A_{|\text{bounds}}$, where $A$ is a PTA, and bounds assigns to each parameter $p$ an interval $[\inf, \sup]$, $(\inf, \sup)$, $[\inf, \sup)$, or $(\inf, \sup)$, with $\inf, \sup \in \mathbb{N}$. We use $\inf(p, \text{bounds})$ and $\sup(p, \text{bounds})$ to denote the infimum and the supremum of $p$, respectively. (Note that we rule out $\infty$ as a supremum.)

We say that a bounded PTA is a closed bounded PTA if, for each parameter $p$, its ranging interval $\text{bounds}(p)$ is of the form $[\inf, \sup]$; otherwise it is an open bounded PTA.

We define similarly bounded L/U-PTAs.

### 2.4 Decision and computation problems
**TCTL** TCTL [ACD03] is the quantitative extension of CTL where temporal modalities are augmented with constraints on duration. Formulae are interpreted over timed transition systems.

Given \( ap \in AP \) and \( c \in \mathbb{N} \), the language of TCTL is given by the following grammar:

\[
\varphi ::= \top | ap | \neg \varphi | \varphi \land \varphi | E\varphi U_{\bowtie \triangleright c} \varphi | A\varphi U_{\bowtie \triangleright c} \varphi
\]

A reads “always”, E reads “exists”, and U reads “until”.

Standard abbreviations include Boolean operators as well as \( EF_{p \bowtie \triangleright c} \varphi \) for \( E\top U_{\bowtie \triangleright c} \varphi \), \( AF_{p \bowtie \triangleright c} \varphi \) for \( A\top U_{\bowtie \triangleright c} \varphi \) and \( EG_{p \bowtie \triangleright c} \varphi \) for \( \neg AF_{p \bowtie \triangleright c} \neg \varphi \). F reads “eventually” while G reads “globally”.

**Definition 16 (Semantics of TCTL).** Given a TA \( v(A) \), the following clauses define when a state \( s_i \) of its timed transition system \( (S, s_0, \rightarrow) \) satisfies a TCTL formula \( \varphi \), denoted by \( s_i \vDash \varphi \), by induction over the structure of \( \varphi \) (semantics of Boolean operators is omitted):

1. \( s_i \vDash E\varphi U_{\bowtie \triangleright c} \psi \) if there is a maximal run \( \rho \) in \( v(A) \) with \( \sigma = s_i \overset{e_0}{\rightarrow} \cdots \overset{e_{i-1}}{\rightarrow} s_j \) \((i < j)\) a prefix of \( \rho \) s.t. \( s_j \vDash \psi \), time(\( \sigma \)) \( \bowtie \triangleright c \), and \( \forall k, i \leq k < j : s_k \vDash \varphi \)
2. \( s_i \vDash A\varphi U_{\bowtie \triangleright c} \psi \) if for each maximal run \( \rho \) in \( v(A) \) there exists \( \sigma = s_i \overset{e_0}{\rightarrow} \cdots \overset{e_{i-1}}{\rightarrow} s_j \) \((i < j)\) a prefix of \( \rho \) s.t. \( s_j \vDash \psi \), time(\( \sigma \)) \( \bowtie \triangleright c \), and \( \forall k, i \leq k < j : s_k \vDash \varphi \).

where, given a concrete run \( \rho \), time(\( \rho \)) gives the total sum of the delays \( d \) along \( \rho \).

In \( E\varphi U_{\bowtie \triangleright c} \psi \) the classical until is extended by requiring that \( \varphi \) be satisfied within a duration (from the current state) verifying the constraint “\( \bowtie \triangleright c \)”.

Given \( v \), a PTA \( A \) and a TCTL formula \( \varphi \), we write \( v(A) \vDash \varphi \) when \( s_0 \vDash \varphi \).

**Decision problems**

*Emptiness and universality of the valuations set.* Let \( \mathcal{P} \) be a given a class of decision problems (reachability, unavoidability, etc.).

| \( \mathcal{P} \)-emptiness problem: |
|---|
| **INPUT:** A PTA \( A \) and an instance \( \phi \) of \( \mathcal{P} \) |
| **PROBLEM:** Is the set of parameter valuations \( v \) such that \( v(A) \) satisfies \( \phi \) empty? |

| \( \mathcal{P} \)-universality problem: |
|---|
| **INPUT:** A PTA \( A \) and an instance \( \phi \) of \( \mathcal{P} \) |
| **PROBLEM:** For all parameter valuations \( v \), does \( v(A) \) satisfy \( \phi \)? |

In this section, we mainly focus on the following decision problems:

- reachability (EF\(^7\)): given a TA \( v(A) \), is there at least one run of \( v(A) \) that reaches a given location? That is, EF-emptiness asks: “is the set of parameter valuations \( v \) such that \( v(A) \) satisfies \( \phi \) empty?”

\(^7\) The names “EF”, “AF”, “EG” come from the TCTL syntax, and are consistent with the notations introduced in [JLR15] and subsequently used in further papers (such as [ALR16; AL17]).
valuations $v$ such that the TA $v(A)$ reaches a given location empty?” And EF-universality asks: “are all parameter valuations such that the corresponding TA reaches a given location?”

- unavoidability (AF): given a TA $v(A)$, do all runs of $v(A)$ eventually reach a given location?
- EG: given a TA $v(A)$ and a subset $T$ of its locations, is there at least one maximal run of $v(A)$ that always stays in $T$?
- AG: given a TA $v(A)$ and a subset $T$ of its locations, do all runs of $v(A)$ stay in $T$?
- deadlock-existence (ED): given a TA $v(A)$, is there at least one maximal run of $v(A)$ that is deadlocked, i.e. has no discrete successor (possibly after some delay)?
- cycle-existence (EC): given a TA $v(A)$, is there at least one run of $v(A)$ with an infinite number of discrete transitions?

Note that AF-emptiness is equivalent to EG-universality, while AG-emptiness is equivalent to EF-universality.

We will finally consider the following two additional emptiness problems:

**Language-preservation-emptiness problem:**
**Input:** A PTA $A$ and a parameter valuation $v'$
**Problem:** Is the set of parameter valuations $v$ such that $v \neq v'$ and for which $v(A)$ has the same untimed language as $v'(A)$ empty?

**Trace-preservation-emptiness problem:**
**Input:** A PTA $A$ and a parameter valuation $v'$
**Problem:** Is the set of parameter valuations $v$ such that $v \neq v'$ and for which $v(A)$ has the same set of traces as $v'(A)$ empty?

**Computation problem** Additionally, we define the following computation problem:

**$P$-synthesis problem:**
**Input:** A PTA $A$ and an instance $\phi$ of $P$
**Problem:** Compute the parameter valuations such that $v(A)$ satisfies $\phi$.

*Example 6.* Let us exemplify some decision and computation problems for the coffee machine PTA in Fig. 1. Assume the unique target location is done, i.e. $T = \{\text{done}\}$.

EF-emptiness asks whether the set of parameter valuations that can reach location done for some run is empty, i.e. there is an execution in which the coffee is not delivered. This is false (e.g. $p_1 = 1$, $p_2 = 2$, $p_3 = 3$ can reach done).

EF-universality asks whether all parameter valuations can reach location done for some run, i.e. all executions allow for delivering the coffee, regardless of the parameters valuation. This is false (no parameter valuation such that $p_2 > p_3$ can reach done).

AF-emptiness asks whether the set of parameter valuations that can reach location done for all runs is empty, i.e. if there is some parameters valuation for
which coffee delivery is not guaranteed. This is false (e.g. \( p_1 = 1, p_2 = 2, p_3 = 3 \) cannot avoid done).

EF-synthesis consists in synthesizing all valuations for which a run reaches location done, i.e. identifies all parameters valuations for which a coffee will eventually be delivered. The resulting set of valuations is \( 0 \leq p_2 \leq p_3 \leq 10 \land p_1 \geq 0 \).

### 2.5 Decidability

**The general class of PTAs** With the rule of thumb that all problems are undecidable for PTAs, we review the decidability of the aforementioned problems.

- EF-emptiness was shown to be undecidable [AHV93], with different flavours and settings: for a single bounded parameter [Mil00], for a single rational-valued or integer-valued parameter [Ben+15], with only one clock compared to parameters [Mil00], or with strict constraints only [Doy07].
- AF-emptiness was shown undecidable in [JLR15].
- AG-emptiness was shown undecidable in [ALR16].
- EG-emptiness (as well as EC and ED) were shown undecidable in [AL17].
- The language and trace-preservation problems were shown undecidable in [AM15].

A complete survey is available in [And19b].

Following the very negative results for PTAs, subclasses have been proposed. We review some in the following.

**The class of L/U-PTAs**

**A main decidability result.** The first (and main) positive result for L/U-PTAs is the decidability of the EF-emptiness problem [Hun+02]. L/U-PTAs benefit from the following interesting monotonicity property: increasing the value of an upper-bound parameter or decreasing the value of a lower-bound parameter necessarily relaxes the guards and invariants, and hence can only add behaviours. Therefore, checking the EF-emptiness of an L/U-PTA can be achieved by replacing all lower-bound parameters with 0, and all upper-bound parameters with a sufficiently large constant; this yields a non-parametric TA, for which emptiness is PSPACE-complete [AD94]. This procedure is not only sound but also complete.

**Undecidability results.** The first undecidability results for L/U-PTAs are shown in [BL09]: the constrained EF-emptiness problem and constrained EF-universality problem (for infinite runs acceptance properties) are undecidable for L/U-PTAs. By constrained it is meant that some parameters of the L/U-PTA can be constrained by an initial linear constraint, e.g. \( p_1 \leq 2 \times p_2 + p_3 \). Indeed, using linear constraints, one can constrain an upper-bound parameter to be equal to
a lower-bound parameter, and hence build a 2-counter machine using an L/U-PTA. However, when no upper-bound parameter is compared to a lower-bound parameter (i.e. when no initial linear inequality contains both an upper-bound and a lower-bound parameter), these two problems become decidable [BL09].

The exact decidability frontier may have not been found yet: the case where a lower-bound parameter is constrained to be less than or equal to an upper-bound parameter fits in none of the considered cases.

A second negative result is shown in [JLR15]: the AF-emptiness problem is undecidable for L/U-PTAs. This restricts again the use of L/U-PTAs, as AF is essential to show that all possible runs of a system eventually reach a (good) state.

Third, in [AM15], the language- and trace-preservation problems were shown to be undecidable for L/U-PTAs.

**Model-checking L/U-PTAs.** In [BL09], a parametric extension of the dense-time linear temporal logic MITL$_{0,\infty}$ (denoted “PMITL$_{0,\infty}$”) is proposed; when parameters are used only as lower or upper bound in the formula (to which we refer as L/U-PMITL$_{0,\infty}$), satisfiability and model checking are PSPACE-complete; this is obtained by translating the formula into an L/U-automaton and checking an infinite acceptance property.

Then, in [DLN15], an extension of MITL allowing parametric linear expressions in bounds is proposed (yielding PMITL). Two sets of (integer-valued) parameter valuations are considered:

1. the set of valuations for which a PMITL formula is satisfiable, i.e. for which there exists a timed sequence (possibly belonging to a given L/U-PTA) satisfying it, and
2. the set of valuations for which a PMITL formula is valid, i.e. for which all timed sequences (possibly belonging to a given L/U-PTA) satisfy it.

Under some assumptions, the emptiness and universality of the valuation set for which a PMITL property is satisfiable or valid (possibly w.r.t. a given L/U-PTA) are decidable, and EXPSPACE-complete. Essential assumptions for decidability include the fact that parameters should be used with the same polarity (positive or negative coefficient, as lower or upper bound in the intervals) within the entire PMITL formula, and each interval can only use parameters in one of the endpoints. Additional assumptions include that no interval of the PMITL formula should be punctual (nor empty), and linear parametric expressions are only used in right endpoints of the intervals (single parameters can still be used as left endpoints). In addition, two fragments of PMITL are showed to be in PSPACE, including one that allows for expressing parameterized response (“if an event occurs, then another event shall occur within some possibly parametric time interval”).

Finally, we showed that the emptiness-problem using nested quantifiers (i.e. beyond EF, EG, AF, AG) automatically leads to the undecidability, even for the very restricted class of U-PTAs with a single parameter (that can even be integer-valued) [ALR18]. In other words, the nested TCTL emptiness problem
is undecidable for U-PTAs. We may wonder if the timed aspect of TCTL (and notably the urgency required by the TCTL formula $\text{EG}\neg\text{AG}x_0$) is responsible for the undecidability. In fact, it is not, and we could modify the proof to show that CTL itself leads to undecidability, i.e. that $\text{EGAX}$-emptiness is undecidable.

**Intractability of the synthesis.** A very disappointing result concerning L/U-PTAs is shown in [JLR15]: despite decidability of the underlying decision problems ($\text{EF}$-emptiness and $\text{EF}$-universality), the solution to the $\text{EF}$-synthesis problem for L/U-PTAs, if it can be computed, cannot be represented using a formalism for which the emptiness of the intersection with equality constraints is decidable. The proof relies on the undecidability of the constrained emptiness problem of [BL09]. A very annoying consequence is that such a solution cannot be represented as a finite union of polyhedra (since the emptiness of the intersection with equality constraints is decidable).

**Liveness.** The $\text{EG}$-emptiness problem stands at the frontier between decidability and undecidability for the class of L/U-PTAs: while this problem is decidable for L/U-PTAs with a bounded parameter domain with closed bounds, it becomes undecidable if either the assumption of boundedness or of closed bounds is lifted [AL17].

The deadlock-existence emptiness problem is undecidable, even for the restricted class of closed bounded L/U-PTAs [AL17].

In contrast to deadlock-freeness that is consistently undecidable, and to $\text{EG}$-emptiness for which the frontier between decidability and undecidability is thin, the existence of a parameter valuation for which there exists at least one infinite run ($\text{EC}$-emptiness) is consistently decidable for L/U-PTAs [AL17].

**The power of integer points** Following works related integer clock and parameter valuations with decidability in [JLR15], we introduced in [ALR16] integer-points parametric timed automata (IP-PTAs for short), i.e a subclass of PTAs in which any symbolic state contains at least one integer point.

**Definition 17.** A PTA $\mathcal{A}$ is an integer points PTA (in short IP-PTA) if, in any reachable symbolic state $(l, C)$ of $\mathcal{A}$, $C$ contains at least one integer point, i.e. $\exists v : P \to \mathbb{N}, \exists w : X \to \mathbb{N}$ s.t. $w|v = C$.

**Example 7.** Consider the PTA in Fig. 3a, containing two clocks $x_1$ and $x_2$, and one parameter $p$. This PTA is not an IP-PTA. Indeed, as can be seen on its parametric zone graph, the (unique) symbolic state with location $l_3$ contains only $p = \frac{1}{2}$, and this symbolic state therefore contains no integer point.

In contrast, the PTA in Fig. 3b is an IP-PTA: each zone in its parametric zone graph contains an integer valuation of all parameters. The coffee machine in Fig. 1 (which has an infinite parametric zone graph) is also an IP-PTA.
In [ALR16], we studied the expressiveness of IP-PTAs: while the class of IP-PTAs is incomparable with the class of L/U-PTAs, any non-strict L/U-PTA, i.e. with only non-strict inequalities, is an IP-PTA.

Concerning decidability, the only non-trivial general class with a decidability result for EF-emptiness is L/U-PTAs [Hun+02]. We extended this class, by proving that EF-emptiness is decidable for bounded IP-PTAs [ALR16]. However, other studied problems turned out to be undecidable.

Summary Table 1 summarises the decidability results. It gives from left to right the (un)decidability for U-PTAs, bounded L/U-PTAs (with either closed
or open bounds), L/U-PTAs, bounded IP-PTAs, IP-PTAs, bounded PTAs, and PTAs. We review the emptiness of TCTL subformulas ($\text{EF}$, $\text{AF}$, $\text{EG}$, $\text{AG}$), full TCTL, cycle-existence, deadlock-existence and language- and trace-preservation. Decidability is given in green, whereas undecidability is given in italic red. Our contributions are emphasized in bold using a plain background, whereas existing results are depicted using a light background. When several papers in the literature proved the same result, we only give the earliest result, and not necessarily the best (in terms of number of clocks and parameters, or complexity).

**Perspective: open subclasses** L-PTAs and U-PTAs [BL09] are very open classes, in the sense that the only known decidability results come from the larger class of PTAs, and no undecidability result was known—with the exception of our recent result concerning TCTL-emptiness [ALR18]. To summarize, the $\text{EG}$-emptiness, $\text{AG}$-emptiness and $\text{AF}$-emptiness problems, as well as the language- and trace-preservation problems, are all undecidable for (general) L/U-PTAs, but remain open for L-PTAs and U-PTAs. Similarly, the $\text{EF}$-synthesis problem (shown intractable for L/U-PTAs in [JLR15] despite the decidability of the $\text{EF}$-emptiness problem) remains open for rational-valued L- and U-PTAs, and would significantly increase the interest of these subclasses if it was shown to be computable.

### 3 Parametric Interval Markov Chains

Parametric probabilities are useful to capture imprecisions, robustness and dimensioning issues. Hence, in this section we consider Parametric Interval Markov Chains (PIMC).

#### 3.1 Introduction to Parametric Markov Chains

![Fig. 4: Markov Chains and their extensions](image)

(a) Markov Chain (MC)  
(b) Interval MC (IMC)  
(c) Parametric IMC (PIMC)

Fig. 4 contains an example of the different flavours of Markov Chains we are addressing in this section. Fig. 4a is a Markov chain (MC). As in an automa-
ton, there are states and transitions between them, but these are labelled by probabilities for the transition to occur. For example, from state $s_0$, there is a probability of 0.7 to go to state $s_1$ and of 0.3 to go to state $s_2$. Therefore, the sum of all probabilities labelling transitions exiting a state must be 1.

When probabilities are not known in advance, it might still be possible to know an interval to which they belong. Hence, we introduce Interval Markov Chains (IMC), as pictured in Fig. 4b. The transitions are then no more labelled by a fixed probability but an interval meaning that the probability should be between the lower and the upper bound of the interval. For example, the probability to move from state $s_1$ to state $s_3$ is between 0.3 and 0.5. The MC in Fig. 4a can be seen as an implementation of the IMC in Fig. 4b which stands as a specification. Notice that state $s_4$ does not appear in Fig. 4a, which is equivalent to having a transition from state $s_2$ to state $s_4$ with a probability 0. Once a probability is chosen in an interval, it imposes constraints on the other probabilities outgoing the same state since they must add up to 1. An IMC is said to be consistent if it admits at least one implementation.

When the upper or lower bounds are unknown, it is convenient to use parameters. Fig. 4c shows a Parametric Interval Markov Chain (PIMC). As compared with the IMC of Fig. 4b, some of the bounds are replaced with parameters $p$ and $q$. Notice that the same parameter occurs at several places in the PIMC, therefore imposing constraints on the interval. For example, from state $s_1$, it is possible to stay in this state with a probability between $q$ and 1, or to move to state $s_3$ with a probability between 0.3 and the same $q$. When assigning a valuation to all parameters in a PIMC, we obtain an IMC.

3.2 Markov Chains definitions

We now formally define the different Markov Chain models. These definitions are detailed in [DLP16].

**Definition 18 (Markov Chain).** A Markov Chain is a tuple $\mathcal{M} = (S, s_0, M, A, V)$, where $S$ is a finite set of states containing the initial state $s_0$, $A$ is a set of atomic propositions, $V : S \rightarrow 2^A$ is a labelling function, and $M : S \times S \rightarrow [0, 1]$ is a probabilistic transition function such that $\forall s \in S, \sum_{t \in S} M(s, t) = 1$.

We now introduce the notation of parameters, and interval ranges that will be used throughout this section. A parameter $p \in P$ is a variable ranging through the interval $[0, 1]$. A valuation for $P$ is a function $\psi : P \rightarrow [0, 1]$ that associates values with each parameter in $P$. We write $\text{Int}_{[0,1]}(P)$ for the set of all closed parametric intervals of the form $[x, y]$ with $x, y \in [0, 1] \cup P$. When $P = \emptyset$, we write $\text{Int}_{[0,1]} = \text{Int}_{[0,1]}(\emptyset)$ to denote closed intervals with real-valued endpoints. Given an interval $I$ of the form $I = [a, b]$, $\text{Low}(I)$ and $\text{Up}(I)$ respectively denote the lower and upper endpoints of $I$, i.e. $a$ and $b$. Given an interval $I = [a, b] \in \text{Int}_{[0,1]}$, we say that $I$ is well-formed whenever $a \leq b$.

The definition of Interval Markov Chains is adapted from [Del+12].
Definition 19 (Interval Markov Chain [Del+12]). An Interval Markov Chain is a tuple $I = (S, s_0, \phi, A, V)$, where $S$, $s_0$, $A$ and $V$ are as for MCs, and $\phi : S \times S \to \text{Int}_{0, 1}$ is a transition constraint that associates with each potential transition an interval of probabilities.

The following definition recalls the notion of satisfaction introduced in [Del+12]. Satisfaction (also called implementation in some cases) allows to characterise the set of MCs represented by a given IMC specification. Satisfaction abstracts from the syntactic structure of transitions in IMCs: a single transition in the implementation MC can contribute to satisfaction of more than one transition in the specification IMC, by distributing its probability mass against several transitions. Similarly many MC transitions can contribute to the satisfaction of just one specification transition. This crucial notion is embedded in the so-called correspondence function $\delta$ introduced below. Informally, such a function is given for all pairs of states $(t, s)$ in the satisfaction relation, and associates with each successor state $t'$ of $t$ – in the implementation MC – a distribution over potential successor states $s'$ of $s$ – in the specification IMC – specifying how the transition $t \rightarrow t'$ contributes to the transition $s \rightarrow s'$.

Definition 20 (Satisfaction Relation [Del+12]). Let $I = (S, s_0, \phi, A, V^I)$ be an IMC and $M = (T, t_0, M, A, V^M)$ be a MC. A relation $R \subseteq T \times S$ is a satisfaction relation if whenever $tR_s$,

1. the labels of $s$ and $t$ agree: $V^M(t) = V^I(s)$,
2. there exists a correspondence function $\delta : T \rightarrow (S \rightarrow [0, 1])$ such that
   (a) for all $t' \in T$ such that $M(t, t') > 0$, $\delta(t')$ is a distribution on $S$,
   (b) for all $s' \in S$, we have $\left(\sum_{t' \in T} M(t, t') \cdot \delta(t')(s')\right) \in \phi(s, s')$, and
   (c) for all $t' \in T$ and $s' \in S$, if $\delta(t')(s') > 0$, then $(t', s') \in R$.

We say that state $t \in T$ satisfies state $s \in S$ (written $t \models s$) iff there exists a (minimal) satisfaction relation containing $(t, s)$ and that $M$ satisfies $I$ (written $M \models I$) iff $t_0 \models s_0$.

The set of MCs satisfying a given IMC $I$ is written $[I]$. Formally, $[I] = \{M \mid M \models I\}$.

Definition 21. An IMC $I$ is consistent iff $[I] \neq \emptyset$.

Although the satisfaction relation abstracts from the syntactic structure of transitions, we recall the following result from [Del15], that states that whenever a given IMC is consistent, it admits at least one implementation that strictly respects its structure.

Theorem 1 ([Del15]). An IMC $I = (S, s_0, \phi, A, V)$ is consistent iff it admits an implementation of the form $M = (S, s_0, M, A, V)$ where, for all reachable states $s$ in $M$, it holds that $M(s, s') \in \phi(s, s')$ for all $s'$.

In the following, we say that state $s$ is consistent in the IMC $I = (S, s_0, \phi, A, V)$ if there exists an implementation $M = (S, s_0, M, A, V)$ of $I$ in which state $s$ is reachable with a non-zero probability.

We now recall to the notion of Parametric Interval Markov Chain, previously introduced in [Del15].
Definition 22 (Parametric Interval Markov Chain). A Parametric Interval Markov Chain is a tuple $I^P = (S, s_0, \phi_P, A, V, P)$, where $S$, $s_0$, $A$ and $V$ are as for IMCs, $P$ is a set of variables (parameters) ranging over $[0, 1]$ and $\phi_P : S \times S \to \text{Int}_{[0,1]}(P)$ associates with each potential transition a (parametric) interval.

Given a pIMC $I^P = (S, s_0, \phi_P, A, V, P)$ and a parameter valuation $\psi : P \to [0, 1]$, we write $\psi(I^P)$ for the IMC obtained by replacing $\phi_P$ by the function $\phi : S \times S \to \text{Int}_{[0,1]}$ defined by $\forall s, s' \in S, \phi(s, s') = \psi(\phi_P(s, s'))$. The IMC $\psi(I^P)$ is called an instance of pIMC $I^P$.

Finally, we say that a MC $\mathcal{M} = (T, t_0, M, A, V^M)$ implements pIMC $I^P$, written $\mathcal{M} \models I^P$, iff there exists an instance $I$ of $I^P$ such that $\mathcal{M} \models I$. We write $[I^P]$ for the set of MCs implementing $I^P$ and say that a pIMC is consistent iff its set of implementations is not empty.

3.3 Consistency of PIMCs

When considering IMCs, one question of interest is to decide whether it is consistent without computing its set of implementations. This problem has been addressed in [Del+12; Del15], yielding polynomial decision algorithms and procedures that produce one implementation when the IMC is consistent. The same question holds for pIMCs, although in a slightly different setting. [Del15] proposed a polynomial algorithm for deciding whether a given pIMC is consistent, in the sense that it admits at least one parameter valuation for which the resulting IMC is consistent.

In order to decide whether a given IMC is consistent, we need to address the set of potential successors of a given state $s$. Let $\text{Succ}(s)$ be the set of states that can be reached from $s$ with a probability interval not reduced to $[0, 0]$: $\text{Succ}(s) = \{s' \in S \mid \phi_P(s, s') \neq [0, 0]\}$.

We now introduce the notion of $n$-consistency in the IMC setting and then adapt this notion to pIMCs. In practice, $n$-consistency is defined by induction over the structure of $I$.

Definition 23 ($n$-consistency). Let $\mathcal{I} = (S, s_0, \phi, A, V)$ be an IMC and let $D : S \to \text{Dist}(S)$ be a function that assigns a distribution on $S$ to each state of $\mathcal{I}$. State $s \in S$ is $(n, D)$-consistent iff for all $s' \in S$, $D(s)(s') \in \phi(s, s')$, and, for $n > 0$, $D(s)(s') > 0$ implies $s'$ is $(n-1, D)$-consistent.

We say that $s$ is $n$-consistent if there exists $D : S \to \text{Dist}(S)$ such that $s$ is $(n, D)$-consistent.

Definition 23 is thus equivalent to the following intuitive inductive definition: a state $s$ is $n$-consistent iff there exists a distribution $\rho$ satisfying all of its outgoing probability intervals and such that for all $s' \in S$, $\rho(s') > 0$ implies that $s'$ is $(n-1)$-consistent.

Theorem 2. Given an IMC $\mathcal{I} = (S, s_0, \phi, A, V)$, $\mathcal{I}$ is consistent iff $s_0$ is $|S|$-consistent.
Example 8. Let us consider the example in Fig. 4b. All states but $s_4$ are 0-consistent. Indeed it is possible to find probabilities within the exiting intervals that add up to 1. It is not the case for state $s_4$, which has a probability of 0.6 to which we should add one that is at least 0.5, so the sum is at least 1.1. Then, for state $s_2$ to be 1-consistent, it must not have $s_4$ as a successor. This is possible by choosing 0 as the probability to go from $s_2$ to $s_4$. In the remaining two intervals, choosing probability 0.5 leads to a sum of 1. Therefore, state $s_2$ is 1-consistent.

One can check that all states but $s_4$ are $n$-consistent, for all $n$.

For the problem of consistency of pIMCs, the aim is not only to decide whether a given pIMC is consistent, but also to synthesise all parameter valuations that ensure consistency of the resulting IMC. For this purpose, we adapt the notion of $n$-consistency defined above to pIMCs.

We first define the local consistency of a state w.r.t. some subset $S'$ of its successors: the sum of upper bounds should be greater than 1, the sum of lower bounds smaller than 1, and all successors in $S'$ have a valid interval.

$$LC(s, S') = \left[ \sum_{s' \in S'} Up(\phi_P(s, s')) \geq 1 \right] \cap \left[ \sum_{s' \in S'} Low(\phi_P(s, s')) \leq 1 \right] \cap \left[ \bigcap_{s' \in S'} Low(\phi_P(s, s')) \leq Up(\phi_P(s, s')) \right]$$

Let us start by fixing a set of states $X$ that we want to avoid and then compute the set of valuations $\text{Cons}_X^n(s)$ that ensure $n$-consistency of $s$ through a distribution $\rho$ that avoids states from $X$. Formally, $\text{Cons}_X^n(s)$ is defined as: let $\text{Cons}_X^0(s) = LC(s, \text{Succ}(s) \setminus X) \cap \bigcap_{s' \in X} Low(\phi_P(s, s')) = 0$ and for $n \geq 1$,

$$\text{Cons}_X^n(s) = \left[ \bigcap_{s' \in \text{Succ}(s) \setminus X} \text{Cons}_{n-1}(s') \right] \cap [LC(s, \text{Succ}(s) \setminus X)] \cap \left[ \bigcap_{s' \in X} Low(\phi_P(s, s')) \right] = 0$$

The set of valuations ensuring $n$-consistency is then the union, for all potential choices of $X$, of $\text{Cons}_X^n(s)$. We need to choose $X$ as a subset of the set $Z(s)$ of states which can be avoided, by transitions that have 0 or a parameter as lower bound. Therefore, we define $\text{Cons}_n(s) = \bigcup_{X \subseteq Z(s)} \text{Cons}_X^n(s)$.

**Theorem 3.** Given a pIMC $\mathcal{I}^P = (S, s_0, \phi_P, A, V, P)$ and a parameter valuation $\psi : P \to [0, 1]$, we have $\psi \in \text{Cons}_{[S]}(s_0)$ if the IMC $\psi(\mathcal{I}^P)$ is consistent.

Example 9. It is easily shown that the consistency of the PIMC in Fig. 4c is $[(q \leq 0.7) \cap (q \geq 0.3)] \cup (q = 1)$.

This section has shown the crucial property of consistency in both parametric and non-parametric interval Markov chains. It thus sets the necessary elements before model checking such probabilistic models.
3.4 Further reading

The definitions and properties stated in this section are detailed in [DLP16]. They were revised in [PP18], where both inductive and co-inductive definitions of consistency are given, implemented with forward and backward algorithms. [DLP16] also addresses some properties: consistent avoidability, existential and universal consistent reachability.

4 Parametric Petri Nets

We now consider a parametric extension of Petri nets in which the weights of the arcs can be parameters. This extension was mainly studied in the Ph.D. thesis of Nicolas David [Dav17] and many of those results can also be found in [Dav+15; Dav+17].

Example 10. In order to illustrate the usefulness of this parameterised formalism, we consider the example, taken from [Dav17], of a financial loan. It is modelled in Fig. 5.

![Fig. 5: Modelling a financial loan with parametric Petri nets](image)

In the general case, a client has a certain amount of money, say \(a\), and is ensured to get income \(b\) every month. To finance his project, the client needs to define with the bank the amount loaned \(c\), the duration of reimbursement \(d\) (in months) and the amount of each reimbursement \(e\). At the end of the process, the bank expects to get back the initial amount loaned \(c\) plus the interest \(f\). The amount of money possessed by the client is depicted in place \(\text{funds}\). The signature of the contract is symbolised through the firing of transition \(\text{grantLoan}\) which imposes to the bank to loan an amount \(c\) for a term of \(d\) months. Each month, we can fire \(\text{reimburse}\): the client receives its own income \(b\) that is added to its capital in \(\text{funds}\) and in parallel an amount \(e\) is reimbursed to the bank. When we consider that the reimbursement is finished, we fire \(\text{endLoan}\) that removes the token from \(\text{lock}_1\) and allows us to enable the transitions of the second part of this example. We can then check if the bank can get its money back by testing if \(\text{loanOk}\) can be marked and if the bank can get the interest by checking \(\text{interestOk}\) can be marked or both by checking if \(\text{loanFinished}\) can be marked.
With this example in mind, we now proceed to the corresponding formal definitions.

**Definition 24 (Parametric Petri Net [Dav+15]).** A (marked) parametric Petri Net (PPN) is a tuple \( N = (P, T, \mathbb{P}, \text{Pre}, \text{Post}, m_0) \) where \( P \) is a finite set of places, \( T \) is a finite set of transitions such that \( P \cap T = \emptyset \), \( \mathbb{P} \) is a finite set of parameters, \( \text{Pre} : P \times T \to \mathbb{N} \cup \mathbb{P} \) is the backward incidence function, \( \text{Post} : P \times T \to \mathbb{N} \cup \mathbb{P} \) is the forward incidence function, \( m_0 \in \mathbb{N}^P \) is the initial marking.

Let \( N \) be a parametric Petri net. A valuation \( v \) of the parameters of \( N \) is a mapping from \( P \) to \( \mathbb{N} \).

We denote by \( v(N) \) the Petri net obtained by replacing all parameters by the value they are given by \( v \).

A marking of a (non-parametric) Petri net is a mapping from \( P \) to \( \mathbb{N} \). Markings can be compared component by component: \( m \geq m' \) if \( \forall p \in P, m(p) \geq m'(p) \).

A transition \( t \in T \) is enabled by marking \( m \) if for all \( p \in P, m(p) \geq \text{Pre}(p,t) \).

A transition \( t \) enabled by marking \( m \) can be fired, leading to a new marking \( m' \) defined by \( \forall p \in P, m'(p) = m(p) - \text{Pre}(p,t) + \text{Post}(p,t) \). We note \( m \xrightarrow{t} m' \).

A run in the Petri net is a possibly infinite alternating sequence of markings and transitions \( m_1t_1m_2\ldots \) such that \( m_1 = m_0 \) and for all \( i \geq 1 \), \( m_i \xrightarrow{t_i} m_{i+1} \).

### 4.1 Problems of interest

We extend classic problems defined for Petri nets to the parametric setting. These problems are reachability, coverability, and (un)boundedness.

A marking \( m \) is reachable if there exists a finite run \( m_1t_1m_2\ldots t_n m_{n+1} \) such that \( m_{n+1} = m \).

A marking \( m \) is coverable if there exists a reachable marking \( m' \) such that \( m' \geq m \).

A (non-parametric) Petri net is \( k \)-bounded if for all reachable markings \( m \), we have \( \forall p \in P, m(p) \leq k \). It is bounded if there exists some \( k \) such that it is \( k \)-bounded. If a net is not bounded, we say it is unbounded and then for all \( B \geq 0 \) there exists a place \( p \) and a reachable marking \( m \) such that \( m(p) > B \).

Similarly a Petri net is simultaneously \( X \) unbounded [Dem13], for some subset \( X \) of \( P \), if for all \( B \geq 0 \), there exists a reachable marking \( m \) such that for all places \( p \in X \), we have \( m(p) > B \).

We consider two associated parametric decision problems: the existential and the universal problems. In the former we want to decide the existence of a parameter valuation for which some property holds, and in the latter we want to decide if it holds for all the possible parameter valuations. The property in question can be any of those defined above.

For instance, the existential parametric reachability problem asks, given a target marking \( m \), if there exists a parameter valuation \( v \) such that \( m \) is reachable in \( v(N) \). Similarly, the universal coverability problem asks, given a target marking \( m \), if for all valuations \( v \) of the parameters, \( m \) is coverable in \( v(N) \).
We finally define synthesis problems, in which we want to effectively compute the set of all parameter valuations for which some property holds. Note that if we can effectively compute this set, and check its emptiness or universality, then we can also solve the two decision problems above.

### 4.2 Undecidability Results for Parametric Petri Nets

We start with a few negative results.

**Theorem 4 ([Dav+15]).** The existential and universal parametric coverability, reachability, and (simultaneous) unboundedness problems are undecidable.

This theorem is proved by reducing the halting, and counter-boundedness problems for 2-counter machines [Min67] to those parametric problems for parametric Petri nets. We encode the value of each counter as the number of tokens in a place and we use parametric arcs to test for emptiness of that place (i.e. counter value 0).

As a consequence, we need to consider meaningful subclasses for which we might obtain some decidability results.

### 4.3 Subclasses of Parametric Petri Nets

The basic observation guiding us in defining interesting subclasses of PPNs is that, in the 2-counter machine reduction briefly outlined above, we need both a post arc with a parametric weight $a$ and a pre arc with the same parametric weight $a$.

We thus define preT-PPNs, postT-PPNs, and distinctT-PPNs according to whether the parameters are allowed only in pre arcs, in post arcs, or in both but not with the same parameters.

**Definition 25 (preT- and postT-PPNs).** A preT-PPN (resp. postT-PPN) is a PPN in which the Post (resp. Pre) function has the form $P \times T \rightarrow N$.

**Definition 26 (distinctT-PPNs).** A distinctT-PPN is a PPN in which the set of parameters used in the Pre function and the set of those used in the Post function are disjoint.

We could also consider classic Petri nets in which the initial marking is parameterised. The corresponding formalism is called P-PPN. Such a parametric initial marking is easily simulated with an initial transition that has parametric weights in its post arcs and sets the initial marking. Interestingly, it can also be proved that postT-PPNs can be (weakly) simulated by P-PPNs [Dav+15].

Fig. 6 [Dav+15] summarises this hierarchy of subclasses.
We now summarise the current state-of-the-art for the study of PPNs. Table 2 gives the decidability results and whenever relevant the complexities for the universal problems, while Table 3 gives them for the existential problems.

In order to establish the decidability results, we use a variety of techniques. The most basic is that preT- and postT-PPNs have a strong monotonicity prop-
property [Dav+15]: increasing (resp. decreasing) the value of parameters in a postT-PPN (resp. preT-PPN) can only add behaviors. Second we can reduce universal coverability in preT-PPNs to simultaneous unboundedness, and existential coverability in postT-PPNs to coverability in the \(\omega\) Petri nets of [Gee+15]. Both of these reductions can be found in [Dav+17]. Finally, we can adapt the Karp & Miller algorithm [KM69] to preT- and postT-PPNs [Dav17].

Table 4 presents results for the synthesis problem [Dav+17]. In the cases where we can compute the set of adequate valuations (✓), we mostly rely on the use of an algorithm to compute upward-closed sets by Valk and Jantzen [VJ85]. The negative results (✗) come from the fact that the emptiness or the universality of the set cannot be decided as a direct consequence of the undecidability results above, so there is little hope to find a useful representation of that set. The case of distinctT-PPNs is similar in so far as if we can compute the solution and test its intersection with equality constraints we can solve the synthesis problem for any PPN, by replacing parameters used both in pre and post arcs by different parameters (which gives a distinctT-PPN) and then constraining the solution set with equality constraints on these different parameters.

| Class      | Reachability | S. Unboundedness | Coverability |
|------------|--------------|------------------|--------------|
| PPN        | ✓            | ✓                | ✓            |
| preT-PPN   | ✓            | ✓                | ✓            |
| postT-PPN  | ✓            | ✓                | ✓            |
| P-PPN      | open         | ✓                | ✓            |
| distinctT-PPN | ✓          | ✓                | ✓            |

Table 4: Results for the synthesis problem

### 4.5 Conclusion

Parametric Petri nets are a powerful formalism to model flexible systems. In the general case, the interesting problems are undecidable but still useful subclasses can be obtained by restricting the use of parameters. For most of these subclasses, it is possible to actually synthesise the values of the parameters such that the net is unbounded, or such that some marking is coverable. It would nevertheless be interesting to design semi-algorithms or incomplete algorithms for the most expressive cases that do not fit in this restricted setting. A problem also remains open, i.e. the decidability of universal reachability for Petri nets with a parameterised initial marking.

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5 Action synthesis

One of the classical approaches to verification and specification of concurrent systems employs Kripke structures as models and branching-time logics such as CTL as property description languages. In contrast to the models presented earlier, Kripke structures allow only for specifying sequential behaviours. Advanced data structures such as Binary Decision Diagrams [Bry86] (BDDs) together with algorithms based on fixed-point specification of CTL enable efficient verification of models whose state spaces exceed $10^{20}$ [Bur+90]. Here, we extend Action-Restricted Computation Tree Logic ARCTL [PR06] and its models with parameters, to obtain a framework that benefits from BDD-based fixed-point algorithms.

5.1 Mixed Transition Systems

Mixed Transition Systems [PR06] (MTS) are essentially Kripke structures with transitions labelled by actions.

**Definition 27 (MTS).** Let $AP$ be a set of propositional variables. A Mixed Transition System is a 5-tuple $M = (S, s^0, A, T, L)$, where:

- $S$ is a finite set of states, and $s^0 \in S$ is the initial state,
- $A$ is a non-empty finite set of actions,
- $T \subseteq S \times A \times S$ is a transition relation,
- $L : S \rightarrow 2^{AP}$ is a (state) valuation function.

We write $s \xrightarrow{a} s'$ if $(s, a, s') \in T$. Let $B \subseteq A$ and $\pi = (s_0, a_0, s_1, a_1, \ldots)$ be a finite or infinite sequence of interleaved states and actions. By $|\pi|$ we denote the number of states in $\pi$ if $\pi$ is finite, and $\omega$ if $\pi$ is infinite. A sequence $\pi$ is a path over $B$ iff $s_i \xrightarrow{a_i} s_{i+1}$ and $a_i \in B$ for each $i < |\pi|$ and either $\pi$ is infinite or its final state does not have a $B$-successor state in $S$, i.e. $\pi = (s_0, a_0, s_1, a_1, \ldots, s_m)$ for some $m \in \mathbb{N}$ and there is no $s' \in S$ and $a \in B$ such that $s_m \xrightarrow{a} s'$. By $\pi_i$, we denote the $i$-th state of $\pi$ for all $i \in \mathbb{N}$.
The set of all paths over $B$ in $\mathcal{M}$ is denoted by $\Pi(\mathcal{M}, B)$, and the set of all paths $\pi \in \Pi(\mathcal{M}, B)$ starting from a given state $s \in \mathcal{S}$ is denoted by $\Pi(\mathcal{M}, B, s)$. We typically omit the symbol $\mathcal{M}$, writing $\Pi(B)$ and $\Pi(B, s)$. By $\Pi^\omega(B)$ and $\Pi^\omega(B, s)$ we mean the corresponding sets restricted to the infinite paths only.

**Example 11 (MTS).** A MTS with $AP = \{p, \text{safe}\}$, $A = \{\text{left, right, forw., back}\}$, and initial state $s_0$ is shown in Fig. 7.

The path $\pi = (s_0, \text{left}, s_1, \text{right}, s_4)$ belongs to $\Pi(\{\text{left, right}\})$, but not to the set $\Pi(\{\text{left, right, back}\})$. The reason is that while $\pi$ is a maximal path over $\{\text{left, right}\}$, it is not maximal over $\{\text{left, right, back}\}$ as it can be extended e.g. into the infinite path $\pi' = (s_0, \text{left}, s_1, \text{right}, s_4, \text{back}, s_0, \text{left}, s_1, \text{right}, s_4, \text{back}, s_0, \ldots) \in \Pi(\{\text{left, right, back}\})$.

### 5.2 Parametric Action-Restricted CTL

The main difference between ARCTL and CTL is that in ARCTL each path quantifier is subscripted with a set of actions, e.g. $E_{\{\text{left, right}\}}G(E_{\text{forw.}}F_{\text{safe}})$ may be read as “there exists a path over left and right, on which it holds globally that a state satisfying safe is reachable along some path over forw.”.

Parametric ARCTL (pmARCTL) extends ARCTL by allowing free variables in place of sets of actions, e.g. $E_Y G(E_Z F_{\text{safe}})$ is a formula of pmARCTL, where $Y$ and $Z$ are free variables.

**Definition 28 (pmARCTL syntax).** Let $A$ be a finite set of actions, $\mathcal{X}$ a finite set of variables, $AP$ a set of propositional variables, and ActSets $= 2^A \setminus \{\emptyset\}$. The set of formulae of pmARCTL is defined by the following grammar:

$$\phi ::= p \mid \neg \phi \mid \phi \lor \psi \mid E_\alpha X \phi \mid E_\alpha G \phi \mid E_\alpha^\omega G \phi \mid E_\alpha (\phi U \psi),$$

where $p \in AP$ and $\alpha \in \text{ActSets} \cup \mathcal{X}$.

The basic path quantifiers and modalities of pmARCTL have the same meaning as in CTL. The superscript $^\omega$ restricts the quantification to the infinite paths, whereas the subscript $\alpha$ restricts the quantification to the paths over $\alpha$.

The semantics of pmARCTL is defined w.r.t. parameter valuations, i.e. functions $v: \mathcal{X} \rightarrow \Pi$. $\text{ParVals}$ denotes the set of all parameter valuations. For conciseness, for $v \in \text{ParVals}$ we write $v(\alpha) = B$ if $\alpha = B \subseteq A$ and $v(\alpha) = v(Y)$ if $\alpha = Y \in \mathcal{X}$. Moreover, we assume that $O^* = O$, for $O \in \{E, A, \Pi\}$.

**Definition 29 (pmARCTL semantics).** Let $\mathcal{M} = (\mathcal{S}, s^0, A, T, \mathcal{L})$ be an MTS and $v \in \text{ParVals}$ a parameter valuation. The relation $|=v$ is defined as follows:

- $s |=v p$ iff $p \in \mathcal{L}(s)$,
- $s |=v \neg \phi$ iff $s \not|=v \phi$,
- $s |=v \phi \lor \psi$ iff $s |=v \phi$ or $s |=v \psi$,
- $s |=v E_\alpha X \phi$ iff $|\pi| > 1$ and $\pi_1 |=v \phi$, for some $\pi \in \Pi(\alpha, s)$,
- $s |=v E_\alpha^\omega G \phi$ iff $\pi_i |=v \phi$ for all $i < |\pi|$, for some $\pi \in \Pi^\omega(\alpha, s)$,
\[ s \models E\phi \iff \pi_i \models \psi \text{ for some } i < |\pi| \text{ and } \pi_j \models \phi \text{ for all } 0 \leq j < i, \text{ for some } \pi \in \Pi(v(\alpha), s), \]

where \( p \in AP, \phi, \psi \in \text{pmARCTL}, r \in \{\omega, e\}, \text{ and } \alpha \in \text{ActSets} \cup \mathcal{X}. \)

**Example 12.** For the MTS in Fig. 7 we have \( s_0 \models E_Y G(E_Z F_{\text{safe}}) \iff \text{forw.} \in v(Z). \)

### 5.3 Parameter Synthesis for pmARCTL

In this subsection we show how to recursively characterise pmARCTL using the basic operator of parametric pre-image. These equivalences give rise to fixed-point algorithms that can be implemented using BDDs.

Let \( \phi \in \text{pmARCTL}. \) Our goal is to construct the function \( f_{\phi} : S \rightarrow 2^{\text{ParVals}} \) s.t. for all \( s \in S \) we have \( s \models \phi \) iff \( v \in f_{\phi}(s) \). In other words, the set \( f_{\phi}(s) \) consists of all the parameter valuations that make \( \phi \) true in state \( s \).

**Boolean Connectives and Non-parametric Modalities.** For each \( p \in AP, \phi, \psi \in \text{pmARCTL} \) we have \( f_p(s) = \text{ParVals} \) if \( p \in \mathcal{L}(s) \) and \( f_p(s) = \emptyset \) otherwise; \( f_{\neg \phi}(s) = \text{ParVals} \setminus f_{\phi}(s) \); and \( f_{\phi \psi}(s) = f_{\phi}(s) \cup f_{\psi}(s) \).

**Parametric Pre-image and NeXt.** Let \( f : S \rightarrow 2^{\text{ParVals}} \) be a function. The parametric pre-image of \( f \) w.r.t. \( Y \in \mathcal{X} \) is defined as the function \( \text{parPre}^2_Y(f) : S \rightarrow 2^{\text{ParVals}} \) s.t. \( \text{parPre}^2_Y(f)(s) = \{ v \mid \exists s' \in S. \exists a \in v(Y). s \xrightarrow{a} s' \wedge v \in f(s') \} \) for each \( s \in S \). Intuitively, for each \( \phi \in \text{pmARCTL} \) in \( \text{parPre}^2_Y(f_\phi)(s) \) we collect all the parameter valuations \( v \) s.t. some state \( s' \) satisfying \( s' \models v \phi \) can be reached by firing in \( s \in S \) an action from \( v(Y) \).

**Parametric Temporal Modalities.** We employ the following equations to deal with two versions of the Globally modality:

\[
\begin{align*}
f_{E_Y G\phi}(s) &= f_\phi(s) \cap \text{parPre}^3_Y(f_{E_Y G\phi})(s), \\
f_{E_Y G\phi}(s) &= f_\phi(s) \cap (\text{parPre}^3_Y(f_{E_Y G\phi})(s) \cup f_{\neg E_Y X_{\text{true}}}(s)).
\end{align*}
\]

The following equation characterises the Until modality:

\[
f_{E_Y (\phi U \psi)}(s) = f_\phi(s) \cup (f_\phi(s) \cap \text{parPre}^3_Y(f_{E_Y (\phi U \psi)})(s)).
\]

We refer to [KMP15; KP15] on how to turn the above equations into fixed-point algorithms and implement them using BDDs. In all the cases, the returned result of running the overall synthesis algorithm for \( \phi \in \text{pmARCTL} \) is the BDD that represents \( f_\phi \). This structure can be then queried for individual parameter valuations; for a certain class of formulae it is possible to synthesise minimal parameter valuations using prime implicants.

As a closing note let us mention that the emptiness problem for pmARCTL, i.e. the question whether \( f_\phi(s^0) \neq \emptyset \) for \( \phi \in \text{pmARCTL} \) is known to be NP-complete [KMP15].
6 Tools

In this section, we finally briefly review tools related to the aforementioned formalisms.

6.1 IMITATOR

IMITATOR [And+12] is a software tool for parametric verification and robustness analysis of PTAs augmented with integer variables and stopwatches. Parameters can be used both in the model and in the properties. Verification capabilities include reachability-synthesis, deadlock-freeness-synthesis [And16], non-Zeno model checking [And+17], minimal-time synthesis [And+19], and trace-preservation-synthesis. IMITATOR is fully written in OCaml, and makes use of the Parma Polyhedra Library [BH08]. It also features distributed capabilities to run over a cluster.

IMITATOR comes with a benchmarks library available under an open source license [And19a].

IMITATOR was successfully used in several application domains such as parametric schedulability analysis of a prospective architecture for the flight control system of the next generation of spacecrafts designed at ASTRIUM Space Transportation [Fri+12], formal timing analysis of music scores [FJ13], verification of software product lines [Lut+17], monitoring logs from the automotive domain against parametric properties [AHW18], and was used to propose a solution to a challenge related to a distributed video processing system by Thales [SAL15].

Related tools  The first tool to support modelling and verification using parametric timed automata was HYTech [HH97]. In fact, HYTech supports linear hybrid automata (including clocks, parameters, stopwatches and general continuous variables); it can compute the state space, and perform operations (such as intersection, convex hull, difference) between sets of symbolic states. Therefore, it can be used to perform parametric model checking using reachability checking [Ace+98]. HYTech is not maintained anymore, but can still be found online in the form of a standalone binary for Linux.\textsuperscript{8}

In [Hun+02], an extension of Uppaal implementing parametric difference bound matrices (PDBMs) and hence allowing for verification using PTAs is mentioned. However, this tool does not seem to be available anywhere online.

PHA\textsuperscript{e}r [Fre08] is a tool for verifying safety properties of hybrid systems. It notably relies on exact arithmetic (with unlimited precision) using the Parma Polyhedra Library [BH08]; on the other hand, it also supports approximations. SpaceEx [Fre+11] can be seen as a successor of PHA\textsuperscript{e}r, and also tackles verification of reachability properties for hybrid systems. Parameters are not natively supported, but can be encoded using variables that are arbitrarily set up upon system start, and then remain subsequently constant (with a 0-slope). SpaceEx seems to have a lot of interesting recent developments.

\textsuperscript{8} \url{https://embedded.eecs.berkeley.edu/research/hytech/}
PSyHCoS [And+13] allows the synthesis of parameters for a parametric extension of the process algebra Stateful timed CSP [Sun+13], itself a timed extension of Hoare’s communicating sequential processes [Hoa85]. When compared to other formalisms such as (parametric) timed automata, (parametric) stateful timed CSP has the advantage of giving the designer the ability to specify hierarchical systems.

Finally, Symrob is not strictly a tool for synthesis, but allows robustness measurement for timed automata [San15].

6.2 Roméo

ROMÉO [Lim+09] is a model-checking tool for a selection of hybrid extensions of Petri nets, enriched with discrete variables. In particular, it supports parametric time Petri nets, a formalism shown to be close to PTAs in terms of expressiveness [Bér+05; TLR09]. ROMÉO allows the use of parametric linear expressions in the time intervals of the transitions, and the addition of linear constraints on the parameters to restrict their domain. ROMÉO provides a simulator and an integrated model-checker supporting a subset of parametric TCTL (including reachability-synthesis and unavoidability-synthesis), in which “Until” modalities cannot be nested. It also features optimal cost reachability and parameter synthesis for cost-bounded reachability. ROMÉO implements in particular an original algorithm for integer parameter synthesis using a symbolic (continuous) representation [JLR15]. ROMÉO is mainly written in C++, and makes use of the Parma Polyhedra Library [BHZ08]. It has been successfully used in a few and diverse case-studies including the analysis of resilience properties in oscillatory biological systems [AMI16]; the synthesis of environment requirements for an aerial video tracking system [Par+16]; and the analysis of operational scenarios modelling in the DGA OMOTESC project [Sei09].

6.3 Spatula

Spatula [Kna; KMP15] implements the theory of action synthesis outlined in Section 5. The tool is written in C++ and employs CUDD library [Som] for representing and manipulating BDDs. Spatula accepts models represented as networks of automata written in a simplified C-like input language and pmARCTL as property description language. The result of synthesis for a given property is a BDD that represents all the valuations that make the property true. Spatula can also list all the minimal valuations (w.r.t. bitwise comparison) for the existential part of the logic.

References

[ACD93] Rajeev Alur, Costas Courcoubetis, and David L. Dill. “Model-Checking in Dense Real-Time”. In: Information and Computation. 104.1 (May 1993), pp. 2–34. DOI: 10.1006/inco.1993.1024 (cit. on p. 13).
[And+13] Étienne André, Yang Liu, Jun Sun, Jin Song Dong, and Shang-Wei Lin. “PSyHCoS: Parameter Synthesis for Hierarchical Concurrent Real-Time Systems”. In: CAV. Ed. by Natasha Sharygina and Helmut Veith. Vol. 8044. Lecture Notes in Computer Science. Saint Petersburg, Russia: Springer, July 2013, pp. 984–989. DOI: 10.1007/978-3-642-39799-8_70 (cit. on p. 33).

[And+15] Étienne André, Giuseppe Lipari, Hoang Gia Nguyen, and Youcheng Sun. “Reachability Preservation Based Parameter Synthesis for Timed Automata”. In: NFM. Ed. by Klaus Havelund, Gerard J. Holzmann, and Rajeev Joshi. Vol. 9058. Lecture Notes in Computer Science. Pasadena, CA, USA: Springer, Apr. 2015, pp. 50–65. DOI: 10.1007/978-3-319-17524-9_5 (cit. on p. 4).

[And+17] Étienne André, Hoang Gia Nguyen, Laure Petrucci, and Jun Sun. “Parametric model checking timed automata under non-Zenoness assumption”. In: NFM. Ed. by Clark Barrett and Temesghen Kahsai. Vol. 10227. Lecture Notes in Computer Science. Moffett Field, CA, USA: Springer, 2017, pp. 35–51. DOI: 10.1007/978-3-319-57288-8_3 (cit. on p. 32).

[And+19] Étienne André, Vincent Bloemen, Laure Petrucci, and Jaco van de Pol. “Minimal-Time Synthesis for Parametric Timed Automata”. In: TACAS, Part II. Ed. by Tomáš Vojnar and Lijun Zhang. Vol. 11428. Lecture Notes in Computer Science. Prague, Czech Republic: Springer, 2019, pp. 211–228. DOI: 10.1007/978-3-030-17465-1_12 (cit. on pp. 4, 32).

[And16] Étienne André. “Parametric Deadlock-Freeness Checking Timed Automata”. In: ICTAC. Ed. by Augusto Cesar Alves Sampaio and Farn Wang. Vol. 9965. Lecture Notes in Computer Science. Taipei, Taiwan: Springer, 2016, pp. 469–478. DOI: 10.1007/978-3-319-46750-4_27 (cit. on pp. 18, 32).

[And19a] Étienne André. “A benchmark library for parametric timed model checking”. In: FTSCS. Ed. by Cyrille Artho and Peter Csaba Ölveczky. Vol. 1008. Communications in Computer and Information Science. Gold Coast, Australia: Springer, 2019, pp. 75–83. DOI: 10.1007/978-3-030-12988-0_5 (cit. on p. 32).

[And19b] Étienne André. “What’s decidable about parametric timed automata?” In: International Journal on Software Tools for Technology Transfer 21.2 (Apr. 2019), pp. 203–219. DOI: 10.1007/s10009-017-0467-0 (cit. on pp. 4, 15).

[AS13] Étienne André and Romain Soulat. The Inverse Method. FOCUS Series in Computer Engineering and Information Technology. 176 pages. ISTE Ltd and John Wiley & Sons Inc., 2013. ISBN: 9781848214477 (cit. on p. 10).

[Aşt+16] Lăcrămioara Aştefănoaei, Saddek Bensalem, Marius Bozga, Chih-Hong Cheng, and Harald Rüffer. “Compositional Parameter Synthesis”. In: FM. Ed. by John S. Fitzgerald, Constance L. Heitmeyer, Stefania Gnesi, and Anna Philippou. Vol. 9995. Lecture Notes in Computer Science. 2016, pp. 60–68. DOI: 10.1007/978-3-319-48989-6_4 (cit. on p. 4).

[Ben+15] Nikola Beneš, Peter Bezděk, Kim Gulstrand Larsen, and Jiří Srba. “Language Emptiness of Continuous-Time Parametric Timed Automata”. In: ICALP, Part II. Ed. by Magnús M. Halldórsson, Kazuo Iwama,
Willem Klop. Vol. 458. Lecture Notes in Computer Science. Amsterdam, The Netherlands: Springer, 1990, pp. 278–297. DOI: 10.1007/BFb0039066 (cit. on p. 9).

[HHW97] Thomas A. Henzinger, Pei-Hsin Ho, and Howard Wong-Toi. “HyTech: A Model Checker for Hybrid Systems”. In: International Journal on Software Tools for Technology Transfer 1.1-2 (1997), pp. 110–122. DOI: 10.1007/s100090050008 (cit. on p. 32).

[Hoa85] C.A.R. Hoare. Communicating Sequential Processes. International Series in Computer Science. Prentice-Hall, 1985 (cit. on p. 33).

[HR04] M. Huth and M. Ryan. Logic in Computer Science: Modelling and Reasoning about Systems. Cambridge University Press, 2004 (cit. on p. 31).

[Hun+02] Thomas Hune, Judi Romijn, Mariëlle Stoelinga, and Frits W. Vaandrager. “Linear parametric model checking of timed automata”. In: Journal of Logic and Algebraic Programming 52-53 (2002), pp. 183–220. DOI: 10.1016/S1567-8326(02)00037-1 (cit. on pp. 4, 5, 10, 12, 15, 18, 32).

[JLR15] Aleksandra Jovanović, Didier Lime, and Olivier H. Roux. “Integer Parameter Synthesis for Real-Time Systems”. In: IEEE Transactions on Software Engineering 41.5 (2015), pp. 445–461. DOI: 10.1109/TSE.2014.2357445 (cit. on pp. 4, 5, 10, 13, 15, 19, 33).

[KM69] Richard M. Karp and Raymond E. Miller. “Parallel program schemata”. In: Journal of Computer and System Sciences 3.2 (1969), pp. 147–195. ISSN: 0022-0000. DOI: 10.1016/S0022-0000(69)80011-5 (cit. on p. 28).

[KMP15] Michał Knapik, Artur Meski, and Wojciech Penczek. “Action Synthesis for Branching Time Logic: Theory and Applications”. In: ACM Transactions on Embedded Computing 14.4 (2015). DOI: 10.1145/2746337 (cit. on pp. 3, 31, 33).

[Kna] Michał Knapik. https://michalknapik.github.io/spatula (cit. on p. 33).

[KP12] Michał Knapik and Wojciech Penczek. “Bounded Model Checking for Parametric Timed Automata”. In: Transactions on Petri Nets and Other Models of Concurrency. Lecture Notes in Computer Science 6900 (2012). Ed. by Kurt Jensen, Susanna Donatelli, and Jetty Kleijn, pp. 141–159. DOI: 10.1007/978-3-642-29072-5_6 (cit. on pp. 4, 12).

[KP15] Michał Knapik and Wojciech Penczek. “Fixed-Point Methods in Parametric Model Checking”. In: IS. Vol. 322. Advances in Intelligent Systems and Computing. Springer, Sept. 2015, pp. 231–242. DOI: 10.1007/978-3-319-11313-5_22 (cit. on pp. 3, 31).

[Li+17] Jiaying Li, Jun Sun, Bo Gao, and Étienne André. “Classification based Parameter Synthesis for Parametric Timed Automata”. In: ICFEM. Ed. by Zhenhua Duan and Luke Ong. Vol. 10610. Lecture Notes in Computer Science. Xi’an, China: Springer, 2017, pp. 243–261. DOI: 10.1007/978-3-319-68690-5_15 (cit. on p. 4).

[Lim+09] Didier Lime, Olivier H. Roux, Charlotte Seidner, and Louis-Marie Tranouez. “Romeo: A Parametric Model-Checker for Petri Nets with Stopwatches”. In: TACAS. Ed. by Stefan Kowalewski and Anna Philippou. Vol. 5505. Lecture Notes in Computer Science. York, United Kingdom: Springer, Mar. 2009, pp. 54–57. DOI: 10.1007/978-3-642-00768-2_6 (cit. on p. 33).

[Lut+17] Lars Luthmann, Andreas Stephan, Johannes Bürdek, and Malte Lochau. “Modeling and Testing Product Lines with Unbounded Parametric Real-
Time Constraints”. In: SPLC, Volume A. Ed. by Myra B. Cohen, Mathieu Acher, Lidia Fuentes, Daniel Schall, Jan Bosch, Rafael Capilla, Ebrahim Bagheri, Yingfei Xiong, Javier Troya, Antonio Ruiz Cortés, and David Benavides. Sevilla, Spain: ACM, 2017, pp. 104–113. DOI: 10.1145/3106195.3106204 (cit. on p. 32).

[Mil00] Joseph S. Miller. “Decidability and Complexity Results for Timed Automata and Semi-linear Hybrid Automata”. In: HSCC. Ed. by Nancy A. Lynch and Bruce H. Krogh. Vol. 1790. Lecture Notes in Computer Science. Pittsburgh, PA, USA: Springer, 2000, pp. 296–309. ISBN: 3-540-67259-1. DOI: 10.1007/3-540-46430-1_26 (cit. on pp. 15, 18).

[Min67] Marvin L. Minsky. Computation: Finite and infinite machines. Upper Saddle River, NJ, USA: Prentice-Hall, Inc., 1967. ISBN: 0-13-165563-9 (cit. on p. 26).

[Par+16] Baptiste Parquier, Laurent Rioux, Rafik Henia, Romain Soulat, Olivier H. Roux, Didier Lime, and Étienne André. “Applying parametric model-checking techniques for reusing real-time critical systems”. In: FTSCS. Ed. by Cyrille Artho and Peter Csaba Ölveczky. Vol. 694. Communications in Computer and Information Science. Tokyo, Japan: Springer, Nov. 2016, pp. 129–144. DOI: 10.1007/978-3-319-53946-1_8 (cit. on p. 33).

[PP18] Laure Petrucci and Jaco van de Pol. “Parameter Synthesis Algorithms for Parametric Interval Markov Chains”. In: FORTE. Ed. by Christel Baier and Luís Caires. Vol. 10854. Lecture Notes in Computer Science. Madrid, Spain: Springer, 2018, pp. 121–140. DOI: 10.1007/978-3-319-92612-4_7 (cit. on p. 24).

[PR06] Charles Pecheur and Franco Raimondi. “Symbolic Model Checking of Logics with Actions”. In: MoChArt. Ed. by Stefan Edelkamp and Alessio Lomuscio. Vol. 4428. Lecture Notes in Computer Science. Springer, 2006, pp. 113–128. DOI: 10.1007/978-3-540-74128-2_8 (cit. on p. 29).

[RL07] Franco Raimondi and Alessio Lomuscio. “Automatic verification of multi-agent systems by model checking via ordered binary decision diagrams”. In: Journal of Applied Logic 5.2 (2007), pp. 235–251. DOI: 10.1016/j.jal.2005.12.010 (cit. on p. 31).

[SAL15] Youcheng Sun, Étienne André, and Giuseppe Lipari. “Verification of Two Real-Time Systems Using Parametric Timed Automata”. In: WATERS. Ed. by Sophie Quinton and Tullio Vardanega. Lund, Sweden, July 2015 (cit. on p. 32).

[San15] Ocan Sankur. “Symbolic Quantitative Robustness Analysis of Timed Automata”. In: TACAS. Ed. by Christel Baier and Cesare Tinelli. Vol. 9035. Lecture Notes in Computer Science. London, UK: Springer, 2015, pp. 484–498. DOI: 10.1007/978-3-662-46681-0_48 (cit. on p. 33).

[Sci09] Charlotte Seidner. “Vérification des EFFBDs : Model checking en Ingénierie Système. (EFFBDs Verification: Model checking in Systems Engineering)”. PhD thesis. University of Nantes, France, 2009 (cit. on p. 33).

[Som] F. Somenzi. CUDD: CU Decision Diagram Package - Release 2.5.0. https://github.com/ivmai/cudd (cit. on p. 33).

[Sun+13] Jun Sun, Yang Liu, Jin Song Dong, Yan Liu, Ling Shi, and Étienne André. “Modeling and Verifying Hierarchical Real-time Systems using Stateful Timed CSP”. In: ACM Transactions on Soft-
ware Engineering and Methodology 22.1 (Feb. 2013), pp. 3.1–3.29. DOI: 10.1145/2430536.2430537 (cit. on p. 33).

[TLR09] Louis-Marie Traonouez, Didier Lime, and Olivier H. Roux. “Parametric Model-Checking of Stopwatch Petri Nets”. In: Journal of Universal Computer Science 15.17 (2009), pp. 3273–3304. DOI: 10.3217/jucs-015-17-3273 (cit. on p. 33).

[VJ85] Rüdiger Valk and Matthias Jantzen. “The residue of vector sets with applications to decidability problems in Petri nets”. In: Acta Informatica 21.6 (1985), pp. 643–674. ISSN: 1432-0525. DOI: 10.1007/BF00289715 (cit. on p. 28).