A granular resampling method based energy-efficient architecture for heartbeat classification in ECG

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Abstract: SVM-based granular resampling method is put forward to obtain a robust classification model for energy-efficient ECG systems. The classification model consists of a low-complexity model to filter most easy-to-learn heartbeats and a high-accuracy classifier to identify the remained heartbeats. Energy-efficient hardware architecture for multi-class heartbeat classification is implemented based on the classification model. The architecture optimizations include memory segmentation to reduce energy consumption and time domain reuse to save resources. We adopt 40-nm CMOS process to implement the proposed design. It provides an average prediction speedup by 57.21% and a significant energy dissipation reduction by 52.22% per classification compared with the design without low-complexity models.

Keywords: heartbeat classification, granular resampling method, energy-efficient architecture, SVM

Classification: Integrated circuits

References

[1] S. Jain, et al.: “Efficient algorithm for classification of electrocardiogram beats based on artificial bee colony-based least-squares support vector machines classifier,” Electron. Lett. 52 (2016) 1198 (DOI: 10.1049/el.2016.1171).
[2] S. Kiranyaz, et al.: “Real-time patient-specific ECG classification by 1-D convolutional neural networks,” IEEE Trans. Biomed. Eng. 63 (2016) 664 (DOI: 10.1109/TBME.2015.2468589).
[3] N. Bayasi, et al.: “Low-power ECG-based processor for predicting ventricular arrhythmia,” IEEE Trans. Very Large Scale Integr. (VLSI) Syst. 24 (2016) 1962 (DOI: 10.1109/TVLSI.2015.2475119).
[4] S.-Y. Hsu, et al.: “A 48.6-to-105.2 µW machine learning assisted cardiac sensor SOC for mobile healthcare applications,” IEEE J. Solid-State Circuits 49 (2014) 801 (DOI: 10.1109/JSSC.2013.2297406).
[5] B.-Y. Shiu, et al.: “Low-power low-noise ECG acquisition system with dsp for heart disease identification,” IEEE BioCAS (2013) (DOI: 10.1109/BioCAS.2013.6679630).
[6] S. Jeong, et al.: “A 12 nW always-on acoustic sensing and object recognition
microsystem using frequency-domain feature extraction and SVM classification,” ISSCC (2017) 362 (DOI: 10.1109/ISSCC.2017.7870411).

[7] R. Batuwita and V. Palade: “FSVM-CIL: Fuzzy support vector machines for class imbalance learning,” IEEE Trans. Fuzzy Syst. 18 (2010) 558 (DOI: 10.1109/TFUZZ.2010.2042721).

[8] Testing and reporting performance results of cardiac rhythm and ST segment measurement algorithms, ANSI/AAMI EC57:1998 standard,” Association for the Advancement of Medical Instrumentation (1998).

[9] M. Llamedo, et al.: “Heartbeat classification using feature selection driven by database generalization criteria,” IEEE Trans. Biomed. Eng. 58 (2011) 616 (DOI: 10.1109/TBME.2010.2068048).

[10] M. Galar, et al.: “An overview of ensemble methods for binary classifiers in multi-class problems: Experimental study on one-vs-one and one-vs-all schemes,” Pattern Recognit. 44 (2011) 1761 (DOI: 10.1016/j.patcog.2011.01.017).

[11] MIT-BIH Arrhythmias Database http://www.physionet.org/physiobank/database/mitdb/.

[12] C. Ye, et al.: “Heartbeat classification using morphological and dynamic features of ECG signals,” IEEE Trans. Biomed. Eng. 59 (2012) 2930 (DOI: 10.1109/TBME.2012.2213253).

1 Introduction

Electrocardiogram (ECG), one of the important diagnostic methods for detecting heart diseases, records the time evolution of the heart electrical activity. In general, an ECG follows a standard morphological and rhythmic pattern. An indication of the abnormity is likely to be detected if the heart rate or rhythm or change in the morphological pattern happens [1]. Nowadays, automatic ECG interpretation based on machine learning techniques has made positive contributions to the timely detection and classification. However, it is challenging to establish a robust model for correct diagnosis, as external noise and between-class imbalance have influence on the classification performance [2]. Besides, designs with low-power consumption based on the robust model are essential for many ECG classification systems [3, 4].

To solve the problems aforementioned, there are several kinds of methods: obtaining low-noise signals with a specific analog frontend and extracting a unique set of ECG features to reduce effects of the noise and imbalance, using an easy-implementation classifier to satisfy low-power demands, such as naive bayes, maximum likelihood classification, linear support vector machine (SVM), and so on [3, 5, 6]. However, most methods in the literature are put forward to establish a robust model or obtain an energy-efficient implementation of the model. Few researches focus on both the two aspects mentioned simultaneously. In addition, most previous works aiming at low-power designs are interested in discriminating between normal and ventricular heartbeats and few works study multi-class heartbeat classification problems [3, 4].

In this letter, we propose a new SVM-based method called Granular Resampling Method (GRM) to ease the effects of noise and between-class imbalance and
obtain a robust classification model for energy-efficient ECG systems, which consists of a low-complexity model and a high-accuracy classifier. The low-complexity model, which is less computationally demanding, is used as a filter to identify most easy-to-learn heartbeats and the high-accuracy classifier is used to discriminate the remained heartbeats. An energy-efficient hardware design for three-class heartbeat classification problems is implemented based on the classification model according to one-vs-all (OVA) strategy. The architecture is optimized with memory segmentation and time domain reuse techniques to save energy and resources further.

2 SVM-based granular resampling method

Support vector machine (SVM) is a kernel-based supervised learning algorithm aiming at two-class problems and widely applied to many real-world classification problems in different domains [7]. The goal of the SVM learning algorithm is to find a hyperplane which has the maximum margin from the two classes and can separate the data samples of the two classes efficiently. To get the classification function of SVMs, informative samples called support vectors (SVs) need to be found. Besides, kernel functions are applied to satisfy the need of solving non-linear classification problems. The commonly used kernel functions are linear, polynomial, and radial-basis function (RBF). In this letter, the RBF-based SVM, which is shown in Eq. (1)–(2), is chosen in terms of its strong applicability and generality.

\[ K(x_i, x_j) = \exp\left(-\gamma \|x_i - x_j\|^2\right) \] (1)

\[ y = \sum_{i=1}^{n_y} y_i a_i K(x_i, x) + b \] (2)

where \(a_i\) is the Lagrange multiplier, \(y_i\) is the class label of a SV, \(x_i\) represents a SV, \(x\) is the input vector, \(K(x_i, x)\) is the kernel function, and \(b\) is the bias.

The proposed SVM-based granular resampling method aims at two-class problems. In order to solve classification problems involving multiple classes, binarization strategy, one of the most popular techniques, is adopted in our method, which means multi-class problems will be transferred before they are processed. It is common to label one class with minor samples as a positive and the other one with major samples as a negative in a two-class problem. The classification problem can be well settled when a classifier which can separate samples of the two classes efficiently is obtained. However, the performance of classification models will be biased if training datasets are highly imbalanced or have noisy samples. Besides, most negative samples can be easily classified correctly by a low-complexity model instead of a high-accuracy classifier. Therefore, we adopt the granular resampling method to get a SVM-based model with the filter feature only using informative samples. Main idea of the proposed method is to use a weak classifier to extract informative samples from the negative class until the user defined condition is satisfied and potential noisy samples will be removed from informative samples in the first iteration. These extracted samples combined with all positive samples will be used as a new training dataset to obtain a high-accuracy
Algorithm GRM ($S_t$)

**input:** the set of training samples $S_t$, which consists of positive samples $S_p$ and negative samples $S_n$.

**Procedure begins:**

Initial a matrix: $S_{newt}$

Do the loop

while (the classification performance does not surpass the maximum)

A weak SVM with radial-basis function (RBF) is applied and SVs are extracted from $S_t$. SVs include positive SVs and negative SVs, named $S_{nsv}$ and $S_{psv}$.

Update training dataset: $S_t = S_t - S_{nsv}$.

if (the first iteration)

Find noisy array $S_{rn}$, whose decision function values are bigger than a user defined value $d_{thn}$.

Update the negative SV array: $S_{nsv} = S_{nsv} - S_{rn}$.

end

Update the new training dataset: $S_{newt} = S_{newt} + S_{nsv}$

if (the last iteration)

Generate a dataset for the low-complexity model: $S_{filter} = S_{nsv} + S_{psv}$.

Use RBF-based SVM to train a low-complexity model with $S_{filter}$.

end if

end while

end loop

Generate the final training dataset: $S_{newt} = S_{newt} + S_p$.

Use RBF-based SVM to train a high-accuracy classifier with $S_{newt}$.

**Procedure ends**

3 Proposed hardware architecture

Heartbeats, which have 16 classes, are re-clustered into five classes according to ANSI/AAMI EC57: 1998 standard [8]. However, not all classes are well represented in the database, hence a class-labeling modification is done in terms of [9] and the database is re-labeled as N class, S class, and V'A class. One-vs-one (OVO) and one-vs-all (OVA) are the most commonly-used strategies in the literature for multi-class classification [10]. Considering the resource consumption of a hardware implementation for three-class classification problems, OVA strategy is chosen in our design. The processing flow of three-class heartbeat classification is showed in Fig. 1. There are two stages to solve the problem and each stage consists of a low-complexity model and a high-accuracy classifier which are obtained from GRM. Most easy-to-learn heartbeats can be filtered by the model with low complexity and the remained hard-to-learn heartbeats will be classified by the classifier with high accuracy.
Fig. 2 shows the corresponding recognition hardware architecture. As models and classifiers obtained from the proposed method have the same processing logic and only one model works at a time, implementing a SVM processing unit is enough and classifications can be completed by reusing the processing unit at a different time. Therefore, if an input cannot be identified, it will be returned to the input and reuse the processing unit with new parameters of the next model or classifier whose selection sequence is decided according to Fig. 1. The classification procedure of an input will not stop until it is identified before or by the high-accuracy classifier2. The parameters of models and classifiers are stored in the memory. However, heartbeat classification is a highly imbalanced problem and most heartbeats can be classified correctly by the model. Hence parameters of low-complexity models will be more constantly accessed than that of high-accuracy classifiers in the memory, resulting in a large amount of access energy dissipation. Therefore, we apply memory segmentation technique to reduce access energy efficiently. The parameter memory is divided into two sets and each set has two banks: one smaller memory storing the model and one large memory for the classifier. The smaller memories have a latch-based architecture, which is used to save access energy further.

Fig. 3 shows the detailed implementation of the RBF-based SVM processing unit. The main computational units are an exponential function unit (EFU) and a parallel vector unit (PVU), which are described in detail as follows:

1) EFU: this unit is responsible for calculating $\exp(x)$. The exponential function is transferred and a new approximation can be represented as in Eq. (3) and Eq. (4).

$$\exp(x) = 2^{\log_2 e} \approx 2^{1.5x}$$

$$2^y = 2^{\text{floor}(y) + \text{delt}} \approx 2^{\text{floor}(y)}(\text{delt} + 1)$$

where $y$ is equal to $1.5x$, floor$(y)$ means the largest integer not greater than $y$, and delt is the fractional part of $y$. The approximation will work well as all
inputs are negative values. In order to implement the exponential approximation, a small area of hardware is occupied as only several adders and shifters will be used.

2) PVU: this unit is responsible for calculating $-\|x - y\|^2 \times \gamma$, where $x = (x_1, x_2, \ldots, x_n)$ represents a test vector, and $y = (y_1, y_2, \ldots, y_n)$ denotes a support vector. Norm squares are computed by multipliers and an adder tree is applied to sum these square values. As the parameter $\gamma$ is a power of two, remaining operations can be completed with a shifter and an adder to obtain the target value.

![Diagram](image_url)

**Fig. 3.** The detailed implementation of the RBF-based SVM processing unit.

### 4 Experiment

To evaluate the performance of our classification model with filter features, the MIT-BIH arrhythmia database [11] is selected and it is a collection of 48 ECG recordings which are digitized at 360 Hz. The four paced recordings are filtered out of database according to ANSI/AAMI EC57: 1998 standard [8]. In this letter, the remaining 44 records are divided following the database division in [12]. Heartbeat segments are obtained by segmenting these ECG signals using the annotations of QRS location. The two types of features, morphological-based features and time-based features, are extracted from ECG signals to represent heartbeat segments. Wavelet transform is used as a morphological feature extraction method as it provides characterizations in both temporal and frequency domains. Annotated R peak locations are used to form windows of ECG beats with length 109 samples and coefficients obtained from wavelet transform at level 4 within the window are extracted as the morphological features for each beat [12]. Time-based features including previous RR features and post RR features are obtained to characterize the dynamic information of the heartbeats. The previous RR feature is the interval...
between a given R peak and its previous R peak, while the post RR feature is calculated between a given R peak and its following R peak. Besides, principal component analysis is applied to reduce the dimensionality of morphological-based features and balance the importance between the two types of features. Four principal components are selected to retain approximately 90% of the variance. Finally, each heartbeat segment is represented with six mixed features.

The classification model is trained based on GRM using the feature array extracted from the MIT-BIH arrhythmia database aforementioned. The classification performance and results compared with three state-of-art methods are listed in Table I and Table II. Results in the tables shows that our method can exhibit comparable performance with other methods.

The proposed recognition architecture based on the obtained classification model is designed in the Verilog HDL and synthesized using a commercial 40-nm CMOS standard cell library. The on-chip memory is generated by the corresponding SRAM compiler in 40-nm process. Classification parameters of classifiers are stored in the generated SRAM and the size of SRAM is selected according to the bit-width and number of parameters. In our heartbeat classifiers, each support vector contains six features which can be represented with 16-bit fix-point and the corresponding Lagrange multiplier is represented with 32-bit fix-point. Hence the bit-width of a support vector combined with the corresponding coefficient is 128. Besides, the support vector number of high-accuracy classifier1 and high-accuracy classifier2 is slightly less than 512 and 256, respectively. Therefore, SRAM with 12 kB size is chosen to store the classification parameters. We follow the typical ASIC design flow to perform the logic synthesis, floor-plan, placement, and routing. Parasitic extraction is done after the layout generation. Three designs, architecture with latch-based low-complexity models, architecture with memory-based low-complexity models, and architecture without low-complexity models, are implemented to evaluate optimizations on energy consumption. Fig. 4 shows

| Table I. The detailed results of the proposed method. |
|-------------------------------------------------------|
| Actual Label | Predicted Label | N class | V' class | S class | Total Number |
|---------------|-----------------|---------|---------|---------|-------------|
| N | 42341 | 199 | 1637 | 44177 |
| V' | 157 | 2837 | 612 | 3606 |
| S | 90 | 99 | 1645 | 1834 |

| Table II. The comparison results between the proposed method and three algorithms from the literature. |
|--------------------------------------------------|
| Method | N class | S class | V' class |
|--------|---------|---------|---------|
| [12] | 88.64% (39157/44177) | 65.38% (1199/1834) | 74.88% (2700/3606) |
| [9] | 94.96% (41950/44177) | 77.54% (1422/1834) | **80.73% (2911/3606)** |
| [2] | 81.21% (40963/50439) | 55.73% (1440/2584) | 72.85% (4744/6512) |
| The proposed | **95.84% (42341/44177)** | **89.69% (1645/1834)** | **78.67% (2837/3606)** |
Table III. The comparison results of hardware implementation among three designs at 1 MHz.

| Parameter          | Latch-based architecture | Memory-based architecture | Without Filter |
|--------------------|--------------------------|---------------------------|----------------|
|                    | N            | V'           | S         | N            | V'           | S         |
| Average Energy (nJ)| 10.13        | 40.06        | 48.95     | 101.08       | 447.92       | 562.5     |
| Average Speed (cycle)| 48.95       | 101.08       | 14.33     | 59.93        | 102.39       | 567.0     |

Ratio of three classes in terms of predicting database:

\[N \text{ class} : V' \text{ class} : S \text{ class} = 85.83\% : 6.32\% : 7.85\%\]

In the first stage, 79.89\% samples from N class can be filtered correctly by the filter.
In the second stage, 70.51\% samples from V' class can be filtered correctly by the filter.
Latch-based architecture: \(A_{\text{energy}} = 15.07 \text{ (nJ)}\), \(A_{\text{speed}} = 159.22 \text{ (cycle)}\).
Memory-based architecture: \(A_{\text{energy}} = 20.19 \text{ (nJ)}\), \(A_{\text{speed}} = 160.92 \text{ (cycle)}\).
Without Filter: \(A_{\text{energy}} = 31.54 \text{ (nJ)}\), \(A_{\text{speed}} = 372.09 \text{ (cycle)}\).

the energy consumption per classification for three designs at different frequency, where the energy consumption of different classes using low-complexity models is calculated in terms of Eq. (5). The maximum frequency of the proposed design is 100 MHz. From the figure, the higher frequency is, the less energy consumes for per classification. However, the advantage of energy saving brought by latch-based design becomes small gradually. Although operation frequency (sub-kHz) is enough for heartbeat classification processing, the frequency at 1 MHz for both memory and processing unit is chosen as operating frequency in our design for energy consideration. Average energy and average speed, as the general criteria in this letter, are calculated according to Eq. (5)–(8) for performance comparison among different designs.

\[E_{\text{per-class}} = P_{\text{filter}} \times F_{\text{energy}} + P_{\text{classifier}} \times C_{\text{energy}}\]  
\[A_{\text{energy}} = P_{N,\text{class}} \times E_{N,\text{class}} + P_{V',\text{class}} \times E_{V',\text{class}} + P_{S,\text{class}} \times E_{S,\text{class}}\]  
\[S_{\text{per-class}} = P_{\text{filter}} \times F_{\text{speed}} + P_{\text{classifier}} \times C_{\text{speed}}\]  
\[A_{\text{speed}} = P_{N,\text{class}} \times S_{N,\text{class}} + P_{V',\text{class}} \times S_{V',\text{class}} + P_{S,\text{class}} \times S_{S,\text{class}}\]

where \(E_{\text{per-class}}\) and \(S_{\text{per-class}}\) represent energy consumption and classification speed per class, \(A_{\text{energy}}\) and \(A_{\text{speed}}\) mean average energy consumption and classification speed of the recognition architecture, \(P_{\text{filter}}\) and \(P_{\text{classifier}}\) are the percent of filtered samples and the remained separately, and \(F_{\text{energy}}\) and \(C_{\text{energy}}\) indicate the energy consumption of the model and the classifier, respectively. Detailed information about performance of hardware architecture is listed in the Table III, which shows our proposed architecture has advantages in the classification speed and energy consumption. Fig. 5 shows the layout along with a summary table, which shows latch-based memory and SRAM for classification parameters are area-consumption. Although latched-based memory for low-complexity models will increase area, it is beneficial for energy saving.
5 Conclusion

This letter proposes an energy-efficient architecture for heartbeat classification. A SVM-based granular resampling method is put forward to obtain a robust classification model which has a low-complexity model to filter most easy-to-learn heartbeats. We implement different kinds of hardware designs which include architecture with filter features and without filter features based on the obtained classification model for the performance comparison. Architecture optimizations including memory segmentation and time domain reuse are adopted to reduce energy consumption and resources, respectively. The designs are synthesized with a 40-nm CMOS technology and the entire layouts are generated for post-layout analysis. The proposed recognition design with filter features occupies 0.134 mm$^2$ area and consumes average 15.07 nJ/classification at operating frequency of 1 MHz and from an operating voltage of 1 V. Results show that the design can save average energy and speed classification up by 52.22% and 57.21% per classification.
compared with non-filter-based architecture. In addition, smaller memories replaced by latch-based architecture reduce read energy of parameter memory by 25.36%.

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