Low-Resolution Massive MIMO Under Hardware Power Consumption Constraints

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Abstract—We consider a fully digital massive multiple-input multiple-output architecture with low-resolution analog-to-digital/digital-to-analog converters (ADCs/DACs) at the base station (BS) and analyze the performance trade-off between the number of BS antennas, the resolution of the ADCs/DACs, and the bandwidth. Assuming a hardware power consumption constraint, we determine the relationship between these design parameters by using a realistic model for the power consumption of the ADCs/DACs and the radio frequency chains. Considering uplink pilot-aided channel estimation, we build on the Bussgang decomposition to derive tractable expressions for uplink and downlink ergodic achievable sum rates. Numerical results show that the ergodic performance is boosted when many BS antennas with very low resolution (i.e., 2 to 3 bits) are adopted in both the uplink and the downlink.

I. INTRODUCTION

To support the exponential growth in mobile data traffic, beyond-5G wireless systems are expected to exploit the large amount of bandwidth available in the sub-THz band (0.1–1 THz) [1]. In this context, fully digital architectures allow one to realize the full potential of massive multiple-input multiple-output (MIMO) by enabling highly flexible beamforming and large-scale spatial multiplexing. In fully digital massive MIMO, each base station (BS) antenna is connected to a radio-frequency (RF) chain that includes a pair of power-hungry analog-to-digital/digital-to-analog converters (ADCs/DACs). In this setting, the power consumption of each ADC/DAC scales linearly with the sampling rate and exponentially with the number of resolution bits [2], [3]. Hence, adopting low-resolution ADCs/DACs (even down to 1-bit [4], [5]) represents a necessary step towards the energy-efficient implementation of fully digital massive MIMO arrays comprising hundreds or thousands of antennas.

In this paper, we analyze the performance trade-off between the number of BS antennas, the resolution of the ADCs/DACs, and the bandwidth under a hardware power consumption constraint. The relationship between these design parameters is determined by means of a realistic model for the power consumption of the ADCs/DACs and the RF chains. In this setting, a lower resolution of the ADCs/DACs allows one to increase the number of BS antennas (and, consequently, the array gain and the spatial resolution) at the expense of a more significant quantization distortion during both the channel estimation and the uplink/downlink data transmission. To analyze this trade-off, we derive tractable expressions for uplink and downlink ergodic achievable sum rates with imperfect channel state information (CSI). Leveraging the Bussgang decomposition [6], we take into account the quantization distortion in the uplink/downlink data transmission as well as in the combining and precoding matrices, which are based on quantized channel estimates. To further simplify the resulting sum rate expressions, we assume that the BS adopts maximum ratio combining (MRC) in the uplink and maximum ratio transmission (MRT) in the downlink. In the evaluated scenarios, the most energy-efficient configurations consist of many antennas with 2 to 3 resolution bits in both the uplink and the downlink. A related study was presented in [7], [8], where the uplink and downlink outage rates were analyzed in the presence of a fronthaul constraint.

II. SYSTEM MODEL

We consider a massive MIMO system where a BS with $M$ antennas and low-resolution ADCs/DACs serves $K$ single-antenna user equipments (UEs) with infinite-resolution ADCs/DACs. We use $\mathbf{H} \doteq [\mathbf{h}_1, \ldots, \mathbf{h}_K] \in \mathbb{C}^{M \times K}$ to denote the uplink channel matrix.1 Each BS antenna is connected to two $b$-bit ADCs in the uplink (resp. to two $b$-bit DACs in the downlink), one for the in-phase and one for the quadrature component of the receive (resp. transmit) signal. Each ADC/DAC in the uplink/downlink consists of a $b$-bit quantizer characterized by a set of $2^b + 1$ quantization thresholds $T_b \doteq \{t_{n}\}_{n=0}^{2^b}$, with $-\infty = t_0 < \ldots < t_{2^b} = \infty$, and by a set of $2^b$ quantization labels $L_b \doteq \{\ell_n\}_{n=0}^{2^b-1}$, with $\ell_n \in (t_n, t_{n+1}]$. In this setting, we introduce the quantization function $Q_b(\cdot)$ such that, for $s \in \mathbb{C}$, $Q_b(s) = \ell_i + j \ell_k$ if $\text{Re}[s] \in (t_i, t_{i+1}]$ and $\text{Im}[s] \in (t_k, t_{k+1}]$; for a matrix or vector input, $Q_b(\cdot)$ is applied entrywise.

We assume that the number of BS antennas, the resolution of the ADCs/DACs, and the bandwidth are related through a hardware power consumption constraint. Let $P_{\text{hw}}$ be the maximum hardware power consumption allowed at the BS and let $P_{\text{ADC}}(b, B)$ and $P_{\text{DAC}}(b, B)$ denote the power consumed by a hardware power consumption constraint. Let $P_{\text{hw}}$ be the maximum hardware power consumption allowed at the BS and let $P_{\text{ADC}}(b, B)$ and $P_{\text{DAC}}(b, B)$ denote the power consumed by a hardware power consumption constraint. Let $P_{\text{hw}}$ be the maximum hardware power consumption allowed at the BS and let $P_{\text{ADC}}(b, B)$ and $P_{\text{DAC}}(b, B)$ denote the power consumed by a hardware power consumption constraint. Let $P_{\text{hw}}$ be the maximum hardware power consumption allowed at the BS and let $P_{\text{ADC}}(b, B)$ and $P_{\text{DAC}}(b, B)$ denote the power consumed by a hardware power consumption constraint. Let $P_{\text{hw}}$ be the maximum hardware power consumption allowed at the BS and let $P_{\text{ADC}}(b, B)$ and $P_{\text{DAC}}(b, B)$ denote the power consumed by a hardware power consumption constraint. Let $P_{\text{hw}}$ be the maximum hardware power consumption allowed at the BS and let $P_{\text{ADC}}(b, B)$ and $P_{\text{DAC}}(b, B)$ denote the power consumed by a hardware power consumption constraint. Let $P_{\text{hw}}$ be the maximum hardware power consumption allowed at the BS and let $P_{\text{ADC}}(b, B)$ and $P_{\text{DAC}}(b, B)$ denote the power consumed by a hardware power consumption constraint. Let $P_{\text{hw}}$ be the maximum hardware power consumption allowed at the BS and let $P_{\text{ADC}}(b, B)$ and $P_{\text{DAC}}(b, B)$ denote the power consumed by a hardware power consumption constraint. Let $P_{\text{hw}}$ be the maximum hardware power consumption allowed at the BS and let $P_{\text{ADC}}(b, B)$ and $P_{\text{DAC}}(b, B)$ denote the power consumed by a hardware power consumption constraint. Let $P_{\text{hw}}$ be the maximum hardware power consumption allowed at the BS and let $P_{\text{ADC}}(b, B)$ and $P_{\text{DAC}}(b, B)$ denote the power consumed by a hardware power consumption constraint. Let $P_{\text{hw}}$ be the maximum hardware power consumption allowed at the BS and let $P_{\text{ADC}}(b, B)$ and $P_{\text{DAC}}(b, B)$ denote the power consumed by a hardware power consumption constraint. Let $P_{\text{hw}}$ be the maximum hardware power consumption allowed at the BS and let $P_{\text{ADC}}(b, B)$ and $P_{\text{DAC}}(b, B)$ denote the power consumed by a hardware power consumption constraint. Let $P_{\text{hw}}$ be the maximum hardware power consumption allowed at the BS and let $P_{\text{ADC}}(b, B)$ and $P_{\text{DAC}}(b, B)$ denote the power consumed by a hardware power consumption constraint. Let $P_{\text{hw}}$ be the maximum hardware power consumption allowed at the BS and let $P_{\text{ADC}}(b, B)$ and $P_{\text{DAC}}(b, B)$ denote the power consumed by a hardware power consumption constraint. Let $P_{\text{hw}}$ be the maximum hardware power consumption allowed at the BS and let $P_{\text{ADC}}(b, B)$ and $P_{\text{DAC}}(b, B)$ denote the power consumed by a hardware power consumption constraint. Let $P_{\text{hw}}$ be the maximum hardware power consumption allowed at the BS and let $P_{\text{ADC}}(b, B)$ and $P_{\text{DAC}}(b, B)$ denote the power consumed by a hardware power consumption constraint. Let $P_{\text{hw}}$ be the maximum hardware power consumption allowed at the BS and let $P_{\text{ADC}}(b, B)$ and $P_{\text{DAC}}(b, B)$ denote the power consumed by a hardware power consumption constraint. Let $P_{\text{hw}}$ be the maximum hardware power consumption allowed at the BS and let $P_{\text{ADC}}(b, B)$ and $P_{\text{DAC}}(b, B)$ denote the power consumed by a hardware power consumption constraint.

The work of I. Atzeni was supported by the Marie Skłodowska-Curie Actions (MSCA-IF 897938 DELIGHT). The work of A. Tölli was supported by the Academy of Finland (318927 6Genesis Flagship and 319059 CCCWEE). The work of G. Durisi was supported by the Wallenberg AI, Autonomous Systems, and Software Program.

1We will consider the special case of i.i.d. Rayleigh fading channels in Section IV.
by each ADC and DAC, respectively, where $B$ represents the bandwidth. Both $P_{\text{ADC}}(b, B)$ and $P_{\text{DAC}}(b, B)$ grow linearly with the bandwidth and exponentially with the number of resolution bits, as in the power consumption model adopted in Section V-A. Hence, the number of BS antennas available in the uplink/downlink for a given resolution of the ADCs/DACs $b$ and a given bandwidth $B$ is

$$M = \begin{cases} P_{\text{ADC}}(b, B) & \text{(uplink)}, \\ P_{\text{DAC}}(b, B) & \text{(downlink)} \end{cases}$$

where $P_{\text{ADC}}$ denotes the power consumed by each RF chain. Note that, in the following, $M$ assumes different values for the uplink and the downlink according to (1).

### III. Ergodic Performance

In this section, we provide expressions for uplink and downlink ergodic achievable sum rates with $b$-bit ADCs and DACs, respectively, for general combining and precoding matrices. These are extended in Section IV to include the effect of imperfect CSI arising from the uplink pilot-aided channel estimation with low-resolution ADCs.

#### A. Uplink Ergodic Achievable Sum Rate

Let $x^u \sim \mathcal{CN}(0, I_K)$ denote the uplink transmit symbol vector. At the BS, the normalized input signal at the ADCs is given by

$$y^u \triangleq \sqrt{\rho_{\text{BS}}}Hx^u + z^u \in \mathbb{C}^{M \times 1}$$

where $z^u \sim \mathcal{CN}(0, I_M)$ is the additive white Gaussian noise (AWGN) vector; since the AWGN power is normalized to one, $\rho_{\text{BS}}$ represents the signal-to-noise ratio (SNR) at the BS. Then, at the output of the ADCs, we have

$$r^u \triangleq Q_b(y^u) \in \mathbb{C}^{M \times 1}.$$  

We assume that the BS obtains a soft estimate of $x^u$ as

$$\hat{x}^u \triangleq [\hat{x}_1, \ldots, \hat{x}_K]^T$$

where $V \triangleq [v_1, \ldots, v_K] \in \mathbb{C}^{M \times K}$ is the combining matrix.

Using the Bussgang decomposition, we can write (3) as

$$r^u = G_b^u y^u + d^u$$

where $d^u \in \mathbb{C}^{M \times 1}$ is the zero-mean, non-Gaussian quantization distortion vector that is uncorrelated with $y^u$ and has covariance matrix $C_d^u \triangleq E[d^u(d^u)^H] \in \mathbb{C}^{M \times M}$. In addition, $G_b^u \in \mathbb{C}^{M \times M}$ is a diagonal matrix obtained by (see, e.g., [2])

$$G_b^u \triangleq \pi \text{Diag}(C_y^u)^{-1} \sum_{n=0}^{2^b-1} f_n \left( -t_n \text{Diag}(C_y^u)^{-1} \right)$$

with $C_y^u \triangleq E[(y^u(y^u)^H)] \in \mathbb{C}^{M \times M}$. Substituting (6) into (5), we obtain

$$\hat{x}^u = V^H(G_b^u(\sqrt{\rho_{\text{BS}}}Hx^u + z^u) + d^u).$$

To derive an easy-to-evaluate expression for an uplink ergodic achievable sum rate, we use the use-and-then-forget bound [9, Thm. 4.4]. Specifically, we write the $k$th entry of (8) as

$$\hat{x}_{k}^u = \sqrt{\rho_{\text{BS}}}E[v_k^u G_b^u h_k] x_k^u + \sqrt{\rho_{\text{BS}}} (v_k^u G_b^u h_k - E[v_k^u G_b^u h_k]) x_k^u + \sqrt{\rho_{\text{BS}}} \sum_{i \neq k} v_k^u G_b^u h_i x_i + v_k^u G_b^u z_k^u + v_k^u d^u.$$  

(9)

Treating $d^u$ as Gaussian noise with covariance matrix $C_d^u$ and assuming that the BS is aware of the value of $E[v_k^u G_b^u h_k]$, we obtain the effective uplink signal-to-interference-plus-noise-and-distortion ratio (SINDR) of UE $k$ as in (10) at the top of the next page. Finally, an uplink ergodic achievable sum rate is given by

$$R^u \triangleq B \sum_{k=1}^{K} \log_2(1 + \gamma_k^u).$$

(11)

#### B. Downlink Ergodic Achievable Sum Rate

Let $x^d \sim \mathcal{CN}(0, I_K)$ denote the downlink transmit symbol vector. At the BS, the output signal at the DACs is given by

$$r^d \triangleq Q_b(Wx^d) \in \mathbb{C}^{M \times 1}$$

where $W \triangleq [w_1, \ldots, w_K] \in \mathbb{C}^{M \times K}$ is the precoding matrix with $E[WW^H] = 1$. Then, the normalized receive signal at the UEs is given by

$$y^d \triangleq [y_1^d, \ldots, y_K^d]^T$$

where $y^d \sim \mathcal{CN}(0, I_K)$ is the AWGN vector; since the AWGN power is normalized to one, $\rho_{\text{BS}}$ represents the SNR at the UEs.

Using the Bussgang decomposition, we can write (12) as

$$r^d = G_b^d Wx^d + d^d$$

where $d^d \in \mathbb{C}^{M \times 1}$ is the zero-mean, non-Gaussian quantization distortion vector that is uncorrelated with $Wx^d$ and has covariance matrix $C_d^d \triangleq E[d^d(d^d)^H] \in \mathbb{C}^{M \times M}$. In addition, $G_b^d \in \mathbb{C}^{M \times M}$ is a diagonal matrix obtained by replacing $C_y^u$ in (7) with $E[WW^H]$. Substituting (15) into (14), we obtain

$$y^d = \sqrt{\rho_{\text{BS}}}H^H(G_b^d Wx^d + d^d) + z^d.$$  

(16)

To derive an easy-to-evaluate expression for a downlink ergodic achievable sum rate, we use the channel hardening bound [9, Thm. 4.6]. Specifically, we write the $k$th entry of (16) as

$$y_{k}^d = \sqrt{\rho_{\text{BS}}}E[h_k^H G_b^d w_k] x_k^d + \sqrt{\rho_{\text{BS}}} h_k^H G_b^d w_k - E[h_k^H G_b^d w_k]) x_k^d + \sqrt{\rho_{\text{BS}}} \sum_{i \neq k} h_k^H G_b^d w_i x_i + \sqrt{\rho_{\text{BS}}} h_k^H d^d + z_{k}^d.$$  

(17)

Treating $d^d$ as Gaussian noise with covariance matrix $C_d^d$ and assuming that UE $k$ is aware of the value of $E[h_k^H G_b^d w_k]$, we obtain the effective downlink SINDR of UE $k$ as in (18) at the top of the next page. Finally, a downlink ergodic achievable sum rate is given by

$$R^d \triangleq B \sum_{k=1}^{K} \log_2(1 + \gamma_k^d).$$

(19)

#### IV. Ergodic Performance with Imperfect CSI

The ergodic achievable sum rate expressions derived in Section III take into account the quantization distortion in the uplink/downlink data transmission. In practice, however, the quantization also affects the combining and precoding matrices, which are based on quantized channel estimates. In this section, we include the effect of imperfect CSI arising from the uplink...
We assume that the BS obtains an estimate of and the combining vector corresponding to UE \( k \). Moreover, we introduce the following preliminary definitions:

- \( G \) is the channel covariance matrix.
- \( A_k \) is the combining vector corresponding to UE \( k \).
- \( K \) is the total number of UEs.
- \( M \) is the number of pilot sequences.
- \( b_{ul} \) is the number of bits per symbol.
- \( \gamma_{tr} \) is the signal-to-noise ratio (SNR).
- \( \rho_{ul} \) is the signal-to-interference ratio (SIR).
- \( \rho_{bs} \) is the signal-to-noise-and-interference ratio (SINR).
- \( \gamma_k \) is the SNR corresponding to UE \( k \).

\( \gamma_k \) can be defined as
\[
\gamma_k = \frac{\rho_{bs} |E[v_k^H G_b^* h_k]|^2}{\rho_{bs} \sum_{i=1}^{K} E[|v_k^H G_b^* h_i|^2] - \rho_{bs} E[|v_k^H G_b^* h_k|^2] + E[|G_b^* v_k|^2] + E[v_k^H C_d^* v_k]},
\]
(10)

\( \gamma_k \) can be defined as
\[
\gamma_k = \frac{\rho_{bs} |E[h_k^H G_b^* w_k]|^2}{\rho_{bs} \sum_{i=1}^{K} E[|h_k^H G_b^* w_i|^2] - \rho_{bs} |E[h_k^H G_b^* w_k]|^2 + \rho_{bs} |E[h_k^H C_d^* w_k]|^2 + \rho_{bs} |E[h_k^H C_d^* h_k]|^2 + 1},
\]
(18)

Pilot-aided channel estimation with low-resolution ADCs. To further simplify the resulting sum rate expressions, we consider the special case of i.i.d. Rayleigh fading channels, where the entries of \( H \) are independent \( CN(0, 1) \) random variables, and we assume that the BS adopts MRC in the uplink and MRT in the downlink. The full derivations are omitted due to the space limitations and will be included in an extended version of this paper, which will also consider correlated channels.

\( A. \text{ Channel Estimation with } b\text{-Bit ADCs} \)

During the uplink pilot-aided channel estimation phase, the UEs simultaneously transmit their pilots of length \( \tau \). Let \( P = [p_1, \ldots, p_K] \in \mathbb{C}^{\tau \times K} \) denote the pilot matrix, where \( p_k \in \mathbb{C}^{\tau \times 1} \) represents the pilot sequence assigned to UE \( k \). In this context, we assume \( \tau \geq K \) and that the pilot sequences are orthogonal, so that \( P^H P = \tau I_K \). Furthermore, we define \( \tilde{P} = p_k \otimes I_M \in \mathbb{C}^{M \tau \times M} \) and \( \tilde{P} = [P_1, \ldots, P_K] \in \mathbb{C}^{M \tau \times MK} \).

At the BS, the normalized input signal at the ADCs is given by
\[
y^w = \frac{1}{\sqrt{\rho_{bs} \tau}} y^* + z^* \in \mathbb{C}^{MK \times 1}.
\]
(20)

with \( h = \text{vec}(H) \in \mathbb{C}^{MK \times 1} \) and where \( z^w \sim \mathcal{CN}(0, I_{M \tau}) \) is the AWGN vector. Then, at the output of the ADCs, we have
\[
r^w = Q_b(y^w) \in \mathbb{C}^{MK \times 1}.
\]
(21)

We assume that the BS obtains an estimate of \( h \) (up to a scaling factor) as
\[
\hat{h} = \frac{1}{\sqrt{\rho_{bs} \tau}} \tilde{P}^H y^* \in \mathbb{C}^{MK \times 1}.
\]
(22)

Using the Bussgang decomposition, we can write (21) as
\[
r^w = G_b^w y^w + d^w,
\]
(23)

where \( d^w \in \mathbb{C}^{MK \times 1} \) is the zero-mean, non-Gaussian quantization distortion vector that is uncorrelated with \( y^w \) and has covariance matrix \( C_d^w = E[d^w(d^w)^H] \in \mathbb{C}^{MK \times MK} \). In addition, \( G_b^w \in \mathbb{C}^{MK \times MK} \) is a diagonal matrix obtained by replacing \( C_d^w \) in (7) with \( C_y^w = E[y^w(y^w)^H] \in \mathbb{C}^{MK \times MK} \). Substituting (23) into (22), we obtain
\[
\hat{h} = \frac{1}{\tau} \left( \tilde{P}^H G_b^w \tilde{P} y^* + \frac{1}{\rho_{bs}} \tilde{P}^H G_b^w z^w + \frac{1}{\sqrt{\rho_{bs}}} \tilde{P}^H d^w \right).
\]
(24)

\( B. \text{ Uplink Ergodic Achievable Sum Rate with MRC} \)

Assuming that MRC is used at the BS, we have \( V = G_b^w H \) and the combining vector corresponding to UE \( k \) is given by
\[
v_k = \frac{1}{\tau} G_b^w \left( \tilde{P}_k^H G_b^w \tilde{P}_k y^* + \frac{1}{\rho_{bs}} \tilde{P}_k^H G_b^w z^w + \frac{1}{\sqrt{\rho_{bs}}} \tilde{P}_k^H d^w \right).
\]
(25)

Under the i.i.d. Rayleigh fading assumption, we have \( C_y^w = \rho_{bs} (K + 1) I_M \) and \( C_d^w = \rho_{bs} \tilde{P} \tilde{P}^H + I_{M \tau} \) (with \( \text{Diag}(C_y^w) = \rho_{bs} (K + 1) I_M \)). Then, we can express each expectation term in (18) as
\[
\frac{1}{\rho_{bs} \tau} \sum_{k=1}^{K} \text{Diag}(G_b^w)^2 z^w_k^2 + \frac{1}{\rho_{bs} \tau} \text{Diag}(G_b^w)^2 d^w_k^2
\]
(36)

\[
\frac{1}{\rho_{bs} \tau} \sum_{k=1}^{K} \text{Diag}(G_b^w)^2 t^w_k^2
\]
(37)

This allows us to write the effective uplink SINDR with imperfect CSI and MRC as in (33) at the top of the next page. Finally, we substitute the resulting expression into (11) to obtain an uplink ergodic achievable sum rate.

\( C. \text{ Downlink Ergodic Achievable Sum Rate with MRT} \)

Assuming that MRT is used at the BS, we have \( W = \frac{1}{\sqrt{\delta}} H \), with \( \delta = E[|h|^2] \) and \( H = \text{vec}(H) \in \mathbb{C}^{MK \times 1} \), and the precoding vector corresponding to UE \( k \) is given by
\[
w_k = \frac{1}{\sqrt{\tau}} \left( \tilde{P}_k^H G_b^w \tilde{P}_k y^* + \frac{1}{\rho_{bs}} \tilde{P}_k^H G_b^w z^w + \frac{1}{\sqrt{\rho_{bs}}} \tilde{P}_k^H d^w \right).
\]
(34)

Under the i.i.d. Rayleigh fading assumption, we have
\[
\text{Diag}(W W^H) = \frac{1}{\delta} K (1 + \frac{1}{\rho_{bs} \tau}) (G_b^w)^2 I_M + \frac{1}{\rho_{bs} \tau} \sum_{k=1}^{K} \text{Diag}(G_b^w)^2 \tilde{P}_k^H C_d^w \tilde{P}_k
\]
(35)

(26)

\( B_k \triangleq (C_d^w \tilde{P}_k \tilde{P}_k^H). \)

Then, we can express each expectation term in (10) as
\[
E[|v_k^H G_b^* h_k|^2] = G_b^w (G_b^w)^* M,
\]
(28)

\[
E[|v_k^H G_b^* h_i|^2] = E[|G_b^* v_k|^2]
\]
(29)

\[
= \left( 1 + \frac{1}{\rho_{bs} \tau} \right) (G_b^w)^2 (G_b^w)^* M + \frac{1}{\rho_{bs} \tau} (G_b^w)^2 A_k \quad (i \neq k),
\]
(30)

\[
E[|v_k^H G_b^* w_k|^2] = \left( M + 1 + \frac{1}{\rho_{bs} \tau} \right) (G_b^w)^2 (G_b^w)^* M + \frac{1}{\rho_{bs} \tau} (G_b^w)^2 A_k,
\]
(31)

\[
E[|v_k^H C_d v_k|] = \left( 1 + \frac{1}{\rho_{bs} \tau} \right) (G_b^w)^2 (G_b^w)^* \text{tr}(C_d) + \frac{1}{\rho_{bs} \tau} (G_b^w)^2 B_k.
\]
(32)

\( ^2 \text{We use } \text{vec}(\cdot) \text{ as the inverse of the } \text{vec}(\cdot) \text{ operator.} \)
This allows us to write the effective downlink SINDR with all the UEs are placed at the same distance from the BS, with BS and UE \( k \) the pathloss between the BS and UE \( k \), \( \rho \) transmit power at the UEs (resp. at the BS), the pathloss, and BS in the uplink (resp. at the UEs in the downlink) includes the bandwidth under hardware power consumption constraints. Finally, we substitute the resulting expression into (19) to obtain a downlink ergodic achievable sum rate.

\[
\gamma_{ul}^{\mathfrak{a}}(R_{\text{ul}}) = \frac{\rho_{\text{ns}}(G_b^2)^2(G_d^4)^2M^2}{\rho_{\text{ns}}(G_b^2)^2(G_d^4)^2M^2 + (\rho_{\text{ns}} + 1)(G_b^2)^2A_k + \rho_{\text{ns}}(1 + \frac{1}{\rho_{\text{ns}}})\delta_{\text{d}}^\mathfrak{a} + \frac{1}{\rho_{\text{ns}}}}. 
\]

(33)

\[
\gamma_{dl}^{\mathfrak{a}}(R_{\text{dl}}) = \frac{\rho_{\text{ul}}(G_b^2)^2(G_d^4)^2M^2}{\rho_{\text{ul}}(G_b^2)^2(G_d^4)^2M^2 + \rho_{\text{ul}}(1 + \frac{1}{\rho_{\text{ul}}})\delta_{\text{d}}^\mathfrak{a} + \frac{1}{\rho_{\text{ul}}}}. 
\]

(40)

This allows us to write the effective downlink SINDR with imperfect CSI and MRT as in (40) at the top of the page. We now evaluate the performance trade-off between the number of BS antennas, the resolution of the ADCs/DACs, and assume that \( \rho_{\text{ul}} K + 1 \). The thermal noise power at both the BS and the UEs is given by \( \sigma_{n}^2 = \sigma_{n,\text{ul}}^2 = \nu - 174 + 10 \log_{10}(B) \) [dBm], where \( \nu = 13 \) dB is the noise figure; note that a larger bandwidth corresponds to an increased thermal noise power. Lastly, the covariance matrices of the quantization distortion \( C_{\text{d}}^\mathfrak{a} \), \( C_{\text{d}} \), and \( C_{\text{d}}^\mathfrak{a} \) are computed via Monte Carlo simulations with \( 10^6 \) independent channel realizations.

### V. Numerical Results and Discussion

We now evaluate the performance trade-off between the number of BS antennas, the resolution of the ADCs/DACs, and the bandwidth under hardware power consumption constraints. To do so, we use the uplink and downlink ergodic achievable sum rate expressions derived in Sections IV-B and IV-C, respectively, which consider imperfect CSI and MRC/MRT. Throughout we disregard the channel estimation overhead, which can be readily included by multiplying (11) and (19) by \((1 - \frac{1}{T})\) for a given coherence time \( T \) (see, e.g., [9, Sec. 4]).

In the numerical results that follow, we consider \( K = 8 \) UEs and assume that \( \mathbf{P} \) comprises the first \( K \) columns of the \( \tau \)-dimensional discrete Fourier transform matrix. The SNR at the BS in the uplink (resp. the UEs in the downlink) includes the transmit power at the UEs (resp. at the BS), the pathloss, and the thermal noise power. Hence, we have \( \rho_{\text{ul}} = \frac{P_{\text{ul}}}{\beta_k} \), where \( P_{\text{ul}} = 20 \) dBm is the transmit power at the UEs, \( \beta_k \) represents the pathloss between the BS and UE \( k \), and \( \sigma_{n,\text{ul}}^2 \) is the thermal noise power at the BS. Likewise, we have \( \rho_{\text{ns}} = \frac{P_{\text{ns}}}{\sigma_{n,\text{ns}}^2} \), where \( P_{\text{ns}} = 30 \) dBm is the transmit power at the BS and \( \sigma_{n,\text{ns}}^2 \) is the thermal noise power at the UEs. The pathloss for each UE \( k \) is expressed as \( \beta_k = d_k^{\alpha} \), where \( d_k \) is the distance between the BS and UE \( k \) and \( \alpha = 4 \) is the pathloss exponent. For simplicity, all the UEs are placed at the same distance from the BS, with \( d_k = 100 \) m \( K \). The thermal noise power at both the BS and the UEs is given by \( \sigma_{n}^2 = \sigma_{n,\text{ul}}^2 = \nu - 174 + 10 \log_{10}(B) \) [dBm], where \( \nu = 13 \) dB is the noise figure; note that a larger bandwidth corresponds to an increased thermal noise power. Lastly, the covariance matrices of the quantization distortion \( C_{\text{d}}^\mathfrak{a} \), \( C_{\text{d}} \), and \( C_{\text{d}}^\mathfrak{a} \) are computed via Monte Carlo simulations with \( 10^6 \) independent channel realizations.

#### A. ADCs/DACs Setting

We assume that the quantization thresholds and labels for each \( b \)-bit ADC/DAC are computed following the same procedure as in [2]. For the ADCs, the Lloyd-Max algorithm is applied to the Gaussian distribution with variance \( \rho_{\text{ul}} K + 1 \), obtaining the thresholds \( T_k^b \) and a set of labels \( \tilde{L}_k^b = \{ \tilde{t}_n \}^b_{n=0} \). Then, we obtain the labels \( L_k = \zeta \tilde{L}_k^b \), with

\[
\zeta \equiv \eta^b \left( \sum_{n=0}^{b-1} \tilde{t}_n^2 \left( \Phi(\eta^{-1}|t_{n+1}|) - \Phi(\eta^{-1}|t_n|) \right) \right)^{-\frac{1}{2}}
\]

(41)

with \( \eta^b \equiv \sqrt{\frac{\rho_{\text{ul}} K + 1}{2}} \) and where \( \Phi(\cdot) \) denotes the cumulative distribution function of the standard Gaussian distribution. Note that such a rescaling of the labels ensures that the variance of each entry of \( r^b \) in (3) and \( r^a \) in (21) is \( \rho_{\text{ul}} K + 1 \). We follow a similar procedure for the DACs, where the Lloyd-Max algorithm is applied to the Gaussian distribution with variance \( \frac{1}{\eta^a} \) and \( \eta^a \) in (41) is replaced by \( \eta^a \equiv \frac{1}{\sqrt{2 M}} \).

To determine the number of BS antennas as a function of the resolution of the ADCs/DACs under a hardware power consumption constraint (see (1)), we use the following power consumption model for the ADCs and DACs [10]:

\[
P_{\text{axc}}(b, B) = 3V_{\text{dd}}^2 I_{\text{min}}(2B + f_{\text{cor}})10^{1.5256 - 4.838},
\]

(42)

\[
P_{\text{axc}}(b, B) = \frac{1}{2} V_{\text{dd}} f_0 (2^b - 1) + b C_p (2B + f_{\text{cor}}) V_{\text{dd}}^2
\]

(43)

3This approach minimizes the mean squared error between the non-quantized and quantized signal.
where $V_{dd} = 3\, \text{V}$, $L_{\text{min}} = 0.5\, \mu\text{m}$, $f_{\text{osc}} = 1\, \text{MHz}$, $I_0 = 10\, \mu\text{A}$, and $C_p = 1\, \text{pF}$ are chosen as in [10, App. E].

### B. Performance Evaluation

Let us assume $P_{\text{RF}} = 40\, \text{mW}$ in the uplink (as in [11]) and $P_{\text{RF}} = 10\, \text{mW}$ in the downlink. In Fig. 1 and Fig. 2, we plot the uplink and downlink ergodic achievable sum rates versus the resolution of the ADCs and DACs, respectively, with $B = 0.1\, \text{GHz}$ and where $P_{\text{RF}}$ is set such that it can supply 10 BS antennas with 10-bit ADCs/DACs at such bandwidth; note that the value of $M$ corresponding to each value of $b$ is detailed at the top of the figures. In general, lowering the resolution of the ADCs/DACs under a hardware power consumption constraint has a two-fold effect: on the one hand, the reduced power consumption allows one to increase the number of BS antennas, which enhances the array gain; on the other hand, the quantization distortion becomes more significant during both the channel estimation and the uplink/downlink data transmission. In the uplink, the ergodic performance is maximized with 2-bit ADCs for all the considered pilot lengths. Moreover, in the downlink, the best ergodic performance is achieved with 3-bit DACs for $\tau = 8$ and with 2-bit DACs for $\tau \geq 16$. In both the uplink and the downlink, the impact of imperfect CSI is more evident for lower resolutions and the extreme case of 1-bit ADCs is nearly optimal in the uplink when accurate channel estimates are available. In Fig. 3 and Fig. 4, we plot the uplink and downlink ergodic achievable sum rates versus the resolution of the ADCs and DACs, respectively, with different values of $B$ and where $P_{\text{RF}}$ is set such that it can supply 10 BS antennas with 10-bit ADCs/DACs at $B = 1\, \text{GHz}$. In the uplink, the ergodic performance is maximized with 2-bit ADCs for all the considered bandwidths. In the downlink, the optimal number of resolution bits increases with the bandwidth.

### VI. Conclusions

Considering a fully digital massive MIMO architecture with low-resolution ADCs/DACs at the BS, we derive tractable expressions for uplink and downlink ergodic achievable sum rates with imperfect CSI. These are used to analyzes the performance trade-off between the number of BS antennas, the resolution of the ADCs/DACs, and the bandwidth under a hardware power consumption constraint, where the relationship between these design parameters follows from a realistic model for the power consumption of the ADCs/DACs and the RF chains. In the evaluated scenarios, the most energy-efficient configurations feature 2 resolution bits in the uplink and 2 to 3 resolution bits in the downlink. Future work will consider correlated channels as well as power consumption models reflecting the latest advancements in ADC/DAC technologies.

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