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Nanostructured Germanium with >99% Absorption at 300–1600 nm Wavelengths

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Near-infrared (NIR) sensors find numerous applications within various industry fields, including optical communications and medical diagnostics. However, the state-of-the-art NIR sensors made of germanium (Ge) suffer from rather poor response, largely due to high reflection from the illuminated device surface. This work demonstrates a method to increase the sensitivity of Ge sensors by implementing nanostructures to the wafer surfaces. The absorbance of nanostructured Ge wafers is measured to be >99% in the whole UV–vis–NIR spectrum up to 1600 nm wavelength, which is a significant improvement to bare Ge wafers that reach absorption of only 63% in maximum. The process is shown to be capable of producing uniform nanostructures covering full 100 mm diameter substrates as well as wafers with etch mask openings of different sizes and shapes, which demonstrates its applicability to complementary metal oxide semiconductor (CMOS) sensor manufacturing. The results imply that nanostructured Ge has potential to revolutionize the sensitivity of Ge-based sensors.

NIR wavelength detection, silicon can be replaced by a smaller bandgap semiconductor, such as germanium (Ge), which can absorb radiation until ≈1600 nm. The selection of Ge would be favorable by its compatibility with complementary metal oxide semiconductor (CMOS) manufacturing lines and lower price compared to its alternatives, such as indium-gallium-arsenic (InGaAs).

It is well known, however, that reflection is one of the main factors that limits the response of Ge sensors. Indeed, such devices conventionally have highly reflective bare surfaces or only a single-layer antireflection (AR) coating optimized for NIR, which has very limited performance for other wavelengths. Moreover, texturing of Ge surfaces has remained challenging due to the lack of standard techniques, such as random pyramids or acidic texturing that are commonly used for silicon, for that purpose. The sensitivity of such devices can hence be greatly improved by enhancing the absorption of radiation. This could potentially be realized by generating a gradually changing refractive index from air to the substrate on the illuminated device surface. Instead of using multilayer AR coatings, an interesting alternative is to use structures that have dimensions similar to the wavelength of the detectable radiation. Such nanostructures have recently been applied to silicon-based photosensors, resulting in record-high sensitivity to UV and visible radiation. Application of similar nanostructures to germanium sensors could extend the superior sensitivity to the NIR region, and indeed, promising preliminary results on NIR sensors with a textured surface have recently been published.

As learnt from research on silicon nanostructures, which are often called black silicon (b-Si) due to the negligibly small reflectance in the visible range, the nanostructure morphology is critical for device performance. Indeed, nanostructures with porous or rough sidewalls or with high aspect ratio have resulted in excessive surface recombination despite surface passivation, whereas excellent performance has been achieved using nanostructures with smooth sidewalls and only modestly increased surface area. Germanium nanostructures have earlier been realized by nickel-catalyzed synthesis, electrochemical etching, and Bosch process-based dry etching, but the morphology of the surface structures has been non-optimized for optoelectronic applications. Additionally, previous
research has been performed only on small substrate pieces, which is impractical for the manufacturing of actual devices, especially since the surface area that is exposed to the etching chemistry may affect the process.\[24\]

The morphology and properties of nanostructured surfaces can be adjusted more accurately by reactive ion etching (RIE).\[25\] A preliminary study on RIE-fabricated Ge nanostructures has focused on a chlorine (Cl\(_2\))-based process.\[26\] However, Cl\(_2\) sets demanding requirements for the process equipment and gas handling due to its high toxicity.\[27\] Sulfur hexafluoride (SF\(_6\))-based processes, which are commonly used for etching of silicon,\[28\] have earlier been considered inapplicable for the fabrication of Ge nanostructures.\[29\] Moreover, the process developed in\[26\] relied on capacitively coupled plasma (CCP) alone. Such process always needs to balance between minimized reflectance and ion bombardment-induced surface damage.\[29\] An inductively coupled plasma (ICP) configuration would avoid the plasma-induced damage entirely.\[29\]

In this work, we demonstrate a reproducible SF\(_6\)-based ICP-RIE process for the fabrication of Ge nanostructures for NIR sensors. The process performance is tested over the whole 100 mm diameter substrate area as well as on wafers with etch mask openings of different sizes and shapes to mimic nanotexturing of actual sensors. We investigate the morphology of the nanostructures and study the uniformity of the nanostructure dimensions over the wafer area. Finally, the optical properties of nanostructured Ge are characterized by reflectance and absorbance measurements. The optical performance is compared to bare Ge wafers and b-Si to evaluate the potential of nanostructured Ge for boosting the detector performance.

The experiments were performed on gallium-doped (p-type) 100 mm diameter Ge wafers, which were 175 \(\mu\)m thick and had a resistivity of 0.01–0.05 \(\Omega\) cm. The polished wafer surface with (100) orientation was exposed to a SF\(_6\)-based ICP–RIE process (Oxford Instruments Plasmalab System 100) at \(-120\) °C for 15 min. The gas flows for SF\(_6\) and oxygen (O\(_2\)) were 25 and 33 sccm, respectively, while the chamber pressure was kept at 10 mTorr. The power of the ICP source was 1000 W, whereas the CCP power was set to as low as 4 W to avoid ion bombardment-induced damage. Another similar wafer was coated with a 22 nm thick atomic-layer-deposited (ALD) aluminum oxide (Al\(_2\)O\(_3\)) etch mask, which was patterned by photolithography and phosphoric acid-based commercial aluminum etchant (Honeywell PWS 80-164-6 (65)). Subsequently, nanostructures were fabricated on the opened areas using the same ICP–RIE process to mimic nanostructuring of sensor active areas.

The nanostructures were characterized by scanning electron microscopy (SEM, Zeiss Supra 40) and atomic force microscopy (AFM, NT-MDT NTEGRA) from different locations on the wafer. The amount of Ge consumed by the process in vertical direction was determined by profilometry (Dektak XT) as the step height of the opened areas on the patterned wafer. Finally, the optical properties of the nanostructured surfaces were evaluated by integrating sphere-based reflectance \((R)\) and transmittance \((T)\) measurements (Agilent Cary 5000). The absorbance of the wafers was derived from 1–R–T.

The developed nanostructuring process was first applied to bare Ge wafers. Figure 1a presents a photograph of a nanostructured 100 mm diameter wafer, which appears uniformly black to the naked eye. The deep black appearance over the whole wafer surface highlights the heavily reduced reflectance, at least for visible light, and demonstrates that the process is well suited for large substrate areas. The surface also retains its darkness when the wafer is tilted, as determined by the eye, which agrees with earlier reports on silicon nanostructures.\[13,30\] This property significantly enlarges the acceptance angle of radiation sensors compared to devices with a bare front surface or an AR coating.\[13,30\] The heavily reduced reflectance of nanostructured surfaces can be explained by the minuscule size of the nanostructures: they provide a gradual increase in refractive index from that of air to that of germanium, and hence, virtually eliminate the optical interface.\[21\]

Since actual devices typically allow texturing only on the active areas, and not below contacts, the nanostructuring process was applied also on Ge wafers protected by an etch mask with openings of various sizes and shapes. Figure 1b shows such wafer with several types of rectangle-shaped nanostructured areas, which all appear uniformly dark independent of their location on the wafer. The results demonstrate that the process is applicable for different areas exposed to the etching chemistry, which is not self-evident due to the loading effect in plasma etching.\[24\] but is a prerequisite for sensors manufacturing.
The density of fluorine radicals, and consequently, increased ICP power results in accelerated etching of germanium.\(^{[33]}\)

The nanostructure morphology is affected also by the energy and directionality of the ions that bombard the substrate, which are determined by the CCP power and chamber pressure. Increased CCP power and/or decreased pressure promote more directional etching, which results in dense arrays of sharp needle-shaped nanostructures with close to vertical sidewalls.\(^{[25]}\) However, too directional ion bombardment results in anisotropic etching of the substrate without nanostructure formation. Indeed, in our experiments the process described above with CCP power increased from 4 to 6 W resulted in a bright area in the middle of the wafer, indicating the lack of nanostructures. The depth of the nanostructures is affected by several process parameters, but it can be controlled most straightforwardly by the etching time. In our experiments, a 5 min shorter etch process resulted in smaller nanostructures and brighter appearance of the wafer.

The SEM images indicate that the morphology of the nanostructures could be well suitable for high-performance devices, where minimization of surface recombination is essential. More specifically, the needle-like structures have smooth sidewalls, which minimize the surface area, and consequently, recombination, while they yet efficiently reduce reflectance. Furthermore, the needles have moderate aspect ratio, i.e., height compared to their width, which suggests that they can be coated conformally by ALD thin films. Indeed, the nanostructure morphology is very similar to b-Si nanotexture obtained by similar methods, where excellent surface passivation has been achieved by ALD Al\(_2\)O\(_3\).\(^{[18]}\) leading to superior b-Si solar cells\(^{[39]}\) and photodiodes.\(^{[23]}\)

In addition to providing accurate control over the morphology, it is important that the nanostructuring process does not consume too large amount of Ge, which might affect the fabrication process and the operation of the sensor and would require the use of thicker wafers. The nanostructuring process removed only \(\approx 4.4\ \mu \text{m}\) of germanium in vertical direction, as determined by profilometry from the center of the wafer, which equals to an etch rate of \(\approx 300\ \text{nm min}^{-1}\). The result was also confirmed by SEM. The process is thus capable of producing sub-micrometer structures without significantly thinning the substrate and, hence, it is applicable also to thin wafers and Ge films with at least a few micrometer thickness, which is important for reducing material costs. Another etch profile scan revealed that the etch depth is slightly smaller \(\approx 3.9\ \mu \text{m}\) at a 5 mm distance from the wafer edge. Also SEM images showed that although the nanostructures remain very homogeneous at least until \(\approx 10\ \text{mm}\) from the wafer edge, they are somewhat shorter near the area that was under the clamp during the etching process. Nevertheless, the slight variation in nanostructure size or etch depth has no effect on the dark appearance of the wafer surface as determined by the naked eye.

The optical performance of the nanostructured surfaces is evaluated quantitatively in Figure 3. The reflectance of the nanostructured Ge wafer is only 0.85% on average in the wavelength range from 250 to 1800 nm, while that of the flat surface is more than 40%. Figure 3a confirms that, in addition to the visible range, the reflectance remains negligible also for NIR wavelengths, which cannot be seen by the eye.
Hence, the behavior of reflectance as a function of wavelength is significantly different from that of AR-coated surfaces, which can be optimized only for a single wavelength.[8] The graded index effect impacts a wide range of wavelengths from UV to NIR due to the wide distribution of nanostructure dimensions on the wafer surface (Figure 2). The local maxima in the reflectance of the polished sample at ~260 and 600 nm wavelengths are characteristic to single-crystalline bulk germanium.[34]

Although the nanostructures were observed to be slightly smaller close to the wafer edge by SEM, the inset in Figure 3a shows that the difference in reflectance between the center and the edge of the 100 mm diameter wafer is negligible. This demonstrates that the process is capable of producing nanostructures with uniform properties irrespective of the position on the wafer, and hence, is applicable to manufacturing of nanostructured Ge sensors on large wafer areas.

Another relevant optical parameter, even more than reflectance, for the sensor performance is the absorbance of radiation, which directly affects the device sensitivity. The extremely low reflectance of the nanostructured wafers results in an absorbance higher than 99% in the UV, visible, and NIR regions until 1600 nm wavelength (Figure 3b), after which the absorption coefficient of Ge starts to reduce.[8] The improvement compared to polished Ge or b-Si wafers is evident: the former reaches an absorption of only 63% at the maximum, while the latter is virtually transparent for wavelengths larger than 1000 nm due to the smaller bandgap of silicon.[35] Furthermore, Figure 3c verifies that the absorbance remains excellent also at large incidence angles. More specifically, the absorbance is not affected by a 30° tilt and it reduces only by a few percent absolute at an incidence angle as large as 60°. The increased absorbance should be visible in the sensor performance as improved external quantum efficiency and response, similar to reported before for b-Si photosensors.[31]

This work demonstrated a reproducible ICP−RIE process to fabricate Ge nanostructures for the application in photosensors. The process was shown to be capable of producing uniform nanostructures on both large-area substrates and patterned wafers, which demonstrates its high applicability to CMOS sensor manufacturing. The morphology of the Ge nanostructures was designed for maximized absorption, consequently, the absorbance of nanostructured Ge wafers was >99% in the 300–1600 nm wavelength range and remained high also at large incidence angles. This result confirms that the basic requirement for achieving photosensors with nearly ideal response from UV to NIR is fulfilled, i.e., all incident photons enter the substrate generating a high amount of charge carriers. The other important requirement is obviously the efficient collection of the generated carriers, which heavily depends on surface passivation. Indeed, the surface recombination at the Ge nanostructures should be addressed next. The morphology of the nanostructures presented here suggest that ALD could be the key technology to achieve excellent passivation of nanostructured Ge surfaces, similar to what has been reported earlier for silicon.

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Conflict of Interest

The authors declare no conflict of interest.

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dry etching, germanium, nanostructures, near-infrared, sensors

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