Research Article

MATLAB Simulation of UPQC for Power Quality Mitigation Using an Ant Colony Based Fuzzy Control Technique

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This paper proposes a fuzzy logic based new control scheme for the Unified Power Quality Conditioner (UPQC) for minimizing the voltage sag and total harmonic distortion in the distribution system consequently to improve the power quality. UPQC is a recent power electronic module which guarantees better power quality mitigation as it has both series-active and shunt-active power filters (APFs). The fuzzy logic controller has recently attracted a great deal of attention and possesses conceptually the quality of the simplicity by tackling complex systems with vagueness and ambiguity. In this research, the fuzzy logic controller is utilized for the generation of reference signal controlling the UPQC. To enable this, a systematic approach for creating the fuzzy membership functions is carried out by using an ant colony optimization technique for optimal fuzzy logic control. An exhaustive simulation study using the MATLAB/Simulink is carried out to investigate and demonstrate the performance of the proposed fuzzy logic controller and the simulation results are compared with the PI controller in terms of its performance in improving the power quality by minimizing the voltage sag and total harmonic distortion.

1. Introduction

Power quality is one of the major concerns in the present era. It has become important, especially with the introduction of sophisticated devices, whose performance is very sensitive to the quality of power supplied. Power quality problem is an occurrence manifested as a nonstandard voltage, current, or frequency that results in a failure of end use equipment [1]. The main objective of devising UPQC is the combined use of series-active and shunt-active filters chiefly to compensate negative-sequence current and harmonics as the SCR controlled capacitor banks compensate reactive power in power frequency [2]. Modeling and simulation of custom power conditioners happen to be inevitable as power electronics-based equipment in use for augmenting power quality in distribution networks [3]. Extended merit of the UPQC includes the following; it has a similar feature to SCR controlled capacitor banks of achieving load compensation resulting in drawing the balanced sinusoidal currents in the current control mode [4, 5]. In addition it is also the most comprehensive power quality enhancing device for susceptible nonlinear loads, which need exact sinusoidal input supply [6].

UPQC can also be operated in different possible configurations for single-phase (2-wire) and 3-phase (3-wire and 4-wire) networks, diverse compensation approaches, and recent developments are also found in the field [7]. Thus this conditioner can achieve reasonable power quality improvement, reducing the power disturbances that are supplied to the customers by the mains using the series unit. Additional facilities for PQ (i.e., mains power interruptions) can be offered to the customers (custom power) from the shunt units [8]. To reiterate again the main principle of UPQC is to ensure quality supply voltage and load current disturbances, namely, sags, swell, imbalance, harmonics, reactive currents, and current unbalance produced by the nonlinear loads [9, 10].
The control signal given to the UPQC plays a significant role in establishing a better operation of the device. Conventional control schemes are widely used in addition to the control schemes derived from artificial intelligence which is also recorded in the literature. Applications of some advanced mathematical tools in general, and wavelet transform in particular, in power quality are also applied. An extensive collection of literature covering applications of fuzzy logic, expert systems, neural networks, and genetic algorithms in power quality is also growing [11]. The ANN-based controller is designed for the current control of the shunt-active power filter and trained offline using data from the conventional proportional-integral controller [12]. Similarly, a fully digital controller based on the TMS320F2812 DSP platform which is implemented for the reference generation as well as control purposes is proposed [13].

In another attempt [14], the authors propose the application of fuzzy logic (FL) technique within microgrid energy system based on the most modern power conditioning equipment devices such as Unified Power Quality Conditioner (UPQC). Similarly [15, 16], a linear quadratic regulator (LQR) control technique embedded with the ANN is used to coordinate the operation of the series and shunt VSIs of the UPQC. In another development a control algorithm based on wavelet transform for UPQC to suppress current harmonics and voltage sags is proposed [17]. Subsequently in [18], the authors emphasize enhancement of power quality by using UPQC with fuzzy logic controller (FLC) and artificial neural network (ANN) controller on the conventional proportional-integral (PI) controller. In [19], the determination of voltage references for series-active power filter is done based on a robust three-phase digital phase locked loop (PLL) system using fuzzy regulator.

In [20], a particle swarm optimization (PSO) method is used to find the solution of the objective function derived for minimizing real power injection of UPQC along with the constraints. Adaptive neurofuzzy inference systems have been used to make the proposed methodology online for minimum real power injection with UPQC by using the PSO-based data for different voltage sag conditions. A fuzzy logic with PWM technique for UPQC for power quality improvement is discussed in [21]. In another extension [22], a fuzzy-expert system for automated detection and classification of power quality disturbances is presented. The paper [23] focuses on the application of wavelet transform technique to extract features from power quality disturbance waveforms and their classification using a combination of artificial neural network and fuzzy logic. Simulations are carried out with PI as well as fuzzy logic controller for both p-q and Id-Iq control strategies by considering different voltage conditions and adequate results were presented in [24]. Thus the main focus of this proposed system is to improve the power quality by compensating the voltage sag and eliminating the harmonics in the distribution network using a fuzzy logic [25, 26] based technique. In this research the fuzzy membership functions are carried out by using an ant colony optimization [27] technique for optimal fuzzy logic control.

The simulation is done using MATLAB and Simulink Toolbox for both control schemes used for the generation of control signals for the UPQC. The rest of the paper is organized in such a way that UPQC is revisited in Section 2. Control strategy and necessary equations of the UPQC are discussed in Section 3. Fuzzy logic and its features used in this research are presented in Section 4. The proposed FL-ACO method is discussed in Section 5. Numerical simulations are shown in Section 6 and the discussions and conclusions follow finally.

2. Unified Power Quality Conditioner: An Overview

UPQC is one of the custom power devices used at the electrical power distribution systems to improve the power quality of distribution system customers [2]. UPQC could be used to cancel current harmonics, to compensate reactive power, to eliminate voltage harmonics, to improve voltage regulation, to correct voltage and current imbalances, to correct voltage sag or swell, and to avoid voltage interruptions [3]. UPQC consists of both shunt and series compensators. A shunt compensator is used to cancel the disturbances in current whereas series compensator is used to cancel disturbances in voltage. Shunt compensator could be connected to the left or right of the series compensator. Ideally, shunt compensator injects current to achieve purely balanced sinusoidal source currents in phase with the supply voltages at rated magnitude and frequency. On the other hand, series compensation is used to inject voltage to maintain terminal voltage at rated magnitude and frequency.

The schematic diagram of a three-phase UPQC is shown in Figure 1. Voltage source inverters are used for shunt and series compensation. One may note that both voltage source inverters are supplied from a common DC link capacitor. One of the voltage source inverters is connected in parallel with the AC system while the other one is connected in series with the AC system through injection transformers. The inverter connected in parallel, together with its control circuit, forms the shunt compensation circuit. On the other hand, the inverter connected in series with appropriate control circuit forms the series compensation circuit. For the successful operation of the UPQC, the DC capacitor voltage should be at least 150% of the maximum line-line supply voltage. To
regulate the capacitor voltage constant, either a PI controller or a fuzzy logic controller could be used. Thus in this research a fuzzy logic integrated with ACO is proposed for the control of UPQC. The expression is given by
\[ V_{inj}^2 = V_{op}^2 x^2 + 2V_{op}^2 (1 - x) (1 - \cos \beta), \] (1)
where \( V_{op} \) is the output required voltage and \( \beta \) is the angle of voltage. Hence,
\[ VA - UPQC = V_{op}I_{op} (G_1(\theta, x, \beta) + G_2(\theta, \beta)) + I_{op}^2 G_2^2(\theta, \beta)Z_{SLC}. \] (2)
This is the final apparent power expression for the UPQC.

### 3. Control Strategy of UPQC

The control scheme of three-phase UPQC is shown in Figure 2. It consists of shunt APF and series APF. Both the shunt and series APFs are current controlled. The shunt APF is indirect current controlled.

#### 3.1. Fundamental Phasor Configuration

The fundamental description of the UPQC with an ideal voltage source and the fuzzy logic is modeled as an ideal current source. The UPQC source voltage \( V_s \) is taken as the reference and is given by [28]
\[ V_{in}\angle\theta = V_{pen}\angle\beta + V_{ph}\angle\delta, \] (3)
where \( V_{ph} \) is the load voltage and \( V_{pen} \) is the penetrated voltage of the UPQC. Note that for a power factor given load and voltage sag requirement to determine the magnitude and constant phase angles of \( V_{pen} \) to maintain the load voltage constant at its rated valued. There are an infinite number of solutions for \( V_{pen} \). The novel control strategy proposed in this paper precalculated this angle of injection \( \gamma \) to optimize the loading of the UPQC. The load current is given by
\[ I_{ph}\angle\delta - \theta = I_{in}\angle\theta + I_{C}\angle\delta. \] (4)
The angle \( \phi \) represents the lagging angle of the load current \( I_{ph} \) with respect to \( V_{ph} \). As assumed power supply only active component of the load current, \( \delta = 0 \). The current \( I_C \) is preferably the reactive load current component. The operation of the UPQC by means of a phasor diagram is easy to understand. The suffix “ph” denotes the load terms and “in” denotes the supply terms. Suffices 1 and 2 denote two stages in time, on the other state of the power supply for UPQC taking action. Initially in Stage 1, the supply voltage has no deficiency:
\[ V_{in} = V_{ph1} = V_{in1} = \text{Constant} = V_0. \] (5)
Corresponding to that situation, the SLCVC current is given by \( I_{C1} \), where angle \( \beta = +90^\circ \); in advance of the supply voltage, it is assumed that the power factor is lagging.

In Stage 2 the supply voltage magnitude has reduced to \( V_{in2} \), which requires the UPQC to take action such that \( V_{ph2} \) is restored to its original magnitude \( (|V_{ph1}| = |V_{ph2}|) \). This is achieved by injecting the series voltage \( V_{inj}\angle\gamma \), and load voltage can be obtained by selecting \( \gamma \) in a range from 0 to 90°, together with the appropriate magnitude of \( V_{inj} \). In Stage 2, according to Figure 3,
\[ I_{in2} \cos \beta = I_{ph2} \cos \theta. \] (6)
\[ I_{C2} = \frac{I_{ph2} \sin(\theta - \beta)}{\cos \beta}. \] (7)
The apparent power of the series compensator is expressed by

\[ P = I_L^2 V \cos \alpha + Q V \sin \alpha \]

Thus, for a given load power factor angle \( \varphi \) and load voltage sag \( x \) p.u., the VA loading will be a function of the advance angle \( \alpha \) of \( V_L \). The minimum VA occur when \( \varphi = \alpha \), so that \( I_{L2} = I_L \) and \( I_{C2} = 0 \). Under these conditions, the optimal injected voltage is given by

\[ V_{\text{inj}} = V_0 \sqrt{x^2 + 2(1-x)(1-\cos\varphi)} \] (20)

And the voltage advance angle for the SC is given by

\[ \sin \gamma = \frac{\sqrt{V_{\text{inj}}^2 - V_{L2}^2 \sin^2 \alpha}}{V_{\text{inj}}} \] (21)

Therefore, (20) and (21) are the control equations which will decide that the optimum angle of voltage injection by the UPQC could be obtained through the proposed ACO-FL control technique (Figure 12).

4. Fuzzy Logic Controller

In FLC, basic control action is determined by a set of linguistic rules. These rules are determined by the system. Since the numerical variables are converted into linguistic variables, mathematical modelling of the system is not required in FLC. The FLC comprises three parts: fuzzification, inference engine, and defuzzification.

4.1. Fuzzification. Membership function values are assigned to the linguistic variables, using seven fuzzy subsets: NB (Negative Big), NM (Negative Medium), NS (Negative Small), ZE (Zero), PS (Positive Small), PM (Positive Medium), and PB (Positive Big). The partition of fuzzy subsets and the shape of membership function adapt the shape up to appropriate system. The values of input error \( E(k) \) and change in error \( \text{CE}(k) \) are normalized by an input scaling factor shown in Figure 3.
In this system the input scaling factor has been designed such that input values are between −1 and +1. The triangular shape of the membership function of this arrangement presumes that for any particular input there is only one dominant fuzzy subset. The input error $E(k)$ for the FLC can be calculated from the maximum power point as given in (20) and (21):

$$E(k) = \frac{P_{ph}(k) - P_{ph}(k-1)}{V_{ph}(k) - V_{ph}(k-1)}.$$  

$$CE(k) = E(k) - E(k-1).$$  

4.2. Interference Method. Several composition methods such as Max-Min and Max-Dot have been proposed in the literature. In this paper Max-Min method is used. The output membership function of each rule is given by the minimum operator and maximum operator. Table 1 shows rule base of the FLC.

4.3. Defuzzification. As a plant usually requires a nonfuzzy value of control, a defuzzification stage is needed. To compute the output of the FLC, height method is used and the FLC output modifies the control output. Further, the output of FLC controls the switch in the inverter.

The FLC uses a rule base as shown in Table 1 and the membership function as shown in Figure 4. The FLC is characterized as (i) seven fuzzy sets for each input and output, (ii) triangular membership functions for simplicity, (iii) fuzzification using continuous universe of discourse, (iv) implication using Mamdani’s “min” operator, and (v) defuzzification using the “height” method. In UPQC, the active power, reactive power, terminal voltage of the line, and capacitor voltage are required to be maintained. In order to control these parameters, they are sensed and compared with the reference values. To achieve this, the membership functions of FLC are error, change in error, and output. Further, the output of FLC controls the switch in the inverter.

In the present work, for fuzzification, nonuniform fuzzifier has been used. If the exact values of error and change in error are small, they are divided conversely and if the values are large, they are divided coarsely. The set of FLC rules is derived from

$$u = -[\alpha E + (1 - \alpha) C],$$  

where $\alpha$ is called the self-adjustable factor which can regulate whole region of operation, $E$ is the error of the system, $C$ is the varying ratio error, and $u$ is the control variable.

A large value of error $E$ indicates that the given system is not in the balanced state. If the system is unbalanced, the controller should enlarge its control variables to balance the system as early as possible. One the other hand, small value of the error $E$ indicates that the system is near to balanced state. Overshoot plays an important role in the system stability. Less overshoot is required for system stability and in restraining oscillations. In such conditions, $C$ in (11) plays an important role, while the role of $E$ is diminished. The optimization is done by $\alpha$. During the process, it is assumed that the UPQC neither absorbs active power nor supplies active power during normal conditions. So the active power flowing through the UPQC is assumed to be constant. The control surface of the proposed FLC is shown in Figure 4. It indicates two inputs, one output, and a surface showing input-output mapping. The proposed defuzzification method is briefed in the next section.

4.4. Weighted Average Method. This is a method that cannot be used for asymmetrical output membership functions and can be used only for symmetrical output membership functions. Weighting each membership function in the obtained output by its largest membership value forms this method. The evaluation expression for this method is

$$z^* = \frac{\sum \mu_z(z)}{\sum \mu_z(z)}.$$  

where $\sum$ is used for algebraic sum.
6. Simulation and Results

In order to test the performance of the UPQC using the proposed FLC, it has been simulated for a 400 V, 50 Hz three-phase AC supply using MATLAB/Simulink. A three-phase diode rectifier feeding an RLC load is considered as nonlinear load. The maximum load power demand is considered as 11 kW + j12 kVAR. The value of source resistance $R_s = 0.2 \Omega$ and value of source inductance $L_s = 0.2 \text{ mH}$. DC link capacitor value is 10 mF. To test the operation of UPQC under the voltage sag condition, 20% sag in line voltage has been created and simulated, the corresponding voltage waveforms $V_a$, $V_b$, and $V_{ab}$ are taken and shown in Figure 6, and the THD in the system is 4.17% as shown in Figure 7. Again under 20% voltage sag condition the performance of UPQC has been simulated using the PI controller (Figure 8) and its corresponding output voltage waveforms $V_a$, $V_b$, and $V_{ab}$ are taken and shown in Figure 9; the voltage sag has been compensated and THD have reduced to 0.49% as shown in Figure 10. When the proposed FLC incorporated UPQC is connected to the system at 0.1s the simulations are carried out; its corresponding output voltage waveforms $V_a$, $V_b$, and $V_{ab}$ are taken and shown in Figure 10; the voltage sag has been compensated and THD have reduced to 0.13% as shown in Figure 11. The DC link capacitor voltage is held constant at its reference value by the FLC.

6.1. Discussion. By observing Figure 6, it can be noted that the voltage at $V_b$ is sagging without the UPQC being connected. This situation will be very serious in a power system and leads to imbalance in the system and may cause severe power quality issues in the overall distribution system. To justify the robustness of the control algorithm for voltage harmonic mitigation, a three-phase SCR rectifier with resistive load on the DC side is connected. Thus, the distortion voltage across the load is introduced. To envisage the performance of the shunt APF and series APF independently, both APFs are made to work at different time intervals. Initially the shunt APF is connected for operation. As shown in Figure 6, the supply currents are normal, sinusoidal, and phase line, with the voltages even under unbalanced system voltage condition. The source current THD in phase “c” is enhanced from 12.28% to 4.15%. Later the series APF is made to operate for conditioning. The series-active power filter starts compensating for the voltage harmonics by penetrating instantaneously the phase harmonic voltage, confirming that the load voltage gets no distortion.

To substantiate this in Figure 9, a small distortion in the DC voltages is introduced, but the DC link is able to regulate the DC voltage only to its past magnitude. Figure 10 conveys that a flow of current in the neutral conductor is circulating which has to be suppressed by the neutral phase of the shunt APF, thus forcing the supply-neutral current to zero. Table 2 summarizes the voltage sag mitigation of various methods.

Due to the operation of the UPQC, the shunt AF current (ica) compensates the reactive power and the harmonics of the load; thus the source current follows the sinusoidal reference currents. The THD of the supply current is 3.19%, while the load current THD is 12.73% as shown in Figures 10 and II.
The Scientific World Journal

ACO based membership function generation
Input fuzzy inference from UPQC control signal
Knowledge base about control signal for UPQC with fuzzification and defuzzification
Output control signal for controlling the UPQC

Figure 6: The proposed flow diagram for the ACO-FL control technique.

Figure 7: Output waveform without UPQC connected to the system.

Table 2: Voltage sag mitigation performance.

| System without UPQC | UPQC with PI controller | System with ACO-FLC controller |
|---------------------|-------------------------|-------------------------------|
| Voltage sag persists | Voltage sag mitigated    | Voltage sag mitigated with high accuracy |

Figure 8: Output waveform with UPQC connected to the system controlled by PI controller.

Table 3: Total harmonic distortion for the proposed system.

| THD without UPQC | THD with PI controller | THD with ACO-FLC controller |
|------------------|------------------------|-----------------------------|
| 4.17%            | 0.49%                  | 0.13%                       |

respectively. Moreover, the peak value of the supply current after compensation is less than the peak value of the load current, thereby increasing the loading capability of the AC mains. The RMS value of phase “a” supply current after compensation is reduced to 27.62 A and before compensation its value is 39.52 A as observed from simulation results. The UPQC effectively maintains the sinusoidal supply currents and a constant DC bus voltage when the load is reduced at 0.52 sec and increased at 0.8 sec as shown in Figure 10. Due to the connection of a single-phase diode rectifier, a current of 17.9 A flows in the load neutral conductor. This current is compensated for by the fourth leg of the shunt AF, thus reducing the supply-neutral current to zero. Table 3 shows the comparison of THD produced by various methods.

7. Conclusion
The confrontation in improving the power quality has become a promising area of research amongst power system and power electronic engineers and researchers. With the ever increasing advent of nonlinear loads and also due to high frequency switching characteristics, suitable conditioners are always a demand. Unified Power Quality Conditioner (UPQC) is one of the promising power electronic circuit modules to overcome voltage sag and total harmonic distortion problems, as the circuit is modeled using both series-active and shunt-active power filters. Thus the benefits of both the power filters are integrated for better power quality mitigation is realized. This paper considers the advantages of the fuzzy logic and proposes a new control scheme for the Unified Power Quality Conditioner (UPQC) for minimizing the voltage sag and total harmonic distortion in the distribution system. The reference signal generated by the fuzzy logic controller was given as input to the UPQC switching module. Exhaustive simulation experiments using MATLAB/Simulink have been done and based on the results and output received it is observed that the proposed fuzzy logic controller is better in improving the power quality by
minimizing the voltage sag and total harmonic distortion when compared to the conventional PI controller. To enable this, a systematic approach for creating the fuzzy membership functions is carried out by using an ant colony optimization technique for optimal fuzzy logic control. In this research the UPQC is only simulated for minimizing the voltage sag and total harmonic distortion in the distribution system but the main purpose of UPQC is to compensate for voltage imbalance, reactive power, negative-sequence current, and harmonics and these parameters will be considered in the near future.

**Conflict of Interests**

The authors declare that there is no conflict of interests regarding the publication of this paper.

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