Voltage Sag Ride-through Performance of Virtual Synchronous Generator

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The Virtual Synchronous Generator (VSG) is an inverter control structure that supports power system stability by imitating a synchronous machine. Because of the restriction of inverter power and current, the VSG performance under disturbances should be evaluated and enhanced. In this paper, the response of the VSG unit to symmetrical and unsymmetrical voltage sags is assessed. A theoretical analysis that traces the trajectory of the state variable of the system during voltage sags is presented. The analysis confirms the effect of the characteristics of symmetrical and unsymmetrical voltage sags on the severity of their consequences. In addition, it is detected that two types of transients appear that must be mitigated: one is the transients during the voltage sag and the other one is the transients after voltage recovery. To prevent overcurrent during voltage sags, voltage amplitude control and output power control are implemented, and to suppress the transients after voltage recovery, virtual inertia control is implemented. The experimental results from a 10 kVA VSG-controlled inverter confirm the effectiveness of the additional controllers.

Keywords: distributed generation, virtual synchronous generator, voltage sag

1. Introduction

Conventional enormous synchronous generators comprise rotating inertia due to their rotating parts. These generators are capable of injecting the kinetic energy preserved in their rotating parts to the power grid during disturbances or sudden changes. Therefore, the system is robust against instability. On the other hand, penetration of Distributed Generating (DG) units in power systems is increasing rapidly. A power system with a big portion of inverter-based DGs is prone to instability due to lack of adequate balancing energy injection within the proper time interval. The solution can be found in the control scheme of inverter-based DGs. By embedding the mathematical model of a synchronous generator in the inverter, it can emulate the behavior of a real synchronous machine. In the Virtual Synchronous Generator (VSG) concept, the power electronics interface of the DG unit is controlled in a way to exhibit a reaction similar to that of a synchronous machine to a change or disturbance.1)

The VSG concept and application were investigated in (2)(3). A similar concept under the title of Synchronverters is described in (4). The VSG systems addressed in (5)–(7) are designed to connect only an energy storage unit to the main grid. A linear and ideal model of synchronous machines is implemented in (8) to produce current reference signals for the hysteresis controller of the inverter. In this Virtual Synchronous Machine (VISMA), authors added an algorithm for small disturbance compensation to improve the quality of the grid voltage. Reference (9) introduces a mechanism for voltage, frequency, and active and reactive power flow control of VSG. Our research group introduced another VSG design(10) and added reactive power control to it to have a constant voltage at VSG terminals(11).

Faults on transmission lines cause voltage drops in several points in power system that affects electrical equipment. Voltage sags (drops) are classified in several types based on the fault type. Three-phase fault on power line produces symmetrical voltage sag, while other fault types cause various unsymmetrical sags.

Several works addressed the effect of inverter based DG units on voltage sags in power system focusing on compensation effect of DGs(12)(13). Reference (14) compared the performance of control strategies in voltage sag ride-through improvement of a single phase converter-connected DG.

Since the VSG is inherently a power electronics-based unit, it is extremely sensitive to grid side faults and disturbances. Consequently, before practical usage, their operation must be evaluated under disturbances conditions. In this paper, VSG is tested under symmetrical and unsymmetrical voltage sags and the influence of the characteristics of voltage sags on the VSG transient current is investigated. To verify the results mathematically, current trajectory analysis in phase plane is introduced. The trajectory of system state variable (VSG current) is monitored in phase plane and its equations are extracted for during and after the sag. The effects of the characteristic of voltage sag can be observed clearly by this method. High magnitude transient current is the major hazardous consequence of voltage sags on inverter based DGs. To limit the overcurrent, three additional controllers of voltage amplitude control, output power control and virtual inertia control are embedded in the model and tested. Experiments were performed on a 10 kVA VSG-controlled inverter and the results showed that the additional controllers enhanced the voltage sag ride-through capability of the VSG system.

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The structure of the VSG system is reviewed in Sect. 2. In Sect. 3, the voltage sag types and characteristics are explained. In Sect. 4, the consequences of voltage sags on the VSG unit are monitored by simulation. In Sect. 5, the effect of the characteristics of voltage sags on its consequences is clarified by a theoretical analysis. In Sect. 6, three additional controllers are introduced to improve the voltage sag ride-through performance of the VSG system. In Sect. 7, simulations corresponding to the experimental system of Sect. 8 were carried out and the performance of the proposed controllers was tested on it. Experimental results are represented through performance of the VSG system. In Sect. 8, conclusions are given.

2. Virtual Synchronous Generator Structure

Figure 1 shows the control block diagram of the VSG system. In this scheme, a distributed resource (DR) is connected to the main power system via an inverter controlled based on the VSG concept. The model of the synchronous generator that is used in this paper is a cylindrical-rotor type synchronous generator connected to an infinite bus. The well-known swing equation of synchronous generators is used as the heart of the VSG model:

\[ P_{in} - P_{out} = J \omega_m d\omega_m/dt + D\Delta \omega \]  

(1)

where \( P_{in} \) is input power (as same as the prime mover power in a synchronous generator), \( P_{out} \) is output power, \( J \) is the moment of inertia of the rotor, \( \omega_m \) is the virtual angular velocity of the virtual rotor and \( D \) is the damping factor. \( \Delta \omega \) is given by \( \Delta \omega = \omega_m - \omega_{grid} \), where \( \omega_{grid} \) is the grid frequency or the reference frequency when the grid is not available. Using voltage and current signals measured at the VSG terminals, its output power and frequency are calculated.

A governor model shown in Fig. 2 is implemented to tune the input power command based on the frequency deviation. Having the essential parameters, (1) can be solved by numerical integration. By solving this equation in each control cycle, the momentary \( \omega_m \) is calculated and by passing through an integrator, the virtual mechanical phase angle, \( \theta_m \) is produced. This phase angle and a voltage magnitude reference are used as the VSG output voltage angle and magnitude commands for generating PWM pulses.

3. Voltage Sags Types and Characteristics

Voltage sag (dip) is a momentary drop in the RMS voltage of at least one of the three phases or line voltages. Voltage sags originate from short-term increase in current in power system due to fault or starting large loads. Severity of the effects of voltage sags on equipment depends on the sag characteristics. Sag magnitude and duration are major characteristics. In this paper, we assume that the implemented sags are rectangular. At the instant when the sag begins, voltage of phase “a”, \( v_a \), is expressed by:

\[ v_a(t)|_{t=t_{sag}} = V_m \sin(\theta(t)) \]  

(2)

where \( V_m \) is the phase voltage amplitude. Voltage phase angle at the sag beginning instant, \( \theta(t) \) is called initial point-on-wave. \( \theta(t) \) is considered as a characteristic of voltage sag that affects the response of appliance to voltage sag. For both symmetrical and unsymmetrical voltage sags, sag duration is the time interval between sag beginning and ending.

Voltage sags can be either symmetrical or unsymmetrical. If the individual phase voltages are equal and the phase angle relationship is 120°, the sag is symmetrical. Otherwise, the sag is unsymmetrical. Reference (16) classified the main voltage sags in 7 types.

Three-phase short circuits produce symmetrical sags classified as type A. In this type, voltage magnitudes of three phases decrease equally. For symmetrical sags, the magnitude of sag is the remained RMS voltage in per unit or percent of rated voltage that expressed by a parameter denoted as “h”.

Type B voltage sags are one kind of unsymmetrical voltage sags that originate from single line-to-ground faults. Single line-to-ground faults after passing through a “Δ-Y” transformer and line-to-line faults on power lines produce voltage sags of type C. In this type, relative phase angles between voltages vectors also change. If the voltage sag of type C passes through a “Δ-Y” transformer, it will be converted to type D voltage sag.

For unsymmetrical sags, “h” is a coefficient that appears in voltage equations and causes difference in their magnitude and/or phase angles as shown in Fig. 3. Lightest and severest characteristics of each voltage sag type are listed in Table 1.

![Fig. 1. Block diagram of the VSG unit](image)

![Fig. 2. Governor diagram](image)

![Fig. 3. Phasor diagram of voltage sag types for h = 0.5](image)

Table 1. Lightest and severest characteristics of voltage sags

| Sag type | Lightest characteristics | Severest characteristics |
|----------|--------------------------|-------------------------|
| duration | \( h_0 \) | \( h \) | duration | \( h_0 \) | \( h \) |
| A | \( 4T \) | - | \( 6T \) | - |
| B | \( k\pi \) | \( \pi/2, 3\pi/2 \) | \( 6T \) | \( 0, \pi \) |
| C | \( k\pi \) | \( 0, \pi \) | \( 6T \) | \( \pi/2, 3\pi/2 \) |
| D | \( k\pi \) | \( \pi/2, 3\pi/2 \) | \( 6T \) | \( 0, \pi \) |

\* \( k \in N \)

\( T \): One cycle time period

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4. VSG Subjected to Voltage Sags

The VSG model discussed in Sect. 2 was simulated in PSCAD/EMTDC software. The parameters of the system are: \( S_{\text{base}} = 35 \text{kVA}, f_{\text{base}} = 60 \text{Hz}, X_p = 42.4\%, \ J = 5.63 \text{kgm}^2 \) and \( D = 17 \text{pu} \). Voltage sags were applied to the VSG and its transient currents were investigated. In this section, results are summarized to voltage sags of types A and B. For all cases of simulation the voltage sag intensity parameter, \( h \) is set to 0.1.

Figure 4 shows the current waveforms of VSG subjected to voltage sag of type A with the duration of 2 cycles. Since this is the lightest characteristic of voltage sag, overcurrent happens only during voltage sag. Whereas, as shown in Fig. 5, for voltage sag of type A with the duration of 1.5 cycles there are significant current oscillations which appear after voltage recovery.

Initial point-of-wave must be taken into account for voltage sag of type B with the duration of 2 cycles, initial point-on-wave of zero, there is the lightest characteristic of voltage sag, overcurrent happens in two phases during voltage sag. On the other hand, for the severest characteristics, which are the duration of 1.5 cycles and initial point-on-wave of zero, there are significant current oscillations during and after the voltage sag observed in Fig. 7.

The point achieved by simulations is that there are two sorts of transients that should be considered: one during the voltage sag and the other one after voltage recovery. In the next part, the effect of the voltage sag characteristics will be clarified analytically.

5. State Variable Analysis in Phase Plane

The disparity of the VSG responses to various sags and their characteristics can be illustrated by VSG state variable (current) analysis in phase plane. In addition, the origin of the transients after voltage recovery can be explained by this analysis.

5.1 Symmetrical Voltage Sags (Type A)

Inverter output current during voltage sag is calculated by integrating the voltage in polar coordinate in stationary frame. To obtain the voltage equation in polar form, three-phase grid voltage is transferred into the stationary \( dq \)-frame and expressed in the form of:

\[
v(t) = v_d(t) + jv_q(t) = V_m e^{j(\omega t + \theta_t - \pi/2)} \quad \cdots \cdots \cdots \cdots \cdots (3)
\]

where \( V_m \) is the voltage vector magnitude in phase plane. This voltage vector rotates with the synchronous frequency on the stationary phase plane with the initial angle of \( \theta_t - \pi/2 \). Using this transformation, the voltage equation of sag type A in the stationary polar coordinate is expressed as:

\[
v_{\text{vsag}}(t) = hV_m e^{j(\theta_t - \pi/2)} e^{j\omega t} \quad \cdots \cdots \cdots \cdots \cdots \cdots (4)
\]

where \( hV_m \) is the remained voltage amplitude and \( \theta_t \) is the initial point-on-wave. As mentioned before, \( h \) is the voltage sag intensity parameter varies from zero (zero remained voltage magnitude) to 1 (normal voltage). The fault current is calculated by integrating the voltage difference as:

\[
i_{\text{tag}}(t) = \frac{1}{L} \int_0^{t_{\text{end}}} (1 - h)V_m e^{j(\omega t + \theta_t - \pi/2)} dt \quad \cdots \cdots \cdots \cdots \cdots (5)
\]

where \( i_{\text{tag}}, L \) and \( t_{\text{end}} \) are the fault current in the stationary reference frame, the interconnecting inductance and the sag ending time, respectively. Solving (5) and transferring to the synchronous reference frame yields:

\[
i_{\text{tag}}^{\text{sych}}(t) = (1 - h) \frac{V_m}{\omega_{\text{grid}}} L e^{j(\theta_t - \pi)} (1 - e^{-j\omega_{\text{grid}}t}) \quad \cdots \cdots \cdots \cdots \cdots (6)
\]

Based on (6), during voltage sag, current vector is circulating in the synchronous \( dq \)-frame phase plane with fixed radius as shown in Fig. 8 (the damping terms is neglected). The radius of the circle, \( (1 - h)V_m/L\omega_{\text{grid}} \), determines the oscillation magnitude during voltage sag. It is independent of the \( \theta_t \) obviously.

If the duration of voltage sag is a multiple of full cycles, the state variable position at the sag ending moment will reach to its normal point (pre-sag point) after passing through circles in phase plane. When the voltage recovered from the sag, the state variable has to move to its normal point and since it is extremely close to it, minimum oscillations happen after voltage sag. Whereas if the duration is half a cycle more than any number of full cycles, the state variable position has maximum distance to its normal point at the sag ending moment based on (6). It means that transient current has its maximum magnitude. The transient current must settle down on
the origin of coordinate (current before fault is neglected) after voltage recovery. The second transient can be expressed as:

\[ t_{\text{after sag}}^{\text{synch}}(t) = 2(1 - h) \frac{V_m}{\omega_{\text{grid}} L_m} e^{j(\theta_0 - \pi)} e^{-j\omega t} \]  

Equation (7) is a circular trajectory with the center coinciding with the origin. The radius of trajectory after voltage sag has its maximum possible value proving that the assumed duration is the severest one. Figure 8 includes the current paths in phase plane. In this paper, d-axis is taken as the reference axis and phase “a” and q-axis lead d-axis by \( \theta_0 \) and \( \pi/2 \), respectively. Part (a) of Fig. 8 is related to the sag with the duration of 1.5 cycles and \( h = 0.1 \). At the sag ending point the state variable goes through circles with a large radius (dotted line) that causes severe current transient. While in part (b) of the figure, when the duration is one full cycle, there is a small distance between the position of the variable after and before the sag and light transient happens.

It must be noted that the transient during voltage sag is not affected by the sag duration. However, the sag magnitude parameter \( h \) determines the transient severity during voltage sag. If the voltage sag is shallow \( (h \) close to unity) and/or if the sag lasts for a relatively long time, the state variable transients during voltage sag will settle down at the center of the solid-line circles before voltage sag ends. Afterwards, when voltage amplitude is recovered, the state variable has to move to its normal point from the center of the first transient circles. It means that the oscillations will have identical amplitude regardless of the duration of the voltage sag.

### 5.2 Unsymmetrical Voltage Sags

The same analysis with more intense mathematical work is performed for voltage sag of type B as the representative of unsymmetrical voltage sags. The three-phase phasor expressions of voltage sag type B are as follows:

\[
\begin{align*}
V_a &= hV_{\text{rms}} \\
V_b &= \frac{1}{2}V_{\text{rms}} - j\frac{\sqrt{3}}{2}V_{\text{rms}} \\
V_c &= -\frac{1}{2}V_{\text{rms}} - j\frac{\sqrt{3}}{2}V_{\text{rms}}
\end{align*}
\]

Transforming into polar form in the stationary coordinate yields:

\[ V_{\text{lag}}(t) = \frac{V_m}{3}(h + 2)e^{j(\omega_{\text{grid}}t + \theta_0 - \pi/2)} \]

After integrating the voltage difference between the inverter and grid, the fault current in the synchronous frame is given as follows:

\[
\begin{align*}
t_{\text{lag}}^{\text{synch}}(t) &= \frac{V_m(1 - h)}{3\omega_{\text{grid}} L_m} e^{j\theta_0} + 2\cos\theta_0 e^{-j\omega_{\text{grid}}t} \\
&= e^{-j\theta_0} e^{-j2\omega_{\text{grid}}t} \]
\]

It is observed that the coefficient \( \cos\theta_0 \) appears in the current equation and affects its magnitude during oscillation. The form of (10) on the phase plane is not exactly circular and depends on \( \theta_0 \). The characteristics of the voltage sags that result in the maximum absolute value of (10) are the critical characteristics for this type of voltage sag. The initial points-on-wave of zero and \( \pi \) bring forth the maximum oscillations during voltage sag type B and with these values of \( \theta_0 \), when the duration of the sag is a multiple of a half-cycle, the state variable position has maximum distance to its normal point. When voltage is recovered at this moment, the state variable returns to its normal point with severest oscillation similar to one of the symmetrical voltage sag, i.e., the circular trace in the phase plane. The terms \( e^{-j\omega_{\text{grid}}t} \) in (10) indicates that the current oscillates with twice of the system frequency during unsymmetrical voltage sags.

The mildest characteristics of voltage sag type B can be deduced from (10) as well. The initial points-on-wave of \( \pi/2 \) and \( 3\pi/2 \) result in the lightest oscillations during voltage sag. For these initial points-on-wave, the duration of a multiple of a half-cycle and the duration of an odd multiple of a quarter-cycle have the mildest and severest consequences after sag, respectively (only the oscillatory component with double system frequency exists in (10)). For unsymmetrical voltage sags also, if the duration is long or if the sag is shallow, the transient will settle down before voltage recovery. When voltage is recovered in this condition, the transients after voltage sag has fixed magnitude only related to \( h \), in regardless of the duration and initial point-on-wave.

### 6. Voltage Sag Ride-through Enhancement

Three approaches are added to the VSG system to limit the overcurrent during and after voltage sag. Figure 9 shows the VSG system with the additional controls.

#### 6.1 Voltage Amplitude Control

Subsystem A in Fig. 9 calculates the Root Mean Square (RMS) value of grid voltage and uses it as the reference voltage for inverter output. By this control, when grid voltage drops during voltage sag, inverter output voltage also will be reduced to prevent overcurrent. The RMS voltage is calculated by

\[ V_{\text{RMS}} = \sqrt{V_d^2 + V_q^2}. \]

During unsymmetrical voltage sags, the calculated RMS voltage has oscillation as mentioned in Sect. 5. A first order Low Pass Filter (LPF) shown in subsystem A of Fig. 9 is used to remove the oscillation.

#### 6.2 Output Power Control

Since inverter output power is proportional to the product of the inverter and grid voltages, once the output voltage follows the grid voltage, inverter output power must be limited proportional to the square of the grid voltage. Subsystem B calculates the output power reference of VSG proportional to the square of the grid voltage.
Fig. 9. VSG unit with the additional controls for voltage sag ride-through enhancement

Fig. 10. Power-angle curve of a synchronous machine subjected to a fault. When a fault happens, operating point moves on the dotted line and reaches to the point $\delta_0$. After fault clearance, it returns to the original curve and oscillates around the equilibrium point $\delta_1$.

Table 2. Machine modes during oscillation

| Segment | $\Delta\omega$ | $d\omega_m/dt$ | Mode     | $J$   |
|---------|----------------|----------------|----------|-------|
| a→b    | $\Delta\omega<0$ | $d\omega_m/dt<0$ | Accelerating | $J_{\text{big}}$ |
| b→c    | $\Delta\omega<0$ | $d\omega_m/dt>0$ | Decelerating | $J_{\text{small}}$ |
| c→b    | $\Delta\omega>0$ | $d\omega_m/dt>0$ | Accelerating | $J_{\text{big}}$ |
| b→a    | $\Delta\omega>0$ | $d\omega_m/dt<0$ | Decelerating | $J_{\text{small}}$ |

6.3 Virtual Inertia Control

Consider the power-angle curve of Fig. 10. When a fault happens, power-angle relation follows the dotted line curve and load angle moves to the point $\delta_0$. After fault clearance, it returns to the original curve and oscillates around the equilibrium point $\delta_1$. The machine condition during each phase of an oscillation cycle is summarized in Table 2. It should be noted that the sign of the $d\omega_m/dt$ does not determine acceleration or deceleration by itself; whereas, its sign respect to the sign of the relative angular velocity defines the acceleration or deceleration. For example during transition from points “c” to “b” in Fig. 10, both of $d\omega_m/dt$ and $\Delta\omega$ are positive and act in the same direction; therefore, it is an acceleration period. Whereas, when they have opposite signs like transition from points “b” to “a”, it is a deceleration period.

The derivative of angular velocity, $d\omega_m/dt$ indicates the rate of acceleration or deceleration. Considering (1), it is observed that this rate has a reverse relation to the moment of inertia, $J$. Based on this fact, one can select a large value of $J$ during acceleration phases (“a” to “b” and “c” to “b”) to reduce the acceleration, and a small value of $J$ during deceleration phases (“b” to “c” and “b” to “a”) to boost the deceleration. Using the transient energy function analysis, the authors proved that this algorithm brings in a damping effect that suppresses the transient quickly [17]. The big moment of inertia $J_{\text{big}}$ and the small one $J_{\text{small}}$ can be chosen within a wide range depending on the rated power so that the difference between $J_{\text{big}}$ and $J_{\text{small}}$ determines the damped power in each half-cycle of oscillation. The subsystem C in Fig. 9 adopts the value of the virtual inertia based on the stated algorithm summarized in Table 2.

The simulations of Sect. 4 were repeated for the VSG with the voltage amplitude, output power, and virtual inertia controls under the severest characteristics condition of voltage sags. To let the voltage amplitude control be quick enough, the LPF of the voltage reference loop of Fig. 9 was not included during the simulations. Figure 11 shows the current of the VSG subjected to a critical sag type A. The overcurrent that appeared during and after voltage sag in Fig. 5 was eliminated effectively by the additional controllers. The current position in phase plane was moved from its normal position toward the origin of the phase plane. This small distance from the normal position caused a slight transient after voltage recovery that was damped slowly.

In the case of unsymmetrical voltage sags, without the LPF in voltage control loop, the calculated RMS voltage will be oscillatory. On the other hand, using the LPF imposes a delay in following the grid voltage and consequently, reduces the effectiveness of the controller. Figure 12 shows the simulation result for the severest voltage sag of type B. It is observed that the overcurrent was reduced compared to Fig. 7. However, because of the oscillatory voltage reference, it was not reduced as effectively as sag type A.

7. Simulation Results

A simulation model similar to the experimental system of next section was built in PSCAD/EMTDC. The system configuration and parameters are shown in Figs. 13 and 14 and
In the simulation, the transformer was replaced by its equivalent reactance, $X_T$ and all system operated at 207 V nominal voltage. Furthermore, the fault path resistance was chosen 3 $\Omega$ to have deeper voltage sags. To see the voltage profile during fault, the VSG was disconnected and three-phase fault stablished for 10 cycles of system frequency. The phase-to-neutral voltages of the point of common coupling (PCC) affected by the fault is shown in Fig. 15. The remained voltage in this case is 45% of the nominal voltage ($h = 0.45$). However, the voltage drop in the presence of VSG is less due to the compensation by VSG.

In the next step, the VSG system without the additional controller was connected and a fault happened while the VSG was injecting 2.6 kW power to the grid. The RMS voltage at PCC, VSG current, and the dc link voltage waveforms are presented in Fig. 16. In this simulation, the RMS voltage is calculated with a smoothing time constant of 0.0159 s (the cut-off frequency of the LPF is 10 Hz). As it is observed, RMS current raised immediately up to 70 A when voltage drop at PCC happened. Moreover, when the voltage magnitude was recovered, the current transient went to negative level (reverse current) and increased the dc-link voltage. The transient during voltage sag may result in the overcurrent failure and the transient after voltage recovery may result in the dc-link overvoltage failure.

The waveforms of the VSG angular velocity, power reference calculated by governor, VSG output active power, and VSG output reactive power are shown in Fig. 17. During transients, the governor calculates the power reference in opposite of the frequency oscillation to maintain the VSG frequency at system nominal frequency. The active power of VSG has a waveform similar to the $I_d$ of Fig. 16. The reverse power that increases the dc-link voltage is observed.
more clearly in power curve. The reactive power transient is similar to the transient of $I_d$ of Fig. 16.

To mitigate the possible failures, the proposed voltage amplitude control and output power control were added to limit the overcurrent current during voltage sag, and the virtual inertia control were added to eliminate the after sag transients and prevent dc-link overvoltage. The RMS voltage at PCC, VSG current, and dc-link voltage waveforms are shown in Fig. 18. The PCC voltage sag was deeper because the current injected by VSG was reduced by additional controllers. With the voltage amplitude and output power controls, although the transient current peak at the sag starting moment was reduced to some extent, however because of the delay in RMS voltage calculation, the voltage amplitude control was not able to apply the voltage reference equal to the PCC voltage promptly and the overcurrent prevention performance was limited by this delay. During the voltage sag, since the voltage reference followed the grid voltage, the overcurrent fell down with a steep ramp. The dc-link voltage rise was reduced by around 70 V via the virtual inertia control. However, by setting a smaller value for $J_{small}$ this controller is more effective.

The VSG angular velocity, power reference calculated by governor, VSG output active power, and VSG output reactive power are shown in Fig. 19. The virtual inertia control suppressed the VSG angular velocity quickly. By the output power control, the power reference calculated by governor is reduced at the sag starting moment proportional to the square of the PCC voltage. However the power reference was increased after a short while to regulate the frequency. Similar to $I_d$, The active power transient during voltage sag was reduced by the additional controllers. After voltage recovery, the transient of output active power had the negative amplitude almost equal to the curve of Fig. 17 that was without additional controllers. However, this transient was suppressed quickly by the virtual inertia control and did not last for the same while as Fig. 17. Therefore the reverse energy and consequently the dc-link voltage rise is reduced. The $J$ calculated by virtual inertia control is shown in Fig. 20. It is observed that the value of inertia is chattering during steady state operation. This chattering appears because of the slight variation of the angular velocity around the equilibrium point. However, the chattering can be removed by setting a threshold for the $\Delta \omega$ in the virtual inertia control algorithm. It is carried out by replacing the $\Delta \omega > 0$ and $\Delta \omega < 0$ in Table 2 by $\Delta \omega > \varepsilon$ and $\Delta \omega < \varepsilon$, respectively which $\varepsilon$ is a negligible positive value. It this paper, this threshold is not applied since experience showed that the chattering $J$ has a better performance in frequency regulation and voltage ripple reduction.

As mentioned before, the delay in sensing, calculating and filtering the PCC voltage to calculate the VSG voltage reference is the main obstacle of the effectiveness of the voltage amplitude control. To see the effect of this delay, the smoothing time constant of the RMS voltage calculation was reduced to 1 ms and the simulation was performed. The result is shown in Fig. 21. It is observed the delay in RMS voltage calculation was reduced and thereby, the overcurrent
Fig. 21. PCC RMS voltage and VSG currents of the system with the additional controllers (with RMS voltage calculation time constant 1 ms), affected by voltage sag

Fig. 22. Symmetrical voltage sag at PCC with 92% remained voltage at the sag starting moment was reduced considerably. The LPF is essential especially in case of unsymmetrical voltage sags that the calculated RMS voltage has oscillation with double system frequency which must be removed. Moreover, in practical systems, the delay is not only by the filter, but also the sensors and measurement devices comprise considerable delays.

The experimental system of Sect. 8 can be tested solely under shallow voltage sags due to current limitation of the inverter. In order to verify the compliance of the simulation results with the subsequent experiments, simulations under shallow voltage sags were performed. For this purpose, the resistance of the fault path of Fig. 13 was changed to 10 Ω. When the three-phase fault was applied, a light voltage sag with 92% remained voltage that is shown in Fig. 22 appeared at the PCC point of Fig. 13 with disconnected inverter.

While injecting 1 kW power to the grid, the VSG system was subjected to the light voltage sag in two conditions of with and without additional controllers. Figures 23 and 24 include the waveforms of the RMS voltage at PCC, the VSG currents, and the dc-link voltage with and without the proposed controllers, respectively. At \( t = 0.5 \) s when the voltage dropped, the VSG currents raised instantly. For the system without the additional controllers, the overcurrent persisted for a while during voltage sag as observed in Fig. 23; whereas for the system with the additional controllers, the overcurrent started to decrease immediately shown in Fig. 24. Moreover, the oscillation after voltage recovery disappeared quickly by the additional controllers. The quick elimination of the oscillation after voltage recovery resulted in a smaller and shorter-time dc-link voltage rise for the system with the additional controllers.

8. Experimental Results

A laboratory-scale system is used to investigate the voltage sag ride-through performance of the VSG. The overall system configuration is depicted in Fig. 25 and the main parameters of the system are presented in Table 4. The transmission unit (TU) in Fig. 25 simulates the \( \pi \) model of a 40 km transmission line shown in Fig. 14.

Due to the strict overcurrent limitation of the inverter unit,
when a deep voltage sag happens, the VSG unit is stopped by its protection system. Therefore, mild voltage sags with magnitudes bigger than 90% with the duration of 10 cycles were tested on this system and voltage sag ride-through performance of the VSG unit was evaluated. For light voltage sags, since the state variable position in phase plane at the sag ending moment does not change considerably by the characteristics, the characteristics do not affect the severity of transient oscillation. The voltage amplitude control in experiments has a LPF with 10 Hz cut-off frequency \( T = 0.0159 \text{ s} \) in Fig. 9.

8.1 Symmetrical Voltage Sag

The voltage sag shown in Fig. 26 appeared due to three-phase short circuit happened at the fault point and measured at the Point of Common Coupling (PCC) indicated in Fig. 25. The voltage magnitude during voltage sag is 92% of the normal value. The fault was controlled by switching thyristors in the fault path. The initial point-on-wave can be adjusted by a phase detection algorithm in the DSP unit. Since the exact turn-off point of the thyristors can not be controlled by the gate signal, the duration of the voltage sag will have utmost a half-cycle error. However, as mentioned before, the duration and initial point-on-wave are not effective for shallow voltage sags. Therefore, these characteristics were not investigated in the experiments.

To assess the performance of the additional controls, first the VSG unit without the controllers is subjected to the voltage sag illustrated in Fig. 26. As stated formerly, two transient states happen: the transient during voltage sag and the one after voltage recovery. To see the importance of the transient after voltage recovery, the VSG was subjected to the voltage sag while injecting 1 kW power to the grid and its current in synchronous \( dq \)-coordinate was monitored. Figure 27 shows the currents and dc-link voltage in this condition. The oscillations after voltage recovery caused power and current oscillation which went to negative value. This reverse current increased the dc-link voltage and resulted in the failure. Using an energy storage unit with high rate of charging or a larger dc-link capacitor can prevent the dc-link overvoltage failure. However, the objective is to prevent the failure of the existing system by a proper control scheme. It is observed later that virtual inertia control will prevent this kind of failure. This case corresponds to the simulation case of Fig. 23. The dc-link voltage rise of the simulation case shown in Fig. 23 happened in this experiment case as well which resulted in the failure.

During the voltage sag, the VSG current is raised due to the voltage difference between the VSG and the grid. Since the voltage sag amplitude (the remained voltage) is high in this research (that means a mild voltage sag), the initial current of the VSG affects the fault ride-through performance. When the VSG unit was loaded at 2.6 kW, the voltage sag type A was applied. Figure 28 shows that the VSG current raised sharply and VSG was stopped. It can be concluded that for lower loading levels, the oscillations after fault recovery causes the failure and for higher loading levels, the overcurrent during voltage sag results in the failure.

In the next step, only the voltage amplitude control and output power control are activated. When the VSG output power was the inconsiderable value of 1 kW, the type A voltage sag happened. As it is observed in Fig. 29, the oscillation after voltage sag fell to the negative level and activated the dc-link voltage protection.

The VSG current and dc-link voltage affected by voltage sag when the VSG power is 2.6 kW is shown in Fig. 30. In this condition, the voltage amplitude and power control...
banned the overcurrent to some extent and the VSG was able to pass-through the fault. When the voltage sag starts, it is expected that the voltage amplitude controller calculates the grid RMS-voltage and applies it as the voltage reference of the inverter. Although the overcurrent magnitude was reduced slightly; however, severe current transient happened. It is because of the delay in sensing, filtering, and applying the voltage reference. In spite of this technical shortcoming, the VSG unit was able to ride-through this voltage sag. It should be noted that the RMS value of $I_d$ and $I_q$ is concerned as the threatening overcurrent.

In the next experiment, all of the additional controllers were activated. First the VSG with the low output power of 1 kW was subjected to the voltage sag type A. As it is observed in Fig. 31, the inertia control suppressed the after sag oscillations by imposing a damping effect. This case corresponds to the simulation case of Fig. 24. Similar to the simulation case, the overcurrent that appeared at the voltage drop moment started to decrease immediately by the voltage amplitude and output power controllers. Unlike the AC power source of the experimental system, the power source on the left hand side of the simulation model of Fig. 13 is an ideal AC source without internal reactance. Therefore the drop in the dc-link voltage during voltage sag was instantaneously compensated by the current from the diode rectifier in the case of simulation. On the other hand, the drop in dc-link voltage was caused by the delay of compensation from the diode rectifier due to internal reactance of the AC power source on the left hand side of Fig. 25 in the case of experiments. This dc voltage drop during voltage sag does not affect the performance of the additional controllers as shown in Figs. 32 and 34.

PCC RMS voltage, power reference calculated by governor, the Alternating Inertia, angular velocity, VSG output active power, and the VSG output reactive power of the system with 1 kW output power reference and with voltage amplitude, output power, and virtual inertia controller subjected to voltage sag type A are depicted in Fig. 32. It is observed that when the voltage dropped, the power reference calculated by governor has been slightly reduced at first, because it is proportional to the square of RMS voltage by the output power control. However, it has been increased by governor afterwards to compensate the frequency drop. It should
be noted that the overcurrent at the sag starting moment has maximum value and should be limited. Accordingly, the reduction in the power reference at the beginning of voltage sag was effective enough to prevent the overcurrent failure in this case. Although, the oscillation after voltage recovery was suppressed by virtual inertia control, still transient excursions are observed in the active and reactive power waveforms. However, the magnitude of the transients was reduced and the inverter was able to ride-through them. It will be shown in the unsymmetrical voltage sag experiment that the virtual inertia control is so effective that even for very lower output power (200 W), the oscillation after voltage recovery does not cause a failure.

In high loading condition, the overcurrent at the sag starting moment is perilous. Figure 33 shows the currents and dc-link voltage of the VSG with 3 kW output power equipped with the additional controllers subjected to the voltage sag type A. Because of the delay in voltage control, output voltage did not follow the grid voltage promptly; therefore, overcurrent appeared right away when the sag starts. However, it fell down quickly and the VSG passed through the sag even with high output power of 3 kW.

PCC RMS voltage, power reference calculated by governor, virtual inertia, angular velocity, VSG output active power, and VSG output reactive power for the experiment case with 3 kW power, and virtual inertia controller subjected to voltage sag type A is shown in Fig. 34. The PCC RMS voltage value shown in this figure is used by voltage amplitude and power controls. As it is observed, the calculated RMS voltage had a delay that resulted in a high current at the beginning of the voltage sag. Nonetheless, the additional controllers limited this current peak to some extent so that the inverter was able to ride-through the voltage sag.

8.2 Unsymmetrical Voltage Sag The VSG unit was also evaluated under unsymmetrical voltage sag condition. A phase-to-phase fault with the line-to-line resistance of 20 Ω happened at the fault point of Fig. 25 which produced type C voltage sag at PCC with 93% remained voltage at phases “a” and “b” as shown in Fig. 35. The PCC RMS voltage value shown in this figure is used by voltage amplitude and power controls. As it is observed, the calculated RMS voltage had a delay that resulted in a high current at the beginning of the voltage sag. Nonetheless, the additional controllers limited this current peak to some extent so that the inverter was able to ride-through the voltage sag.

Fig. 33. Currents and dc-link voltage of the VSG with 3 kW output power and with voltage amplitude, output power, and virtual inertia controller subjected to voltage sag type A

Fig. 34. PCC RMS voltage, power reference calculated by governor, the Alternating Inertia, angular velocity, VSG output active power, and the VSG output reactive power of the system with 3 kW output power reference and with voltage sag type A

Fig. 35. Type C voltage sag at PCC due to phase-to-phase fault

Fig. 36. Currents and dc-link voltage of the VSG with 1 kW output power and without additional controller subjected to voltage sag type C
the sag, and it failed due to overcurrent during the sag when the output power is higher than 2.6 kW. Figure 38 depicts the current and dc-link voltage of the VSG with voltage sag ride-through controllers, at low output power of 200 W, subjected to the unsymmetrical voltage sag type C. It is observed that when the additional controllers were activated, the oscillation after voltage recovery was eliminated and hence, the failure because of the dc-link overvoltage was prevented.

The same experiment was done when the VSG works at 2.6 kW output power and the results are represented in Fig. 39. It is observed that the additional controllers improved the voltage sag ride-through capability of the VSG system effectively. However, there might be a problem in calculating the RMS voltage of the grid during unsymmetrical voltage sag. Since the dq-component voltages oscillate with double system frequency during unsymmetrical voltage sag, the calculated RMS voltage is oscillatory as well. The oscillatory RMS voltage is not suitable to be used as the voltage reference of VSG. The LPF of the voltage amplitude controller removes this oscillation. However, in accordance with the theoretical analysis of Sect. 5.2, the transient current during unsymmetrical voltage sag has double system frequency component that is observed in Figs. 36 to 39.

9. Conclusion

VSG has been invented to support grid stability by inserting virtual inertia into power system. VSG may be vulnerable in fault condition because of its power electronics basis. Evaluation of voltage sag consequences is the first step of VSG protection in fault condition. Symmetrical and unsymmetrical types of voltage sag were applied to the VSG and its response was monitored by checking the transient current. It was observed that when a VSG unit is subjected to a voltage sag, two sorts of transients appear during and after voltage sag that their severity depends on the characteristics of the voltage sag. If a symmetrical voltage sag (type A) lasts a half cycle more than a multiple of full cycles, severest current oscillation appears after voltage recovery. For unsymmetrical voltage sags, initial point-on-wave is also important. The initial points-on-wave of zero and π result in the severest transients during and after voltage sag for both voltage sag types B and D; whereas for type C, π/2 and 3π/2 are the critical ones. With the stated critical initial points-on-wave, the durations of a half cycle more than any multiple of full cycles produce the severest transients after voltage recovery. The origin of the transients was explained by analyzing the state variable trajectory in phase plane. By extracting the radius of the circulations of the transient current in phase plane, the severity of the transients can be evaluated.

Finally, voltage amplitude control and output power control were added to the VSG system to suppress the overcurrent during voltage sag and virtual inertia control was implemented to eliminate the oscillation after voltage recovery. Experimental results showed that the additional controls improved voltage sag ride-through capability of VSG effectively for symmetrical and unsymmetrical voltage sags.

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