Transport in strongly-coupled graphene-LaAlO$_3$/SrTiO$_3$ hybrid systems

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We report on the transport properties of hybrid devices obtained by depositing graphene on a LaAlO$_3$/SrTiO$_3$ oxide junction hosting a 4 nm-deep two-dimensional electron system. At low graphene-oxide inter-layer bias the two electron systems are electrically isolated, despite their small spatial separation, and very efficient reciprocal gating is shown. A pronounced rectifying behavior is observed for larger bias values and ascribed to the interplay between electrostatic depletion and tunneling across the LaAlO$_3$ barrier. The relevance of these results in the context of strongly-coupled bilayer systems is discussed.

Keywords: coupling, graphene, LAO/STO, tunneling

Graphene is widely investigated in view of possible device applications owing to its excellent, electric-field tunable electronic properties,$^1$ its chemical and structural robustness, its ease of production and integrability with a plethora of other material systems.$^2$ In addition, because of its single-atom thickness, its properties can be very sensitive to the local environment. In particular, interaction with the host substrate offers new ways to tune the properties of graphene and the case of functional transition metal oxides is of significant interest. For instance, graphene field-effect transistors (FETs) built on ferroelectric Pb(Zr,Ti)O$_3$ substrates display pronounced memory effects, ultra-low voltage operation,$^3$ and open the way to novel nanoplasmonic devices.$^4$ Similarly, graphene photo-sensitivity was shown to increase 25 times on TiO$_2$ substrates$^5$ and intriguing magnetic phenomena are actively pursued in devices combining graphene with EuO substrates$^6$ or magnetic La$_x$Sr$_{1-x}$MnO$_3$ electrodes.$^7$

In the family of transition metal oxides, interfaces between the bulk band insulators LaAlO$_3$ (LAO) and SrTiO$_3$ (STO) occupy a special place due to their multiple, electric-field tunable properties, such as conductivity$^8,9$ superconductivity$^{10,11}$, magnetism$^{12,13}$ and spin-orbit coupling$^{14}$. These phenomena stem from the emergence of an interfacial two-dimensional electron system (2DES) when more than 3 unit cells (u.c.) of LAO are grown on a TiO$_2$-terminated STO crystal. Such 2DES is located only a few nm below the surface and its properties are therefore extremely sensitive to other materials deposited on this surface, such as metals$^{15}$ or other oxides$^{16}$. In this scenario, hybrid structures combining graphene with LAO/STO junctions represent an exciting platform in which novel phenomena may emerge from the strong electronic coupling of the respective 2DESs.$^8$ In particular, collective interlayer-correlated phases driven by the strong Coulomb interactions are expected at low temperatures, in analogy to what observed in graphene/GaAs/ALGaAs systems.$^{17}$ Furthermore, magnetic or superconducting properties may be induced in graphene due to the proximity interaction with the ordered phases of the interfacial 2DES. In order to enable the exploration of these possibilities, the necessary starting point is a clear understanding of the transport behavior of these hybrid systems. LAO/STO-graphene systems, however, have been so far little studied. In a recent paper$^{18}$, graphene was integrated with insulating LAO/STO substrates with subcritical LAO thickness, where a scanning probe microscope was used to “write” nanometer-wide conductive regions on the oxide system$^{19}$ and graphene was used as a gate electrode controlling the conduction of these nanowires at the LAO/STO interface.

Here we report on extended ($\approx 10^4 \mu m^2$) junctions between monolayer graphene grown by chemical vapor deposition (CVD) and a conductive, 4 nm-deep LAO/STO interface hosting a 2DES. Our results demonstrate that strong electrostatic coupling with virtually no leakage can be obtained at room-temperature (RT) between a two-dimensional hole gas in graphene and the LAO/STO 2DES, as long as the graphene-oxide bias $V_{GO}$ is sufficiently small ($|V_{GO}| \lesssim 1 V$). At larger values of $|V_{GO}|$, we shall report strongly non-linear transport across the vertical graphene-LAO/STO junction that will be linked to tunneling currents between the two electron systems.

The device structure adopted in this work is illustrated in Fig. 1. High-quality 2DESs were obtained by growing 10 u.c.-thick LAO films on TiO$_2$-terminated STO chips by pulsed laser deposition$^{20}$. Cr/Au alignment markers were fabricated by e-beam lithography and thermal evaporation and no additional processing was carried out in order to minimize contamination and damage of the oxide system. Single-crystalline graphene flakes typically measuring 100 µm in diameter were grown by CVD on
FIG. 1. Monolayer of CVD graphene crystal deposited on LAO/STO. (a) Sketch of the device architecture and (b) optical image of a representative three-terminal graphene/LAO/STO device for electrical transport studies at room temperature. Raman microspectroscopy of the contacted CVD graphene monocrystal in a fabricated device, showing the (c) map of the integrated intensity of the 2D peak and the (d) Raman spectroscopic analysis of the graphene crystal. The substrate-free graphene spectrum (red line) was obtained by subtracting Raman spectra acquired on regions covered by/free of graphene (dotted pink/dashed blue); the spectrum is compatible with a single layer crystal and the absence of D peak reveals that graphene’s good quality is maintained during the fabrication procedure.

flat, ex-situ passivated Cu foils. Graphene crystals were finally deposited on the clean surfaces of LAO/STO heterostructures using a polymer-based transfer process and localized thanks to the Cr/Au markers. The device architecture was designed taking particular care to avoid spurious electrical losses across the ≈ 4 nm-thin LAO barrier. An insulating layer of 200 nm of cross-linked PMMA was inserted between the LAO/STO and graphene electrodes (see the sketch of Fig. 1a and the optical picture of Fig. 1b), so that the only electrical path between the contact leads and the LAO/STO 2DES occurred vertically through the graphene-LAO/STO junction. The correct alignment of the flakes and their quality at the end of the process were assessed through Raman spectroscopy, as shown in Fig. 1c,d for one of the studied devices. Given the relatively strong background signal from the LAO/STO substrate, Raman spectra were processed by subtracting the response of the bare oxide heterostructure (see Fig. 1d). Once this background signal was removed, Raman emission clearly indicated that the graphene flake was a high-quality monolayer, with a single-Lorentzian 2D peak, the expected G/2D peak ratio for monolayer graphene and no discernible D peak throughout the crystal. The correct alignment and uniformity of the flake is demonstrated in Fig. 1c, where we report a Raman map integrated over the spectral region 2600 − 2800 cm⁻¹ corresponding to the 2D peak.

Device parameters and structure were chosen to achieve strong electrostatic coupling between the 2DESs in graphene and in the LAO/STO heterojunction, with negligible tunneling in the small interlayer bias (V_{GO}) limit. Based on electron affinity values, we estimate a large (∼ 2 eV) graphene - LAO barrier (see Fig. 2a) that should electrically decouple the two electronic systems. At present many of the band parameters of the junction, in particular the exact band offsets and, consequently, the built-in field in the LAO layer, are still debated and references therein. In addition, the triangular quantum well potential at the LAO/STO interface confines the motion of oxide electrons to a non-trivial set of 2D subbands characterized by different and anisotropic effective masses. In the following, the interface 2DES
will be simply described by a single isotropic 2D subband and charge transport between the two 2DESs will be assumed to occur via direct tunneling across the $\approx 4$ nm LAO barrier. The electrostatic coupling between the two 2DES will be described in terms of a capacitor in which the electrodes have a finite density of states (DOS)\(^{29}\).

Figure 2b reports the predicted evolution of the carrier density in the two 2DESs, setting the values at zero bias to theoretical modeling\(^{29}\). For this measurement, multi-terminal Hall-bar structures were realized by a lift-off technique\(^{30}\) with a nominally identical 10 u.c. LAO/STO heterointerface. The electron densities at 4.2K were found to be $1.6 \times 10^{13}$ cm\(^{-2}\) and $-6 \times 10^{13}$ cm\(^{-2}\) for the LAO/STO and the graphene 2DES respectively. The negative density indicates that the as-deposited graphene is p-doped, as typically observed. Based on this experimental input, the model predicts that graphene can be tuned up to densities of $\sim 10^{13}$ cm\(^{-2}\) both on the electron and hole sides and that LAO/STO can be fully depleted by biasing graphene at a few Volts. Such a strong electrostatic coupling is due to the small thickness of the LAO barrier: assuming $\kappa_{\text{LAO}} = 20$ for the LAO relative permittivity, the geometric capacitance per unit area is estimated to be $4.5 \, \mu\text{F/cm}^2$, corresponding to a gating efficiency $\approx 400$ times higher than available in typical 300 nm thick oxidized Si. As a consequence, the charge-neutrality point (CNP) is expected to occur in graphene at very low (negative) bias values ($|V_{\text{GO}}| < 0.5$ V).

The typical transport and gating properties of a representative graphene-LAO/STO hybrid device are reported in Fig. 3. The insulating properties of the PMMA layer were preliminarily studied on $\approx 200 \times 200 \, \mu\text{m}^2$ test pads covered by unconnected metallic electrodes: negligible ($\lesssim 0.1$ nA) vertical currents were observed for vertical biases in the studied $V_{\text{GO}}$ range. Graphene devices were measured using a piezo-actuated microprobe system from Imina Technologies in order to avoid inducing electrical loss through the PMMA as a consequence of wire-bonding. We studied the RT vertical transport between the oxide interfacial 2DES and graphene by applying a DC bias $V_{\text{GO}}$ to the graphene electrode while holding the interfacial 2DES at ground. As shown in Fig. 3a, a strongly non-linear transport characteristic was observed, where different regimes can be distinguished. Around zero bias, graphene and the 2DES are insulated and no current could be detected above the noise: a current below few pA is measured for $|V_{\text{GO}}| < 0.7$ V, corresponding to a resistance in the range $100 \, \text{G}\Omega - 1 \, \text{T}\Omega$. Based on the measured carrier densities, graphene’s CNP is expected to lie in this voltage range (see Fig. 2b). Outside this interval, vertical transport sets on, with an approximately exponential current-voltage characteristic. For $V_{\text{GO}} < -2.5$ V, vertical current saturates to a fraction of nA: as discussed in the following, this is due to a field-effect depletion of the oxide interfacial 2DES. The exponential growth is much more pronounced for positive bias values: the current grows by 5 orders of magnitude in less than 1V of bias change and it is ultimately limited (for $V_{\text{GO}} > 2$ V) by the resistive load of the 2DES paths connecting the junction area to the electrodes.

Transport in the latter regime is governed only by the vertical junction properties and was therefore amenable to theoretical modeling\(^{29}\). The observed transport characteristics at positive bias are compatible with direct tunneling of electrons between graphene and the interfacial 2DES. In fact, the large graphene-LAO barrier ($\sim 2$ eV) suggests that thermionic emission should be suppressed even at RT, while tunneling remains possible because of the ultra-thin barrier. Zener tunneling across the LAO should be negligibly small for 10 u.c.-
FIG. 3. Pronounced non-linear transport and field-effects in graphene-LAO/STO systems. (a) Graphene-oxide 2DES (black solid) current density as a function of vertical bias $V_{GO}$ measured in a representative device at room temperature and the corresponding graphene-2DES tunneling characteristic calculated in the full WKB (red dashed) and Simmon’s approximation (blue dotted). (b) Room temperature 2-point resistance of the single layer graphene crystal as a function of $V_{GO}$, suggesting the tuning of graphene carrier density across the Dirac point. The insets are schematics of the electrical setups employed for studying the transport properties of the system. The blue and red arrows represent respectively the vertical and in-plane current flow in our device.

In the literature, apart from the barrier effective mass that was obtained by fitting the data in the Simmon’s approximation (dotted blue curve in Fig. 3a)\(^{29}\), Fig. 3a compares the resulting theoretical prediction (red dashed line) with a typical experimental curve (solid black line): the model captures the main transport features in the whole bias range and it is quantitatively accurate in the intrinsic regime, thus providing evidence that graphene-interfacial 2DES transport is indeed dominated by direct tunneling of electrons through the LAO barrier. The remaining discrepancies between the theoretical prediction and experimental data in the intrinsic regime point to the possible role played by the inner structure of the oxide interfacial 2DES\(^{27,28}\) (the population of several subbands changes the transport characteristics), or the possible contribution of tunneling processes assisted by phonons and/or in-gap defect states in the LAO barrier (e.g. O vacancies). The deviations observed for $V_{GO} < 0.5$V are due to electronic noise and interface depletion issues and are not addressed by the present model. Finally, we note that similar rectifying behavior was already reported in metal(Pt,Au)/LAO/STO junctions\(^{11,31}\) and was also attributed mainly to quantum tunneling\(^{31}\). The similarities are due to the metallic behavior of graphene for large positive values of $V_{GO}$, as shown in Fig. 2b, and the comparable electron affinities of graphene and Pt, Au.

In the low inter-layer bias limit, and for the full range $V_{GO} < 1.0$V, graphene and the interfacial 2DES can be considered electrically insulated, with vertical currents lower than 0.5 nA. The existence of this quasi-insulating regime makes it possible to explore Coulomb coupling between the two electron systems. We monitored the 2-point RT resistance of graphene as a function of $V_{GO}$: the result is shown in Fig. 3b for a representative device and demonstrates the significant modulation of the graphene resistivity by the electric field of the 2DES electrode. The resistance curve indicates that graphene can be easily tuned between p-type and n-type conduction at a $V_{GO}$ value as little as a fraction of a Volt, despite the non-negligible intrinsic doping $-6 \times 10^{11}$ cm$^{-2}$. This behavior confirms the extremely large gating efficiency of the system.

The analogous field-effect of graphene on the interfacial 2DES predicted from the capacitor model in Fig. 2b can be inferred both from gating and vertical transport features. In fact, the graphene gating efficiency experiences a sudden drop for $V_{GO} < -1$V and is totally suppressed for $V_{GO} < -2.5$V, as signaled by the resistivity saturation in Fig. 3b. This effect matches the kink and the subsequent current saturation in the vertical transport curve in Fig. 3a, which persists up to $V_{GO} = -90$V (data not shown). These observations provide experimental evidence of the predicted depletion of the 2DES for large negative bias: the creation of an insulating region below graphene interrupts the conducting paths to the LAO/STO electrodes and therefore limits vertical transport.

In conclusion, we reported on the transport properties...
of the graphene-LAO/STO hybrid system and showed that it is characterized by strong electrostatic coupling at low inter-layer bias and direct tunneling coupling at large bias values. Efficient gating could be fruitfully exploited for the realization of hybrid “dual” FETs. with very-low operating voltage, where graphene can be employed either as gate, or as the conducting channel back-gated by the 2DES. The present device architecture can be relevant to the investigation of collective phases with very strong inter-layer correlations at low temperatures.

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Supplemental Materials

Here we summarize details on the theoretical modeling of electrostatic effects and inter-layer tunneling in graphene-LaAlO$_3$/SrTiO$_3$ hybrid systems.

I. ELECTROSTATIC CONSIDERATIONS

The vertical voltage bias $V_{GO}$ is defined as the electro-chemical potential difference between the two-dimensional electron system (2DES) in graphene and the 2DES at the oxide interface (the “interfacial 2DES”), i.e.

$$V_{GO} = \phi_G + \frac{\mu_G + E_{G}^{(0)}}{-e} - \phi_O - \frac{\mu_O + E_{O}^{(0)}}{-e}, \quad (SE1)$$

where $-e$ is the electron charge, $\phi_{G(O)}$ is the electric potential of the graphene sheet (interfacial 2DES), $E_{G(O)}^{(0)}$ is the energy of the Dirac point (bottom of the 2D subband, represented by the red line inside the triangular well in Fig. 2a) of graphene (the interfacial 2DES) measured from the vacuum level, and $\mu_{G(O)}$ is the corresponding chemical potential measured with respect to the Dirac point (2D subband edge).

The two 2DESs can be modeled as a parallel plate capacitor. The electric potential difference can therefore be written as:

$$\phi_G - \phi_O = \frac{e(n_G - n_O)}{2e'\epsilon_T} + \tilde{\phi} = \frac{e(n_G - n_O)}{2C} + \tilde{\phi}. \quad (SE2)$$

Here, $n_{G(O)}$ is the density of free electrons in the graphene sheet (interfacial 2DES), $\epsilon_T$ is the relative dielectric constant of the barrier material, $\phi$ is a contribution arising from bound charges in the system (e.g., charges bounded at the termination surface of LAO, charged impurities adsorbed on the graphene flake, etc.), and $C = \epsilon_0\epsilon_T/d$ is the geometrical capacitance of the junction per unit area.

In the $T \to 0$ limit the chemical potential coincides with the Fermi energy,

$$\mu_G(n_G, T = 0) = \hbar v_F \sqrt{2n_G} \quad (SE3)$$

$$\mu_O(n_O, T = 0) = \frac{\pi \hbar^2}{m_b} n_O. \quad (SE4)$$

In writing the previous equation we have dramatically simplified the description of the interfacial 2DES: we assume that it occupies a single 2D subband of the interfacial triangular quantum well, and assume the subband dispersion to be parabolic and isotropic with an effective band mass $m_b$. Following Ref. S1, we identify the band mass with the mass of light quasiparticles and set $m_b \sim m_e$, where $m_e$ is the bare electron mass in vacuum.

In Eq. (SE3), $v_F \sim 10^6$ m/s is the graphene Fermi velocity. At finite temperature $T$, $\mu_O(n_O, T)$ and $\mu_G(n_G, T)$ can be found numerically. Increasing temperature, the chemical potential moves towards (away from) the Dirac point (subband edge) in graphene (the interfacial 2DES).

At zero bias, Eq. (SE1) reduces to

$$0 = \frac{e(n_{O}^{(0)} - n_{G}^{(0)})}{2C} - \tilde{\phi} + \mu_G^{(0)} - \mu_O^{(0)} - E_{O}^{(0)} + E_{G}^{(0)} \frac{-e}{\epsilon}, \quad (SE5)$$

where $n_{G(O)}^{(0)}$ are the electron densities at zero bias, while $\mu_{O(G)}^{(0)} = \mu_{O(G)}(n_{O(G)}^{(0)}, T)$ are the chemical potentials at zero bias.

We now assume that, increasing the bias voltage, charge is transferred only between graphene and the interfacial 2DES. We write the carrier densities as $n_{G(O)} = n_{G(O)}^{(0)} \mp \delta n$, where the minus (plus) sign applies to graphene (the interfacial 2DES). We can therefore write an equation for the unknown $\delta n$, which can be easily solved numerically. It reads as following

$$V_{GO} = \frac{e\delta n}{C} + \frac{\mu_G(n_G^{(0)} - \delta n, T) - \mu_G^{(0)}}{-e} - \frac{-\mu_O(n_O^{(0)} + \delta n, T) - \mu_O^{(0)}}{-e}. \quad (SE6)$$

The input parameters that are needed to solve this equation are: i) the capacitance per unit area $C$, ii) the electron densities $n_{G(O)}^{(0)}$ at zero bias, iii) the effective band mass $m_b$ of the interfacial 2DES, and iv) temperature $T$.

The solution of this equation for $n_G^{(0)} = -0.7 \times 10^{11}$ cm$^{-2}$, $n_O^{(0)} = 1.6 \times 10^{13}$ cm$^{-2}$, $C = 3.3 \mu$F/cm$^2$, and $T = 300$ K is reported in Fig. 2b of the main text.
II. THEORY OF THE INTER-LAYER TUNNELING CURRENT BETWEEN GRAPHENE AND THE INTERFACIAL 2DES

The current density flowing from graphene to the interfacial 2DES can be written as:

\[ J = \frac{e}{S} \sum_{\alpha \in \mathcal{O}} \sum_{\beta \in \mathcal{G}} W_{\alpha\beta}(n_{\alpha} - n_{\beta}), \tag{SE7} \]

where \( \alpha \) (\( \beta \)) labels the electronic states localized at the oxide interface (in the graphene layer), \( n_{\alpha(\beta)} \) is the occupation probability of the state \( \alpha \) (\( \beta \)), \( W_{\alpha\beta} \) is the transition probability per unit time from the state \( \alpha \) to the state \( \beta \), and \( S \) is the surface of the device.

In deriving Eq. (SE8) we have used the following identity

\[ \delta(\epsilon + E^{(0)}_\alpha - \epsilon \phi_\alpha - \epsilon \beta - E^{(0)}_\beta + e\phi_\beta) = \int_{-\infty}^{\infty} d\epsilon \delta(\epsilon - \epsilon_\alpha - \frac{e^2\delta n}{2C} + \mu_\alpha^{(0)}) \delta(\epsilon - \epsilon_\beta + \frac{e^2\delta n}{2C} + \mu_\beta^{(0)}) \delta(\epsilon - \gamma^{(0)}) w(\epsilon_\alpha)[f_{FD}(\epsilon_\alpha - \mu_\alpha) - f_{FD}(\epsilon_\beta - \mu_\beta)]. \tag{SE8} \]

Wentzel-Kramers-Brillouin (WKB) approximation:

\[ T_{TB}(\epsilon, \Delta_1, \Delta_2) = \exp \left[ -\frac{\alpha}{d} \int_0^x \frac{d\Delta_1 + (\Delta_2 - \Delta_1)x}{d} - \epsilon \right] \]
\[ = \exp \left[ -\frac{2\alpha}{3} \Theta(\Delta_2 - \epsilon) \frac{(\Delta_2 - \epsilon)^{3/2}}{\Delta_2 - \Delta_1} \right] \times \exp \left[ -\frac{2\alpha}{3} \Theta(\Delta_1 - \epsilon) \frac{(\Delta_1 - \epsilon)^{3/2}}{\Delta_1 - \Delta_2} \right]. \tag{SE11} \]

where \( \Theta(x) \) is the usual Heaviside step function and \( \alpha = 2\sqrt{2m^*d}/h \) with \( m^* \) being the conduction band effective mass in the LAO barrier, \( x^* \) is the classical turning point of the electron trajectory, while \( \Delta_{1(2)} \) are the heights of the two sides of the barrier.

Introducing the density-of-states per unit area of the interfacial 2DES and graphene \( D_{O(G)}(\epsilon) = S^{-1} \sum_{\alpha \in \mathcal{O(G)}} \delta(\epsilon - \epsilon_\alpha) \) or, more explicitly, \( D_O(\epsilon) = n_b/(\pi\hbar^2) \), and \( D_G(\epsilon) = 2|\epsilon|/(\pi\hbar^2v_F^2) \), we find the final result for the tunneling current:

\[ J = \frac{2\pi e \Gamma^2}{h} \int_{-\infty}^{\infty} d\epsilon \ D_{O(G)} \left( \epsilon + \mu_\alpha^{(0)} - \frac{e^2\delta n}{2C} \right) D_G \left( \epsilon + \mu_\beta^{(0)} + \frac{e^2\delta n}{2C} \right) T_{TB}(\epsilon, \Delta_1, \Delta_2) \]
\[ \times \left[ f_{FD}(\epsilon_\alpha - \mu_\alpha^{(0)} - \frac{e^2\delta n}{2C}) - f_{FD}(\epsilon_\beta + \mu_\beta^{(0)} + \frac{e^2\delta n}{2C}) \right]. \tag{SE12} \]
In writing the previous equation, we have introduced the barrier height at the graphene-LAO interface with respect to the common Fermi level at zero bias \( \Delta_G = \Delta_O + \phi - e^2 (n_O^{(0)} - n_G^{(0)})/(2C) \). Eq. (SE12) represents the main result of our model and has been used to generate the vertical transport curve shown in red dashed line in the Fig. 3a of the main text.

We now make some simplifying assumptions to obtain an approximate analytical result for the tunneling current density \( J \), in order to both compare it with well-known formulas for 3D-to-3D tunneling\(^{S2-S5}\) and use it to fit the experimental data.

Neglecting quantum capacitance effects (i.e. the variation of the chemical potentials \( \mu_G(0) \) with density) Eq. (SE12) reduces to

\[
J = \frac{2\pi e^2}{\hbar} \int_{-\infty}^{\infty} d\epsilon \, D_O \left( \epsilon + \mu_O^{(0)} - \frac{eV_{GO}}{2} \right) D_G \left( \epsilon + \mu_G^{(0)} + \frac{eV_{GO}}{2} \right) T_{TB} \left( \epsilon, \Delta_O + \frac{eV_{GO}}{2}, \Delta_G - \frac{eV_{GO}}{2} \right)
\]

Setting \( T = 0 \) and approximating the density-of-states of each 2DES with its value calculated at the chemical potential, we obtain

\[
J \approx A \int_{-eV_{GO}/2}^{eV_{GO}/2} d\epsilon \, T_{TB} \left( \epsilon, \Delta_O + \frac{eV_{GO}}{2}, \Delta_G - \frac{eV_{GO}}{2} \right),
\]

where \( A = 2\pi e^2 D_O(\mu_O^{(0)}) D_G(\mu_G^{(0)})/\hbar \).

We notice that between the noise floor and the on-set of resistive saturation, i.e. for \( 0 < V_{GO} < 1.8 \text{ V} \), the transport curves, as described in the following for the tunneling current on applied bias, which hinders the reproduction of the experimental data through direct fitting; therefore an indirect route was chosen to reproduce the transport curves, as described in the following for the representative curve in Fig. 3a in the main text.

III. ANALYTICAL FITTING PROCEDURE FOR POSITIVE BIAS

Eq. (SE12) reveals a very complicated dependence of the tunneling current on applied bias, which hinders the reproduction of the experimental data through direct fitting; therefore an indirect route was chosen to reproduce the transport curves, as described in the following for the representative curve in Fig. 3a in the main text.
for the particular curve shown in Fig. 3a, the \( J - V_{GO} \) relationship reduces to a simple exponential function, as indicated by the linear dependence exhibited in the semi-logarithmic plot. The blue dotted curve in Fig. 3a represents the best fit to the data in this bias range with a two-parameter exponential function

\[
J = c_1 \exp(c_2 V_{GO}),
\]

yielding \( c_1 = 3.6 \times 10^{-5} \mu\text{A}/\text{cm}^2 \) and \( c_2 = 11 \text{ V}^{-1} \).

To compare this result with our theoretical model, we need a simplified version of Eq. SE16. To this end, we notice that for typical values of \( \alpha \approx 40 \) and positive bias only the first exponential term in Eq. SE16 is relevant, the second being suppressed by several orders of magnitude. Fixing a reference point \( V^* = 1.3 \text{ V} \) at the center of the relevant bias interval and expanding to first order in \( V_{GO} - V^* \), we obtain

\[
J \approx \text{Const}(A, \alpha, \bar{\Delta}, V^*) \times \exp \left( \frac{\alpha e V_{GO}}{4 \sqrt{\bar{\Delta} - eV^*/2}} \right), \tag{SE18}
\]

so that we can identify \( c_2 \) with \( \alpha e / \left( 4 \sqrt{\bar{\Delta} - eV^*/2} \right) \).

We estimate an average barrier height \( \bar{\Delta} \approx 2 \text{ eV} \) and consequently extract a value of \( m^* = 1.5m_e \), which is significantly larger than previously reported values.\(^{86,87}\)

The barrier is estimated in the following way: combining the reported electron affinities values of graphene (4.6 eV)\(^{88}\) and LAO (2.4 eV)\(^{86,89}\) and the experimentally measured \( n_G^{(0)} \), yields \( \Delta_G = 2.3 \text{ eV} \); furthermore, by assuming typical values for the conduction band offset (2 eV) at the oxide interface\(^{22}\), the triangular well confinement energy (0.1 – 0.2 eV), the interfacial subband effective mass \( m_0 = m_e \)\(^{81,86}\) and using the experimentally measured \( n_O^{(0)} \), we obtain \( \Delta_O \approx 1.8 \text{ eV} \). The parameters concerning the electrostatics of the junction, i.e \( d = 4 \text{ nm}, \epsilon_r = 15 \), and the zero bias densities of the two electron systems were chosen the same as in Fig. 2. With these parameters, we generated the RT vertical transport characteristics in the full WKB approximation shown in dashed red line in Fig. 3a. The curve reproduces qualitatively the observed transport behavior in the whole bias range and is quantitatively accurate in the intrinsic bias range, providing evidence that the vertical graphene-oxide transport is dominated by direct tunneling of electrons through the LAO barrier.

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