A consensus-based secondary control layer for stable current sharing and voltage balancing in DC microgrids

Michele Tucci\textsuperscript{1}, Lexuan Meng\textsuperscript{2}, Josep M. Guerrero\textsuperscript{2}, and Giancarlo Ferrari-Trecate\textsuperscript{3}

\textsuperscript{1}Dipartimento di Ingegneria Industriale e dell’Informazione, Università degli Studi di Pavia, Italy
\textsuperscript{2}Institute of Energy Technology, Aalborg University, Denmark
\textsuperscript{3}Automatic Control Laboratory, École Polytechnique Fédérale de Lausanne (EPFL), Switzerland

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Abstract

In this paper, we propose a secondary consensus-based control layer for current sharing and voltage balancing in DC microGrids (mGs). To this purpose, we assume that Distributed Generation Units (DGUs) are equipped with decentralized primary controllers guaranteeing voltage stability. This goal can be achieved using, for instance, Plug-and-Play (PnP) regulators. We analyze the behavior of the closed-loop mG by approximating local primary control loops with either unitary gains or first-order transfer functions. Besides proving exponential stability, current sharing, and voltage balancing, we describe how to design secondary controllers in a PnP fashion when DGUs are added or removed. Theoretical results are complemented by simulations, using a 7-DGUs mG implemented in Simulink/PLECS, and experiments on a 3-DGUs mG.
1 Introduction

Power generation and distribution are rapidly changing due to the increasing diffusion of renewable energy sources, advances in energy storage, and active participation of consumers to the energy market \cite{1,2,3}. This shift of paradigm has motivated the development of microgrids (mGs), commonly recognized as small-scale power systems integrating Distributed Generation Units (DGUs), storage devices and loads. Since AC power generation is the standard for commercial, residential, and industrial utilization, several studies focused on the control of AC mGs \cite{4,5,6,7,8}. However, nowadays, DC energy systems are gaining interest \cite{9,10} because of the increasing number of DC loads, the availability of efficient converters, and the need of interfacing DC energy sources and batteries with minimal power losses \cite{3,10,11}. As reviewed in \cite{10}, DC mGs are becoming more and more popular in several application domains, such as avionics, automotive, marine and residential systems \cite{10}.

The basic problems in control of DC mGs are voltage stabilization \cite{9,12,13,14,15,16,17} and current sharing (or, equivalently, load sharing), the latter meaning that DGUs must compensate constant load currents proportionally to given parameters (for example, the converter ratings) and independently of the mG topology and line impedances. Current sharing is crucial for preserving the safety of the system, as unregulated currents may overload generators and eventually lead to failures or system blackout \cite{18}. An additional desirable goal is voltage balancing, i.e. to keep the average output voltage of DGUs close to a prescribed level. Indeed, load devices are designed to be supplied by a nominal reference voltage: it is therefore important to ensure that the voltages at the load buses are spread around this value.

These objectives can be realized using hierarchical control structures. In particular, current sharing is often realized by coupling secondary-layer consensus algorithms with primary voltage controllers. Secondary regulators, however, can have a detrimental effect and closed-loop stability of the mG must be carefully analyzed. In order to guarantee stability, several existing approaches focus on specific mG topologies or provide centralized control design algorithms, i.e. the synthesis of a local controller for a DGU requires knowledge about all other DGUs and lines. For instance, in \cite{19,20} only mG with a bus topology are considered and local control design is performed in a centralized fashion. The synthesis procedures proposed in \cite{21,22} suffer from similar limitations. Indeed, in these works, the computation of local controllers guaranteeing collective stability relies on the knowledge of a global closed-loop transfer function matrix, and the largest eigenvalue of the Laplacian matrix associated with the communication network. Consensus-based secondary controllers for mG with general topologies have been presented in \cite{23}: also in this case, however, stability and the closed-loop mG equipped with primary and secondary control layers is studied through centralized analysis (i.e. the root locus), or via simulations \cite{9}. Similar drawbacks affect the approach in \cite{24}, whereas, in \cite{25}, current sharing is achieved by means of a centralized controller which receives informations from all the DGUs in the mG. All the synthesis algorithms mentioned above become prohibitive for large mGs. Moreover, they are unsuitable for mGs with flexible structure because, to preserve voltage stability, the plugging-in or -out of DGUs might require to update all local controllers in the mG. This motivated the development of scalable design procedures for local controllers as in \cite{12,26,13}. In \cite{12} and \cite{26}, the aim is to stabilize the voltage only via primary decentralized controllers. These regulators, termed Plug-and-Play (PnP) have the following features: (i) the existence of a local controller for a DGU can be tested on local hardware, and control design is cast into an optimization problem, (ii) each optimization problem exploits information about the DGU only \cite{26} or, at most, the power lines connected to it \cite{12}, and (iii) when a DGU is plugged-in, no other DGUs \cite{26}, or at most neighboring DGUs \cite{12}, must update their local controllers.

Paper contributions. In this paper, we present a secondary regulation scheme for achieving stable current sharing and voltage balancing in DC mGs. We assume that the proposed higher-level scheme is build on top of a primary stabilizing voltage control layer; moreover, similarly to \cite{13}, at the secondary level we exploit consensus filters requiring DGUs to communicate in real-time over a connected network. There is, however, a key difference between our approach and the
one presented in [13]. In the latter work, the authors assume DGUs to be controllable current sources; on the contrary, we consider primary-controlled DGUs behaving as voltage generators. In this setting, by properly choosing the reference values for the output voltages of each DGU, one can always regulate the currents that flow through the power lines, even when load conditions change. In this way, unwanted circulating currents (which may open the safety breakers in order to prevent damages on the devices) can be avoided. Instead, if all the DGUs in the mG are ideal current sources, the primary regulation scheme alone cannot guarantee such control on the flowing currents [13].

At the modeling level, we propose two abstractions for the DGUs controlled with primary voltage regulators: unit gain and first-order transfer function approximation. The first one is used only for tutorial purposes and for developing basic mathematical tools that will allow us to extend the key results to the second (more realistic) approximation of the primary loops.

Another contribution of this paper is the study of the eigenstructure of the product of three matrices (LDM), where (i) $L$ and $M$ are the graph Laplacians associated with the electrical and the communication graphs, respectively, and (ii) $D$ is a diagonal positive definite matrix defining the desired ratios between balanced currents. While several studies focused on the properties of the product of stochastic matrices (see e.g. [27]), which are central in discrete-time consensus, to our knowledge weighted products of Laplacians received much less attention. In particular, we show that, under two different conditions, $LDM$ preserves some key features of Laplacian matrices. In this case, the asymptotic achievement of current sharing and voltage balancing in a globally exponentially stable fashion is proved.

Finally, we provide an experimental validation of our approach using a lab-size DC mG. The results show the robustness of the proposed controllers to non-idealities that are unavoidably present in a real mG.

**Paper organization.** The paper is organized as follows. Section 2 summarizes the electrical model of DGUs and PnP controllers. The secondary control layer is developed and analyzed in Sections 3 and 4. In particular, Section 4.4 shows that, similarly to the regulators in [12] and [26], our knowledge weighted products of Laplacians received much less attention. In particular, we present experimental results and more detailed simulations.

A preliminary version of the paper will be presented at the 20th IFAC World Congress. Different from the conference version, the present paper includes (i) the proofs of Propositions 2-5 and Theorem 1, (ii) the more realistic case where primary control loops are approximated with first-order transfer functions, and (iii) experimental results and more detailed simulations.

**Notation and basic definitions.** The cardinality of the finite set $S$ will be denoted with $|S|$. A weighted directed graph (digraph) $G = (V, E, W)$ is defined by the set of nodes $V = \{1, \ldots, n\}$, the set of edges $E \subseteq V \times V$ and the diagonal matrix $W \in \mathbb{R}^{[|V| \times |E|]}$ with $W_{ii} = w_i$, where $w_i \in \mathbb{R}$ is the weight associated with the edge $e_i \in E$. The set of neighbors of node $i \in V$ is $N_i = \{j : (i, j) \in E \text{ or } (j, i) \in E\}$. A digraph $G$ is weakly connected if its undirected version is connected [29]. $Q(G) \in \mathbb{R}^{[|V| \times |E|]}$ is the incidence matrix of $G$ [30]. The Laplacian matrix of $G$ is $L(G) = Q(G)WQ(G)^T$, and it is independent of the orientation of edges.

The average of a vector $v \in \mathbb{R}^n$ is $\langle v \rangle = \frac{1}{n} \sum_{i=1}^{n} v_i$. We denote with $H^1$ the subspace composed by all vectors with zero average [31, 32] i.e. $H^1 = \{ v \in \mathbb{R}^n : \langle v \rangle = 0 \}$. The space orthogonal to $H^1$ is $H^1_\perp$. It holds $H^1_\perp = \{ \alpha 1_n, \alpha \in \mathbb{R} \}$ and dim($H^1_\perp$) = 1. Moreover, the decomposition $\mathbb{R}^n = H^1 \oplus H^1_\perp$ is direct, i.e. each vector $v \in \mathbb{R}^n$ can always be written in a unique way as

$$v = \hat{v} + \bar{v} \quad \text{with } \hat{v} \in H^1 \text{ and } \bar{v} \in H^1_\perp. \tag{1}$$

Consider the matrix $A \in \mathbb{R}^{n \times n}$. With $A(H^1|H^1)$ we indicate the linear map $A : H^1 \rightarrow H^1$ (i.e. the restriction of the map $A : \mathbb{R}^n \rightarrow \mathbb{R}^n$ to the subspace $H^1$). For a subspace $V \subset \mathbb{R}^n$, we denote
with $P_V(v)$ the projection of $v \in \mathbb{R}^n$ on $V$. The subspace $V \subset \mathbb{R}^n$ is said to be $A$-invariant if $v \in V \Rightarrow Av \in V$. Moreover, with $\lambda_i(A), i = 1, \ldots, n$, we denote the eigenvalues of $A$.

Let $A \in \mathbb{R}^{n \times n}$ be a matrix with real eigenvalues. The inertia of $A$ is the triple $i(A) = (i_+(A), i_-(A), i_0(A))$, where $i_+(A)$ is the number of positive eigenvalues of $A$, $i_-(A)$ is the number of negative eigenvalues of $A$, and $i_0(A)$ is the number of zero eigenvalues of $A$, all counted with their algebraic multiplicity [33]. We use $A > 0$ for indicating that the real symmetric matrix $A$ is positive-definite.

Laplacian matrices have the key properties summarized in the next Proposition [34, 35, 31].

**Proposition 1.** For a weakly connected graph $\mathcal{G}$ with weights $w_{ij} > 0$, $A = L(\mathcal{G}) \in \mathbb{R}^{n \times n}$ has the following properties:

1. it has non positive off-diagonal elements;
2. $\lambda_1(A) \geq \cdots \geq \lambda_{n-1}(A) \geq 0 = \lambda_n$;
3. $\ker(A) = H^1_1$ and $\text{range}(A) = H^1_1$;
4. $A(H^1 | H^1)$ is invertible.

**Proof.** Points (i)–(iii) are shown, e.g. in [34, 35]. Point (iv) has been shown in [31] with the framework of partial difference equations. Next, we provide a proof based on linear algebra only. We start noticing that the linear map $A(H^1 | H^1)$ invertible if it is surjective and injective [26].

First, we show the surjectivity of $A$ on $H^1$. By construction, $\text{rank}(A) = n - 1$ because $\text{rank}(A) = \text{dim}(\text{range}(A)) = \text{dim}(\mathbb{R}^n) - \text{dim}(H^1_1) = n - 1$.

Moreover, since $A$ is symmetric, each column of $A$ has zero sum and hence is a vector in $H^1$. Since $\text{range}(A)$ is the column span, then $\text{range}(A) \subset H^1$. Since $\text{dim}(H^1) = n - 1$, one obtains $\text{range}(A) = H^1$. This proves that the map $A(H^1 | H^1)$ is surjective.

Next, we prove that $A(H^1 | H^1)$ is also injective. By definition, this holds if

$$\forall b \in H^1 \ \forall x, y \in H^1 \ (Ax = b \text{ and } Ay = b) \Rightarrow x = y.$$ 

Now, $Ax = Ay = b$ implies that $A(x - y) = 0$. It means that $x - y \in \ker(A)$, therefore $\exists \alpha \in \mathbb{R}$ such that $x - y = \alpha 1_n$. However, since $x - y \in H^1$, $x - y = \alpha 1_n$ is verified only for $\alpha = 0$; this leads to $x = y$.

### 2 Plug-and-play primary voltage control

#### 2.1 DGU electrical model

As in [12], we consider a DC mG composed of $N$ DGUs, whose electrical scheme is shown in Figure 1. In each DGU, the generic renewable resource is modeled as a battery and a Buck converter is used to supply a local load connected to the Point of Common Coupling (PCC) through an RLC filter. Furthermore, we assume that loads $I_{Li}$ are unknown and treated as current disturbances [12, 26]. The controlled variable is the voltage at each PCC, whereas the control input is the command to the Buck (indicated with $V_{bi}$). From Figure 1 by applying Kirchhoff’s voltage and current laws and exploiting Quasi Stationary Line (QSL) approximation of power lines [12, 26], we obtain the following model of DGU $i$.

$$\begin{align*}
\text{DGU } i : & \\
\frac{dV_i}{dt} = \frac{1}{C_{ti}} I_{ti} + \sum_{j \in N_i} \left( \frac{V_j}{C_{ti}R_{ij}} - \frac{V_i}{C_{ti}R_{ij}} \right) - \frac{1}{C_{ti}} I_{Li} \\
\frac{dI_{ti}}{dt} = \frac{1}{L_{ti}} V_i - \frac{R_{ti}}{L_{ti}} I_{ti} + \frac{1}{L_{ti}} V_{bi}
\end{align*}$$

\footnote{For the detailed model derivation, we defer the reader to [12].}
where \((V_{ti}, I_{Li})\) are the inputs, \((V_i, I_{Li})\) the states (i.e. the measured voltage at the \(i\)-th PCC and \(i\)-th output current, respectively), \(V_j\) is the voltage at the PCC of each neighboring DGU \(j \in \mathcal{N}_i\), the constants \(R_{ti}, C_{ti}, L_{ti}\) identify the electrical parameter of the \(i\)-th Buck filter, and \(R_{ij}\) is the conductance of the power line connecting DGUs \(i\) and \(j\) (see Figure 1).

### 2.2 Plug-and-play regulators

In this Section, we briefly summarize the PnP scalable approach in [12, 26] for designing primary decentralized controllers guaranteeing voltage stability in DC mGs. This will allow us to justify the approximations of primary control loops used in Section 4. Moreover, we will describe local updates that must be performed when DGUs are added or removed. These operations will be mirrored by those described in Section 4.4 for updating local secondary controllers, hence showing that both the primary and secondary control layer can be designed in a modular and scalable fashion.

The local regulator of DGU \(i\) exploits measurements of \(V_i\) and \(I_{Li}\) to compute the command \(I_{Li}\) of the \(i\)-th Buck converter and make \(V_i\) track a reference signal \(V_{ref,i}\) (see the scheme in Figure 1). Each controller is composed of a vector gain \(K_i\) and an integral action is present for offset-free voltage tracking. The decentralized computation of these vector gains is the core of PnP controller synthesis: (i) the design of \(K_i\) requires knowledge of the dynamics of DGU \(i\) only [26] or, at most, the parameters of power lines connecting it to its neighbors [12], (ii) \(K_i\) is automatically obtained by solving a Linear Matrix Inequality (LMI) problem.

For modeling the electrical interactions between multiple DGUs, we represent the mG with a digraph \(G_{el} = (\mathcal{V}, \mathcal{E}_{el}, W)\) (see the example in Figure 2), where (i) each node is a DGU with local PnP controller and local current load, (ii) edges \((i, j)\) are power lines whose orientation define a reference direction for positive currents, (iii) weights are line conductance \(\frac{1}{R_{ij}}\), and (iv) we set \(N = |\mathcal{V}|\) and \(M = |\mathcal{E}_{el}|\).

Next, we describe how to handle plugging -in/-out of DGUs while preserving the stability of the mG. Whenever a DGU (say DGU \(i\)) wants to join the network (e.g. DGU 6 in Figure 2), it sends a plug-in request to its future neighbors, i.e. DGUs \(j \in \mathcal{N}_i^{cd}\) (e.g. DGUs 1 and 5 in Figure 2). Then, DGU \(i\) [26] or each DGU in the set \(\{i\} \cup \mathcal{N}_i^{cd}\) [12] solves an LMI problem ((24) in [26] and (25) in [12]) that, if feasible, gives a vector gain \(K_i\) guaranteeing voltage stability in the whole mG after the addition of DGU \(i\). Otherwise, if one of the LMIs is infeasible, the plug-in of DGU \(i\) is denied and no update of matrices \(K_j\), \(j \in \mathcal{N}_i^{cd}\) is performed.

The unplugging of a DGU (say DGU \(m\)) follows a similar procedure. It is always allowed without redesigning any local controller [26] or it requires to successfully update, at most, controllers of DGUs \(k, k \in \mathcal{N}_m^{cd}\) before allowing the disconnection of the DGU [12].

**Remark 1.** Local PnP controllers can be enhanced with pre-filters so as to shape in a desired way the transfer function \(F_{ii}(s)\) between voltages \(V_{ref,i}\) and \(V_i\) represented in Figure 1. The closed-loop transfer function \(F_{ii}(s)\) has 3 poles and in the sequel it will be approximated by a unit gain or a first-order system. The first approximation will be used mainly for tutorial reasons. The second one is very mild at low and medium frequencies, as can be noticed from the Bode plots of \(F_{ii}(s)\) in [12]. Moreover, the presence of an integrator in Figure 1 allows the voltage \(V_i\) at the PCC to track constant references without offset when the disturbances (i.e. load currents \(I_{Li}\)) are constant.

### 3 Secondary control based on consensus algorithms

#### 3.1 Control objectives

Primary stabilizing controllers, such as the PnP regulators described in Section 2.2, have the goal of turning DGUs into controlled voltage generators, i.e. to approximate, as well as possible, the
identity $V_i = V_{ref,i}$. As such, they do not ensure current sharing and voltage balancing, defined in the sequel.

**Definition 1.** For constant load currents $I_{Li,i} = 1, \ldots, N$, current sharing is achieved if, at steady state, the overall load current is proportionally shared among DGUs, i.e. if

$$\frac{I_{ti}}{I_{si}} = \frac{I_{tj}}{I_{sj}} \quad \text{for all } i, j \in V,$$

where $I_{si} > 0$ are constant scaling factors.

We recall that current sharing is desirable in order to avoid situations in which some DGUs are not able to supply local loads, thus requiring power from other DGUs. A very common goal is to make DGUs share the total load current proportionally to their generation capacity. This can be obtained by measuring the output currents in per-unit (p.u.), i.e. setting each scaling factor $I_{si}$ in (2) equal to the corresponding DGU rated current (see Section 5.1 for an example). On the other hand, if the scaling factors are all identical, the current sharing condition becomes

$$I_{ti} = \langle I_L \rangle \quad i = 1, \ldots, N,$$

where $I_L = [I_{L1}, I_{L2}, \ldots, I_{LN}]^T$ is the vector of the local load currents.
Assumption 1. Voltage references are identical for all DGUs, i.e. \( V_{\text{ref}, i} = V_{\text{ref}} \), \( \forall i \in V \).

Definition 2. Under Assumption 1, voltage balancing is achieved if

\[
(V) = V_{\text{ref}}.
\] (4)

where vector \( V = [V_1, V_2, \ldots, V_N]^T \) collects the PCC voltages.

In order to guarantee current sharing and voltage balancing, we use a consensus-based secondary control layer, as described next.

### 3.2 Consensus dynamics

Consensus filters are commonly employed for achieving global information sharing or coordination through distributed computations [37, 29]. In our case, as shown in Figure 3, we adopt the following consensus scheme for adjusting the references of each primary voltage regulator

\[
\dot{\Delta V}_i(t) = -k_l \sum_{j=1,j\neq i}^N a_{ij} \left( \frac{I_{ti}(t)}{I_{ts}} - \frac{I_{tj}(t)}{I_{ts}} \right),
\] (5)

where \( a_{ij} > 0 \) if DGUs \( i \) and \( j \) are connected by a communication link \( (a_{ij} = 0, \text{otherwise}) \), and the coefficient \( k_l > 0 \) is common to all DGUs. The use of consensus protocols has been thoroughly studied for networks of agents with simple dynamics, e.g. simple integrators [37, 29], with the goal of proving convergence of individual states to a common value. In our case, however, (5) is interfaced with the mG dynamics and convergence of currents \( I_{ti} \) to the same value does not trivially follow from standard consensus theory. This property will be rigorously analyzed in Section 4.

In the sequel, we assume bidirectional communication, i.e. \( a_{ij} = a_{ji} \). The corresponding communication digraph is \( G_c = (V, \mathcal{E}_c, W_c) \) where \( (i, j) \in \mathcal{E}_c \iff a_{ij} > 0 \) and \( W_c = \text{diag}(a_{ij}) \).

Considerations on the topologies of \( G_c \) and \( G_{el} \) guaranteeing stable current sharing and voltage balancing are detailed in Section 4.2. In all cases, however, the following standing assumption must hold.

Assumption 2. The graph \( G_{el} \) is weakly connected. The graph \( G_c \) is undirected and connected.

From a system point of view, the collective dynamics of the group of DGUs following (5) can be expressed as

\[
\dot{\Delta V} = -k_l L D \mathbf{I}_t,
\] (6)

where \( \Delta V = [\Delta V_1, \ldots, \Delta V_N]^T = V - V_{\text{ref}}, V_{\text{ref}} = [V_{\text{ref}, 1}, V_{\text{ref}, 2}, \ldots, V_{\text{ref}, N}]^T, \mathbf{I}_t = [I_{t1}, I_{t2}, \ldots, I_{tN}]^T, D = \text{diag} \left( \frac{1}{I_{ts}^1}, \ldots, \frac{1}{I_{ts}^N} \right) = \text{diag}(d_1, \ldots, d_N) \) and \( L = \mathcal{L}(G_c) \). Note that \( L \) is the Laplacian matrix of \( G_c \) with \( W_c \) replaced by \( k_l W_c \).

### 4 Modeling and analysis of the complete system

The hierarchical control scheme of a DGU equipped with primary and secondary regulators is shown in Figure 3. For studying the behavior of the closed-loop mG, we first approximate DGUs under the effect of primary controllers by unit gains (Section 4.1) and prove that current sharing is achieved in a stable way. We also provide conditions for voltage balancing. Results derived in this simple setting will be instrumental for studying the more complex scheme where primary control loops are abstracted into first-order transfer functions (Section 4.3).
4.1 Unit-gain approximation of primary control loops

By approximating primary loops with ideal unit gains, we have the relations $V_i = V_{\text{ref},i} + \Delta V_i$, $\forall i \in \mathcal{V}$.

Figures 4a-4b show the resulting control scheme, used for deriving the dynamics of the overall mG as a function of the inputs $I_L$ and $V_{\text{ref}}$. Starting from the left-hand side of Figure 4a, we have, in order, (6) and

$$V = \Delta V + V_{\text{ref}}. \quad (7)$$

Then, from basic circuit theory, we derive the relation between the vector of voltages $V$ and the vector of line currents $I_\ell = [I_{\ell 1}, \ldots, I_{\ell M}]^T$ as

$$I_\ell = -WB_{el}^T V, \quad (8)$$

where $W$ and $B_{el} = Q(G_{el})$ are the weight and the incidence matrix of $G_{el}$, respectively. Next, we get

$$I_i = I_L - B_{el} I_\ell \quad (9)$$

and, merging equations (6)-(9), we finally obtain

$$\Sigma : \dot{\Delta V} = -LD B_{el} W B_{el}^T M \Delta V - LD I_L - LD B_{el} W B_{el}^T M V_{\text{ref}} = -Q\Delta V - LD I_L - Q V_{\text{ref}} \quad (10)$$

where $M = L(G_{el}) = B_{el} W B_{el}^T$ is the Laplacian matrix of the electrical network and $Q = LD M$.

4.2 Properties of the matrix $Q$

The matrix $Q$ in (10) captures the interaction of electric couplings and communication. From (10), it governs the voltage dynamics and hence the achievement of current sharing and voltage balancing. Notice that $Q$ is obtained pre- and post- multiplying a diagonal matrix by a Laplacian ($L$ and $M$, respectively). It follows that $Q$ is not a Laplacian matrix itself because it might fail
to be symmetric and have positive off-diagonal entries, even if weights of $G_{el}$ and $G_c$ are positive. Nevertheless, in the sequel we provide two distinct conditions under which $Q$ preserves some key features of Laplacian matrices. Before proceeding, we introduce the following preliminary result.

![Hierarchical control scheme](image)

**Figure 4:** Hierarchical control scheme and Primary Loop (PL) approximations.

**Proposition 2.** It holds $P_{H^1}(D \mathcal{M} H^1) = H^1$.

**Proof.** From Proposition 1, we know that $\mathcal{M}(H^1|H^1)$ is invertible, hence surjective. Then, $D \mathcal{M} H^1 = DH^1$.

We now study the projection map $P_{H^1} : DH^1 \rightarrow H^1$. Since $D$ is invertible, we have that $\dim(DH^1) = \dim(H^1) = N - 1$. Therefore, one has

$$\dim(\text{Range}(P_{H^1})) + \dim(\text{Ker}(P_{H^1})) = N - 1. \quad (11)$$

The next step is to show that

$$\text{Ker}(P_{H^1}) = \{0\}, \quad (12)$$

so that, from (11), $P_{H^1}$ is surjective. Let $u \in DH^1$ and set $u = \bar{u} + \hat{u}$, where $\bar{u} \in H^1$ and $\hat{u} \in H^1$. Hence, $P_{H^1} u = P_{H^1} \hat{u} = \hat{u}$ and, if $u \in \text{Ker}(P_{H^1})$, then $\hat{u} = 0$. In other words, $u \in \text{Ker}(P_{H^1})$ verifies

$$\begin{cases} u = \alpha u \mathbf{1} & \text{for some } \alpha u \in \mathbb{R} \vspace{1mm} \\ u = Dv & \text{for some } v \in H^1 \end{cases}$$

One has

$$||u||_{D^{-1}}^2 = u^T D^{-1} u = \alpha u^T I^T D^{-1} D v = \alpha u^T v = 0$$

where the last identity follows from the fact that $v \in H^1$ has zero average. But, since $||\cdot||_{D^{-1}}$ is a norm, $||u||_{D^{-1}}^2$ implies $u = 0$. This shows (12).

At this point, we can introduce two assumptions which allow us to characterize the eigenstructure of matrix $Q$; then, we discuss their impact on the choice of the communication graph topology as well as on the value of coefficients $a_{ij}$ in (5).
**Assumption 3.** The diagonal matrix containing the scaling factors for the output currents coincides with the identity, i.e. $D = I$.

**Assumption 4.** It holds $D \neq I$, $D > 0$ and the product $LDM$ commutes (i.e. $LDM = MDL$).

**Remark 2.** Under Assumption 3, the desired goal is that all converters in the $mG$ produce the same current (measured in Ampere and not in p.u.), i.e. that $\| \mu \|$ holds. This can be desirable, for instance, when all the converters in the network have the same generation capacity. We notice that Assumption 3 does not enforce constraints on the graphs $G$ and $G_c$, which must fulfill Assumption 4 only. In particular, the topology of $G_c$ can be chosen based on considerations on the network technology and for optimizing the performance of the secondary control layer, independently of the electrical interactions.

**Remark 3.** Assumption 4 is suited to the case of converters with different ratings. However, it requires matrix $\mathcal{Q}$ to be symmetric. An interesting case where this condition is verified is given by

$$L = \mu M,$$

where $\mu > 0$ is a global parameter, common to all the DGUs. Relation (13) holds if the following conditions are simultaneously guaranteed:

(a) $G_{el}$ and $G_c$ have the same topology;

(b) coefficients in $[L]$ are chosen as $a_{ij} = \mu \frac{1}{n_{el}}$ if DGUs $i$ and $j$ are connected by a communication link.

Condition (b) has an impact on the design of local Laplacian control laws $[L]$. Notably, each agent (say DGU $i$) needs to know the global parameter $\mu$ and the value of the conductances $\frac{1}{n_{el}}$ connecting it to its electrical neighbors $j$ belonging to set $N^{el}_i$ (which, in this particular case, coincides with $N^{el}_c$).

**Remark 4.** Assumptions 3 or 4 are instrumental in guaranteeing asymptotic stability of the hierarchical control architecture in Figure 3. Indeed, as will be shown in the following Proposition, the above conditions imply that the eigenvalues of $\mathcal{Q}$ are nonnegative. Note that, if neither Assumption 3 nor 4 are verified, $\mathcal{Q}$ can have negative eigenvalues (see the example in Appendix C).

**Proposition 3.** The matrix $\mathcal{Q} = LDM$ has the following properties:

(i) $\text{Ker}(\mathcal{Q}) = H^1_1$;

(ii) $\text{Range}(\mathcal{Q}) = H^1$;

(iii) the linear transformation $\mathcal{Q}(H^1|H^1)$ is invertible;

(iv) under Assumption 3 or 4, $\mathcal{Q}$ is diagonalizable and has real nonnegative eigenvalues.

(v) under Assumption 3 or 4, the zero eigenvalue of $\mathcal{Q}$ has algebraic multiplicity equal to one.

**Proof.** We start by proving point (i). Since, from Proposition 1, $\text{Ker}(\mathcal{M}) = H^1_1$, one has $\text{M}_R^N = \text{M}(H^1_1 \oplus H^1) = \text{MH}^1$. Furthermore, from Proposition 2, $P_{ij}(DMH^1) = H^1$. Proposition 1 applied to the Laplacian $\mathcal{L}$ shows that $\text{L}(DMR^N) = H^1$, which is (i).

For proving point (ii), we recall that $\text{Ker}(\mathcal{M}) = H^1_1$ and then $\text{Ker}(\text{LDM}) \supseteq H^1_1$. From (i) we have that $\dim(\text{Range}(\mathcal{Q})) = N - 1$. The equation $\dim(\text{Range}(\mathcal{Q})) + \dim(\text{Ker}(\mathcal{Q})) = N$ implies that $\dim(\text{Ker}(\mathcal{Q})) = 1$. Since $\dim(H^1_1) = 1$, we have $\text{Ker}(\mathcal{Q}) = H^1_1$.

In order to prove point (iii), we show that $\mathcal{Q}(H^1|H^1)$ is both surjective and injective [36, p. 50]. The surjectivity of $\mathcal{Q}$ on $H^1$ has been shown when proving point (ii). For proving the injectivity, we need to check if it holds

$$\forall b \in H^1 \quad \forall x, y \in H^1 \quad (\mathcal{Q}x = b \text{ and } \mathcal{Q}y = b) \Rightarrow x = y.$$

From [15], $\mathcal{Q} = LDM = \mu MDM$, which commutes since $M$ and $D$ are symmetric and $\mu$ is a scalar.
Now, $Qx = Qy = b$ implies that $Q(x - y) = 0$. It means that $x - y \in \text{Ker}(Q) = H^1_\perp$, therefore there exists an $\alpha \in \mathbb{R}$ such that $x - y = \alpha 1_n$. However, since $x - y \in H^1$, $x - y = \alpha 1_n$ is verified only for $\alpha = 0$; this leads to $x = y$.

As regards statement (iv), we first consider the case in which Assumption 3 holds. Since $D = I$, we have that $Q = LM$. Hence, $Q$ is the product of two matrices, both positive semidefinite in the real sense. Moreover, since $L$ and $M$ are symmetric, they are positive semidefinite also in the complex sense. The proof concludes by applying Corollary 2.3 in [33], which states that the product of two complex positive semidefinite matrices is diagonalizable and has nonnegative real eigenvalues.

We now prove statement (iv) when Assumption 4 holds. Since $Q$ is diagonal with positive elements, the matrix $D^{\frac{1}{2}}$ verifying $D = D^{\frac{1}{2}}D^{\frac{1}{2}}$ exists and is invertible. Then, $Q$ can be written as follows

$$Q = D^{-\frac{1}{2}}D^{\frac{1}{2}}L^{\frac{1}{2}}L^{\frac{1}{2}}D^{\frac{1}{2}}M^{\frac{1}{2}}M^{\frac{1}{2}}D^{\frac{1}{2}}D^{\frac{1}{2}}. \quad (14)$$

Matrices $L$ and $M$ in (14) are positive semidefinite in the real sense and symmetric; hence, they are positive semidefinite also in the complex sense. Therefore, also in this case, we can use Corollary 2.3 in [33] to state that $LM$ is has nonnegative real eigenvalues. Now, since $D^{-\frac{1}{2}}$ in (14) is symmetric, matrix $LM$ is congruent to $Q$. Thus, since under Assumption 4 $LM$ is symmetric, by Sylvester’s law of inertia [39], the inertia of $Q$ and $LM$ coincide, i.e.

$$(i_+(LM), 0, i_0(LM)) = (i_+(Q), 0, i_0(Q)).$$

This concludes the proof of statement (iv) under Assumption 4.

Finally, point (v) follows from points (i) and (iv). Indeed, since $Q$ is diagonalizable, the algebraic and geometric multiplicity of null eigenvalues coincide. From point (i), since $\dim(H^1_\perp) = 1$, we conclude that the zero eigenvalue of $Q$ is unique. \(\square\)

### 4.2.1 Analysis of equilibria

In order to evaluate the steady-state behavior of the electrical signals appearing in Figure 4a-4b, we study the equilibria of system (10). Hence, for given constant inputs $(I_L^i, V_{\text{ref}}^i)$, we characterize the solutions $\Delta V^*$ of equation

$$Q \Delta V^* = -LDI_L^i - QV_{\text{ref}}^i \quad (15)$$

through the following Proposition.

**Proposition 4.** For equation (15),

(i) there is only one solution $\Delta V^* \in H^1$;

(ii) all solutions $\Delta V^* \in \mathbb{R}^N$ can be written as

$$\Delta V^* = \tilde{\Delta} V^* + \alpha 1_N, \quad \alpha \in \mathbb{R}. \quad (16)$$

**Proof.** Proposition 3 shows that equation (15) has solutions only if $-LDI_L^i - QV_{\text{ref}}^i \in H^1$. From Propositions 3(iii) and 4(iii), this is always true. Statement (i) directly follows from Proposition 3(iii).

For the proof of statement (ii), we split $\Delta V^* \in \mathbb{R}^N$ as in (1), i.e. $\Delta V^* = \tilde{\Delta} V^* + \Delta V^*$. From (15) and Proposition 3(iii), one has that, irrespectively of $\Delta V^* = \tilde{\Delta} V^* \in H^1_\perp$, $Q\Delta V^* = -LDI_L^i - QV_{\text{ref}}^i$. Moreover, from the first part of the proof, it holds $\Delta V^* = \Delta V^*$. \(\square\)

Next, we relate properties of the equilibria of (10) to current sharing and voltage balancing.

\[\text{Indeed, } (LM)^T = ML = D^{\frac{1}{2}}MD^{\frac{1}{2}}D^{\frac{1}{2}}D^{\frac{1}{2}} = LM.\]
Proposition 5. Consider system \([10]\) with constant inputs \((I_L, V_{ref})\). Then, current sharing is achieved at steady state. Moreover, if \(V_{ref} = V_{ref} f 1_N\) (i.e., if Assumption \([7]\) holds) and \(\alpha\) in \([16]\) is equal to zero, then the equilibrium \(\hat{V}^*\) verifies the voltage balancing condition \([4]\).

Proof. At the equilibrium, since \(\text{Ker}(L) = H^1_\perp\) (see Proposition \([1\, iii]\)), from \([6]\) we have that
\[
-\mathbb{L} \hat{I}_N^* = 0_N \Leftrightarrow \hat{D} \hat{I}_N^* = \hat{I}_N 1_N
\]
\[
\Leftrightarrow \begin{bmatrix} \hat{I}_{11}^* & \cdots & \hat{I}_{1N}^* \end{bmatrix}^T = \hat{I}_t 1_N,
\]
which is \([2]\). Let now \(\Delta V^*\) be an equilibrium for system \([10]\). Replacing \([16]\) in \([7]\) and averaging the resulting vector, we get \(\langle V^* \rangle = \langle \Delta V^* \rangle + N_\alpha + V_{ref}\), which is \([4]\). \(\square\)

4.2.2 Stability analysis
The similarities established in Proposition \([3]\) between the spectral properties of graph Laplacians and the matrix \(Q\) will allow us to study the stability properties of \([10]\) using methods similar to the ones adopted for analysis of classical consensus dynamics. Results in this section follow the approach in \([32]\), where stability of consensus is analyzed through the use of invariant subspaces. An advantage of this rationale is that it carries over almost invariably to the case of more complex models of primary loops (Section \([1, 3]\)).

In the sequel, we prove exponentially stable convergence of \(\Delta V\) in \([10]\) to an equilibrium ensuring both current sharing and voltage balancing for constant \(I_L^*\) and \(V_{ref} = V_{ref} f 1_N\). We first show that projections \(P_{H^1_\perp}(\Delta V) = \Delta V\) and \(P_{H^1}(\Delta V) = \Delta V\) have non-interacting dynamics (or, equivalently, that subspaces \(H^1\) and \(H^1_\perp\) are invariant for \([10]\)).

Proposition 6. If \(\Delta V\) is given by system \(\Sigma\) in \([10]\) for \(\Delta V(0) = \Delta V_0\), then \(\Delta V = \Delta V + \Delta V^*\), where \(\Delta V \in H^1_\perp\) and \(\Delta V^* \in H^1\) fulfill
\[
\Sigma : \begin{cases} 
\hat{\Delta} V = 0_N \\
\Delta V(0) = \langle \Delta V_0 \rangle 1_N
\end{cases}
\]
and
\[
\hat{\Sigma} : \begin{cases} 
\hat{\Delta} V = -Q \Delta V - \mathbb{L} \hat{I}_L - Q V_{ref} \\
\Delta V(0) = \Delta V_0 - \Delta V^*_0.
\end{cases}
\]

Proof. We write vectors \(\Delta V(0), I_L\) and \(V_{ref}\) according to the decomposition \([1]\), i.e., using \(^*\) and \(^\perp\) for denoting their \(H^1\) and \(H^1_\perp\) components, respectively. As described in \([32]\), we analyze the dynamics of \(\Delta V\) by averaging both sides of \([10]\) and \(\Delta V(0) = \Delta V_0\), respectively. From points \([i, iii]\) of Proposition \([3]\), we have \(-Q \Delta V = 0\) and \(-Q V_{ref} = 0\). Since \(\text{Range}(L) = H^1\) (see Proposition \([1\, iii]\)), we also have \(-\mathbb{L} \hat{I}_L = 0\), hence obtaining \(\frac{d}{dt} (\Delta V) = 0\). Recalling that \(\Delta V_0 = \langle \Delta V_0 \rangle 1_N\), we obtain \([18]\).

Next, we analyze \(\Delta V = \Delta V - \Delta V^*\). We have
\[
\hat{\Delta} V = \Delta V - \Delta V^* = -Q \Delta V - \mathbb{L} \hat{I}_L - Q V_{ref}
\]
and \(\Delta V(0) = \Delta V_0 - \Delta V^*_0\). From Proposition \([3, 4]\), \(Q \Delta V = Q \Delta V^*\) and then we have \([19]\). \(\square\)

Remark 5. The splitting of \(\Sigma\) into systems \(\Sigma\) and \(\hat{\Sigma}\) implies that, if \(\Delta V_0\) has zero average, then \(\Delta V(t)\) has the same property, \(\forall t \geq 0\) and irrespectively of inputs \((I_L, V_{ref})\). This behavior can be realized by suitable initialization of the integrators appearing in Figure \([4a]\).
According to system $\Sigma$, the value of $P_{H_1}(\Delta V) = \Delta V$ remains constant over time and equal to $\Delta V_0$. Hence, in order to characterize the stability of equilibria (16), it is sufficient to study the dynamics (19). In an equivalent way, one can consider system (10) and the following definition of stability on a subspace.

**Definition 3.** Let $V$ be a subspace of $\mathbb{R}^n$. The origin of $\dot{x} = Ax$, $x(t) \in \mathbb{R}^n$ is Globally Exponentially Stable (GES) on $V$ if $\exists \kappa, \eta > 0: \|P_\mathbb{V}x(t)\| \leq \kappa e^{-\eta t}||P_\mathbb{V}x(0)||$. The parameter $\eta$ is termed rate of convergence.

Note that $\Sigma$ is a linear system and, for stability analysis, we can neglect inputs, hence obtaining

$$\Delta V(0) = \Delta V_0. \quad (20)$$

**Theorem 1.** Under Assumption 3 or 4, the origin of (20) is GES on $H^1$. Moreover, the rate of convergence is the smallest strictly positive eigenvalue of $Q$.

**Proof.** The proof of Theorem 1 is presented in Appendix A.

The above results reveal that, given an initial condition $\Delta V(0) = \Delta V_0$ for system (10) and constant inputs $I_L$ and $V_{ref} = V_{ref} 1_N$, the state $\Delta V$ converges to the equilibrium (16) with $\alpha = \langle \Delta V_0 \rangle$. Summarizing the main results of this Section, we have that the consensus scheme described by (5), Assumption 1 and $\langle \Delta V_0 \rangle = 0$ (21) guarantee the asymptotic achievement of current sharing and voltage balancing in a GES fashion.

### 4.3 First-order approximation of primary control loops

Figure 4a and Figure 4c show the overall closed-loop scheme of an mG equipped with \(i\) consensus current loops and \(ii\) primary control loops modeled as first-order transfer functions. Different from the case analyzed in Section 4.1, each local dynamics is now described by means of two states which are the state of the consensus current loop ($\Delta V_i$) and the state of the controlled DGU ($V_i$ in Figure 4c). We highlight that relations (6) and (7) still hold, while the additional state equation is

$$\dot{V} = \Omega \Delta V - \Omega V + \Omega V_{ref}, \quad (22)$$

where vectors $V^\circ = [V_1^\circ, V_2^\circ, \ldots, V_N^\circ]^T$ and $V$ belong to $\mathbb{R}^N$, and the diagonal matrix $\Omega = \omega_c I \in \mathbb{R}^{N \times N}$, $\omega_c > 0$, collects on its diagonal the approximate bandwidth of each controlled DGU. In view of Remark 1, assuming equal approximate bandwidths for all the controlled DGUs is a mild constraint.

As in Section 4.1, in order to find the dynamics of the closed-loop scheme, we write relations among mG variables. From Figure 4a, we notice that (8) holds, and

$$V^\circ = \Delta V + V_{ref}. \quad (23)$$

Always from Figure 4a, we have that, for line and output currents, equations (8) and (9) are still valid. By merging relations (6), (23), (22), (8) and (9), we can write the dynamics of the overall mG as

$$\begin{cases} \Delta \dot{V} = -Q \Delta V - LDI_L \\ \dot{V} = \Omega \Delta V - \Omega V + \Omega V_{ref} \end{cases} \quad (24a)$$

or, equivalently, in compact form,

$$\begin{bmatrix} \Delta \dot{V} \\ \dot{V} \end{bmatrix} = \begin{bmatrix} 0_{N \times N} & -Q \\ \Omega & -\Omega \end{bmatrix} \begin{bmatrix} \Delta V \\ V \end{bmatrix} + \begin{bmatrix} 0_{N \times N} & -LD \\ \Omega & -\Omega \end{bmatrix} \begin{bmatrix} V_{ref} \\ I_L \end{bmatrix}, \quad (24b)$$

with $Q \in \mathbb{R}^{2N \times 2N}$ and $[\Delta V^T V^T]^T \in \mathbb{R}^N \times \mathbb{R}^N$. 

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4.3.1 Analysis of equilibria

The equilibria of system (24) for constant inputs \((I^*_L, V^*_\text{ref})\), are obtained by computing the solutions \([\Delta V^* T, V^* T]^T\) to the system

\[
\begin{align*}
QV^* &= -LDI^*_L \\
0_N &= \Omega \Delta V^* - \Omega V^* + \Omega V^*_\text{ref}.
\end{align*}
\] (25a) (25b)

Since matrix \(\Omega\) is invertible, equation (25b) becomes

\[V^* = \Delta V^* + V^*_\text{ref}.\] (26)

By substituting (26) in (25a), we get

\[Q\Delta V^* = -LDI^*_L - QV^*_\text{ref}.
\]

that is exactly (15). We can then exploit Proposition 4 for concluding that there are infinitely many solutions \(\Delta V^* \in \mathbb{R}^N\) in the form (16). Replacing (16) in (26), we can write equilibria of system (24) as

\[
\begin{bmatrix}
\Delta V^* \\
V^*
\end{bmatrix} = \begin{bmatrix}
\hat{\Delta} V^* + \alpha 1_N \\
\Delta V^* + \alpha 1_N + V^*_\text{ref}
\end{bmatrix}.
\] (27)

Relations between the equilibria and current sharing/voltage balancing are given in the next Proposition.

Proposition 7. Consider system (24) with constant inputs \((I^*_L, V^*_\text{ref})\). At the equilibrium, current sharing is achieved. Moreover, if \(V^*_\text{ref} = V^*_\text{ref} 1_N\) (i.e. if Assumption 7 holds) and \(\alpha\) in (27) is equal to zero, then the equilibrium \([\Delta V^* T, V^* T]^T\) verifies the voltage balancing.

Proof. Since equation (6) holds, one has that, at the equilibrium, relation (17) is verified. Then, the proof is identical to the one of Proposition 5.

Next, similarly to the simplified case described in Section 4.2.2, we evaluate the stability properties of the closed-loop system (24) so as to show the convergence of state \([\Delta V^T V^T]^T\) to an equilibrium which guarantees current sharing and voltage balancing.

4.3.2 Stability analysis

Proposition 8. If \([\Delta V^T V^T]^T\) verifies (24), then

\[
\begin{bmatrix}
\Delta V \\
V
\end{bmatrix} = \begin{bmatrix}
\hat{\Delta} V \\
\hat{V}
\end{bmatrix} + \begin{bmatrix}
\hat{\Delta} \hat{V} \\
\hat{\hat{V}}
\end{bmatrix},
\]

where \(\hat{v} \in H^1_1 \times H^1_1\) and \(\hat{\hat{v}} \in H^1 \times H^1\) fulfill

\[
\tilde{\Sigma}_1 : \begin{cases}
\hat{\Delta} V = 0_N \\
\hat{V} = \Omega \hat{\Delta} V - \Omega \hat{V} + \Omega \hat{V}_\text{ref}
\end{cases}
\] (28a) (28b)

and

\[
\tilde{\Sigma}_1 : \begin{cases}
\hat{\Delta} \hat{V} = -Q\hat{V} - LD I_L \\
\hat{\hat{V}} = \Omega \hat{\Delta} V - \Omega \hat{V} + \Omega \hat{V}_\text{ref},
\end{cases}
\] (29a) (29b)

respectively.
Proof. The dynamics of $\Delta V$ and $\hat{\Delta V}$ can be derived proceeding as in the proof of Proposition 6. In a similar way, by averaging both sides of (24b), one derives the (independent) dynamics of $\hat{V}$ and $\hat{V}$.

The above decomposition allows us to evaluate the evolution of state $v$ on $\mathbb{R}^{N \times N}$ by separately analyzing dynamics (28) and (29), i.e. studying the behavior of projections $\hat{v} = P_{V}(v)$ and $\hat{v} = P_{W}(v)$, with $V = H_{1}^{} \times H_{1}^{}$ and $W = H^{1} \times H^{1}$.

First we focus on $\tilde{\Sigma}$. Without loss of generality, for stability analysis we can neglect the input vector $V_{\text{ref}}$ in (28b), thus having:

$$\begin{align*}
\dot{\Delta V} &= 0 \quad \text{(30a)} \\
\dot{\hat{V}} &= \Omega \Delta V - \Omega \hat{V} \quad \text{(30b)}
\end{align*}$$

By construction, (30b) collects the decoupled equations

$$\dot{V}_{i} = \omega_{c} \Delta V_{i} - \omega_{c} V_{i} \quad \forall i = 1, \ldots, N, \quad \text{(31)}$$

where, according to (28a), each term $\Delta V_{i}$ in can be treated as an exogenous input (thus not affecting stability properties). It follows that dynamics (31) is asymptotically stable, since $\omega_{c} > 0$.

In summary, system (28) tells us that the average $\Delta V$ will remain constant in time (and equal to $\langle \Delta V_{0} \rangle$), while $\hat{V}$ will converge to the origin. For studying stability properties of system $\tilde{\Sigma}_{1}$, we consider (24) without inputs, i.e.

$$\begin{align*}
\Delta V &= -Q V \\
\dot{\hat{V}} &= \Omega \Delta V - \Omega \hat{V} \quad \text{(32)}
\end{align*}$$

and analyze stability on $H^{1} \times H^{1}$. We have the following result.

**Theorem 2.** Under Assumption 3 or 4, the origin of (32) is GES on $H^{1} \times H^{1}$. Furthermore, matrix $Q$ has a simple zero eigenvalue and the rate of convergence is the maximum among real parts of all other eigenvalues.

**Proof.** The proof is given in Appendix B.

By means of Theorem 2, we have that, given an initial condition $[\Delta V_{0}^{T} V_{0}^{T}]^{T}$ for system (24), the state $[\Delta V^{T} V^{T}]^{T}$ will converge to the equilibrium in (27), with $\alpha = \langle \Delta V_{0} \rangle$.

The results above show that, for system (24), current sharing is achieved in a GES fashion. In a similar way, asymptotic voltage balancing is ensured if Assumption 1 and (21) are fulfilled.

### 4.4 PnP design of secondary control

We now describe the procedure for designing secondary controllers in a PnP fashion. We will show that, as for the PnP design of primary regulators, when a DGU is added or removed, the secondary control layer can be updated only locally for preserving current sharing and voltage balancing.

**Plug-in operation.** Under Assumption 3 when a DGU (say DGU $i$) sends a plug-in request at a time $\bar{t}$, it chooses a set $\mathcal{N}_{c}^{i}$ of communication neighbors (which must not necessarily coincide with $\mathcal{N}_{el}^{i}$) and fixes parameters $a_{ij} > 0, \forall j \in \mathcal{N}_{c}^{i}$, in order to design the local consensus filter (5). At the same time, each DGU $j, j \in \mathcal{N}_{c}^{i}$, updates its consensus filter by setting $a_{ji} = a_{ij}$ in (5). If, instead, $D \neq I$, one can fulfill Assumption 4 by allowing the entering DGU to receive the value of conductances $\frac{1}{\eta_{ij}}$ from neighboring DGUs $j \in \mathcal{N}_{el}^{i}$ and by setting $\mathcal{N}_{c}^{i} = \mathcal{N}_{el}^{i}$ (see Remark 3). By doing so, DGU $i$ can choose parameters $a_{ij} = \mu_{ij} \eta_{ij}$, and each DGU $j \in \mathcal{N}_{c}^{i}$ sets $a_{ji} = a_{ij}$, thus updating its consensus filter (5).

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Overall, Theorems 1 and 2 ensure that the disagreement dynamics of the mG states is GES. Let Assumption 1 hold for all the interconnected DGUs in the mG before \( t \) and let us denote the common reference voltage by \( V_{\text{ref}} \). If DGU \( i \) sets \( V_{\text{ref},i} = V_{\text{ref}} \) and if we choose \( \Delta V_i(\bar{t}) = 0 \) (thus having \( \langle \Delta V_i(\bar{t}) \rangle = 0 \), where \( \Delta V_i(\bar{t}) \) is the vector \( \Delta V \) prior the plugging-in of DGU \( i \)), both current sharing and voltage balancing are preserved in the asymptotic régime (see Propositions 5 and 7).

**Unplugging operation.** Under Assumption 3 or 4 when a DGU (say DGU \( j \)) is unplugged at time \( t \), provided that the new graphs \( \mathcal{G}_{el} \) and \( \mathcal{G}_{c} \) fulfill Assumption 2, the key condition that must be guaranteed is that the vector \( \Delta V_{-j} \) (i.e. \( \Delta V \) without element \( j \)) verifies \( \langle \Delta V_{-j}(\bar{t}) \rangle = 0 \). If \( \langle \Delta V(\bar{t}) \rangle = 0 \), this can be achieved by re-setting \( \Delta V_i(\bar{t}) = \Delta V_i(\bar{t}^0) + \frac{\Delta V_i(\bar{t}^0)}{\left|N_i^c\right|} \) for all \( i \in N_i^c \), and keeping \( \Delta V_i(\bar{t}) = \Delta V_i(\bar{t}^0) \) for all \( i \notin N_i^c \cup \{j\} \). Indeed, this yields

\[
\langle \Delta V_{-j}(\bar{t}) \rangle = \frac{1}{N-1} \sum_{i \in N_i^c} \Delta V_i(\bar{t}) + \frac{1}{N-1} \sum_{i \notin N_i^c \cup \{j\}} \Delta V_i(\bar{t}) = \\
= \frac{1}{N-1} \left( \sum_{i \in N_i^c} \Delta V_i(\bar{t})^0 + \frac{1}{\left|N_i^c\right|} \sum_{i \in N_i^c} \Delta V_i(\bar{t})^0 + \sum_{i \notin N_i^c \cup \{j\}} \Delta V_i(\bar{t})^0 \right) = \\
= \frac{1}{N-1} \langle \Delta V(\bar{t})^0 \rangle = 0.
\]

### 5 Validation of secondary controllers

#### 5.1 Simulation results

In this Section, we demonstrate the capability of the proposed control scheme to guarantee current sharing and voltage balancing when DGUs are plugged in/out and load changes occur. Simulations have been performed in Simulink/PLECS. We consider an mG composed of 7 DGUs, interconnected as in Figure 3, with non-identical electrical parameters and power lines. Notice the presence of loops in the electrical network, that complicates voltage regulation. Primary PnP voltage regulators are designed according to the method in (12), whereas, for the secondary control layer, we choose \( k_j \) in (6) equal to 1. DGUs have rated currents \( I_{\text{r}}^i = 10 \text{ A} \), \( i = 1, 2, 3, I_{\text{r}}^4 = I_{\text{r}}^5 = 5 \text{ A} \) and \( I_{\text{r}}^i = I_{\text{r}}^7 = 3.33 \text{ A} \). Since, in this scenario, we aim to achieve the current sharing condition (2), we set \( I_{\text{r}}^i = I_{\text{r}}^i, i = 1, \ldots, 7 \), thus having, in (6), \( D \neq I \). Then, in order to guarantee the asymptotic stability of the hierarchical control scheme, we fulfill Assumption 3 by (i) letting \( \mathcal{G}_c \) have the same topology of \( \mathcal{G}_{el} \), and (ii) picking \( a_{ij} = \frac{1}{R_{ij}} \), if DGUs \( i \) and \( j \) are connected by a communication link (see Remark 3). Furthermore, the voltage reference in Assumption 4 is \( V_{\text{ref}} = 48 \text{ V} \), and the electrical parameters are given in Appendix D.

In the following, we describe Figure 4 which illustrates the evolution of the main electrical quantities (i.e. measured DGU output currents in Amperes, DGU output currents in p.u., PCC voltages and average PCCs voltage) during the consecutive simulation stages shown in Figure 5.

**Stage 1:** At time \( t_0 = 0 \), all the DGUs are isolated and only the primary PnP voltage regulators, designed as in (12), are active. Therefore, as shown in Figure 5 (i) each DGU supplies its local load while keeping the corresponding PCC voltage at 48 V, and (ii) the DGU output currents in p.u. are different. We further highlight that primary controllers have been designed assuming that all the switches in Figure 5 connecting DGUs 1-6 are closed. From (12), however, they also stabilize the mG when all switches are open.
Stage 2: Subsystems 1-6 are connected together at time $t_1 = 2\ s$ and, as described before, no update of primary controllers is needed. As shown in the plot of $V_{PCC}$ in Figure 6, voltage stability and fast transients after the plug-in operations are ensured by PnP primary regulators. The secondary control layer is still disabled at this stage.

Stage 3: At time $t_2 = 5\ s$, we activate the secondary control layer for DGUs 1-6, thus ensuring asymptotic current sharing among them (see the plot of the currents in p.u.). This is achieved by automatically adjusting the voltages at PCC (as shown in the plot of $V_{PCC}$). Moreover, the top plot of Figure 6 reveals that, as expected, DGUs 4 and 5 share half of the current of DGUs 1-3, while the output current of DGUs 6 is one third of the currents of DGUs 1-3. We further highlight that, by setting $\Delta V_i(t_2) = 0, i = 1, \ldots, 6$, as described in Section 4.4, condition (21) is fulfilled and asymptotic voltage balancing is guaranteed (see the plot of the average PCCs voltage, indicated with $V_{av}$).

Stage 4: For evaluating the PnP capabilities of our control scheme, at $t_3 = 15\ s$, DGU 7 sends a plug-in request to DGUs 4 and 5. Previous primary controllers of DGUs 4 and 5 still fulfill the plug-in conditions in [12]: they are therefore maintained and the plug-in of DGU 7 is performed. In the light of Assumption 3 at $t_3$ also the secondary controller of DGU 7 is activated, thus enabling the DGU to contribute to current sharing. This can be noticed in Figure 6, as all PCC voltages change in order to let all the output currents in p.u. converge to a common value. We also notice that the measured output currents (top plot of Figure 6) are still shared accordingly (i.e. $I_{t_1} = I_{t_2} = I_{t_3} = 2I_{t_4} = 2I_{t_5} = 3I_{t_6} = 3I_{t_7}$). Furthermore, choosing $\Delta V_7(t_3) = 0$ (as described in Section 4.4), we maintain the average PCCs voltage at 48 V (see $V_{av}$, stage 4).

Stage 5: At $t_4 = 25\ s$, we halve the load of DGU 1, thus increasing the corresponding load current $I_{L1}$ and causing a peak in the corresponding output current. However, after few seconds, all the DGUs share again the total load current, while the averaged PCC voltage converges to the reference value.

Stage 6: Finally, we assess the performance of the proposed hierarchical scheme when the sudden disconnection of a DGU occurs. To this aim, at time $t_5 = 35\ s$, we disconnect DGU 3. Figure 6 (stage 6) shows that voltage stability, current sharing and voltage balancing are preserved in the mG composed of DGU 1-2 and 4-7.

5.2 Experimental results

Performance brought about by the presented hierarchical scheme been also validated via experimental tests based on the mG platform in the top panel of Figure 7, which consists of three Danfoss inverters, a dSPACE1103 control board and LEM sensors. In order to properly emulate DC/DC converters (i.e. Buck converters), only the first phase of each inverter has been used. Buck converters operate in parallel to emulate DGUs while different local load conditions have been obtained by connecting each PCC to a resistive load. All the converters are supplied by DC source generators.

For this scenario, primary PnP voltage controllers are designed using the approach in [20], and Assumption 3 holds. Hence, since $D = I, I_{t_1}^* = I_{t_2}^* = I_{t_3}^* = \bar{I}_t$, i.e. we aim to achieve the asymptotic current sharing condition (4). Moreover, we set $k_I$ in (6) equal to 0.5, while coefficients $a_{ij}$ in (5) are equal to 1 if DGUs $i$ and $j$ are connected by a communication link, 0 otherwise. We also recall that, under Assumption 5, the stability of the mG equipped with our hierarchical scheme is preserved even if the topologies of $G_c$ and $G_{el}$ differ. Notably, we consider the mG in Figure 7 where $G_{el}$ and $G_c$ are highlighted in blue and red, respectively, and the edges of $G_{el}$ are RL lines.

The controllers have been implemented in Simulink and compiled to the dSPACE system in order to command the Buck switches at a frequency of 10 kHz.
Figure 5: Simulation stages: numbered nodes represent DGUs, while black lines denote power lines. The secondary control layer is activated for the DGUs contained in the red area. Open switches in stages 1, 2, 3 and 6 denote disconnected DGUs. The arrow in stage 5 represents a step up in the load current of DGU 1.
Figure 6: Simulation results: evolution of measured output currents, output currents in p.u., voltages at PCCs and average PCCs voltage. Lines in the plots are associated with different DGUs and they are color-coded as in Figure 5. Simulation stages are those shown in Figure 5.
The evolution of the main electrical quantities is shown in Figure 8. At time \( t_0 = 0 \) s, all the DGUs are isolated and do not communicate. At times \( t_1 \approx 2.5 \) s, \( t_2 \approx 5 \) s and \( t_3 \approx 10 \) s, we connect DGU 1 to 2, 2 to 3 and 1 to 3, respectively, thus obtaining a loop in the electrical topology. As described in [26], no update of primary PnP controllers is required when units are connected. As shown in the plot of the PCC voltages in Figure 8, PnP primary voltage regulators ensure smooth transitions and stability. Since for \( t < t_3 \) the secondary layer is not active, the output currents are not equally shared and the PCC voltages coincide with the reference (notably, Assumption 1 holds, with \( V_{\text{ref}} = 48 \) V). Next, at time \( t_4 \approx 15 \) s, we set \( \Delta V_i(t_4) = 0, i = 1, 2, 3 \) and enable the secondary control layer. As expected, the three output currents converge to the same value (see \( I_t \) in Figure 8). Furthermore, the fulfillment of condition (21) guarantees asymptotic voltage balancing (see \( V_{\text{av}} \) in Figure 8). Finally, at time \( t_5 \approx 25 \) s we decrease the load of DGU 3, causing an increment in the corresponding load current. As a consequence, the value of \( \bar{I}_t = \langle I_L \rangle \) increases as well. Also in this case, the total load current is equally shared among DGUs while \( V_{\text{av}} \) does not deviate from 48 V.

As for the voltage discrepancies induced by secondary controllers, they are within 5% of the nominal voltage \( V_{\text{ref}} = 48 \) V. This fulfills, for instance, the standards for DC mGs used as uninterruptible power supply systems for telecommunication applications [40].

6 Conclusions

In this paper, a secondary consensus-based control layer for current sharing and voltage balancing in DC mGs has been presented. Under the assumption that DGUs are equipped with decentralized primary controllers that guarantee voltage stability, we proved stability of the hierarchical control scheme. Moreover, we presented a method for designing secondary controllers in a PnP fashion for handling plugging-in/out of DGUs.

Future developments will study the impact of non-idealities (such as transmission delays, data quantization and packet drops) on the performance of closed-loop mGs. The analysis of networked control systems has received considerable attention in the recent past [41] and our goal will be to reappraise methods and tools developed within this area in the context of microgrids. Another fundamental challenge that must be addressed when higher-level networked schemes are included in the mG control architecture is security against cyberattacks [42].
Figure 8: Experimental results for the mG in Figure 7. In the time interval from 2.5 s to 15 s, DGUs are connected together and primary PnP voltage regulators are enabled. From time 15 s onwards, also the secondary control layer is active. At time 25 s, the local load of DGU 3 is halved.
A Proof of Theorem 1

We introduce a preliminary Lemma, partly taken from Theorem 19 in [43].

Lemma 1. For $A \in \mathbb{R}^{n \times n}$, let $V$ and $W$ be $A$-invariant subspaces of $\mathbb{R}^n$ such that $\dim(V) = k$, $\dim(W) = n - k$ and $\mathbb{R}^n = V \oplus W$. Then:

1) there is a matrix $T \in \mathbb{R}^{n \times n}$ such that $A = T^{-1}AT$ has the block-diagonal structure

$$A = \begin{bmatrix} A_{11} & 0 \\ 0 & A_{22} \end{bmatrix},$$

(33)

with $A_{11} \in \mathbb{R}^{k \times k}$ and $A_{22} \in \mathbb{R}^{(n-k) \times (n-k)}$. In particular, if $\{b_1, \ldots, b_k\}$ and $\{b_{k+1}, \ldots, b_n\}$ are basis for $V$ and $W$, respectively, the transformation matrix $T$ has the block structure

$$T = [b_1 | \cdots | b_k | b_{k+1} | \cdots | b_n].$$

(34)

Therefore, if $x \in V$, then $T^{-1}x = [\hat{x}_1^T \ | \ 0]^T$, with $\hat{x}_1 \in \mathbb{R}^k$. Similarly, if $x \in W$, then $T^{-1}x = [0 \ | \ \hat{x}_2]^T$, with $\hat{x}_2 \in \mathbb{R}^{n-k}$.

II) The origin of $\dot{\hat{x}} = Ax$ is GES on $V$ if and only if the origin of $\dot{\hat{x}}_1 = A_{11}\hat{x}_1$ is GES. Moreover, parameters $\kappa, \eta > 0$ verifying $\|\hat{x}_1(t)\| \leq \kappa e^{-\eta t}\|\hat{x}_1(0)\|$, also guarantee $\|P_Vx(t)\| \leq \kappa e^{-\eta t}\|P_Vx(0)\|$.

Proof. For the proof of point I, we defer the reader to the proof of Theorem 19 in [43]. The proof of point II directly follows from the block-diagonal structure of matrix $A$ in (33). Indeed,

$$\dot{\hat{x}} = Ax \iff \dot{\hat{x}} = A \begin{bmatrix} \hat{x}_1 \\ \hat{x}_2 \end{bmatrix} \iff \begin{cases} \dot{\hat{x}}_1 = A_{11}\hat{x}_1 \\ \dot{\hat{x}}_2 = A_{22}\hat{x}_2, \end{cases}$$

i.e. $A_{11}$ is the matrix representation of the map $A(\mathcal{V} \mid \mathcal{V})$. In other words, studying the stability of $A$ on $\mathcal{V}$ is equivalent to study the stability of $A_{11}$. Moreover, by construction, $P_Vx = T[\hat{x}_1^T \mid 0]^T$. Then,

$$\|P_V(x(t))\| \leq \|T\|\|\hat{x}_1(t)\| \leq \|T\|\|kek^{-\eta t}\|\hat{x}_1(0)\|,$$

(35)

Since $\|\hat{x}_1(0)\| \leq \|T^{-1}\|\|P_V(x(0))\|$, inequality (35) becomes

$$\|P_V(x)\| \leq \|T\|kek^{-\eta t}\|\hat{x}_1(0)\| \leq \kappa e^{-\eta t}\|P_V(x(0))\|.$$
respect to the basis $\mathcal{B}$ are $\tilde{v}_1 = T^{-1}\Delta\mathbf{v} = [\xi_1, \ldots, \xi_{N-1}, 0]^T$ and $\tilde{v}_\perp = T^{-1}\Delta\mathbf{v} = [0, \ldots, 0, \xi_N]^T$, respectively. Now we prove that the origin of

$$\dot{\tilde{v}}_1 = -Q_{11}\tilde{v}_1$$  \hspace{1cm} (37)

is GES. Since $Q$ and $Q$ are similar matrices, they have the same eigenvalues. Therefore, by exploiting points (iv) and (v) of Proposition 3, one has that all the eigenvalues of $Q_{11}$ are strictly positive. This proves that (37) is GES and, as shown in [13], the convergence rate is $-\lambda$, where $\lambda$ is the minimal eigenvalue of $Q_{11}$. The remainder of the proof follows directly from point (i) of Lemma 1.
B Proof of Theorem 2

We first present two Propositions which provide preliminary results that will be used to prove Theorem 2.

**Proposition 9.** Subspaces $H^1 \times H^1$ and $H^1_1 \times H^1_1$ are $Q$-invariant.

*Proof.* We first show that, for any vector $\hat{v} = [\hat{v}_1^T \hat{v}_2^T]^T$, it holds $Q\hat{v} \in H^1 \times H^1$. Indeed,

$$Q\hat{v} = \begin{bmatrix} 0_{N \times N} & -Q \\ \Omega & -\Omega \end{bmatrix} \begin{bmatrix} \hat{v}_1 \\ \hat{v}_2 \end{bmatrix} = \begin{bmatrix} -Q\hat{v}_2 \\ \Omega(\hat{v}_1 - \hat{v}_2) \end{bmatrix},$$

and, from Proposition 3-(iii), the rightmost vector belongs to $H^1 \times H^1$.

Similarly, for any vector $\bar{v} = [\bar{v}_1^T \bar{v}_2^T]^T \in H^1_1 \times H^1_1$, we have that

$$Q\hat{v} = \begin{bmatrix} 0_{N \times N} & -Q \\ \Omega & -\Omega \end{bmatrix} \begin{bmatrix} \bar{v}_1 \\ \bar{v}_2 \end{bmatrix} = \begin{bmatrix} 0_N \\ \Omega(\bar{v}_1 - \bar{v}_2) \end{bmatrix},$$

and then $Q\bar{v} \in H^1_1 \times H^1_1$. \(\Box\)

**Proposition 10.** Matrix $Q$ has two eigenvalues equal to zero and $-\omega_c$, respectively. All other eigenvalues have strictly negative real part.

*Proof.* By definition, vector $[\Delta V^T V^T]^T \neq 0_{2N}$ is an eigenvector of $Q$, if there exists $\lambda_i$ such that

$$\begin{bmatrix} 0_{N \times N} & -Q \\ \Omega & -\Omega \end{bmatrix} \begin{bmatrix} \Delta V \\ V \end{bmatrix} = \lambda_i \begin{bmatrix} \Delta V \\ V \end{bmatrix}. \quad (38)$$

From (38), one gets:

$$-QV = \lambda_i \Delta V \quad (39a)$$

$$\omega_c(\Delta V - V) = \lambda_i V. \quad (39b)$$

By isolating $\Delta V$ in (39b) and substituting it in (39a), we obtain

$$-QV = \lambda_i (\lambda_i + \omega_c) \frac{\omega_c}{\lambda_i} V. \quad (40)$$

where $\lambda_i$ are, by construction, eigenvalues of $-Q$. From points (iv) and (vi) of Proposition 3, we have

$$\lambda_N = 0$$

$$\hat{\lambda}_i = -\gamma_i, \quad \gamma_i > 0 \quad i = 1, \ldots, N - 1. \quad (41a)$$

$$\lambda_i(\lambda_i + \omega_c) = 0 \quad (41b)$$

From (41a) and (40), one has

$$\lambda_i(\lambda_i + \omega_c) = 0$$

and hence $Q$ has a single eigenvalue equal to zero and an eigenvalue equal to $-\omega_c$. By substituting in (41b) the expression of $\hat{\lambda}_i$ in (40), one gets:

$$\lambda_i^2 + \lambda_i + \gamma_i = 0 \quad i = 1, \ldots, N - 1. \quad (42)$$

Since all the coefficients of the polynomial in (42) are strictly positive, we can conclude that matrix $Q$ has $2(N-1)$ eigenvalues with $\text{Re}(\lambda_i) < 0$. \(\Box\)
Proof of Theorem 2. Similarly to the proof of Theorem 1, we can exploit Lemma 1 with $V = H^1 \times H^1$ and $W = H^1_\perp \times H^1_\perp$. In fact, we know that (i) subspaces $H^1 \times H^1$ and $H^1_\perp \times H^1_\perp$ are $Q$-invariant (see Proposition 9) and (ii) $\mathbb{R}^N \times \mathbb{R}^N = V \oplus W$. Hence, there exists a transformation matrix $T \in \mathbb{R}^{2N \times 2N}$ such that the linear map $Q$ has an equivalent block-diagonal representation of the form (33), i.e.

$$T^{-1}QT = Q = \begin{bmatrix} Q_{11} & 0 \\ 0 & Q_{22} \end{bmatrix},$$

(43)

with $Q_{11} \in \mathbb{R}^{2(N-1) \times 2(N-1)}$ and $Q_{22} \in \mathbb{R}^{2 \times 2}$. By construction, matrices $Q_{11}$ and $Q_{22}$ in (43) represent the maps $Q(V|V)$ and $Q(W|W)$, respectively. In particular, in the light on the consideration made for system (28), we have that the eigenvalues of $Q_{22}$ are zero and $-\omega_c$. Moreover, by construction, the eigenvalues of $Q_{11}$ are the $2(N - 1)$ eigenvalues of $Q$ with strictly negative real part (see Proposition 10).
C On the eigenvalues of $Q = LDW$

In this appendix, we provide an example which shows that, by pre- and post- multiplying a generic positive definite diagonal matrix by two positive semidefinite Laplacians (associated with graphs having different topologies), one can obtain a matrix with some negative eigenvalues.

Let us consider the graphs depicted in Figures 9 and 10.

![Figure 9: Topology of $G_1$.](image1)

![Figure 10: Topology of $G_2$.](image2)

According to the assigned edge directions, the incidence matrices associated with $G_1$ and $G_2$
have the form

\[
B_1 = \begin{bmatrix}
-1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & -1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & -1 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & -1 & 0 & 0 & 0 & 0 & 0 \\
1 & 1 & 0 & 0 & -1 & 0 & 0 & 0 & 0 \\
0 & 0 & 1 & 0 & 1 & -1 & -1 & -1 & 0 \\
0 & 0 & 0 & 0 & 0 & 1 & 0 & 1 & -1 \\
0 & 0 & 0 & 0 & 0 & 0 & 1 & -1 & 0 \\
0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 \\
1 & 1 & 0 & 0 & 0 & 0 & 0 & 0 & 1
\end{bmatrix}
\]

and

\[
B_2 = \begin{bmatrix}
-1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
1 & -1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & 1 & -1 & -1 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & 1 & 0 & 1 & -1 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & -1 & 0 & 1 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & -1 & 1 & -1 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 & -1 & 0 \\
0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 & 1 \\
0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 \\n0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & -1 & 1
\end{bmatrix}
\]

respectively. As regards the (positive) weights of the edges of \(G_1\) and \(G_2\), they are collected in the following diagonal matrices

\[
W_1 = \text{diag}[0.8842, 0.8676, 0.9167, 0.8456, 0.2113, 0.0038, 0.4139, 0.1918, 0.9815]
\]

and

\[
W_2 = \text{diag}[0.6074, 0.9785, 0.8275, 0.3907, 0.4405, 0.2719, 0.1663, 0.8310, 0.3885, 0.8292].
\]

At this point, we have all the ingredients for computing the Laplacians of \(G_1\) and \(G_2\) (called \(L\) and \(M\), respectively) as

\[
L = B_1W_1B_1^T = \\
\begin{bmatrix}
0.8842 & 0 & 0 & 0 & 0 & -0.8842 & 0 & 0 & 0 & 0 \\
0 & 0.8676 & 0 & 0 & 0 & -0.8676 & 0 & 0 & 0 & 0 \\
0 & 0 & 0.9167 & 0 & 0 & -0.9167 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0.8456 & 0 & 0 & 0 & 0 & 0 & -0.8456 \\
-0.8842 & -0.8676 & 0 & 0 & 0 & 1.9631 & -0.2113 & 0 & 0 & 0 \\
0 & 0 & -0.9167 & 0 & -0.2113 & 1.5458 & -0.0038 & -0.4139 & 0 & 0 \\
0 & 0 & 0 & 0 & -0.0038 & 1.1771 & -0.1918 & -0.9815 & 0 & 0 \\
0 & 0 & 0 & 0 & -0.4139 & -0.1918 & 0.6057 & 0 & 0 & 0 \\
0 & 0 & 0 & -0.8456 & 0 & 0 & -0.9815 & 0 & 1.8271 & 0
\end{bmatrix}
\]

and

\[
M = B_2W_2B_2^T = \\
\begin{bmatrix}
0.6074 & -0.6074 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
-0.6074 & 1.5859 & -0.9785 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & -0.9785 & 2.1967 & -0.8275 & 0 & 0 & 0 & 0 & -0.3907 & 0 \\
0 & 0 & -0.8275 & 1.5399 & -0.4405 & -0.2719 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & -0.4405 & 0.6068 & -0.1663 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & -0.2719 & -0.1663 & 1.2091 & -0.8310 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & -0.8310 & 1.2195 & -0.3885 & 0 & 0 & 0 \\
0 & 0 & 0 & -0.3907 & 0 & 0 & 0 & -0.3885 & 1.6085 & -0.8292 \\
0 & 0 & 0 & 0 & 0 & 0 & 0 & -0.8292 & 0.8292 & 0
\end{bmatrix}
\]
Next, we pick the positive definite diagonal matrix

\[ D = \text{diag}[0.5977, 0.4297, 0.4937, 0.0058, 0.4643, 0.0005, 0.6299, 0.8209, 0.3597] \]

and pre- and post- multiply it by \( L \) and \( M \), respectively, thus obtaining

\[ Q = LDM = \]

\[
\begin{bmatrix}
0.3210 & -0.3210 & 0 & 0.1808 & -0.2491 & 0.0683 & 0 & 0 & 0 \\
-0.2264 & 0.5912 & -0.3647 & 0.1774 & -0.2444 & 0.0670 & 0 & 0 & 0 \\
0 & -0.4428 & 0.9941 & -0.3744 & 0.0001 & 0.0006 & 0.0004 & -0.1768 & 0 \\
0 & 0 & 0 & -0.0040 & 0.0075 & -0.0021 & 0.0013 & 0 & 0.2522 & -0.2522 \\
-0.0946 & -0.2701 & 0.3647 & -0.4015 & 0.5531 & -0.1517 & 0.0001 & 0 & 0 \\
0 & 0.4428 & -0.8614 & 0.4175 & -0.0597 & 0.0193 & 0.1284 & -0.3688 & 0.2817 \\
0 & 0 & 0.0615 & 0.0000 & 0.0000 & -0.6161 & 0.9653 & -0.2485 & -0.1622 \\
0 & 0 & -0.1943 & 0.0001 & 0.0000 & 0.1001 & -0.3403 & 0.8467 & -0.4123 \\
0 & 0 & 0.0040 & -0.0075 & 0.0021 & 0.5150 & -0.7539 & -0.3048 & 0.5450
\]

Now, if we compute the eigenvalues of \( Q \), we get

\[ \text{eig}(Q) = \{1.3891 + 0.1564i, 1.3891 - 0.1564i, 0.9210 + 0.0000i, 0.5879 + 0.0000i, \\
0.4509 + 0.0000i, 0.1057 + 0.0000i, -0.0002 + 0.0039i, \\
-0.0002 - 0.0039i, 0.0000 + 0.0000i\} \]

that proves the desired result. This example led us to introduce Assumptions 3 and 4 which define conditions under which the eigenvalues of \( Q \) are always real and nonnegative.
### D Electrical parameters

In this appendix, we provide the electrical parameters of the simulation scenario described in Section 5.1.

#### Converter parameters

| DGU   | $R_t$ (Ω) | $L_t$ (mH) | $C_t$ (mF) |
|-------|-----------|------------|------------|
| $\Sigma_{[1]}$ | 0.2       | 1.8        | 2.2        |
| $\Sigma_{[2]}$ | 0.3       | 2          | 1.9        |
| $\Sigma_{[3]}$ | 0.1       | 2.2        | 1.7        |
| $\Sigma_{[4]}$ | 0.5       | 3          | 2.5        |
| $\Sigma_{[5]}$ | 0.4       | 1.2        | 2          |
| $\Sigma_{[6]}$ | 0.6       | 2.5        | 3          |
| $\Sigma_{[7]}$ | 0.3       | 2          | 2.1        |

#### Power line parameters

| Connected DGUs $(i,j)$ | Resistance $R_{ij}$ (Ω) | Inductance $L_{ij}$ (µH) |
|------------------------|--------------------------|---------------------------|
| (1, 2)                 | 0.05                     | 2.1                       |
| (1, 3)                 | 0.07                     | 1.8                       |
| (3, 4)                 | 0.06                     | 1                         |
| (2, 4)                 | 0.04                     | 2.3                       |
| (4, 5)                 | 0.08                     | 1.8                       |
| (1, 6)                 | 0.1                      | 2.5                       |
| (5, 6)                 | 0.08                     | 3                         |
| (4, 7)                 | 0.09                     | 2.3                       |
| (7, 5)                 | 0.05                     | 2.4                       |

Table 1: Electrical parameters.
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