Signal Generation for Switched Reluctance Motors using Parallel Genetic Algorithms

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Abstract: Switched reluctance motors (SRMs) are an inherent part in robotics and automation systems where energy and cost efficiency is required. This motor type has no windings and permanent magnets on the rotor which results in a simple and robust structure. However, SRMs require a complex electronic control system to generate a specified number of voltage pulses for each motor phase. This paper presents the signal generation of multiple phases using only one current sensor in an asymmetric half bridge (AHB). In addition to maintain the predetermined phase voltages, sufficient current measurement windows and a minimal current ripple for the individual phases are further optimization criteria for signal generation. The generation of a state vector which controls the individual semiconductor for each motor phase to achieve a required phase voltage and simultaneously fulfill the multi-objective optimization criteria is challenging. Due to the vast number of possible solutions, a genetic algorithm (GA) was used to find state combinations that are suitable for the formulated optimization criteria. The results were discussed and recommendations about the genotype representation and the used genetic operators were given. Interested readers will find detailed information about the software technical implementation using the Global Optimization Toolbox from MATLAB.

Keywords: Genetic algorithms, Parallel programs, Switched reluctance motors, Measuring span, Multi-objective optimization

1. INTRODUCTION

The working principle of switched reluctance motors (SRMs) has been known for more than 100 years. However, research on the topic has increased only during the past three decades. Due to its simple and low cost construction without permanent magnets, SRMs are highly reliable and can reach mechanical velocities of up to more than 100×10³ min⁻¹. This motor type can also be used for position control and since it does not require brushes, this motor can be used in hazardous and explosive environments.

Many researchers have tried to find optimal design parameters with the help of techniques inspired by natural evolution such as Shaked and Rabinovici (2006); Sihem et al. (2012); Prabhu et al. (2015). These results are the motivation for this paper to focus on the optimization of switching signals for semiconductors. For cost reduction, integration density improvement and redundancy, several position sensorless algorithms have been developed. Using one current sensor for multiple measurements nearly simultaneously is another concept discussed in this paper. The focus here is on an asymmetric half bridge converter topology with one current sensor for two phases, based on Kjaer and Gallegos-Lopez (1997) and Chen and Lu (2013). Fig. 1 a) shows the asymmetric half bridge for two phases with possible current sensor locations. Fig. 1 b) shows a variant, where only one current sensor is used to sense all phase currents. This requires a single current path through the sensor for an unambiguous measurement of each phase current. Thus, the objectives of this paper are: (i) to present a circuit state-based way of looking at switched reluctance motor control and (ii) to propose methods to generate a control sequence using genetic algorithms (GA) with multi-objective fitness functions.

Fig. 1. AHB with a) different current sensor placements and b) a single sensor. The lines show current paths for a valid (green) and an invalid (red) measurement.
2. GENETIC ALGORITHM

2.1 Representation

Fig. 1 shows an asymmetric half bridge with two switches $n_s = 2$ for each of the two phases $n_p = 2$. This results in 16 possible states $z_i$ in one time step $T_i$. A control sequence for the SRM with $n_{st}$ time steps can be described easily with a state vector $Z$ consisting of $n_{st}$ states. This state vector, which can be called phenotype in this form, has to be transformed into a suitable genotype representation to be operated by the genetic algorithm. First of all, one individual can be understood as an array containing the switching states $s_{s,p}$ of each semiconductor. This way a binary string represents the genetic information for a specific individual which is shown in Fig. 2.

The decimal value $h_{st}$ of an asymmetric half bridge with $n_s$ switches for each of the $n_p$ phases is defined using

$$h_{st} = \sum_{j=0}^{n_p-1} \sum_{i=0}^{n_s-1} (s_{j,i} 2^{n_p j + n_s i - 1})$$

which is used as the state number for the practical implementation.

The switching state of every semiconductor can be considered as a single gene with two alleles. The first allele stands for a conducting switch ($=1$) and the second allele stands for non-conducting switch ($=0$). More detailed information about the state vector concept can be found in Pfurtsch (2019).

The following three different types of genotype representation are discussed in this paper:

Binary-representation

Every bit of the whole stream can be manipulated independently. The genetic algorithm only operates at the binary level.

Grouped-binary-representation

The genetic algorithm is not allowed to apply the crossover mechanism to each bit of the stream. Only specific points between binary groups of $n_s n_p$ bits are possible breakages. Instead, the mutation operator can be used on every bit.

Integer-representation

The whole bit stream is interpreted as a number of binary groups represented by their integer value $h_{st}$. Genetic operators only work at this level.

Fig. 2. Population of bit string individuals for a SRM with $n_s = 2$ and $n_p = 2$ according Fig. 1

2.2 Fitness function formulation

The use of a state vector for control of an asymmetric half bridge will have some effects on the electrical, and furthermore, the magnetic and mechanical behavior of the motor. The resulting CPU requirements also have to be taken into account. The following list contains possible properties:

- average phase voltages
- current ripples
- current measurement windows
- capacitors balancing
- switching frequency
- switching equality between high-side and low-side switches
- necessary memory space
- efficiency

In this paper, the first three requirements will be considered.

To define a fitness function that evaluates the fitness of each individual corresponding to the phase voltage achievement, an error needs to be defined that represents the deviation between a number of specified reference phase voltages $v_{ref}$ and the actual average voltages $v_{ref}(Z)$ forced by the state vector for each phase. The fitness value $f_{it\text{voltage}}$ can be calculated using the Euclidean distance from the error vector $|e|$ as follows:

$$f_{it\text{voltage}} = |e| = \left( (v_{ref} - v_{ref}(Z))^2 + \ldots \right)^{1/2}$$

For the provision of current measurement windows, the genetic algorithm is used to minimize the difference between the desired number of measurement windows $n_{p_m,des}$ and the actual number of measurement windows $n_{p_m,act}$ for a specified phase current $i_p$ in phase $p$. The corresponding fitness function $f_{it\text{window}}$ is therefore

$$f_{it\text{window}} = \max(n_{p_m,des} - n_{p_m,act}, 0)$$

The $\max$ operator allows the user to set the lower bound of the fitness value to zero, because additional measurement windows are not necessary after achieving the desired number $n_{p_m,des}$.

The ripple which represents the difference between the maximum current $\max(i_p(Z))$ and the minimum current $\min(i_p(Z))$ of a phase controlled by the state vector is the third optimization criterion for this paper which can be calculated as

$$f_{it\text{ripple}} = \max(i_p(Z)) - \min(i_p(Z))$$

To merge all fitness functions described above, a weighting function needs to be defined. This over-all fitness function allows to weight each optimization criterion using the weighting factors $w_{\text{voltage}}, w_{\text{ripple}},$ and $w_{\text{window}}$, that is,

$$f_{it\text{g}} = w_{\text{voltage}} f_{it\text{voltage}} + w_{\text{ripple}} f_{it\text{ripple}} + \ldots + w_{\text{window}} f_{it\text{window}}$$

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3. PRACTICAL IMPLEMENTATION

For the practical implementation a set of properties for all possible states needs to be defined. The system to be implemented is represented by \( 2^n = 16 \) states which are shown in Table 1. Each of these states forces a phase voltage \( V_p \) which results in a current \( I_p \). To be able to measure the phase currents every state includes information about the necessity of a current sensor (green marked fields in Table 1). Each state is accessed by a binary code that includes information about the switching state of each semiconductor.

| State Number | Binary Code | Voltage | Meas. |
|--------------|-------------|---------|-------|
| 1            | 0000        | -12 V   | 0     |
| 2            | 0001        | -12 V   | 1     |
| 3            | 0010        | 0 V     | 0     |
| 4            | 0011        | 0 V     | 1     |
| 5            | 0100        | +12 V   | 0     |
| 6            | 0101        | +12 V   | 1     |
| 7            | 0110        | 0 V     | 0     |
| 8            | 0111        | 0 V     | 1     |
| 9            | 1000        | 0 V     | 0     |
| 10           | 1001        | 0 V     | 1     |
| 11           | 1010        | +12 V   | 0     |
| 12           | 1011        | +12 V   | 1     |
| 13           | 1100        | 0 V     | 0     |
| 14           | 1101        | 0 V     | 1     |
| 15           | 1110        | -12 V   | 0     |
| 16           | 1111        | -12 V   | 1     |

The results presented in this section are based on the GA parameter settings in Table 2. The basics section in the upper part of the table includes parameters that are used in each representation (see section 2.1). A weighted fitness function according to (5) was used for the requirements evaluation (see section 2.3). The results presented in this section are based on the GA parameter settings in Table 2. The basics section in the upper part of the table includes parameters that are used in each representation (see section 2.1). A weighted fitness function according to (5) was used for the requirements evaluation (see section 2.3).
Table 2. GA parameters

| GA parameter     | value |
|------------------|-------|
| Population size  | 200   |
| Number of generations | 150 |
| Crossover fraction | 0.7  |
| Fitness function  | Rank based fitness scaling |
| Elite count       | 4     |
| Mutation fraction | 0.005 (Bin.), 0.02 (Group.), 0.04 (Int.) |

**Binary and grouped-binary-representation**
- Creation function: Uniform
- Population type: Bitstring
- Mutation function: Uniform

**Binary and integer-representation**
- Crossover: Two point crossover

**Grouped-binary-representation**
- Crossover: Two point crossover (custom function)

**Integer-representation**
- Creation function: Uniform integer (custom function)
- Population type: Double vector
- Mutation function: Uniform integer (custom function)

### 4.1 Convergence behavior

The three representations presented in Section 2.1 were used for convergence analysis. Each phase should have an average voltage value of about 10 V. As can be seen in Fig. 4 b), the best individual is not changed anymore after generation 54. Fig. 4 a) and 4 c) will need more generations to get the solution. The reason for the worse convergence of the binary-representation in comparison to the grouped-binary-representation is due to “additional mutations” when a crossover is carried out as a result of the arbitrary choice of breakages. This often leads to new states if the break position is not an integer multiply of $n_s \times n_p$. The reason for the poorer convergence of the integer-representation in comparison to the grouped-binary-representation needs to be searched for in their mutation algorithm. In the first step, the mutation algorithm selects several states in the state vector of an individual. Secondly, the algorithm replaces these state elements with a uniformly distributed random number between 0 and 15 ($2^{n_s} - 1$). For comparison, the mutation algorithm used in the binary- or grouped-binary-representation changes only by single bits in the bit string. This leads to mutated states which only differ in one bit from the original state. This optimal mutation behavior cannot be reproduced by the integer mutation algorithm. Furthermore, the mutation fraction parameter of the integer-representation, where the genotype length corresponds to the state vector length of 50, needs to be increased with regards to the grouped-binary-representation. For these reasons, the grouped-binary-representation is favored and will be used for the following optimizations.

Fig. 5 shows the convergence of the GA. Each row contains the states of the best state vector found in each generation. The color indicates the state number which is generated by the switching states of each semiconductor as a binary string converted into its decimal representation. It shows that the evolutionary convergence is very fast until generation 60. After that, only small changes can be observed until the algorithm finds the optimum at generation 120.

### 4.2 Optimized PWM pattern

The optimization result has to be translated back into a usable PWM pattern to be applied to each of the four switches. Fig. 6 shows the generated pattern within a time interval of 50 µs for a voltage of about 10 V per phase and a number of phase current measurement windows of about 5 windows. Fifty states were optimized to reach an average reference voltage, a given number of current measurement windows and minimal current ripple for each phase. The state number is shown for every state upper the time axis in the upper plot of Fig. 6.

### 4.3 Multi-objective optimization

In previous tests, a weighted fitness function was used. This requires a suitable choice of the weighting factors to achieve the qualitatively desired result. This often is a trial and error process which results in many test runs. The Global Optimization Toolbox from MathWorks provides a genetic algorithm named gamultiobj to solve multi-objective optimization problems by finding an evenly distributed set of points on the Pareto front. This approach also allows the optimization of nonsmooth prob-
suitable weighting factors for a classical GA. The design idea is to find a weighting factor set which leads to the smallest fitness value $f_{n_w}$ for the chosen optimal individual in comparison to all other individuals of the Pareto set.

### 4.4 Parallelization

To analyze the benefit of parallelization, the GA algorithm was run on a Windows 64-bit operating system using MATLAB 2016b on a Dell Precision T5600 Workstation with two Intel Xeon E5-2665 processors. This allows the usage of 16 processor cores. For the tests, the grouped-binary-representation with the Simulink system in Fig. 3 was used. All tests ran three times with a high priority set for the MATLAB process. Fig. 8 shows the computing time with different worker threads $n_w$. The ideal and the real speedup as well as modeled speedup curves $S(n_w)$ using Amdahl’s law (adapted for GA)

$$S(n_w) = \frac{T_s + n_p T_c}{T_s + T_c \frac{n_p}{n_w}}$$

and a developed speedup equation for GA

$$S(n_w) = \frac{T_s + n_w T_c + T_{ep} \frac{n_p}{n_w}}$$

which combines the speedup equations of Cantú-Paz (2000) and Trobec (2009) are shown in Fig. 9. This equation considered all the observed effects by the parallel GA computation and allows a better modeling of the real speedup in comparison to Amdahl’s Law. The usage of two individual values for the single fitness calculation time (serial execution: $T_c$, parallel execution: $T_{ep}$) allows the modeling of the superlinear speedup for a low number of workers ($n_w < 6$) as a result of a more efficient usage of resources using the Parallel Computing Toolbox ($T_s > T_{ep}$). These two values also acknowledge the fact that the fitness calculation time is dependent on the simulation time of the Simulink system which can vary. According to Trobec (2009), the value of $T_{ep}$ describes the time for the concurrent execution of $n_w$ fitness evaluations in a group and, hence, it corresponds to the longest period of the several fitness evaluations in the group. The serial start of all worker processes as well as the communication time between the GA (master) and the fitness evaluations (slaves) are considered by the product of $n_w T_c$ according to Cantú-Paz (2000). The ceiling function $[\tilde{f}]$ in the calculation of the split groups $\left[ \frac{n_w}{n_w} \right]$ considers the fact that the last group with fewer fitness evaluation calls as available worker threads needs the same time as the other groups. The parameters in (6) and (7) are calculated from regression using the measured computing times. Table 3 includes the parameter descriptions and the calculated values. The analysis of (7) shows that with the increasing number of workers, the computation time for all fitness evaluations decreases while the communication time increases. The number of workers needed to approach the minimal execution time can be solved using $\frac{\partial S(n_w)}{\partial n_w} = 0$. For these tests, the optimal number $n_w$ is 29, where a maximum speedup $S(n_w)$ of 14.75 will be achieved. An estimation of the necessary workers for an efficient work can be determined using the parallel efficiency.
Table 3. Speedup equation parameters

| Parameter                                      | Value   |
|------------------------------------------------|---------|
| Population size $n_p$                         | 200     |
| Number of generations                         | 150     |

**Amdahl’s Law (6)**

Single generation time without fitness calculations $T_1$ = 0.487 s
Single fitness evaluation time $T_e$ = 0.230 s
Equation for GA (7)

Single generation time without fitness calculations $T_1$ = 0.132 s
Single fitness evaluation time (serial execution) $T_e$ = 0.236 s
Communication time $T_c$ = 0.053 s
Single fitness evaluation times (parallel execution) $T_{ep}$ = 0.222 s

$$E(n_w) = \frac{S(n_w)}{n_w}$$ (8)

Using an efficiency $E$ of 0.8, which corresponds to an 80% performance of the parallel GA algorithm of its ideal potential, results in a number of workers $n_w = 15.6$ or rather 15. This means the usage of more than 15 workers does not lead to any significant computing speedup effects. The equations presented above and the relationships allow a rough estimation of the necessary hardware to solve GA algorithms efficiency in terms of time and cost.

5. CONCLUSION

This paper presented a concept to generate control sequences for a SRM with a reduced amount of current sensors using GA. Different genotype representations were tested for increasing the convergence rate. To fulfill all requirements, a weighted multi-objective fitness function as well as a Pareto front solution were used successfully. An approach to determine the number of cores to solve GA algorithms efficiently was presented. The software implementation based on MATLAB is discussed in detail.

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