Pulsed chemical vapor deposition of conformal GeSe for application as an OTS selector†

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The ovonic threshold switch (OTS) selector based on the voltage snapshot of amorphous chalcogenides has received tremendous attention as it provides several desirable characteristics such as bidirectional switching, a controllable threshold voltage, high drive currents, and low leakage currents. GeSe is a well-known OTS selector that fulfills all the requirements imposed by future high-density storage class memories. Here, we report on pulsed chemical vapor deposition (CVD) of amorphous GeSe by using GeCl₂-C₄H₈O₂ as a Ge source and two different Se sources namely bis-trimethylsilylselenide [(CH₃)₃Si]₂Se (TMS)₂Se and bis-triethylsilylselenide [(C₂H₅)₃Si]₂Se (TES)₂Se. We utilized total reflection X-ray fluorescence (TXRF) to study the kinetics of precursor adsorption on the Si substrate. GeCl₂-C₄H₈O₂ precursor adsorption on a 300 nm Si substrate showed under-dosing due to limited precursor supply. On the other hand, the Se precursor adsorption is limited by low reaction efficiency as we learned from a better within-wafer uniformity. Se precursors need Cl sites (from Ge precursor) for precursor ligand exchange reactions. Adsorption of (TMS)₂Se is found to be much faster than (TES)₂Se on a precoated GeCl₂ layer. Atomic layer deposition (ALD) tests with GeCl₂-C₄H₈O₂ and (TMS)₂Se revealed that the growth per cycle (GPC) decreases with the introduction of purge steps in the ALD cycle, whereas a higher GPC is obtained in pulsed-CVD mode without purges. Based on this basic understanding of the process, we developed a pulsed CVD growth recipe (GPC = 0.3 Å per cycle) of GeSe using GeCl₂-C₄H₈O₂ and (TMS)₂Se at a reactor temperature of 70 °C. The 20 nm GeSe layer is amorphous and stoichiometric with traces of chlorine and carbon impurities. The film has a roughness of ~0.3 nm and it starts to crystallize at a temperature of ~370 °C. GeSe grown on 3D test structures showed excellent film conformity.

Introduction

Computer memory can be broadly categorized into two segments: volatile memory and non-volatile memory (NVM).¹ Volatile memory requires power to maintain the stored information and its typical examples are static random-access memory (SRAM) and dynamic RAM (DRAM). Major advantages of volatile memory include high speed and fast access. On the other hand, the access to NVM is relatively slower but it can retain the stored information even when powered-off and it is therefore considered as long-term persistent storage. Typical NVM examples are magnetic storage devices (hard disks) and NAND flash. The storage density, cost, and performance gap between DRAM and NAND can be fulfilled by a new non-volatile memory (NVM) candidate: resistive random-access memory (RRAM).¹ RRAM – an emerging type of memory based on resistive switching is regarded as a promising technology for next-generation embedded and standalone NVM.² RRAM promises near-DRAM performance, but with lower cost and higher density.

The typical RRAM configuration is the so-called cross-point array, which includes bit lines, word lines, and memory cells located at the intersection of each bit and word line. Ideally, writing or reading a memory cell in a cross-point array is meant to take place only on the selected cell, leaving the rest of the cells unaffected. However, current sneak through unselected cells during operation, degrading the selectivity of the cell and leading to false information retrieval.³ An attractive approach is to have an additional selection device called a selector in series...
with each memory element that suppresses sneak currents through highly nonlinear current-voltage (IV) characteristics. Ovonic threshold switching (OTS) selector devices have received much attention as they provide full cell selectivity, high drive currents, and low leakage currents.\(^1\) The concept of the OTS mechanism refers to a rapid and reversible transition between a highly resistive and a conductive state effected by an electric field.\(^2\) The 3D RRAM approach, which combines the advantages of excellent electrical performance in RRAM cells and an ultra-high storage density with low cost due to the 3D configuration, becomes a very attractive candidate for next generation high density NVM applications. A 3D integration route for RRAM involves multiple selector-resistor stacks in sequence, without requiring litho steps in between. To fabricate this innovative 3D vertical RRAM, a main challenge remains – the uniform and conformal deposition of the chalcogenide selector material on 3D substrates.

Recent studies have shown that PVD grown GeSe is one of the most promising candidates for OTS selector devices owing to its high amorphous stability, large on/off current ratio, high selectivity, and large driving current.\(^5\)\(^–\)\(^8\) Note that this amorphous stability is critical since threshold OTS switching is a characteristic of the amorphous phase and no crystallization is involved for the material to function as a selector. Amorphous stability can be enhanced by addition of ternary and quaternary elements in binary germanium chalcogenide materials that impede easy crystallization; for example, it has been demonstrated that PVD-grown N-doped GeSe,\(^9\) SiGeTe\(^10\) and SiAsGeTe\(^11\) showed better OTS switching characteristics with enhanced amorphous stability. Nevertheless, the adopted PVD method with its limited step coverage cannot be used for conformal deposition on 3D memory structures. Atomic layer deposition (ALD) stands out as a material deposition technique that enables the conformal growth of thin films with sub-angstrom step coverage (SE) have been used to gather insights into different ALD processes.\(^25\) However, we have performed growth in a manufacturing-scale reactor where it is not possible to use in situ measurements such as quartz crystal microbalance (QCM) and spectroscopic ellipsometry (SE) have been used to gather insights into different ALD processes.\(^25\) However, we have performed growth in a manufacturing-scale reactor where it is not possible to use in situ QCM and SE. Instead, we have studied the precursor adsorption kinetics and the saturation behaviour by means of ex situ total reflection X-ray fluorescence (TXRF) measurements. Based on insights from adsorption studies, we designed a pulsed CVD process for GeSe deposition on 300 mm wafers at a reactor temperature of 70 °C. Then, we conducted a parame-

Experimental

The GeSe layers were deposited by CVD/ALD on 300 mm Si (100) wafers (covered with native SiO\(_2\)) in a hot-wall, cross-flow-type ASM Pulsar 3000 reactor, connected to a Polygon 8300 platform. GeCl\(_2\)·C\(_3\)H\(_4\)O\(_2\) (solid crystalline form) was used as a Ge source.
and two different Se sources (liquid form) namely bis-tri-methylsilylselenide ((CH$_3$)$_3$Si)$_2$Se (TMS)$_2$Se) and bis-triethyl-silylselenide ((C$_2$H$_5$)$_3$Si)$_2$Se (TES)$_2$Se) were used. N$_2$ was employed as a carrier gas. The carrier gas flowed over the ampoule where the precursor vapor was carried by the carrier gas and taken into the reactor. The GeCl$_2$/C$_2$H$_5$OH ampoule temperature was varied between 70 and 90 $^\circ$C. The (TES)$_2$Se ampoule temperature was varied between 70 and 85 $^\circ$C. The (TMS)$_2$Se ampoule was kept at room temperature (RT) except in the precursor chemisorption study where it was kept at 85 $^\circ$C. Reactor temperature was varied between 64 and 150 $^\circ$C. For material characterization, a thick GeSe (20 nm thick) layer was grown with the following recipe: (1) 25 s pulse length of GeCl$_2$/C$_2$H$_5$OH, (2) 0.5 s purge, (3) 2 s pulse length of (TMS)$_2$Se, and (4) 0.5 s purge. This ~20 nm GeSe was grown at a growth temperature of 70 $^\circ$C with GeCl$_2$/C$_2$H$_5$OH (ampoule temp. 80 $^\circ$C) and (TMS)$_2$Se (ampoule at RT) as Ge and Se sources, respectively.

Total reflection X-ray fluorescence spectrometry (TXRF) inspection was performed using a Rigaku TXRF300 system equipped with a rotating X-ray source with a tungsten target. The spatial distribution of the deposited elements can be determined by measuring predetermined locations on the wafer surface. Here we used a pattern of 165 locations with a separation distance of 21 mm. Each location was exposed during 10 seconds to an X-ray beam with an energy of 24 keV (Beam 3), which probed the surface at a glancing angle of 0.05 degrees. The spectra recorded by the energy-dispersive Si(Li) detector are used to create a spatial map for the detected Ge and Se elements. With quantification capabilities in the 10$^{10}$ at cm$^{-2}$, total-reflection X-ray fluorescence (TXRF), a contamination analysis tool seems promising to study precursor adsorption and get in-depth insight into the precursor adsorption and reaction kinetics of the chemistry.

Results and discussion
Precursor adsorption studies using TXRF
First, we studied the GeCl$_2$/C$_2$H$_5$OH adsorption kinetics on Si/SiO$_2$ substrates for different reactor temperatures as shown in Fig. 1. We analyzed the Ge atomic density as a function of the GeCl$_2$/C$_2$H$_5$OH pulse length by ex situ TXRF measurements at different positions on the 300 mm wafer (Fig. 1). The Ge concentration values shown in Fig. 1(a) are the average Ge atomic densities over the full wafer from 165 data collection points. A few selected Ge distribution maps are shown in

![Fig. 1](image-url)
Fig. 1 (b) and (c). As we increase the pulse length of GeCl2-dioxane, we observe an increase in average Ge concentration until it saturates at 50 s (Fig. 1a). Fig. 1b and c show 300 mm TXRF maps that reveal the distribution of Ge atomic density over the 300 mm wafer. A uniform coverage of Ge on the Si/SiO2 substrates can only be attained with a minimum of 50 s precursor exposure. A non-uniform partial coverage (under dosing) is observed below 50 s due to limited precursor supply. Limited precursor supply is mainly due to slow sublimation behavior of the precursor. With slow sublimation, the precursor dose can decrease as a function of pulse length. In order to investigate this, we have injected GeCl2-dioxane precursor into the reactor with two different pulsing schemes [mentioned in Fig. 1 (pulse/purge/repetitions): (1) one continuous GeCl2-dioxane pulse, and (2) sequential GeCl2-dioxane pulses and N2 purges. We found similar Ge concentrations for both pulsing schemes, which indicates that the precursor does not need a break (purging time) between the pulses to regenerate the vapors and the precursor dose does not decrease as a function of pulse length (Fig. 1a).

The Ge saturation coverage of $\sim 2.7 \times 10^{14}$ at cm$^{-2}$ is only $\sim 25\%$ of the potentially complete coverage ($\sim 1 \times 10^{15}$ at cm$^{-2}$ atomic density for complete monolayer coverage of Ge). One possibility to explain this low value of the saturation coverage is steric hindrance of the ligands of the chemisorbed species that could shield part of the reactive surface (OH groups) from being accessible to the incoming GeCl2-dioxane reactant. In the case of molecular adsorption (Fig. 2(a)), we assume that the weakly bonded dioxane ligands desorb quickly to be replaced by coordinative bonds with surface oxygen or surface hydroxyl groups, with the formation of GeCl2 surface species. However, the Ge atomic density of $2.7 \times 10^{14}$ at cm$^{-2}$ at saturation is still lower than the maximum Ge content allowed for GeCl2 surface species based on steric hindrance of the Cl ligands. The maximum coverage of Cl using the steric hindrance model on a flat surface has been calculated as $8.8 \times 10^{14}$ at cm$^{-2}$ (8.8 $\times 10^{14}$ Cl at cm$^{-2}$ $\rightarrow 4.4 \times 10^{14}$ Ge at cm$^{-2}$).27

In addition, the GeCl2 molecule can undergo ligand exchange reactions with the reactive OH groups, with release of HCl (Fig. 2(b) and (c)). However, these reactions could result in even more accommodation of Ge. We conclude that all reactions shown in Fig. 2 are possible and the saturation coverage cannot be explained by steric hindrance of the Cl ligands. Another possibility is that the coverage is limited by a low density or reactivity of the surface sites.

When the same experiment was repeated with (TES)$_2$Se exposure to a native SiO2/Si surface, it resulted in no detected Se adsorption (TXRF, Fig. S1, ESI†). According to the reported dehalosilylation chemistry17 for growth of GeSbTe, ligand exchange of the alkylsilylchalcogenide precursor occurs with chloride surface species available from the previous GeCl2 reaction. Since no such reactive sites are present on the native SiO2/Si substrate, (TES)$_2$Se is not able to adsorb.

In view of the above, the native SiO2/Si surface was first treated with GeCl2-dioxane. A long GeCl2-dioxane exposure consisting of 500 cycles of GeCl2-dioxane pulse/purge (5 s pulse/5 s purge) was carried out on the SiO2/Si surface to saturate the surface with chloride-reactive GeCl2 species. This indeed provides reactive sites for Se precursor adsorption, as TXRF shows the presence of Se on the surface. Fig. 3 shows the Se concentration as measured by TXRF as a function of the (TES)$_2$Se pulse length on a GeCl2-dioxane pretreated Si/SiO2 surface. The Se concentration saturates only after 400 s of (TES)$_2$Se exposure, irrespective of the different process conditions. We tested similar pulsing sequences for (TES)$_2$Se [continuous dose or pulse/purge/repetition (mentioned in Fig. 3a)], for the reasons as aforementioned for the Ge precursor. We found that the (TES)$_2$Se precursor dose is not decreasing as a function of pulse length (pulse/purge/repetition sequence provides the same Se atomic density as a continuous dose) and a continuous (TES)$_2$Se pulse is providing optimal injection to increase the throughput. The TXRF Se atomic density distribution maps reveal much better within wafer uniformity below the saturation pulse length (0–400 s) as compared to Ge (Fig. 3b). This indicates that Se adsorption on a precoated GeCl2 surface is limited by a rather low reaction efficiency. The Se saturation content is $\sim 3.5 \times 10^{13}$ at cm$^{-2}$, which shows that Se is covering only 15% of the available Ge sites. This low Se content is partly explained by steric hindrance of rather bulky ethyl silyl ligands of (TES)$_2$Se precursor.

Fig. 4 shows the reactor temperature effect on (TES)$_2$Se adsorption. Clearly, Se adsorption decreases at a higher reactor temperature (Fig. 4(a)): $\sim 64\%$ decrease in Se atomic density is observed, when going from 100 °C to 150 °C reactor temperature. Fig. 4(b) and (c) show the TXRF Se atomic density distribution maps with 80 s of (TES)$_2$Se pulse length at 100 °C and 150 °C reactor temperatures, respectively. A decrease in the presence of Se is also observed in these maps when going to the 150 °C reactor temperature albeit still uniform indicating that it is not the underdosing issue. On the other hand, Ge adsorption did not show any appreciable dependence on the reactor temperature changes in this experiment, which shows that reaction between Ge and Se precursors is favoured in a narrow low temperature window. In the literature17,18,21 chalcogenide materials grown with the same type of chemistry have shown a decrease in GPC with increase in growth temperature, which has been attributed to desorption of precursors at higher temperature.

The adsorption studies showed that GeCl2-dioxane and (TES)$_2$Se precursor adsorption is very slow, which poses a hurdle for the development of an ALD GeSe process with a decent throughput. Out of these two precursors, particularly
(TES)$_2$Se is impractical due to its slow reaction with the GeCl$_x$-terminated surface and its low vapor pressure. T. Hatanpää et al. reported thermogravimetric analysis curves for different Te and Se compounds where different compounds including (TES)$_2$Se, (TES)$_2$Te, and (TMS)$_2$Te showed pure evaporation behaviour with minimal residues. The vapor pressure of (TMS)$_2$Te was found to be very high compared to (TES)$_2$Te, due to its lower molecular weight. Additionally, TMS has a smaller size compared with TES, which can provide better reactivity and low steric hindrance. This provided good motivation for us to use (TMS)$_2$Se as a Se source in order to make Se adsorption faster. Hence, the next Se adsorption tests were performed using a (TMS)$_2$Se source.

Fig. 5 shows the Se concentration vs. pulse length of the (TMS)$_2$Se precursor as measured via TXRF. For comparison purposes, Se adsorption with (TES)$_2$Se is also shown in the same graph. We employed the same reactor and processing conditions for studying the adsorption of both precursors. It is evident that (TMS)$_2$Se demonstrates faster, higher, and uniform Se saturation concentration with Se saturating with 100 s of (TMS)$_2$Se. The saturation concentration is $1.1 \times 10^{14}$ at cm$^{-2}$, which amounts to 75% coverage of available Ge sites. This is significantly higher coverage of available Ge sites compared to that of (TES)$_2$Se (15% Ge site coverage with (TES)$_2$Se). Faster saturation and higher coverage with (TMS)$_2$Se in comparison with (TES)$_2$Se can be attributed to higher vapor pressure, better...
reactivity, and low steric hindrance (relatively smaller ligands) of the \((\text{TMS})_2\text{Se}\) precursor. Hence, this study confirms that \((\text{TMS})_2\text{Se}\) is a better Se precursor for dehalosilylation reaction with metal chloride precursors to form GeSe.

On the other hand, keeping in mind the slow adsorption of GeCl\(_2\), the alternative precursors are other Ge halides such as Ge bromide (GeBr\(_2\)) and germanium iodide (GeI\(_2\)). However, a study has shown that GeBr\(_2\) reaction with alkylsilyl compounds proceeds relatively slowly with more impurities in the final product.\(^\text{28}\) This was attributed to the soft base character of Br\(^-\). The size of the nucleus increases down the group of halides (Cl \(\sim\) Br \(\sim\) I), which makes Br and I softer bases compared to Cl, hence making the reaction of metal bromides and iodides with alkylsilyl compounds less favourable. Therefore, the GeCl\(_2\)-stabilizer still seems to be the best metal halide precursor for this chemistry to form the GeSe product.

**Pulsed CVD process development of GeSe**

In this section, we show the development of a pulsed CVD process for GeSe using GeCl\(_2\)-dioxane and \((\text{TMS})_2\text{Se}\). We have investigated the effect of purge length, precursor temperature, and reactor temperature on growth per cycle (GPC) of GeSe. Afterwards, a \(\sim\) 20 nm GeSe film is grown at a reactor temperature of 70 °C, GeCl\(_2\)-C\(_4\)H\(_8\)O\(_2\) temperature of 80 °C, and \((\text{TMS})_2\text{Se}\) at RT for morphological and compositional characterization.

At first, the effect of purge length on GPC of GeSe was investigated and is shown in Fig. 6a. It is evident that GPC increases sharply, while moving to a pulsed CVD regime (zero purge between precursor exposures). Overall, there is 75% increase in GPC, while decreasing the purge time from 5 s to 0 s. In general, purges should not influence the ALD growth regime, if they are enough in removing all the excess precursors and by-products. However, it is seen that GPC of GeSe also decreases in the ALD regime with increase in the purge length. This decrease can be assigned either to incomplete/slow precursor adsorption or involvement of some physisorption. This would imply that unreacted precursor groups sitting on the surface are pushed along with the increase in purge time resulting in a decrease of GPC.

Fig. 6b shows GPC as a function of Ge precursor temperature and reactor temperature in a pulsed CVD regime. A higher Ge
precursor temperature implicates increased delivery of Ge precursor. Increased delivery of GeCl₂ precursor brings additional Cl-reactive sites for the ligand exchange reactions with the incoming (TMS)₂Se in the next pulse, which results in higher GPC. On the other hand, reactor temperature can also be lowered to increase the GPC of GeSe.

A thick GeSe layer is grown (reactor temp. = 70°C, GeCl₂/C₄H₈O₂ = 80°C, and (TMS)₂Se = RT) to characterize the surface morphology, composition, and crystalline state of the film. For this growth, GeCl₂–dioxane and (TMS)₂Se pulse times were kept at 25 s and 0.5 s, respectively to increase the throughput of the process. Under these process conditions, we obtain a GeSe GPC of 0.34 Å per cycle. Fig. S2 (ESI†) shows thickness contour maps (ellipsometry mapping with 49 measurement points) revealing the thickness uniformity of the pulsed CVD-grown GeSe film across a 300 mm wafer. The std deviation in thickness is 1.6%, which implies excellent GeSe thickness uniformity across the wafer. Fig. 7a shows a cross-sectional TEM image of the GeSe layer grown on native SiO₂/Si. The layer is about 20 nm thick and shows some surface roughness. The inset of Fig. 7a(a1) shows a dark field (DF) TEM image of the same GeSe film. No crystal grains are observed in the film revealing its amorphous nature. The high-resolution TEM image in Fig. 7b confirms the amorphous phase of the as-grown GeSe layer.

GeSe was grown on 3D test structures to check the conformality of the film (Fig. 8a). The grown GeSe was found to be exceptionally conformal as revealed by uniform thickness in deep trenches of 3D structures. Although there is a pulsed CVD component involved in the growth, the slow reaction chemistry can lead to excellent conformality. In this case, relatively long reaction time can permit diffusion of the precursor inside the deep trenches of 3D structures allowing for a more conformal deposition. Fig. 8b shows the surface morphology of the film as measured by AFM (left Fig. 8b) and SEM (right Fig. 8b). GeSe has a smooth surface morphology as shown by AFM and SEM images. No grains are visible showing the amorphous nature of the film. AFM showed a RMS surface roughness of ~0.3 nm. Fig. 8(c) shows EDX elemental maps of Ge and Se obtained from the GeSe thin film sample deposited on Si(100). The colorized maps show strong contrast among Ge and Se and they reveal the uniform elemental distribution along the scanned area.

Fig. S3 (ESI†) shows a cross-sectional EDS line scan from the air–GeSe interface to the GeSe–Si interface. The entire film composition is stoichiometric, except its surface, which is found to be Ge rich. The surface oxygen concentration is also higher, which indicates that there is formation of GeOₓ at the surface. GeOₓ formation is probably due to long atmospheric exposure of the film before EDX measurement. An oxygen content of 4% was found inside the GeSe film. As the film was not in situ capped during atmospheric exposure, ambient oxygen can diffuse inside the GeSe film from the air–GeSe interface. Hence, it is difficult to deconvolute the oxygen
coming from atmospheric exposure and from reactor and/or deposition itself.

In situ temperature programmed XRD measurements were conducted to study the thermal phase stability characteristics. For this study, SiO$_2$ and TiN were used as substrates as they are the device-relevant substrates. The measurement results are shown in Fig. 9 and the results are summarized in Table 1.

Fig. 9(a) shows that the GeSe layer on SiO$_2$ crystallizes at temperatures $\geq 370$ °C. The measurement results before annealing, after annealing (at 410 °C), and after cooling the sample are shown in Fig. 9b. The peaks corresponding to the (111), (040), and (002) planes of orthorhombic GeSe are visible after annealing the sample and the same peaks with similar FWHM value persisted after cooling the sample to room temperature. This indicates that the material crystallizes at $\geq 370$ °C and remains with the same phase after cooling to RT. For the samples deposited on the TiN substrate, studies were done under annealing conditions the same as those on SiO$_2$ substrates. For examination of the peaks, the sample was heated to 410 °C and a full $2\theta$ scan was performed. XRD patterns before and after the annealing confirm the appearance of a new peak at 33°, (040), which is due to the orthorhombic GeSe phase (see Fig. 9d).

The TiN substrate has a face centered cubic crystalline (FCC) structure, while SiO$_2$ is an amorphous substrate. It is probable that the TiN substrate is acting as a crystalline seed layer to crystallize the GeSe towards a single 040 plane of orthorhombic GeSe. On the other hand, GeSe is crystallizing in various planes of the orthorhombic structure due to the amorphous nature of the SiO$_2$ substrate.

### Conclusion

In conclusion, the GeSe deposition process was explored at low deposition temperatures by using GeCl$_2$, C$_2$H$_4$O$_2$ and (TMS)$_2$Se/ (TES)$_2$Se as Ge and Se sources, respectively. Ex situ TXRF is found to be a useful technique to gather insights into precursor adsorption kinetics for a process developed in a manufacturing-scale reactor where it is not possible to use conventional in situ characterization techniques like a quartz crystal microbalance (QCM) and spectroscopic ellipsometry. We obtained a GeSe GPC of 0.34 Å per cycle in pulsed CVD mode at a reactor temperature of 70 °C, GeCl$_2$, C$_2$H$_4$O$_2$ temperature of 80 °C, and (TMS)$_2$Se at room temperature. Moving to an ALD regime by including purges between precursor exposures decreases the GPC strongly. However, excellent conformality is obtained in the pulsed CVD regime. Long precursor pulsing times coupled with slow reaction of precursors enabled the conformal GeSe process with reasonable uniformity. This contrasts with the ALD GST process developed in a lab-scale

| Substrate | Material | Crystallization temperature (°C) | Peaks | Phase |
|-----------|----------|----------------------------------|-------|-------|
| TiN       | GeSe     | 365                              | (040) | Orthorhombic GeSe |
| SiO$_2$   | GeSe     | 370                              | (111), (040), and (002) | Orthorhombic GeSe |

Table 1  Crystallization temperature of GeSe grown on SiO$_2$ and TiN
reactor where 1–3 seconds of precursor pulse time was enough to saturate the GST GPC. The GPC is mainly limited by GeCl$_2$ delivery and slow surface reaction kinetics. One option to increase the throughput of the process is by faster injection of the GeCl$_2$ precursor using large area precursor canisters.

Overall GeCl$_2$+(R,Si)$_3$Se/Te chemistry appears very powerful for both ALD and pulsed CVD of GeSe-and GST-like materials. This has the potential to enable 3D RRAM memory vehicles by combining conformal OTS and phase change memory materials.

**Conflicts of interest**

There are no conflicts to declare.

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