Two-Probe Setup with Improved Calibration for Online Impedance Measurement of Electrical Assets

Hui-Lin Chou

Abstract

The online impedance serves as one of the most crucial specifications to evaluate the health status and efficiency of an electrical device or system. The inductive coupling technique is a preferred approach to measure the online impedance due to the ease of implementation of the circuit which has zero physical contact to the live electrical system. The existing inductive coupling method deployed to measure the online impedance of an electrical device under test (DUT) adopts two probes in total: an injecting inductive probe (IIP) and a receiving inductive probe (RIP). An open/short/load (OSL) calibration procedure is implemented to eradicate the ramifications of the probe-to-probe coupling, however, based on the assumption that the calibration criterions (shorted, open and 50Ω) are approximated to their theoretical values in a specified frequency range. Hence, any measurement with frequency outside the specified range (i.e. larger than 1 MHz) will not be accurate due to the frequency-dependent residual inductances and capacitances of the calibration model. To overcome the aforesaid limitation, this paper introduces an improved calibration procedure which is applicable for a wider frequency range which takes the frequency-dependent characteristics into consideration. With the two-probe measurement setup (TPMS), the adopted improved calibration procedure is introduced to eradicate the ramifications of the probe-to-probe coupling with the intention to refine the accuracy of the extracted online impedance.

I. Introduction

The online impedance serves as one of the most crucial specifications to determine the health status and condition of an electrical system [1]. For example, the online impedance of the power grid has a consequential effect on the capability of the grid-connected power converters. Hence, inaccurate analysis of the online impedance can corrupt the performance and reliability of the grid-connected power converters [2-4]. In addition, the online impedance plays a significant role in the operation of an Induction Motor (IM) as early fault detection can prevent irreparable damage, reduce safety threats, minimize motor interruption, and the cost from reparation works and interruption [5, 6]. Recognizing the significance of the online impedance of an electrical system, it is essential to establish a compelling method to extract the online impedance of an electrical system. Primarily, there are three means of approach in deriving the online impedances directly from electrical systems; being the voltage-current analysis method, the capacitive coupling method, and the inductive coupling method. Among the aforementioned, the latter approach is a preferred method as it has zero physical contact to the live electrical system under test (SUT). Additionally, the clamp-on inductive probes utilised in this method can simply be secured and detached from the insulated cable that transmits power to the SUT which notably simplifies the implementation with minimal electrical risk [7].

The prevailing inductive coupling setup adopts two probes in total: an injecting inductive probe (IIP) and a receiving inductive probe (RIP), which is also known as the Two-Probe Measurement Setup (TPMS). An open/short/load (OSL) calibration procedure is implemented to eradicate any possible ramifications of the probe-to-probe coupling [7], however, based on the assumption that the calibration criterions (shorted, open and 50Ω) are approximated to their theoretical values in a specified frequency range. Hence, any measurement with frequency outside the specified range (i.e. ≥ 1 MHz) will not be accurate due to the frequency-dependent residual inductances and capacitances of the calibration model.

The motivation of this paper is to introduce and adopt a more refined and reliable measurement setup to tackle the aforementioned limitations of the existing measurement setup, thereby exploring new applications and improving the accuracy of the extracted online impedance. This paper is organized as follows: Section II reviews the literature of existing online impedance measurement methods. Section III presents the methods and principles adopted for the TPMS and discusses the existing OSL calibration procedure, improved calibration procedure and the proposed measurement configuration. Section IV discusses about the experimental results and its validation on the improved calibration procedure and proposed measurement configuration. Finally, Section V concludes this paper and proposes future works that are worthy of consideration.

Section II. Literature Review of Online Impedance Measurement

Taking into consideration the significance of the online impedance of electrical assets, the extraction of online impedance is now a notable process, thus, a few methods to extract the online impedance will be comprehensively
examined with its integrity and limitations highlighted. The extraction methods can be categorized into three types, that is the voltage-current measurement method [8-13], capacitive coupling method [14-16] and lastly, the inductive coupling method [17-22].

A. Voltage-Current Measurement Method

The voltage-current measurement method is a direct method to obtain the online impedance. The online impedance of the DUT ($Z_{DUT}$) can be approximated through the extraction of the test signal voltage across the DUT and test signal current flowing through the DUT. It should be taken into account that the test signal may either be in the form of harmonics contributed by the DUT [8] or a signal generated by an external device, for example, a current transformer or a voltage transformer [23].

![Figure 1](image1.png)

Figure 1. Fundamental setup of the voltage-current measurement method with an external signal injection device connected.

Figure 1 illustrates the fundamental setup of the voltage-current measurement method with an external signal injection device connected. The test signal will be injected into the wire connecting the power source and DUT. Thereafter, a voltage sensor and current sensor will then be used to extract the voltage across the DUT induced by the test signal and the induced current flowing through the DUT. Following, an analog-to-digital converter (ADC) is applied to process the analog voltage and current signals, followed by a digital signal processing (DSP) program to derive $Z_{DUT}$.

B. Capacitive Coupling Method

Figure 2 illustrates the fundamental setup of the capacitive coupling method to obtain the online impedance of an electrical DUT, indicated as $Z_{DUT}$. The DUT can be energized by either a DC or low-frequency alternating current (AC) power source, which is connected by a pair of inductors ($L_1$ and $L_2$) and capacitors ($C_1$ and $C_2$) to a measurement instrument, such as an impedance analyzer (IA) or a vector network analyzer (VNA) [14-16]. Capacitors $C_1$ and $C_2$ are included to prevent any form of power signal contributed by the power source from flowing into the measurement instrument meanwhile allowing high-frequency test signal generated by the measurement instrument to flow through the DUT.

![Figure 2](image2.png)

Figure 2. Fundamental setup of the capacitive coupling method.

Contrastingly, inductors $L_1$ and $L_2$ are included to serve as a blockage to prevent high-frequency test signal generated by the measurement instrument from flowing into the power source but they establish insignificant impedance characteristics for the low-frequency power signal. For DC or low-frequency AC power signal, capacitors $C_1$ and $C_2$ are treated as open switches as shown in Figure 3. Correspondingly, for high-frequency test
signal, inductors $L_1$ and $L_2$ can be considered as open switches, and the corresponding impedances of the capacitors $C_1$ and $C_2$ are labelled as $Z_{C1}$ and $Z_{C2}$ respectively as shown in Figure 4.

![Figure 3. Capacitive coupling measurement setup for DC or low-frequency AC power signal.](image)

![Figure 4. Capacitive coupling measurement setup for high-frequency test signal.](image)

As illustrated in Figure 4, the overall equivalent impedance $Z_{\text{Total}}$ can be measured straight with an IA, where $Z_{\text{Total}}$ can be expressed as

$$Z_{\text{Total}} = Z_{\text{DUT}} + Z_{C1} + Z_{C2} \quad (1)$$

$Z_{\text{DUT}}$ can then be calculated by de-embedding $(Z_{C1} + Z_{C2})$ from the overall equivalent impedance $Z_{\text{Total}}$. On the other hand, if the VNA is being chosen as the measurement instrument [16], Figure 2 can be further organized as a two-port network circuit, as illustrated in Figure 5.

![Figure 5. Equivalent two-port network circuit with respect to Figure 3.](image)

As shown in Figure 5, the two-port network $Q$ can be equated in terms of DUT, $C_1$ and $C_2$, where $Q$ can be expressed corresponding to transmission (ABCD) parameters.
\[ Q = \begin{bmatrix} A & B \\ C & D \end{bmatrix} \]  

Since \( B = Z_{\text{DUT}} + Z_{C1} + Z_{C2} \), the scattering parameters (S-parameters) of \( Q \) can then be obtained straight with the VNA followed by the conversion of the S-parameters to \( B \) where [23]

\[ B = 50 \times \frac{(1 + S_{11})(1 + S_{22}) - S_{12}S_{21}}{2S_{21}} \]  

The de-embedding of \((Z_{C1} + Z_{C2})\) from \( B \) is then executed to obtain the value of \( Z_{\text{DUT}} \).

**C. Inductive Coupling Method**

The initial findings on the inductive coupling method was published in [17]. The project was further built on and optimized to streamline the online impedance measurement procedure based on the cascaded two-port network theory [18, 19].

![Figure 6. Two-Probe Measurement Setup (TPMS) for the inductive coupling method.](image)

The inductively coupled measurement configuration to extract the online impedance of a DUT is shown in Figure 6, where the DUT is energized by \( V_S \) which can either be a DC source or a low-frequency AC power source via the wires connected to the DUT. A measurement instrument, IIP and RIP embodies the inductive coupling measurement setup, where the IIP functions as an injecting probe while the RIP functions as a receiving probe to receive the signal. Assuming that the computer-controlled signal generation and acquisition system (SGAS) is employed as the measurement instrument illustrated in Figure 2-6, a sweep-frequency test signal is generated and injected into the wire that serves as a connection between the source voltage (\( V_S \)) and DUT, via channel 0 of the SGAS through the injecting probe. The response signal is then being detected by channel 1 of the SGAS via the receiving probe. The online impedance of the DUT is labelled as \( Z_{\text{DUT}} \), whereas \( Z_S \) and \( Z_W \) denotes the impedance of the source voltage and circuitry wiring excluding the clamped sections of the wire, respectively [18]. The points where the wire are being clamped by the two inductive probes are denoted as \( p_1 \) and \( p_2 \) in Figure 6. The equivalent impedance measured by the RIP can be expressed as

\[ Z_X = Z_S + Z_W + Z_{\text{DUT}} \]  

where \( Z_X \) is the summation of the impedances contributed by the source voltage (\( Z_S \)), wiring connection excluding the clamped section (\( Z_W \)) and DUT (\( Z_{\text{DUT}} \)). Developed upon the idea of cascaded two-port networks, Figure 7 illustrates the equivalent circuit model of Figure 6, where \( Z_{\text{SGC}} \) is the corresponding impedance of the signal generation card (SGC) of the SGAS and \( Z_{\text{CHR}} \) and \( Z_{\text{CCH}} \) are the respective input impedances of channel 0 and 1 of the signal acquisition card (SAC) of the SGAS. As illustrated in Figure 7, the section of the wire where is being clamped by the IIP can be devised as a two-port ABCD matrix, denoted \( Q_I \), where one port serves as the input terminal of the IIP and the other serves the two terminals of the wire being clamped. Correspondingly, the section where the wire is being clamped by the RIP can be devised as a two-port ABCD matrix as well, denoted \( Q_R \), where one port serves as the output terminal of the RIP and the other serves the two terminals of the wire being clamped. Moreover, \( Q_X \) represents the two-port ABCD matrix for the resultant impedance of the measuring setup (\( Z_X \)). To measure the value of \( Z_X \), the SGC of the SGAS generates a sinusoidal test signal of a pre-determined amplitude (\( V_{\text{SGC}} \)) and frequency (\( f_{\text{sig}} \)) to be injected into the circuit. It is worth noting that the resultant impedance is extracted at \( f_{\text{sig}} \) ranging from a few kHz to a few MHz. Given that \( f_{\text{sig}} \) is significantly larger compared to the frequency of the source voltage (\( V_s \)), whereby DC (0 Hz) or low-frequency AC power source (50–60Hz), \( V_s \) can then be considered as AC short at \( f_{\text{sig}} \).
Considering the three dual-port networks \( Q_I, Q_R \) and \( Q_X \) are arranged in a cascaded manner, the resultant dual-port network \( Q \) can be equated as

\[
Q = Q_I \cdot Q_X \cdot Q_R
\]  
(5)

Thence, the three dual-port networks can be expressed with reference to their corresponding transmission (ABCD) parameters

\[
\begin{bmatrix} A & B \\ C & D \end{bmatrix} = \begin{bmatrix} A_I & B_I \\ C_I & D_I \end{bmatrix} \begin{bmatrix} A_X & B_X \\ C_X & D_X \end{bmatrix} \begin{bmatrix} A_R & B_R \\ C_R & D_R \end{bmatrix}
\]  
(6)

By solving \( Q_X, Z_X \) can be determined as \( B_X = Z_X \) [24]. \( Q_X \) can then be calculated following the values of \( Q, Q_I \) and \( Q_R \) being known. The value of \( Q \) can be obtained straight from the measuring instrument, in this case, the SGAS, in Figure 6. With the measured equivalent impedance \( Z_X, Z_{DUT} \) can then be calculated by de-embedding \( (Z_S + Z_W) \) from \( Z_X \). \( (Z_S + Z_W) \) can be pre-established by using a capacitor of appropriate rating to bypass the DUT which gives an AC short circuit to the test signal. In real circumstances, the magnitude of \( Z_{DUT} \) is usually much larger than the magnitude of \( (Z_S + Z_W) \).

In summary, this chapter studies and analyses three widely practiced online impedance extraction methods; that is the voltage-current measurement method, capacitive coupling method and finally, the inductive coupling method. Out of the three fore mentioned methods, the inductive coupling method surpassed the other two methods in terms of its non-intrusive nature and ease of implementation. To deduce the online impedance, the voltage sensor that constitutes the voltage-current measurement setup and the pair of capacitors that constitute the capacitive coupling method will inevitably require physical contact to the live DUT and hence, increasing the threat posed by electrical hazards. Additionally, for a live DUT powered with a high voltage level, the voltage sensor and the pair of capacitors from the voltage-current measurement method and capacitive coupling method respectively, are strained with considerable stresses (e.g. thermal), which in turn account for consistent maintenance and renewal; which may result in redundant downtime of the DUT. Despite its dominance in measuring the online impedance, the inductive coupling method still inevitably encounter some drawbacks that need to be improved on. This paper will address these drawbacks and explain in detail on how the inductive coupling method can be further developed. The drawbacks are outlined as follows: the capability to eliminate any ramifications generated by the coupling between the two inductive probes and the competency to obtain the online impedance of DUTs or electrical devices where electrical noise and power surges are considerably notable.

**Section III. Methods and Principles**

*A. Basic Setup with Two Inductive Probes*

The inductive coupling method utilizes a two-probe measurement setup (TPMS) concept. Figure 8 illustrates the basic measurement configuration to extract the online impedance of a DUT, whereby a DC power source or low-frequency AC power source is being used to energize the DUT via connecting wires.
The computer-controlled signal generation and acquisition system (SGAS), injecting inductive probe (IIP) and receiving inductive probe (RIP) embodies the measurement configuration of the inductive coupling approach to extract the online impedance of the DUT ($Z_{DUT}$). The computer-controlled SGAS comes with built-in signal generation card (SGC) and signal acquisition card (SAC) integrated together. The SGC then generates a sinusoidal test waveform of a pre-determined frequency ($f_{sig}$). The sinusoidal test signal travels from channel 0 of the SGC through the IIP via the connected wire which lies between the source voltage ($V_S$) and DUT, meanwhile the RIP processes the feedback signal at the position where the two probes are clamped at ($p_1$ and $p_2$). The voltage of the test signal at the input terminal and voltage of the feedback signal at the output terminal of the IIP is then being processed by channel 0 (CH0) and channel 1 (CH1) of the signal acquisition card (SAC) within the SGAS, respectively. The impedance contributed by the source voltage and electrical wirings excluding the sections that are being clamped by the probes of the measurement setup is denoted as $Z_S$ and $Z_W$ respectively, as shown in Figure 8 [25]. The equivalent impedance measured by the two probes can be represented by $Z_X$, where

$$Z_X = Z_S + Z_W + Z_{DUT}$$  \hspace{1cm} (7)

Figure 9 illustrates the cascaded dual-port network model of the inductive coupling method. The voltage and internal impedance at the SGC are denoted as $V_{SGC}$ and $Z_{SGC}$ respectively, while $Z_{CH0}$ and $Z_{CH1}$ represent the input impedances of CH0 and CH1 of the SAC, respectively. The equivalent voltage and current of the sinusoidal signal at the input terminal of the IIP are denoted by $V_1$ and $I_1$, respectively; $V_2$ and $I_2$ represents the voltage and current of the sinusoidal signal at the output terminal of the RIP respectively. $Q_1$ represents the two-port network of the IIP which includes the segment of the wire that has been clamped by the IIP, where $L_{Leak1}$ and $C_{pl}$ denote the
leakage inductance and parasitical capacitance at the IIP side, respectively. \( \mathbf{Q}_R \) represents the two-port network at the RIP side which includes the segment of the wire that has been clamped by the RIP, where \( L_{\text{Leak2}} \) and \( C_{p2} \) denote the leakage inductance and parasitical capacitance at the RIP side, respectively. \( \mathbf{Q}_X \) represents the dual-port network of the equivalent impedance \( (Z_X) \) measured by the two probes. In consideration of the significantly high frequency of the generated sinusoidal test signal compared to the frequency of the source voltage \( (V_S) \), \( V_S \) can be considered as an AC short. Interpreting the three fore mentioned two-port networks \( (\mathbf{Q}_I, \mathbf{Q}_R \text{ and } \mathbf{Q}_X) \) in accordance with their corresponding ABCD parameters, the correlation of the input terminal of the IIP and the output terminal of the RIP can be expressed as

\[
\begin{bmatrix} V_1 \\ I_1 \end{bmatrix} = \begin{bmatrix} A_I & B_I \\ C_I & D_I \end{bmatrix} \begin{bmatrix} A_X & B_X \\ C_X & D_X \end{bmatrix} \begin{bmatrix} A_R & B_R \\ C_R & D_R \end{bmatrix} \begin{bmatrix} V_2 \\ I_2 \end{bmatrix}
\]

(10)

Given that \( I_2 = \frac{V_2}{Z_{CH1}} \) and \( \mathbf{Q}_X \) can be equated as [24]

\[
\mathbf{Q}_X = \begin{bmatrix} 1 & Z_X \\ 0 & 1 \end{bmatrix}
\]

(11)

\( Z_X \) can be worked out by

\[
Z_X = \frac{1}{A_I(C_R+\frac{B_R}{Z_{CH1}})} \cdot V_1 - \frac{A_R+\frac{B_R}{Z_{CH1}}}{C_R+\frac{B_R}{Z_{CH1}}} - \frac{B_I}{A_I}
\]

(12)

\( Z_{CH1} \) can be retrieved from the datasheet of the SGC. Thence, \( Z_X \) can be determined through the computation of \( V_1 \) and \( V_2 \). Immediately after \( Z_X \) is determined, \( Z_{DUT} \) can be approximated by de-embedding \( (Z_S + Z_W) \) from \( Z_X \).

B. Open/Short/Load Calibration

The inductive coupling method is a preferred technique to extract the online impedance of an electrical asset due to its non-intrusive feature. However, in real circumstances where space scarcity is a factor to be concerned of, the injecting and receiving probes connected to the measurement setup will be closely spaced thus inducing probe-to-probe coupling which may produce undesirable effects on the measurement procedure and cannot be overlooked. Thus, the open/short/load (OSL) calibration procedure is implemented to eliminate the ramifications of the coupling between the two inductive probes with the intent to eliminate any error that may compromise the accuracy and precision of the measurement.

![Figure 10. Three-port network depiction of Figure 8.](image)

From Figure 10, \( Z_{SGC} \) is the equivalent impedance of the SGC; \( Z_{CH0} \) and \( Z_{CH1} \) are the corresponding input impedances of CH0 and CH1 of the SAC. \( V_1 \) and \( I_1 \) denotes the equivalent voltage and current of the sinusoidal test signal generated by the SGC at the input terminal of the IIP (Port 1), respectively. \( V_2 \) and \( I_2 \) denotes the equivalent voltage and current at the output terminal of the RIP (Port 2) while \( V_3 \) and \( I_3 \) are the voltage and current produced at the input terminal (Port 3) of the resultant impedance \( (Z_X) \) to be measured. As mentioned earlier, in consideration of the significantly high frequency of the generated sinusoidal test signal \( (f_{\text{sig}}) \) compared to the
frequency of the source voltage \( (V_s) \), \( V_c \) can be considered as an AC short. Hence, the induced coupling between the two probes, also known as the “probe-to-probe coupling” can be expressed in terms of an impedance matrix given by

\[
Z = \begin{bmatrix}
Z_{11} & Z_{12} & Z_{13} \\
Z_{21} & Z_{22} & Z_{23} \\
Z_{31} & Z_{32} & Z_{33}
\end{bmatrix}
\] (13)

The correlation of the corresponding voltages and currents of each ports can be expressed by

\[
\begin{bmatrix}
V_1 \\
V_2 \\
V_3
\end{bmatrix} = \begin{bmatrix}
Z_{11} & Z_{12} & Z_{13} \\
Z_{21} & Z_{22} & Z_{23} \\
Z_{31} & Z_{32} & Z_{33}
\end{bmatrix} \cdot \begin{bmatrix}
I_1 \\
I_2 \\
I_3
\end{bmatrix}
\] (14)

Given \( I_2 = \frac{V_2}{Z_{CH1}} \) and \( I_3 = \frac{V_3}{Z_X} \), the above equation can be re-written as

\[
\begin{bmatrix}
V_1 \\
V_2 \\
V_3
\end{bmatrix} = \begin{bmatrix}
Z_{11} & Z_{12} & Z_{13} \\
Z_{21} & Z_{22} & Z_{23} \\
Z_{31} & Z_{32} & Z_{33}
\end{bmatrix} \cdot \begin{bmatrix}
I_1/V_1 \\
I_2/V_2 \\
I_3/V_3
\end{bmatrix}
\] (15)

By dividing \( V_2 \) throughout the above equation on both sides,

\[
\begin{bmatrix}
V_1/V_2 \\
1 \\
V_3/V_2
\end{bmatrix} = \begin{bmatrix}
Z_{11} & Z_{12} & Z_{13} \\
Z_{21} & Z_{22} & Z_{23} \\
Z_{31} & Z_{32} & Z_{33}
\end{bmatrix} \cdot \begin{bmatrix}
I_1/V_2 \\
1/Z_{CH1} \\
(V_3/V_2)/Z_X
\end{bmatrix}
\] (16)

By solving the equation, \( \frac{V_1}{V_2} \), \( \frac{V_3}{V_2} \) and \( \frac{I_1}{V_1} \) can be expressed with reference to the three-port impedance parameters from matrix \( Z \), \( Z_{CH1} \) and \( Z_X \), where \( V_i/V_2 \) can be equated as

\[
\frac{V_1}{V_2} = \frac{a_1}{Z_X + a_3}
\] (17)

Where the detailed derivation is shown in the Appendices. In addition, the comprehensive expressions of \( a_1 \), \( a_2 \) and \( a_3 \) are also included in the appendices. Eventually, \( Z_X \) can be expressed in terms of \( V_i/V_2 \) as follows

\[
Z_X = \frac{a_3 \cdot \frac{V_1}{V_2} \cdot a_2}{\frac{V_3}{V_2} \cdot a_1}
\] (18)

\( Z_X \) can be obtained through \( \frac{V_1}{V_2} \) once \( a_1 \), \( a_2 \) and \( a_3 \) are made known, taking into account that \( V_1 \) and \( V_2 \) can be obtained straight from the measurement setup in Figure 8. In consideration of a SAC with a unique \( Z_{CH1} \), the values of \( a_1 \), \( a_2 \) and \( a_3 \) are dependent on the impedance parameters of the \( i \) (i.e. \( Z_0 \)) where \( i = 1, 2, 3 \). The three-port impedance parameters are influenced by the physical characteristics of the inductive probes and sinusoidal test signal frequency \( (f_0) \). In addition, the distance between the two inductive probes, denoted as \( d \) in Figure 8, also has an influence on the three-port impedance parameters as the probe-to-probe coupling fluctuates with the distance between the two inductive probes. To obtain the values of \( a_1 \), \( a_2 \) and \( a_3 \), the resultant impedance \( (Z_X) \) can be substituted with three pre-determined specifications before executing the actual measurement. The three prevailing calibration standards used by industries are open, short and 50 \( \Omega \) resistive load, in which this project also adopts the exact same calibration criterions. When Port 3 in Figure 10 is open-circuited, \( Z_X \) can be considered as \( \infty \) and this criterion is denoted as \( R_\infty \) where

\[
a_1 = \frac{V_1}{V_2} |_{R_\infty}
\] (19)

When Port 3 in Figure 10 is shorted, \( Z_X \) can be considered as 0 \( \Omega \) and this criterion is denoted as \( R_0 \) where

\[
a_2 = a_3 \cdot \frac{V_3}{V_2} |_{R_0}
\] (20)

Lastly, when Port 3 in Figure 10 is terminated with a 50 \( \Omega \) resistive load, \( Z_X \) can be considered as 50 \( \Omega \) and this criterion is denoted as \( R_{50} \), and it can be expressed as
\[ R_{50} = \frac{a_3 - \frac{v_1}{v_2}r_{50}}{\frac{v_1}{v_2}r_{50} + a_2} \]  

In the above expressions of \( a_1, a_2 \) and \( R_{50} \), \( \frac{v_1}{v_2} \) represents the voltage ratio at the input terminal of the IIP and the output terminal of the RIP, where “c” denotes the criterion selected, either \( R_\infty, R_0 \) or \( R_{50} \). In view of the fact that \( \frac{v_1}{v_2} \) can be determined via the measurement setup, \( a_2 \) and \( a_3 \) can then be obtained through the following expressions, respectively,

\[ a_2 = R_{50} \cdot \frac{v_1}{v_2}r_0 \cdot \frac{(\frac{v_1}{v_2}r_\infty - \frac{v_1}{v_2}r_{50})}{(\frac{v_1}{v_2}r_{50} - \frac{v_1}{v_2}r_0)} \]  

\[ a_3 = R_{50} \cdot \frac{v_1}{v_2}r_0 \cdot \frac{(\frac{v_1}{v_2}r_\infty - \frac{v_1}{v_2}r_{50})}{(\frac{v_1}{v_2}r_{50} - \frac{v_1}{v_2}r_0)} \]  

Thence, \( Z_X \) can be derived through the expression as follows

\[ Z_X = R_{50} \cdot \frac{v_1}{v_2} \frac{v_1}{v_2}r_0 \frac{v_1}{v_2}r_\infty \frac{v_1}{v_2}r_{50} \]  

However, it should be taken into consideration that the prerequisite of deriving \( Z_X \) from the above expression requires \( V_1|_c \) and \( V_2|_c \) to be determinable. Considering \( V_1|_c \) is the signal at the input terminal of the IIP, the signal-to-noise-ratio (SNR) should be satisfactory given that the signal should exceed the measurement system’s noise floor. Under the criterion of \( Z_X = \infty \), if the probe-to-probe coupling is strong, \( V_1|_c \) is still determinable, thus \( Z_X \) can be measured. If the coupling between the two probes is poor, \( V_2|_c \), will fall below the measurement system’s noise floor which is too insignificant to influence the measurement and thus negligible. Hence, the influence of the coupling between the two inductive probes on the precision of the measured online impedance \( Z_X \) is insignificant thus negligible, and \( Z_X \) can be accurately derived through

\[ Z_X = \frac{1}{A_1(C_R + \frac{b_R}{r_{CH1}})} \cdot \frac{v_1}{v_2} - \frac{A_R + \frac{b_R}{Z_{CH1}}}{C_R + \frac{b_R}{Z_{CH1}}} - \frac{b}{A_1} \]  

To simplify the above expression, it can be revised as

\[ Z_X = k \cdot \frac{v_1}{v_2} + b \]  

Where

\[ k = \frac{1}{A_1(C_R + \frac{b_R}{Z_{CH1}})} \]  

\[ b = -\frac{A_R + \frac{b_R}{Z_{CH1}}}{C_R + \frac{b_R}{Z_{CH1}}} - \frac{b}{A_1} \]  

The introduced calibration procedure can also be employed to obtain the values of \( k \) and \( b \). When \( Z_X \) is terminated with a short-circuit, where \( Z_X = 0 \), \( Z_X \) can be expressed as

\[ 0 = k \cdot \frac{v_1}{v_2} + b \]  

Likewise, when \( Z_X \) is terminated with a 50 \( \Omega \) resistive load, where \( Z_X = R_{50} \), \( Z_X \) can be expressed as

\[ R_{50} = k \cdot \frac{v_1}{v_2} + b \]  

By solving the two equations above, \( k \) and \( b \) can be determined by

\[ k = \frac{R_{50}}{(\frac{v_1}{v_2}r_{50} - \frac{v_1}{v_2}r_0)} \]
Using Cramer’s rule to solve the above matrix, a new calibration procedure can be obtained. For instance, the corresponding frequency dependent impedances (shorted, open and 50Ω resistive load) are approximated to their theoretical values in a specified frequency range. Hence, any measurement with frequency outside the specified range (i.e. ≥ 1 MHz) will not be accurate due to the frequency-dependent residual inductances and capacitances of the calibration model.

C. Proposed Improved Calibration Procedure

To overcome the aforesaid drawback, an improved calibration procedure will be proposed and comprehensively discussed in this section. By executing the improved calibration procedure, the coupling between the two inductive probes can be carefully assessed and have its ramifications be eliminated. As mentioned in the above section, the three calibration criterions adopted by the OSL calibration model are required to successfully devise a shorted, open and 50Ω resistive load termination at the input terminal (Port 3) of Z. As seen in Figure 10. Likewise, to work out the values of a1, a2 and a3 of a designated TPMS with unique ZCH1 and Z, the proposed improved calibration procedure also requires three distinguishable calibration criterions, denoted by CC1, CC2 and CC3. In consequence of the frequency-dependent characteristics of CC1, CC2 and CC3, their respective frequency-dependent impedances (ZCC1, ZCC2 and ZCC3, where ZCC1 ≠ ZCC2 ≠ ZCC3) can be obtained by utilizing an impedance analyzer prior the calibration procedure. With reference to

\[
Z_X = \frac{a_3 \cdot V_1 - a_2}{V_2 + a_1}
\]  

When the input terminal (Port 3) of Z in Figure 10 is being terminated with CC1, CC2 and CC3 respectively, the corresponding frequency-dependent impedances ZCC1, ZCC2 and ZCC3 can be expressed by

\[
Z_{CC1} = \frac{a_3 \cdot \left(\frac{V_1}{V_2}\right)_{|CC1} - a_2}{\frac{V_1}{V_2} + a_1}
\]  

\[
Z_{CC2} = \frac{a_3 \cdot \left(\frac{V_1}{V_2}\right)_{|CC2} - a_2}{\frac{V_1}{V_2} + a_1}
\]  

\[
Z_{CC3} = \frac{a_3 \cdot \left(\frac{V_1}{V_2}\right)_{|CC3} - a_2}{\frac{V_1}{V_2} + a_1}
\]

where \(\frac{V_i}{V_2} (i = 1, 2 or 3)\) can be established straight utilizing the TPMS. Reordering the above equations, they can be expressed as

\[
Z_{CC1} \cdot a_1 + a_2 - \left(\frac{V_1}{V_2}\right)_{|CC1} \cdot a_3 = \left(\frac{V_1}{V_2}\right)_{|CC1} \cdot Z_{CC1}
\]

\[
Z_{CC2} \cdot a_1 + a_2 - \left(\frac{V_1}{V_2}\right)_{|CC2} \cdot a_3 = \left(\frac{V_1}{V_2}\right)_{|CC2} \cdot Z_{CC2}
\]

\[
Z_{CC3} \cdot a_1 + a_2 - \left(\frac{V_1}{V_2}\right)_{|CC3} \cdot a_3 = \left(\frac{V_1}{V_2}\right)_{|CC3} \cdot Z_{CC3}
\]

and in their corresponding matrix form, are given by

\[
\begin{bmatrix}
Z_{CC1} & 1 - \left(\frac{V_1}{V_2}\right)_{|CC1} \\
Z_{CC2} & 1 - \left(\frac{V_1}{V_2}\right)_{|CC2} \\
Z_{CC3} & 1 - \left(\frac{V_1}{V_2}\right)_{|CC3}
\end{bmatrix} \begin{bmatrix}
a_1 \\
a_2 \\
a_3
\end{bmatrix} = \begin{bmatrix}
\left(\frac{V_1}{V_2}\right)_{|CC1} \cdot Z_{CC1} \\
\left(\frac{V_1}{V_2}\right)_{|CC2} \cdot Z_{CC2} \\
\left(\frac{V_1}{V_2}\right)_{|CC3} \cdot Z_{CC3}
\end{bmatrix}
\]

Using Cramer’s rule to solve the above matrix, a1, a2 and a3 can be expressed by
where \( \det \) stands for the determinant of the matrix, and

\[
\gamma = \det \begin{bmatrix}
Z_{cc1} & 1 & -\frac{v_1}{v_2} & l_{cc1} \\
Z_{cc2} & 1 & -\frac{v_1}{v_2} & l_{cc2} \\
Z_{cc3} & 1 & -\frac{v_1}{v_2} & l_{cc3}
\end{bmatrix}
\]

(45)

Hence, the final expressions for \( a_1 \), \( a_2 \) and \( a_3 \) respectively are

\[
a_1 = \left[ \left( \frac{v_1}{v_2} \right)|_{cc1} \left( \frac{v_1}{v_2} \right)|_{cc2} \right] \left( Z_{cc1} - Z_{cc2} \right) + \left( \frac{v_1}{v_2} \right)|_{cc1} \left( \frac{v_1}{v_2} \right)|_{cc3} \left( Z_{cc1} - Z_{cc3} \right)
\]

(46)

\[
a_2 = \left[ \left( \frac{v_1}{v_2} \right)|_{cc1} \left( \frac{v_1}{v_2} \right)|_{cc2} \right] \left( Z_{cc1} - Z_{cc2} \right) + \left( \frac{v_1}{v_2} \right)|_{cc1} \left( \frac{v_1}{v_2} \right)|_{cc3} \left( Z_{cc2} - Z_{cc3} \right)
\]

(47)

\[
a_3 = \left[ \left( \frac{v_1}{v_2} \right)|_{cc1} \left( \frac{v_1}{v_2} \right)|_{cc2} \right] \left( Z_{cc1} - Z_{cc2} \right) + \left( \frac{v_1}{v_2} \right)|_{cc2} \left( \frac{v_1}{v_2} \right)|_{cc3} \left( Z_{cc2} - Z_{cc3} \right)
\]

(48)

Where,

\[
\Lambda = \left( \frac{v_1}{v_2} \right)|_{cc1} \left( Z_{cc3} - Z_{cc2} \right) + \left( \frac{v_1}{v_2} \right)|_{cc2} \left( Z_{cc1} - Z_{cc3} \right) + \left( \frac{v_1}{v_2} \right)|_{cc3} \left( Z_{cc2} - Z_{cc1} \right)
\]

(49)

After deriving the values of \( a_1, a_2 \) and \( a_3 \), \( Z_{cc} \) can be found with the measured values of \( V_1 \) and \( V_2 \) with the use of the TPMS. Finally, the online impedance of the DUT can be obtained by extracting \( (Z_{s} + Z_{w}) \) from \( Z_{s} \).

Section IV. Experimental Results and Validation

Figure 11 shows the actual implementation of the inductively coupled two-probe measurement setup with improved calibration configuration. As the effectiveness of the calibration procedure holds a great influence on the precision and accuracy of the measured impedance, thus, a 0.1\( \Omega \), 50\( \Omega \) and a 1k\( \Omega \) resistive load are employed in the calibration procedure as calibration standards as shown in Figure 12. The computer-controlled signal generation and acquisition system (SGAS) is a hardware which utilises the National Instruments (NI) PXI interface. A signal generation card (SGC), dual-channel signal acquisition card (SAC) and an embedded controller for the PXI system embodies the SGAS as shown in Figure 13. Two clamp-on inductive probes are employed as the injecting inductive probe (IIP) and the receiving inductive probe (RIP), respectively. It is worth bearing in mind that it is necessary to consider the maximum current flowing through the device under test (DUT) when choosing the inductive probes, so as to prevent the probes from reaching its core saturation. The information of the instruments employed in the calibration configuration are listed in Table 1.
Figure 1. Impedance Measurement setup and calibration configuration for substantiation.

Figure 11. Impedance Measurement setup and calibration configuration for substantiation.

Figure 12. 0.1Ω, 50Ω and a 1kΩ resistive load employed in the pre-measurement calibration procedure as shown in Figure 11.

Figure 12. 0.1Ω, 50Ω and a 1kΩ resistive load employed in the pre-measurement calibration procedure as shown in Figure 11.

Figure 13. A close-up on the computer-controlled SGAS.

Table 1. Instruments that embodies the calibration configuration.
On the basis of the measurement setup, a total of three DUTs namely two through-hole resistors (15Ω, 110Ω) and one film capacitor (1nF) are used to run a number of tests. The impedance of each DUT is measured at 19 different frequencies, from 100 kHz to 10 MHz. Through the two-probe measurement setup (TPMS), followed by the proposed improved calibration procedure and the prevailing OSL calibration procedure, the online impedances of each DUT are extracted and compared against the reference values obtained from the impedance analyzer. The calibration criterions for the prevailing OSL calibration are as follows: a 0.1m copper wire left opened, 0.1Ω resistive load to mimic a short-circuit and a resistive load of 50Ω. Figures 14 to 19 shows the comparison of the obtained online impedances ($Z_X$) on the basis of the proposed improved calibration procedure and the prevailing OSL calibration procedure.

| Instrument | Descriptions               |
|------------|-----------------------------|
| SGAS       | NI PXIe-1082                |
| SGC        | NI PXI-5412                 |
| SAC        | NI PXI-5922                 |
| HIP        | Solar 9144-1N               |
| RIP        | Solar 9134-1                |
| Embedded Controller | PXIe-8840                   |

![Figure 14](image1.png)

Figure 14. Extracted Online Impedance (Magnitude) of DUT 1: 15Ω using the proposed calibration procedure compared with the existing OSL calibration procedure.

![Figure 15](image2.png)

Figure 15. Extracted Online Impedance (Phase) of DUT 1: 15Ω using the proposed calibration procedure compared with the existing OSL calibration procedure.
Figure 16. Extracted Online Impedance (Magnitude) of DUT 2: 110Ω using the proposed calibration procedure compared with the existing OSL calibration procedure.

Figure 17. Extracted Online Impedance (Phase) of DUT 2: 110Ω using the proposed calibration procedure compared with the existing OSL calibration procedure.

Figure 18. Extracted Online Impedance (Magnitude) of DUT 3: 1nF using the proposed calibration procedure compared with the existing OSL calibration procedure.
Figure 19. Extracted Online Impedance (Phase) of DUT 3: 1nF using the proposed calibration procedure compared with the existing OSL calibration procedure.

As seen in Figure 13 to Figure 19, the deviations through the medium of the proposed improved calibration and the prevailing OSL calibration procedure are rather insignificant for all DUTs when the range of the frequency is below 1MHz. This is for the reason that the influence the residual capacitances and inductances hold are insignificant for frequency below 1MHz, thus it can be neglected in low frequency levels. Conversely, the deviations of the online impedance measured using the prevailing OSL calibration procedure starts to increase along with the increasing frequency of the injecting test signal ($f_{\text{sig}}$). When the frequency of $f_{\text{sig}}$ is comparably large, for example, above 1MHz, the OSL calibration criterions diverge from the corresponding optimal values, which are $\infty$, 0 and 50 $\Omega$ $\angle 0^\circ$, which thus refuses the assumption in consequence. In essence, it is validated that the prevailing OSL calibration will result in considerable inaccuracy in the extracted values of the online impedances in high frequencies systems. On the other hand, if the frequency-dependent characteristics of the calibration criterions are factored in and developed on the proposed improved calibration procedure, the accuracy and precision of the extracted online impedances enhanced notably.

Section V. Conclusion

In view of the significance and influence the online impedance has on many vital electrical systems, an extensive literature review on the prevailing online impedance extraction methods has been carefully explored. Taking into account the constraints and drawbacks of the existing methods, this paper has introduced an improved and optimized calibration procedure for the inductive coupling impedance measurement setup. First off, by utilizing a computer-controlled signal generation and acquisition system (SGAS) and two clamp-on inductive probes, namely the injecting inductive probe (IIP) and the receiving inductive probe (RIP) that embodies the fundamental structure of the measurement setup, this improved calibration measurement setup is capable of extracting the online impedance of critical electrical assets more precisely and accurately.

In practical situations, there may be space constraints which thus inevitably reduce the distance between the two probes, which may induce a coupling effect which may affect the integrity of the measured impedance to a certain extent. On the basis of the three-port network theory, an extensive circuit model of the proposed improved calibration measurement setup was introduced in Section III, where the influence of the probe-to-probe coupling between the two fore mentioned inductive probes should be taken into consideration. The improved calibration procedure is proposed to eliminate any ramifications of the probe-to-probe coupling, which further increase the reliability and accuracy of the impedance measurement procedure. Hence, with the proposed improved calibration, the online impedance measurement procedure will still be able to uphold its accuracy and integrity despite the influence of the probe-to-probe coupling. Additionally, by integrating surge protection modules into the inductive coupling measurement configuration, the system is less prone to unpredicted power surges, and thus minimizing the risk of irreparable damages. Thence, it broadens the purview of implementation of this method to electrical systems with notable power surges.
References

[1] Z. Zhao, "Measurement setup consideration and implementation for inductively coupled online impedance extraction," Ph.D. Thesis, School of Electrical and Electronic Engineering, Nanyang Technological University, Singapore, 2021, doi: 10.32657/10356/146738.

[2] S. Cobreces, E. J. Bueno, D. Pizarro, F. J. Rodriguez, and F. Huerta, "Grid impedance monitoring system for distributed power generation electronic interfaces," *IEEE Trans. Instrum. Meas.*, vol. 58, no. 9, pp. 3112-3121, Sep. 2009, doi: 10.1109/TIM.2009.2016883.

[3] Y. He, S.-H. Chung, C.-T. Lai, X. Zhang, and W. Wu, "Active cancelation of equivalent grid impedance for improving stability and injected power quality of grid-connected inverter under variable grid condition," *IEEE Trans. Power Electron.*, vol. 33, no. 11, pp. 9387-9398, Nov. 2018, doi: 10.1109/TPEL.2018.2793459.

[4] L. Asiminoaei, R. Teodorescu, F. Blaabjerg, and U. Borup, "A new method of on-line grid impedance estimation for PV inverter," in *Proc. IEEE Appl. Power Electron. Conf. Expo. (APEC)*, Anaheim, CA, USA, 2004, pp. 1527-1533, doi: 10.1109/APEC.2004.1296067.

[5] Z. Zhao et al., "Online insulation fault detection of stator winding of induction motor based on a non-intrusive impedance extraction technique," in *Proc. IEEE Int. Conf. Intelligent Rail Transportation (ICIRT)*, Singapore, 2018, pp. 1-5, doi: 10.1109/ICIRT.2018.8641627.

[6] Z. Zhao, F. Fan, W. Wang, Y. Liu, and K. Y. See, "Detection of stator interturn short-circuit faults in inverter-fed induction motors by online common-mode impedance monitoring," *IEEE Trans. Instrum. Meas.*, vol. 70, 2021, Art no. 1000908, doi: 10.1109/TIM.2020.3013688.

[7] Z. Zhao et al., "Measurement setup consideration and implementation for inductively coupled online impedance extraction," Ph.D. Thesis, School of Electrical and Electronic Engineering, Nanyang Technological University, Singapore, 2021, doi: 10.32657/10356/146738.

[8] S. Cobreces, E. J. Bueno, D. Pizarro, F. J. Rodriguez, and F. Huerta, "Grid impedance monitoring system for distributed power generation electronic interfaces," *IEEE Trans. Instrum. Meas.*, vol. 58, no. 9, pp. 3112-3121, Sep. 2009, doi: 10.1109/TIM.2009.2016883.

[9] Y. He, S.-H. Chung, C.-T. Lai, X. Zhang, and W. Wu, "Active cancelation of equivalent grid impedance for improving stability and injected power quality of grid-connected inverter under variable grid condition," *IEEE Trans. Power Electron.*, vol. 33, no. 11, pp. 9387-9398, Nov. 2018, doi: 10.1109/TPEL.2018.2793459.

[10] Z. Zhao et al., "Online insulation fault detection of stator winding of induction motor based on a non-intrusive impedance extraction technique," in *Proc. IEEE Int. Conf. Intelligent Rail Transportation (ICIRT)*, Singapore, 2018, pp. 1-5, doi: 10.1109/ICIRT.2018.8641627.

[11] Z. Zhao, F. Fan, W. Wang, Y. Liu, and K. Y. See, "Detection of stator interturn short-circuit faults in inverter-fed induction motors by online common-mode impedance monitoring," *IEEE Trans. Instrum. Meas.*, vol. 70, 2021, Art no. 1000908, doi: 10.1109/TIM.2020.3013688.

[12] Z. Zhao et al., "Online insulation fault detection of stator winding of induction motor based on a non-intrusive impedance extraction technique," in *Proc. IEEE Int. Conf. Intelligent Rail Transportation (ICIRT)*, Singapore, 2018, pp. 1-5, doi: 10.1109/ICIRT.2018.8641627.

[13] Z. Zhao et al., "Online insulation fault detection of stator winding of induction motor based on a non-intrusive impedance extraction technique," Ph.D. Thesis, School of Electrical and Electronic Engineering, Nanyang Technological University, Singapore, 2021, doi: 10.32657/10356/146738.

[14] S. Cobreces, E. J. Bueno, D. Pizarro, F. J. Rodriguez, and F. Huerta, "Grid impedance monitoring system for distributed power generation electronic interfaces," *IEEE Trans. Instrum. Meas.*, vol. 58, no. 9, pp. 3112-3121, Sep. 2009, doi: 10.1109/TIM.2009.2016883.

[15] Y. He, S.-H. Chung, C.-T. Lai, X. Zhang, and W. Wu, "Active cancelation of equivalent grid impedance for improving stability and injected power quality of grid-connected inverter under variable grid condition," *IEEE Trans. Power Electron.*, vol. 33, no. 11, pp. 9387-9398, Nov. 2018, doi: 10.1109/TPEL.2018.2793459.
[17] R. A. Southwick and W. C. Dolle, "Line impedance measuring instrumentation utilizing current probe coupling," IEEE Trans. Electromagn. Compat., vol. EMC-13, no. 4, pp. 31-36, Nov. 1971, doi: 10.1109/TEM.1971.303150.

[18] K. Li, K. Y. See, and X. Li, "Inductive coupled in-circuit impedance monitoring of electrical system using two-port ABCD network approach," IEEE Trans. Instrum. Meas., vol. 64, no. 9, pp. 2489-2495, Sep. 2015, doi: 10.1109/TIM.2015.2403091.

[19] S. B. Rathnayaka, K. Y. See, and K. Li, "On-line impedance monitoring of transformer based on inductive coupling approach," IEEE Trans. Dielectr. Electr. Insul., vol. 24, no. 2, pp. 1273-1279, Apr. 2017, doi: 10.1109/TDEI.2017.006111.

[20] Z. Zhao, K.-Y. See, E.-K. Chua, A. S. Narayanan, A. Weerasinghe, and W. Chen, "Extraction of voltage-dependent capacitances of SiC device through inductive coupling method," in Joint Proc. IEEE Int. Symp. Electromagn. Compat. and Proc. Asia-Pacific Int. Symp. Electromagn. Compat. (Joint EMC & APEMC), Singapore, 2018, pp. 1301-1304, doi: 10.1109/ISEM.2018.8393999.

[21] A. S. Narayanan et al., "Study on the parameters affecting the impedance extraction accuracy by inductive coupling method," in Proc. IEEE Int. Conf. Intelligent Rail Transportation (ICIRT), Singapore, 2018, pp. 1-5, doi: 10.1109/ICIRT.2018.8641624.

[22] Z. Zhao et al., "Voltage-dependent capacitance extraction of SiC power MOSFETs using inductively coupled in-circuit impedance measurement technique," IEEE Trans. Electromagn. Compat., vol. 61, no. 4, pp. 1322-1328, Aug. 2019, doi: 10.1109/TEM.2019.2914704.

[23] C. J. Kikkert, "An on-line plc frequency impedance analyzer," in Proc. IEEE Int. Conf. Smart Grid Comm., Vancouver, BC, Canada, 2013, pp. 606-611, doi: 10.1109/SmartGridComm.2013.6688025.

[24] D. M. Pozar, Microwave engineering. Hoboken, NJ, USA: Wiley, 2012.

[25] Z. Zhao, K. Y. See, E. K. Chua, A. S. Narayanan, W. Chen, and A. Weerasinghe, "Time-variant in-circuit impedance monitoring based on the inductive coupling method," IEEE Trans. Instrum. Meas., vol. 68, no. 1, pp. 169-176, Jan. 2019, doi: 10.1109/TIM.2018.2838198.