Microhotplates for Low Power, and Ultra Dense Gaseous Sensor Arrays Using Recessed Silica Aerogel for Heat Insulation

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Abstract—In the operation of air pitted gaseous sensor the microhotplate (µHP) consumes almost all the power used by the sensor. The required area to micromachine the air pit for the µHP of a single sensor is several times more than the actual area required by the sensor itself. The feasibility of implementing low power and ultra dense gaseous sensor array is investigated by developing a new µHP structure using recessed silica aerogel. In comparison with the conventional µHP structure, the recessed aerogel not only has decreased the utilized area of the chip almost ten folds (181 × 181 µm$^2$ vs. 573 × 573 µm$^2$) to maintain a temperature of 360°C but also has decreased the power consumed by each µHP more than two folds (1 mW vs. 2.1 mW). As the number of sensors increases in a sensor array, the saved area of the chip increases quadratic by using the new structure. Moreover, the power consumed by the new designed structure reduces drastically.

Index Terms—Micromachining, low energy, gaseous sensor arrays, metal oxides, microhotplate, silica aerogel, recessed aerogel, step coverage.

I. INTRODUCTION

Silica aerogel has gained attention both in research and industrial communities due to its unique properties of ultra-low thermal conductivity, high thermal stability, high specific surface area, etc. [1], [2]. Recently there have been several studies on aerogel applications in the various fields including but not limited to capacitive deionization of water [3]aero capacitor due to high surface area of aerogel [4], [5], and sound absorption in ultrasonic devices [6], [7]. More recently, aerogels have been successfully synthesized in different forms of microspheres [8], thin films [9], and flexible sheets [10], [11] among which thin films have received more interest in applications such as ideal dielectrics for ultrafast integrated circuits [12] and heat insulator in gaseous sensors [13] due to the low thermal conductivity of aerogel. Combining it with microelectromechanical systems (MEMS) expands the applications of aerogel even more in devices requiring a steady high temperature.

The Metal Oxide (MOX) gas sensors should operate at temperature range of 200°C to 500°C [13] for maximum sensitivity. The sensing material is deposited on a plate called microhotplate (µHP). The high operating temperature of metal oxide (MOX) gas sensor demands an efficient design to consume low power in order to raise the temperature of µHP to desired level as quickly as possible. Various designs and materials have been considered for microhotplate fabrication [14]–[16]. Specifically, a micro hotplate fabricated with an active surface area of 50 × 60 µm$^2$ was reported to consume 30 mW power when operating at 350°C [16]. In another article, a circular active surface area of 80 µm$^2$ in diameter was developed to operate at higher temperature of 400°C consuming some 8.9 mW only [14], [15]. The latter achieves significant improvement in power efficiency by increasing its pit height to 400 µm based on front side bulk micromachining [15]. However, this power efficiency is at a significant expense of chip area taken by each sensor. By simple calculation one can quickly realize that minimum chip area needed to micromachine such a deep pit would be in order of 566 × 566 µm$^2$, which is 64 times the size of microhotplate itself. As a result, microsensor arrays obtained using such microhotplate design will undoubtedly suffer from a very low sensor density. Briand et al. [17] have reported to make microhotplate gas sensor on a polyimide layer (as insulating layer) to avoid tedious front micromachining involved in processing air pit microhotplate gas sensors. However, they end up with a hotplate size of 1.5 mm wide and power consumption of 66 mW to reach the temperature of 325°C. Therefore, we are in urgent need of area efficient gaseous sensor design with low power consumption to present a cost-effective manufacturing of sensor arrays on the wafer.

To overcome the limitation of low density sensor arrays and high power consumption to reach high temperature on microhotplate, we proposed a novel approach of using aerogel as heat insulator rather than using air. The conventional method to insulate the microhotplate from the silicon substrate is to micromachine the Si substrate to form an air pit. Wet micromachining is a post processing step, where over etching may occur while masking the sensor and its circuitry due to improper etching time control or pinholes in the masking mate-
rial. This can reduce the yield and compromise the mechanical stability of the $\mu$HP. During the fabrication process of $\mu$HP, Laconte et al. [18] reported broken membranes due to backside micromachining with Tetra Methyl Ammonium Hydroxide (TMAH) etchant, which also damaged aluminum interconnections when masking layers failed. Furthermore, many of those surviving membranes were broken during subsequent deposition of the sensing layer material, photolithography processes, and selective wet etching. Dicing is another cause of the yield loss, since conventional dicing utilizes high water pressure to remove debris from the chip surface [19].

We have previously reported [13] that using relatively thick (40-100 $\mu m$) aerogel material instead of air as heat insulator yields the following advantages: (1) ultra low power consumption, (2) area-efficient design to support high sensor density, (3) excellent temperature uniformity across the microhotplate surface, (4) low manufacturing costs (due to high yields), (5) high mechanical stability, and (6) fast fabrication. However, processing a thick layer of aerogel of 5 $\mu m$ or more is extremely difficult using multilayer processing since the spin coating of aerogel is limited to 0.6 to 3 $\mu m$ per layer [20]–[22].

In view of the fact that a relatively thick layer of aerogel is required for ultra low power MOX sensors, the step coverage problem for the metal interconnection lines between the sensor array and the CMOS chip circuitry will pose significant yield problem. However, we have demonstrated in this paper that the recessing of the thick aerogel in a selected area of the chip not only resolves the step coverage problem but also avoids tedious and difficult multilayer processing of thick aerogel film. Furthermore, the recessed aerogel processing will not adversely lower the yield caused by post processing of the sensor array.

II. AIR PIT DESIGN AND CHIP AREA CONSIDERATIONS

The whole structure of the air pit is made of 3 layers as depicted in Fig.1(a). At the bottom there is a $p$-type silicon substrate in which an air pit is created to provide thermal insulation. The first layer is a dense 2 $\mu m$ thick thermally grown SiO$_2$ or Si$_3$N$_4$ serving as micromachining mask shaped into four suspended bridges for mechanical support of the sensor shown in Fig.1(a). But in case of aerogel, this layer is a complete layer of SiO$_2$ on top of the aerogel and the thickness can be as low as 0.2 $\mu m$ as illustrated in Fig.1(a). The second layer is the NiCr (Ni80/Cr20) on top of SiO$_2$ which is also 0.2 $\mu m$. This layer can be highly doped polysilicon for CMOS process compatibility. Finally, the third layer is an SiO$_2$ layer of 0.6 $\mu m$ thickness to provide electrical insulation of heater from the micro hotplate. It also yields better temperature uniformity across the microhotplate since SiO$_2$ is relatively a good heat conductive material. As reported in our previous study for the non-recessed spin coated thin aerogel, micromachining of the silicon is completely eliminated. Although the thicker aerogel reduces the power drastically, high step coverage decreases the yield severely. In this work we have investigated the recessed aerogel with micromachining a large area of silicon for sensor arrays prior to the fabrication of the sensor. Then we fill the anisotropically etched cavity with aerogel.

![Fig. 1. Design Schematic of (a) single $\mu$HP on airpit and (b) array of $\mu$HP on recessed aerogel.](image)

Anisotropic etching is used to form the cavity underneath the hotplate of the gas sensor. The etching is called anisotropic since the etching rate is high in the (100) direction and low in the (111) direction. Etch rate in the two direction can be different as 300 to 1 [23]. In the silicon crystal lattice structure, the (111) planes are oriented at 54.7° relative to the (100) plane (Fig.1(b)). A square mask opening on the surface of the wafer will yield an etched feature in the shape of inverted pyramid at the depth determined by the intersection of (111) plane. To suspend the microhotplate in the air a mask is made with four trapezoids (with dimensions: $a_1$ = short base, $b_1$ = long base, and $h_T$ = altitude) placed close together from their short bases $a_1$ to form an area of square of $a \times a$ where $a = a_1 + 2\Delta w$ for the hotplate (Fig.1(a)). The area considered for the hotplate is 181 $\mu m^2$ ($a = 181$). The four long bases of the trapezoids $b_1$ will form the mask opening area of $b \times b$ where $b = b_1 + 2\Delta w$. The four straps that hold the micro hotplate suspended in the air after micromachining have the width $w = \sqrt{2}\Delta w$ and the length of straps $l = \frac{\sqrt{2}(b-a)}{2}$.

The thermo-electrical analysis of our designed structures is performed by utilizing a MEMS simulation software, IntelliSuite. The software is equipped with the following modules: IntelliMask to design the mask; 3D Builder to create the meshed solid blocks and differentiate their entities on different layers; TEM (ThermoElectroMechanical) to assign the properties of each entity, load the initial conditions, and simulate the temperature gain by applying voltage to one end of the heater, keeping the other end at zero potential; the IntelliEtch will figure out the final shape of micromachined structure by using an etchant like KOH buffer. The temperature at the bottom of the silicon substrate is set to room temperature of 27°C to resemble the reference temperature. Input power can then be calculated by knowing the applied voltage and reading the

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\begin{align*}
\text{Input Power} & = \frac{V^2}{R} \\
\text{where} & = \text{Voltage} \\
\text{and} & = \text{Resistance}
\end{align*}
\]
current density passed through the heater.

III. RESULTS AND DISCUSSION

To etch out the silicon from underneath of an $a \times a$ hotplate $h$ deep, the required square size opening is:

$$b = a + \frac{2h}{\tan(54.7^\circ)}.$$  \hspace{1cm} (1)

The etchant will etch the silicon through the opening area of the trapezoid to make the air pit as shown in Fig.1(a). On the other hand, in order to make the pit filled with aerogel there is no need of a trapezoid mask, but a simple square mask would create the pit as illustrated in Fig.1(b). According to (1) the area of the mask opening or the total area used increases as a square function of the height of the pit. Hence, one can calculate the percentage of saved area as:

$$\frac{b^2 - a^2}{a^2} \times 100,$$  \hspace{1cm} (2)

which implies that the micromachined air pit for each individual sensor in a sensor array uses much more area of the chip than using aerogel on the wafer for the same array. Fig.2 demonstrates the percentage of the saved area for different depths of the micromachined pit. For instance, by having a depth of only 160 $\mu$m pit filled with aerogel, we can significantly save four times less area than that of air pit. In another word, for every sensor processed with air pit we can have 5 sensors using aerogel. As the height of the aerogel increases more space would be saved by a parabolic factor. Hence, a denser sensor array can be fabricated quite easily by using aerogel as compared to micromachined air pit.

![Fig. 3. 3D image of airpit simulated by IntelliEtch.](image)

As shown in Fig.4, to maintain 360 $^\circ$C the power consumption by $\mu$HP array reduces exponentially as the height of the recessed aerogel increases. For the height equal and greater than 160 $\mu$m the power consumed by sensor array will reach to a minimum value of 2.0 mW.

![Fig. 4. Power consumption to maintain 360 $^\circ$C.](image)

In Fig.5 temperature versus the consumed power per $\mu$HP is plotted for an air pitted, recessed aerogel, and $3 \times 3$ array
(573 × 573 μm²) of μHP on recessed aerogel. The height of the pit is 277 μm. The power consumed by a μHP of an array made on the recessed aerogel gave the best result. In Fig.2 for applied power of 2.4 mW per heater, the superior heat insulation of recessed aerogel is observed. However, for the air pitted μHP that the height is not sufficient to have a large b for a given a according to (1), the temperature would stay low because of the heat sink path through the unetched column. The two structures depicted in Fig.6 demonstrate the airpit with single heater and recessed aerogel with array of heaters.

Fig. 6. 3D view of (a) air pit with single heater (b) recessed aerogel with array of heaters (3 × 3).

IV. CONCLUSION

We have reported a remarkably efficient μHP for ultra dense gaseous sensor array that utilizes recessed aerogel in a single micromachined pit for the entire array. The recessed aerogel not only improves the efficiency of the μHP but also eliminates the problem of step coverage that can severely reduce the yield of IC-microsensors array chips in manufacturing. Moreover, by recessing aerogel the problem of multilayer processing of aerogel to obtain a thick layer is removed. Also, the recessed aerogel has the advantage of micromachining the desired cavity of any size and height prior to fabrication of the sensor arrays. In addition to the aforementioned advantages of the new structure, the recessed aerogel is shown to have extremely low power consumption as low as 3 μW/μm² for μHP to maintain the temperature at 360°C and it can also save the area as much as ten times (2.95 × 10⁵ μm²) compared to conventional μHP structure.

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