ELEMEANTS EVALUATION OF SOFT-HARDWARE OBJECTS WITH COMPLEX PHYSICAL, INFORMATION STRUCTURES LINKS

Pavel Khusainov
Military Institute of Telecommunications and Informatization

Abstract. It is offered to consider the index of element importance for multi-element soft-hardware objects with complex structure physical, information links. Elements evaluation is carried out in the planning of implementation of diagnostic tests. The methodological basis is based on the use of formal objects “network of uniformity soft-hardware devices”, “processes operating environment”, the diagnostic model of inter-process communication tract.

Keywords: technical diagnostics, inter-process communication, complex network, logical conclusion

OCENA ELEMENTÓW OBIEKTÓW OPROGRAMOWANIA I SPRZĘTU O ZŁOŻONEJ STRUKTURZE KOMUNIKACJI FIZYCZNYCH, INFORMACYJNYCH

Streszczenie. W artykule przedstawiono analizę możliwości wykorzystania węża wieloelmentowych obiektów oprogramowania i sprzętu o złożonej strukturze komunikacji fizycznej i informacyjnej. Ocena elementów odbywa się przy planowaniu wykonania testów diagnostycznych. Podstawą metodologiczną oparta jest na wykorzystaniu obiektów formalnych "sieć tego samego typu oprogramowania i sprzętu", "środowisko operacyjne procesów obliczeniowych", modelu komunikacji międzyprocesowej.

Słowa kluczowe: diagnostyka techniczna, komunikacja międzyprocesowa, sieć złożona, wniosek logiczny

Introduction

Technical diagnostics soft-hardware objects with complex structure physical, information links (the object of diagnosis, OD) should be carried out taking into account the systemic role of their elements. The methodological basis of technical systems' analysis with a sequential, sequential-parallel, parallel-sequential structure of elements is sufficiently developed and tested. At the same time, evaluation of elements soft-hardware objects with a more complex structural organization requires further research.

A perspective approach to evaluating of the structural properties of elements of multi-element soft-hardware objects is their consideration as elements of the cybernetic system [3, 10].

Cybernetic system is an abstract model of a technical system as a set of related objects (elements) capable to receive, store, process, and exchange information. The scientific research of cybernetic systems is aimed to determine the most common patterns for all such objects [5, 8]:

- structure – a set of elements and relations between them;
- state – a set of states of elements and connections between them;
- the complexity is due to the dimensional (the number of parameters which describe elements’ states), number and variety of relationships between elements.

The presentation of OD as a set of physical, informational complex structures of links cybernetic system connections allows us to use a wide range of interpretations for complex networks. The theory of complex networks offers the most developed approach to description and evaluation of the structural properties of multi-element technical objects [2, 4].

1. Formulation of the problem

Based on the foregoing, the model of the selected OD is the formal object “network of uniformity soft-hardware devices”. The assumption of “uniformity” is accepted on the basis of analysis of trends, principles of practical implementation (modularity, multi-level, unification) software, hardware support of Open System Interconnection (OSI) and inter-process communication.

It is offered to evaluate the element importance to support physical, informational connectivity of multi-element software-hardware ODs. The multipliers of additive index $E_{ij}$ are the relative weights of element inclusion in the structures of physical, informational connectivity on the observation interval $\chi$. The initial data are the adjacency matrix of the formal object “network of uniformity soft-hardware devices” in the observation interval $\chi$ for the graph of physical links $[\|P\|]$ for the graph of information links $[\|I\|]$.

2. The network of uniformity soft-hardware devices

The network of uniformity soft-hardware devices is a formal object based on a one-to-one mapping of physical and information connectivity of OD elements to the set of nodes complex network and relationships between them (Fig. 1):

$$M \leftarrow V, U, W, T \right.$$

Notation:

$V = \{v_i\}$ is a set of uniformity soft-hardware devices;
$U$ is a relation of physical connectivity;
$W$ is a relation of informational connectivity;

![Fig. 1. Example of a formal object “network of uniformity soft-hardware devices”](image)

$T = \{\{t\}\}$ is a set of discrete values of registration time of OD's states, $s \in Z^{*}_{+}, T = \bigcup_{\chi=1}^{N} T^X, \bigcap_{\chi=1}^{N} T^X = \emptyset, X$ is a set of time intervals for monitoring the OD, $T^X = \{t_{xy}\}$ is a set of discrete time values of registration OD's states on the observation interval.

The composition of uniformity soft-hardware device [6, 7]:

- hardware (CPU, RAM, system bus, timer, etc.);
- data communication equipment, DCE (I/O device);
- kernel of operating system of soft-hardware device;
- processes.
A process is a unit for planning computing work of a soft-hardware device and the subject of access to resources. The algorithm of process software implementation in RAM determines the computational work of soft-hardware device on the time interval for its execution.

The basis for building a process of software implementation in RAM is the loadable module of a program. The memory address space required for the process is isolated from unforeseen I/O (Input/Output) operations.

The operating system kernel is a software implementation of control algorithms for scheduling and allocating CPU time, address space RAM, I/O devices between processes, providing their access to system services. The role and place of the kernel of operating system is to provide inter-process communication in the context of the I/O operations. Its software implementation is provided by functionality of OSI levels: transport, network, data link. For this, its software implementation includes: firstly, the functionality of the transport, network, data link layer; secondly, the provision of FIFO service discipline, stream multiplexing, error control, etc.

Vectors of parameter values and settings of functional components of a uniformity soft-hardware device, \( v_i \in V \), \( i = 1, n \) (Fig. 1.2):

\[
v_i = \langle Y_i, \Psi_i, \Theta_i, A' \rangle.
\] (2)

Notation:

- \( Y_i \subseteq Y \), \( Y = \{ y_{i0} \} \) is a set of variants of vector fixed values \( y_{i0} = (\Xi) \) where \( \Xi = \{ \xi_{i0} \} \) is a set of parameters and settings of hardware components, \( \alpha = 1, n \); 
- \( \Psi_i \subseteq \Psi \), \( \Psi = \{ y_{i0} \} \) is a set of variants of vector fixed values of parameters and settings DCE, \( \zeta = 1, n \); 
- \( \Theta_i \subseteq \Theta \), \( \Theta = \{ y_{i0} \} \) is a set of options and settings of software implementation kernel components to provide inter-process communication, \( q_{i0} = \{ f_{i0}, f_{i0}^1, f_{i0}^2, \ldots \} \); 
- \( f_{i0} \in F(X) \), \( f_{i0}^1 \in F(D), f_{i0}^2 \in F(Y), f_{i0}^3 \in F(Z), f_{i0}^4 \in F(L), x = 1, n \); 
- \( d = 1, n \); 
- \( y = 1, n \); 
- \( z = 1, n \); 
- \( l = 1, n \); 
- \( F(X), F(D), F(Y), F(Z), F(L) \) is a set of options and settings of software implementation typical kernel services to provide inter-process communication: 

- \( f_{i0}' \) – access to I/O buffers of the kernel when executing processes software implementations \( A' \) in the RAM; 
- \( f_{i0}'' \) – buffering of blocks of data when executing I/O call of the kernel of process software implementations \( A' \) in the RAM; 
- \( f_{i0}'' \) – multiplexing (demultiplexing) I/O streams of the kernel; 
- \( f_{i0}'' \) – dispatching I/O streams of the kernel; 
- \( f_{i0}'' \) – buffering I/O data blocks of the DCE.

The role and place of kernel components of soft-hardware device are considered in the context of formal object "processes operating environment" (Fig. 2):

\[
v_i = \langle A', O', G', f_{i0}', f_{i0}', f_{i0}', f_{i0}', f_{i0}' \rangle.
\] (3)

where \( A' = \{ a_{ab} \} \) is a set of processes software implementations in the RAM, \( n = 1, A' \), \( b = 1, F(B) \), \( F(B) \) is a set of variants of processes software implementation and setting its algorithm in the RAM \( a_{ab} \in A' \) created on the basis of programs loading module \( b \); \( O' = \{ o_{bi} \} \) is a set of kernel I/O buffers, \( e = 1, O \); 
- \( G' = \{ g_{bh} \} \) is a set of DCE I/O buffers, \( h = 1, H' \), \( k = 1, K \).

Access to the kernel I/O buffers when executing processes software implementations on the observation interval \( \chi \) is determined by the truth of the predicate \( X^\chi(a_{ab}, o_{bi}) \) and is provided by the operability \( f_{i0}', f_{i0}' \):

\[
X^\chi(a_{ab}, o_{bi}) = A' \times O' = \chi_{i0}' = \begin{cases} 1, \text{TRUE} \\ 0, \text{FALSE} \end{cases},
\] (4)

where \( \chi_{i0}' \) is access matrix for transferring data blocks between memory address spaces of process \( a_{ab} \in A' \) and kernel I/O buffers \( o_{bi} \in O' \).

Multiplexing (demultiplexing) of kernel I/O streams on the observation interval \( \chi \) is determined by the truth of the predicate \( Y^\chi(o_{bi}, g_{bh}) \) and is provided by the operability \( f_{i0}', f_{i0}', f_{i0}' \):

\[
Y^\chi(o_{bi}, g_{bh}) = O' \times G' = \chi_{i0}'' = \begin{cases} 1, \text{TRUE} \\ 0, \text{FALSE} \end{cases},
\] (5)

where \( \chi_{i0}'' \) is access matrix for transferring data blocks between memory address spaces of kernel I/O buffers \( o_{bi} \in O' \) and DCE I/O buffers \( g_{bh} \in G' \).

Dispatching of kernel I/O streams on the observation interval \( \chi \) is determined by the truth of the predicate \( Z^\chi(o_{bi}, g_{bh}) \) and is provided by the operability \( f_{i0}', f_{i0}', f_{i0}' \):

\[
Z^\chi(o_{bi}, g_{bh}) = X^\chi_{i0}'' \times Y^\chi_{i0}'' = \begin{cases} 1, \text{TRUE} \\ 0, \text{FALSE} \end{cases},
\] (6)

where \( \chi_{i0}'' \) is access matrix for transferring data blocks of kernel I/O streams between memory address spaces of kernel I/O buffers \( o_{bi} \in O' \) and DCE I/O buffers \( g_{bh} \in G' \).

---

![Fig. 2. A formal object processes operating environment](image-url)
Principles to evaluate the element importance of OD on the basis of a formal object "network of uniformity soft-hardware devices":

- the relation of nodes’ physical connectivity \( U \) is mapping of operability physical connection between OD’s elements (data transmission channel);
- the relation of nodes’ information connectivity \( W \) is mapping of operability information links between processes OD’s elements (inter-process communication tract);
- the graphs of physical, information links between elements of OD are mapping of technical state of inter-process communication tracts and data transmission channels in their composition.

Predicates of physical connectivity \( v_i, v_j \in V \) on the observation interval \( \chi \):

\[
U^\chi (v_i, v_j) = V \times V = \{1, \text{TRUE}, 0, \text{FALSE} \} \quad (10)
\]

\[
U^\chi (h_i, h_j) = H \times H = \{1, \text{TRUE}, 0, \text{FALSE} \} \quad (11)
\]

\[
U^\chi (v_i, v_j) = U^\chi (h_i, h_j) , \quad (12)
\]

where \( \{a_{ij}^h\} \) is the relationship matrix of the nodes physical connectivity for formal object "network of uniformity soft-hardware devices" is an equivalent to the adjacency matrix of graph of physical connections between the elements of OD \( M^h(V) \) on the observation interval \( \chi \) (data transmission channels), \( \chi = [1, \varnothing], \quad H = H^i \cup H^j, \quad H^i \cap H^j = \varnothing, \quad h_i = H^i \), \( h_j = H^j \), \( i, j = 1, |V|, \quad i \neq j .

Predicates of information connectivity \( v_i, v_j \in V \) on the observation interval \( \chi \):

\[
W^\chi (v_i, v_j) = V \times V = \{1, \text{TRUE}, 0, \text{FALSE} \} \quad (13)
\]

\[
W^\chi (a_{i,j}^h, a_{i,j}^h) = A^i \times A^j = \{1, \text{TRUE}, 0, \text{FALSE} \} \quad (14)
\]

\[
W^\chi (v_i, v_j) = W^\chi (a_{i,j}^h, a_{i,j}^h) , \quad (15)
\]

where \( \{a_{ij}\} \) is the relationship matrix of nodes information connectivity for formal object "the network of uniformity soft-hardware devices" is an equivalent to the adjacency matrix of graph of information links between OD’s elements \( M^h(V) \) on the observation interval \( \chi \) (inter-process communication tracts), \( \chi = [1, \varnothing], \quad i, j = 1, |V|, \quad i \neq j .

Recall [1, 4, 9] that a graph is a set of nodes (vertices) and their ordered (unordered) pairs. An ordered pair of nodes (vertices) is called an edge; an unordered pair is called an arc. A pair of nodes (vertices) can be connected by two or more edges (arcs). Common graphs in applied research and engineering tasks is complex network. The element of the graph adjacency matrix is a mapping of links number between nodes, the element of the graph incidence matrix – nodes which are incidential to links. The graph node degree is mapping of edges number that is incidental to the node of the graph. The sum of the graph edges is equal to the sum of the degrees of its nodes.

Two nodes of the graph are called connected if there is a route between them. A route is a finite sequence edges to move from one node to another. Cartesian product of sets is mapping binary relation of elements connectivity and can be given in terms of the predicate calculus. A binary relation is any subset of ordered pairs of Cartesian product.

The truth of the predicate indicates the existence of a relationship. The truth of the predicate indicates the validity of a binary relation connectivity as a result of the Cartesian product.

To define a relation means to set a predicate (method) of selecting a subset of ordered pairs from the set of results of Cartesian product. A visual representation of binary relations is the relation matrix, an oriented (undirected) graph or its adjacency matrix.

3. The formal axiomatic apparatus for determining the technical state of inter-process communication tract

The conclusion about the technical state of the inter-process communication tract is equivalent to the truth’s conjunction of results of diagnostic tests of its functional elements (Fig. 3):

\[
W^\chi (a_{i,j}^h, a_{i,j}^h) \Rightarrow R(w_{i,j}^h) \quad (16)
\]

\[
R(v_i) \land R(a_{i,j}^h) \land R(v_j) \land R(a_{i,j}^h) \land R(v_r) \Rightarrow R(w_{i,j}^h) \quad (17)
\]

\[
R(f_{i,j}^h) \land R(f_{i,j}^h) \land R(f_{i,j}^h) \land R(f_{i,j}^h) \land R(f_{i,j}^h) \Rightarrow R(v_i) \quad (18)
\]

\[
R(f_{i,j}^h) \land R(f_{i,j}^h) \land R(f_{i,j}^h) \land R(f_{i,j}^h) \land R(f_{i,j}^h) \Rightarrow R(v_j) \quad (19)
\]

\[
R(f_{i,j}^h) \land R(f_{i,j}^h) \land R(f_{i,j}^h) \Rightarrow R(v_r) \quad (20)
\]

Notation:

- \( R(v_i), R(v_j), R(v_r) \) are indicators of results diagnostic tests that is mapping of technical state of the processes operating environment (2–9), \( v_i, v_j, v_r \in V \), \( i, j, r = 1, |V|, \quad i \neq j, r : R(u_{i,j}^h), R(a_{i,j}^h) \) are indicators of results diagnostic tests that is mapping of technical state of the data transmission channels;
- \( R(f_{i,j}^h), R(f_{i,j}^h), R(f_{i,j}^h), R(f_{i,j}^h), R(f_{i,j}^h) \) are indicators of results diagnostic tests that is mapping of typical kernel services to provide inter-process communication of the terminal soft-hardware device;
- \( R(f_{i,j}^h), R(f_{i,j}^h) \) are indicators of results diagnostic tests that is mapping of typical kernel services to provide inter-process communication of the transit soft-hardware device.

![Fig. 3. An inter-process communication tract](image-url)

The conclusion about the technical state of data transmission channels is equivalent to the truth conjunction of results diagnostic tests of its DCE and physical transmission medium, \( i, j, r = 1, |V|, \quad i \neq j, r \).
The index of the element OD's importance

Based on the foregoing, on (10–15) as an index of the element importance of OD $E^i_k$ on the observation interval $\chi$ it is proposed, $i \in [1, |V|]$, $v_j \in V$:

$$E^i_k = \frac{\sum_{i=1}^{[|V|]} N^{i}(U)}{\sum_{i=1}^{[|V|]} N^{i}(W)}.$$ (24)

$$N^{i}(U) = \frac{\sum_{j=1}^{[|V|]} u^i_j}{|V|},$$ (25)

$$N^{i}(W) = \frac{\sum_{j=1}^{[|V|]} w^i_j}{|V|}. $$ (26)

where $N^{i}(U)$ is a node degree of physical connectivity for formal object "network of uniformity soft-hardware devices" which is equivalent to the degree element OD of physical connections; $N^{i}(W)$ is a node degree of information connectivity for formal object "network of uniformity soft-hardware devices" which is equivalent degree element OD of information links. At the same time, the degree of nodes information connectivity which is a mapping of transit elements OD $v_j \in V$ is defined as the sum of inter-process communication tracts between $v_j, v_j \in V$:

$$N^{i}(W) = \sum_{i=1}^{[|V|]} \sum_{j=1}^{[|V|]} w^{ij}. $$ (27)

Consider an example of assessing the OD's elements importance as nodes of a formal object "network of uniformity soft-hardware devices" and appropriate diagnostic model (Fig. 4).

Analysis of adjacency matrix of graphs of physical $M^{i}_k(V)$, information $M^{i}_k(W)$ connectivity revealed such values of the degrees of its nodes on the observation interval $\chi$: $N^{i}(U) = 8, N^{i}(W) = 6, N^{i}(W) = 4$. Based on the foregoing, index of the element OD importance is $E^i_k = 0.01, E^i_k = 0.02, E^i_k = 0.03, E^i_k = 0.04, E^i_k = 0.05$. The prevailing index of the element OD's importance ($E^i_k = 0.17$) is explained by the obvious transit role of this OD's element. The main element of the tracts inter-process communication is the functional component $v_j \in V$ described by the formal object "processes operating environment".

5. Conclusions

Adequacy of the element OD importance index (24–25) is based on generally accepted interpretation of a node degree of a graph (complex network), it is informative, invariant, and resistant to evaluation interference.

Formal objects "network of uniformity soft-hardware devices" (1–2, 10–15), "process's operating environment" (3–9) are the basis of a new scientific approach to the synthesis and analysis of multi-element soft-hardware objects with a complex structure of physical, information links. The formal axiomatic apparatus (16–23) is a logical conclusion system about the technical state of an inter-process communication tracts. The conclusion about the technical state of "network of uniformity soft-hardware devices" can be made after performing diagnostic tests based on a comparison of their results using index of the element OD importance (24–25).

Further research is aimed to develop a model of diagnostic parameter of inter-process communication tracts and methods of evaluation of technical state network of uniformity soft-hardware devices.

References

[1] Gerasimov B. M., Eydel'man S. D., Sirchenko Z. F., Shuman B. M.: Matematicheskiye osnovy issledovaniy operatsiy (I). Diskretnye struktury: Uchebnoye posobiye. KVIPTU PVO, Kiev 1989.

[2] Holovach Yu.: Skladni merezhi. Zhurnal fizychnykh doslidzhen (I), 4/2006, 247–289.

[3] Klyueva V. V., Parkhomenko P. P. (red.): Nadezhnost’ i effektivnost’ informativnykh sistem: Spravochnik (t. 4). Moscow 1977.

[4] Ore O.: Teoriya grafov. Nauka, Moscow 1980.

[5] Svoropov. USE im. M.P. Berezanu, Kiev 1989.

[6] Tanenbaum E.: Sovremennye operatsionnyye sistemy. Piter, Sankt Petersburg 2012.

[7] Uazynder S.: Spravochnik po tekhnologiyam i sredstvam svyazi. Mir, Moscow 2000.

[8] Vinogradov I. M. (red.): Matematicheskaya entsiklopediya. Sovetskaia Entsiklopediia (t. 4). Moscow 1984.

[9] Vinogradov I.M. (red.): Matematicheskkaya entsiklopediya (t. 1). Sovetskaya Entsiklopediia, Moscow 1977.

[10] Zherdev M. K., Lyenkov S. V., Kredentser B. P. ta in.: Fizychni osnovy teoriyi nadnysti: pidruchnyk. Kyivs’kyi universytet, Kiev 2008.