GaN-Based Readout Circuit System for Reliable Prompt Gamma Imaging in Proton Therapy

Vimal Kant Pandey 1,2, Cherming Tan 1,2,3,4,∗ and Vivek Sangwan 1

Abstract: Prompt gamma imaging is one of the emerging techniques used in proton therapy for in-vivo range verification. Prompt gamma signals are generated during therapy due to the nuclear interaction between beam particles and nuclei of the tissue that is detected and processed in order to obtain the position and energy of the event so that the benefits of Bragg’s peak can be fully utilized. This work aims to develop a gallium nitride (GaN)-based readout system for position-sensitive detectors. An operational amplifier is the module most used in such a system to process the detector signal, and a GaN-based operational amplifier (OPA) is designed and simulated in LTSpice. The designed circuit had an open-loop gain of 70 dB and a unity gain frequency of 34 MHz. The slew rate of OPA was 20 V/µs and common mode rejection ratio was 84.2 dB. A simulation model of the readout circuit system using the GaN-based operational amplifier was also designed, and the result showed that the system can successfully process the prompt gamma signals. Due to the radiation hardness of GaN devices, the readout circuit system is expected to be more reliable than its silicon counterpart.

Keywords: GaN; operational amplifier; proton therapy; prompt gamma imaging

1. Introduction

Proton therapy has a unique feature that makes it more advantageous compared to conventional radiotherapy. This unique feature stems from its dose distribution which remains quasi-constant at a low level along the path traveled by proton and rises sharply to a maximum value at Bragg’s peak towards the end. Beyond Bragg’s peak, the dose absorbed is almost negligible, reducing the risk of damage to the healthy tissues [1–3]. To fully leverage this Bragg’s peak, accurate in-vivo range verification is essential.

Two main techniques are employed for this in vivo range verification which are based on the by-products of the interaction between patient and the incident irradiation. They are PET (positron emission tomography) which uses the delayed gamma [4] and prompt gamma imaging which uses prompt gamma [5]. Prompt gamma signals can easily penetrate through the tissues and emerges without much interaction with the tissues [4], hence the information of the location of its origin is not distorted, renders accurate identification of the Bragg’s peak position [6–8], and thus it is commonly used.

Silicon photomultipliers (SiPM) and position-sensitive multi-output detectors are commonly employed for detecting the prompt gamma signals. The detector commonly consists of a multi anode position-sensitive photomultiplier tube (PSPMT) which has 8 × 8 multi-anode spaced with a pixel size of 6 mm × 6 mm per anode [7]. A scintillator crystal is used along with the detector to generate fluorescence when a prompt gamma signal is incident on it. This light is then converted into photoelectrons on the photocathode of the...
PSPMT and a current signal is generated. The PSPMT current signals are conventionally processed by a preamplifier and a shaping circuit, and the shaped signals are converted into digital signals using analog to digital converters. A special algorithm is used to extract the energy and position of the radiation from the digital signals [9,10]. Conventionally, these circuits that process the PSPMT signals are built using an operational amplifier (OPA), and they must be placed in very close proximity to the detector to reduce signal loss and distortion, and hence their operation environment is radiative.

In proton therapy, secondary neutrons are also generated along with the prompt gamma signals. The main source of secondary neutrons is the treatment unit and the patients themselves. The neutrons generated from the treatment unit are known as the external neutrons and are generated due to the interaction of proton with beam delivery system, whereas neutrons generated from the patient is a result of proton interaction within the patient body which are known as internal neutrons [11,12]. These neutrons will negatively affect the health of the patient as well as the electronics present in the treatment room such as electronics/data acquisition systems, monitoring devices, robotic patient positioners and onboard imaging, etc. [13,14]. The radiation hardness of these electronic devices is of great concern as they are operating in the radiation environment and their durability will be compromised, as can also be observed in our treatment room. The secondary and scattered radiation field present in and around the passive proton treatment nozzle was evaluated and their results showed that the dose at any point is highly dependent on the position relative to the isocenter [12]. It was found that the dose on the gantry is approximately 3 to 6 times higher than that at the gantry floor. Since the readout circuits are in close proximity to the isocenter, their reliability and performance are greatly affected by the neutrons.

Several studies [15–21] have been conducted recently on the commercial OPA Integrated Circuits (ICs) under neutron radiation and they found that the characteristics of OPAs such as gain, slew rate, offset current, etc. are degraded after the radiation. The most used OPA, µA741 was studied under neutron and gamma rays, and results showed that the frequency behavior of the IC i.e., the gain-bandwidth product and slew rate, degraded after irradiation [19]. These changes in the gain and slew rate will affect the shape of the output signal; for example, a sinusoidal signal became a sawtooth wave due to these changes. In reference [16], LM124 from three different manufactures was irradiated under 1 MeV neutron radiation at two different fluences of $1 \times 10^{12}$ nc–cm$^{-2}$ and $5 \times 10^{12}$ nc–cm$^{-2}$, respectively. All three ICs showed a decrease in the supply bias current, open-loop gain, and slew rate, except one of the devices which showed an increase in the slew rate after irradiation, due to the increase in the base current of the transistor used in the buffer stage.

Most of the ICs mentioned above are designed using bipolar transistors, however, neutrons can also significantly affect the characteristics of the metal oxide semiconductor devices (MOS) [22–24]. Neutrons are non-ionizing particles which upon interaction with electronic devices causes displacement damage (DD) or non-ionizing energy loss (NIEL) damage [25]. Both forms of damage will affect the properties of bipolar, MOS as well as the passive components [25]. The silicon dioxide layer in Metal Oxide Semiconductor Field Effect Transistor (MOSFET) is most sensitive to neutron radiation. Neutron irradiation creates oxide trapped charges and interface traps which affect the characteristics of MOSFET significantly [22,26]. These traps resulted in neutron-induced oxide degradation in MOSFET as observed by Vaidya et al. [27].

Gallium nitride (GaN) is emerging as one of the promising technologies for harsh environments as it is more reliable under radiation as compared to the silicon counterpart [28–30]. The mean displacement energy of GaN is higher (approximately 21.3 eV) than the silicon (approximately 11.07 eV), and there is no oxide layer beneath the gate electrode of GaN High Electron Mobility Transistor (HEMT), hence it performs better in the radiation environment [31,32]. Low noise and high-speed operation are other advantages of GaN which makes it a more viable solution for designing a readout circuit for high detection rate radiation detectors. Due to its low noise characteristics, GaN has already been used
for designing low noise amplifiers [33–35]. In view of the aforementioned advantages, we developed an operational amplifier (OPA) using GaN transistors and used it to design a readout circuit for prompt gamma signals. Such a GaN-based OPA is the first of its kind reported, to the best knowledge of the authors.

2. Materials and Methods

The reliability of GaN HEMT is superior than Si MOSFET in a neutron radiation environment as proven experimentally in our previous work in [36]. Thus, one can assure the reliability of the GaN Operational Amplifier (OPAMP) designed in this work. In this work, we chose GaN HEMT transistors from efficient power conversion (EPC) in order to design the operational amplifier and readout circuit because they show better performance in a radiation environment, as reported in the literature, where negligible variations in the GaN HEMT parameters were reported in a fast neutron ambient (up to a fluence of $1 \times 10^{15}$) [37–39].

OPA s are the most used devices for designing the various modules of the readout system as shown in Figure 1 which is modified from reference [9], including transimpedance amplifiers for current to voltage conversion, adders for signal regrouping and integrators for charge to time conversion. The operational amplifier required to design these circuits should have high gain, higher bandwidth, and high speed so that it can process the short-rise time and weak signals obtained from the photomultiplier tube (PMT). The first block of the system is a scintillator crystal along with PSPMT for detecting the prompt signal and generating the corresponding current signal. The PSPMT has 64 readout channels which is reduced to 4 channels (I1, I2, I3, and I4) by the discretized position circuit (DPC) proposed in [40]. The four current signals from the PSPMT are then converted into four voltages (V1, V2, V3, and V4) by four transimpedance amplifiers (TIAs). The output voltage of the TIA is proportional to the input current i.e.,

$$V = I \cdot R$$

where $I$ is the input current to the TIA and $R$ is the feedback resistance in the TIA circuit. The signals obtained from the TIAs are then fed to the signal regrouping block where they are combined to provide the following:

$$V_{S1} = V1 + V2 + V3 + V4$$

$$V_{S2} = V1 + V2 - V3 - V4$$

$$V_{S3} = V1 + V4 - V2 - V3$$

The gain of the adder circuit in the signal regrouping block is set to unity so that Equations (2)–(4) can be obtained in terms of the current from the PMT as follows:

$$V_{S1} = (I1 + I2 + I3 + I4) \cdot R$$

$$V_{S2} = (I1 + I2 - I3 - I4) \cdot R$$

$$V_{S3} = (I1 + I4 - I2 - I3) \cdot R$$

The output signal $V_{S1}$ is fed to the integrator and upon integration, the total charge accumulated at the detector can be obtained which provides the energy information of the radiation event. The output signals $V_{S1}$, $V_{S2}$, $V_{S3}$, and VIE (output of the integrator) are then sent for data processing for computing the energy and position information of the radiation event. To prevent the pulse pile-up, the output of the integrator is reset to the initial state after each detection by the reset signal provided by the data processing unit. The details of the energy and position calculation are discussed in the next section.
Figure 1. Signal readout block diagram.

Figure 2 shows the schematic diagram of the proposed operational amplifier. The first stage of the circuit is a dual input balanced differential amplifier formed by M1 and M2. Since a complimentary GaN transistor is not available, passive load is used throughout the design. Transistors M3, M4, M7, M9 and resistor R3 are in current mirror configuration to provide the tail current of the first, second and third stage of the circuit. The second stage is the dual input unbalanced output differential amplifier formed by M5 and M6. M10 and M11 are configured to a common source with degeneration mode and cascaded together to achieve high open-loop gain. M8 is used in common drain configuration between the second stage and the common source stage to prevent loading. Capacitor C1 is added to improve the stability of the circuit. LTSpice is employed for the design and simulation of the OPA by importing the Simulation Program with Integrated Circuit Emphasis (SPICE) model of GaN HEMT provided by EPC.

Figure 2. Proposed gallium nitride (GaN) operational amplifier. Exact values of the components cannot be shown here due to confidentiality pertaining to patent filing.

3. Results and Discussion

The open loop gain of the proposed operational amplifier circuit is obtained by AC simulation in LTSpice. From Figure 3, the open loop gain of the proposed circuit is 70 dB and the unity gain frequency is approximately 34 MHz.
Slew rate is another important factor of the OPA which can be measured by applying a step signal at the input of OPA and the rate of change of the signal from 10% to 90% at the output of OPA is measured \[41\]. For computing the slew rate, the proposed OPA is configured in unity gain mode and an ideal pulse of 1 V amplitude is applied to input terminal. Figure 4b shows the simulation result, the output peak-to-peak voltage obtained is 619 mV. CMRR is calculated by using the following equation:

\[
CMRR = 1 + \frac{R_F}{R_I} \cdot \frac{Vin(\text{peak} - \text{to} - \text{peak})}{Vout(\text{peak} - \text{to} - \text{peak})}
\] (8)

On putting the values in Equation (8), we obtain:

\[
CMRR = 1 + \frac{560k\Omega}{100\Omega} \cdot \frac{2V}{619mV} = 16158.3
\]

\[
\text{CMRR in dB} = 20\log(16158.3) = 84.2\text{dB}
\] (9)

Figure 4. (a) Common mode rejection ratio (CMRR) simulation setup. (b) Simulation output.

Slew rate is another important factor of the OPA which can be measured by applying a step signal at the input of OPA and the rate of change of the signal from 10% to 90% at the output of OPA is measured \[41\]. For computing the slew rate, the proposed OPA is configured in unity gain mode and an ideal pulse of 1 V amplitude is applied to input terminal as shown in Figure 5a. The corresponding simulation output is shown Figure 5b. Slew rate is computed by the following equation:

\[
\text{Slew rate} = \frac{\Delta V_{out}}{\Delta \text{time}} = \frac{(V_{out90\%} - V_{out10\%})}{(t_{90\%} - t_{10\%})}
\] (10)
On putting the simulated value from Figure 5b in Equation (10), the slew rate obtained is given as
\[
\text{Slew rate} = \frac{(898.96\text{mV} - 97.92\text{mV})}{(1.045\mu\text{s} - 1.005\mu\text{s})} = 20.03 \frac{\text{V}}{\mu\text{s}}
\]  
(11)

Figure 5. (a) Slew rate simulation model. (b) Simulation output.

3.1. Operational Amplifier (OPA) as Current to Voltage Converter

To convert the current from PMT into voltage, four transimpedance amplifiers are required as shown in Figure 2. The proposed OPA is configured as transimpedance amplifier as shown in Figure 6a. Resistor R1 is used as feedback resistor and the output voltage of the circuit is given by:
\[
V_{out} = I_1 \cdot R_1
\]  
(12)

Figure 6. (a) Operational amplifier (OPA) as transimpedance amplifier (b) Simulation result (c) Simulation results showing the Linearity of TIA for 1 μA to 1 mA input current.
Figure 6b is the simulation result of the transimpedance amplifier, and it shows that, for the applied input current of 1 μA the corresponding output voltage is 1 mV, according to that given by Equation (9). The current from the PSPMT is in the range of tens of μA and Figure 6c shows the linearity of the TIA for the current range from 1 μA to 1 mA.

3.2. OPA as Adder

As given in Equations (2)–(4), an adder circuit is required for signal regrouping. The proposed OPA is configured as an adder circuit as shown in Figure 7a. Figure 7b shows the transient simulation result of the circuit. The output voltage of the circuit is the sum of the applied voltages V1, V2, and V3. The gain of the circuit is set to unity by choosing all the resistors of the same value. The amplitude of the applied input voltages is 1 mV, 2 mV, and 3 mV, respectively, and the output voltage is 6 mV as can be seen from Figure 7b.

Figure 7. (a) Adder circuit. (b) Simulation output.

3.3. OPA as Integrator

In the readout circuit, integrators are commonly used for pulse shaping or charge-to-time conversion. Our proposed OPA is configured as an integrator as shown in Figure 8a, and the corresponding simulation result is shown in Figure 8b. It can be seen from Figure 8b that the square wave input signal is converted into an approximated ramp signal at the output, verifying the operation of the integrator circuit. A perfect ramp signal cannot be obtained due to the exponential charging and discharging characteristic of capacitor C1.

Figure 8. (a) Integrator circuit. (b) Simulation output.

3.4. Gallium Nitride (GaN) Readout Circuit for Position-Sensitive Photomultiplier Tube (PSPMT)

With the various modules designed using our proposed OPA and verified, the complete GaN-based readout circuit is built as shown in Figure 9. Block X1 is discretized position circuit; X2, X3, X4, X5 are the transimpedance amplifiers; X6 is the adder; X7, and
X8 are the adder-subtractors. The outputs from the adder and adder-subtractor circuits VS1, VS2, and VS3 will be sent to the data processing unit as shown in Figure 1 for computing the position of the radiation event. The output VS1 is also fed to the input of integrator whose output VIE will be fed to the data processing unit for computing the energy of the radiation event.

To verify the behavior of the readout circuit, the current obtained from the SiPM due to radiation interaction is mathematically modeled in LTSpice. The PM can be represented by a current source in parallel to a capacitor and the current is given by the Equation (11) [42,43]:

\[
I_s = \frac{Q_e \times N_{ph} \times \text{PDE} \times G}{\tau_d - \tau_r} \left( e^{-\frac{t}{\tau_r}} - e^{-\frac{t}{\tau_d}} \right)
\]

(13)

where \(Q_e\) is the charge of electron, \(G\) is the gain of PMT, \(\text{PDE}\) is photon detection efficiency of the scintillation crystal, \(N_{ph}\) is the number of the photons detected, \(\tau_r\) and \(\tau_d\) are the rise and decay time of the scintillator. The value of the parameters in Equation (13) for S11828-3344M SiPM and CsI(Tl) crystal is given in Table 1 [43]. Figure 10 shows the equivalent circuit of PMT and the corresponding current pulse generated by Equation (13), and in our case, the current pulse is 40 \(\mu\)A.

This pulse was applied randomly to different channels of DPC mimicking random radiation events, and the corresponding current signal (I1, I2, I3, I4) obtained from the four channels of the DPC is shown in Figure 11a. These current signals are then converted into voltage signals V1, V2, V3 and V4 by the TIAs shown in Figure 11b. The simulation result shows that the output signal obtained from the TIA has minimal shape distortion and follows Equation (1).

Figure 12 shows the outputs of adder X6, and integrator, respectively. Figure 12 shows that the reset signal remains low until the integration is performed, after that it toggles to the high stage and discharges the capacitor so that the integrator returns to initial state and the next signal will be integrated. The outputs VS1 is the sum of the input currents obtained from DPC as given in Equation (5). Suppose Q1, Q2, Q3 and Q4 are the integral of currents I1, I2, I3 and I4; \(Q_T\), which is the sum of Q1 to Q4 representing the total charge collected by the PSPMT and proportional to the energy of the prompt gamma signal.

Thus, VS1 has the information of the energy of the gamma signal. The output VIE of the integrator X9 is given as:

\[
VIE = a \int VSI dt + c
\]

(14)

where \(a\) and \(c\) are constant. The output of the integrator is then feed to the data processing unit for extracting the energy information.

The positioning of the radiation event from the DPC circuit can be determined by the following equations [44,45]:

\[
X = \frac{(A + B) - (C + D)}{A + B + C + D}
\]

(15)

\[
Y = \frac{(A + D) - (B + C)}{A + B + C + D}
\]

(16)

where \(A\), \(B\), \(C\) and \(D\) represents the peak values of the signals obtained from the four corner channels of the DPC. To verify if the circuit provides the correct position information, the current pulse given by Equation (13) was injected sequentially to each of the input positions of the DPC circuit mimicking the radiation event, and 64 such simulations were independently performed. The detected position information is computed using Equations (15) and (16), where the numerator of Equation (15) is VS2, the denominator of Equation (15) is VS1, the numerator of Equation (16) is VS3, and the denominator of Equation (16) is VS1. Figure 13a shows the detected positions of the PMT arranged in an 8 x 8 array and Figure 13b shows the corresponding position coordinates. The identification of the positions is accurately determined.
Table 1. Silicon photomultiplier (SiPM) current pulse parameters.

| Parameter | Value |
|-----------|-------|
| $N_{ph}$ | 1000  |
| $G$       | $7.5 \times 10^5$ |
| PDE       | 0.40  |
| $\tau_i (ns)$ | 50    |
| $\tau_r (\mu s)$ | 1     |

Figure 9. Simulation model of the GaN readout system.

Figure 10. (a) Equivalent circuit of SiPM. (b) Current pulse generated from scintillation.
Figure 10. (a) Equivalent circuit of SiPM. (b) Current pulse generated from scintillation.

Figure 11. (a) Output current from four channels of discretized position circuit (DPC). (b) Output voltage of transimpedance amplifiers (TIAs).

Figure 12. Simulation output of adder and integrator.

Figure 13. Position verification of the readout circuit. (a) position of the input current pulse applied mutual exclusively. (b) Computed position coordinates that correspond to the individual input current pulse.
4. Conclusions

Prompt gamma imaging is an important technique to locate the Bragg’s peak position and energy. The readout circuits used for prompt gamma detection and processing are in close proximity to the radiation source. Secondary neutrons generated during proton treatment can also negatively affect the performance of the electronic devices present in the treatment room that renders their reliability important. GaN HEMT is more rad-hard compared to the silicon counterpart, and GaN-based operational amplifier is designed in this work.

Simulation results show that the designed OPA has open loop gain of 70 dB and unity gain frequency of 34 MHz. The slew rate of the OPA is 20 V/µs and common mode rejection ratio is 84.2 dB. The proposed OPA is configured for different applications such as transimpedance amplifier, integrator, and the adder which are needed in the prompt gamma readout system. Simulation results show successful operation for these applications. When these different applications are put together, a complete GaN-based prompt gamma readout circuit is implemented, and the result shows successful processing of the prompt gamma signal where its energy and position can be accurately provided for subsequent digital conversion and information extraction.

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