A ENHANCE PLL STRATEGY FOR DISTRIBUTION GRID INTEGRATED TO PHOTOVOLTAIC SYSTEM FOR POWER QUALITY ENHANCEMENT

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Abstract
Distribution grid integrated to photovoltaic system is gaining more attention now-a-days. The photovoltaic system can provide active power to the load and grid at reduced cost. In this paper, photovoltaic energy conversion system is used for providing power to the load and grid. Perturb and observe method is utilize to track maximum power under varying irradiance condition to provide constant active power. The detection and extraction of accurate phase angle under unbalance utility condition is achieved by using three phase magnitude PLL. The solar power is transfer to the load during day time and voltage source inverter act as a shunt active power filter at night which minimizes the burden of active power filter. The modelling and simulation of system is carried out in MATLAB software to reveal its usefulness.

Key Words : Three Phase-Magnitude phase lock loop (PLL), photovoltaic energy conversion system.

1. Introduction
In urban area, power generation from photovoltaic system is proving major tactics and utilization of PV panels on rooftops of building in commercial and industrial area are increasing[1]. Due to the advantages of photovoltaic system such as it provide clean energy at minimum cost. Thus, it is gaining more attention.

As the demand for power is increasing from few decades, it become necessary to increase power generation to meet the power demand. In such condition, utilization of fossil fuels results in emission of greenhouse gases. The use of photovoltaic system overcomes the drawback of fossil fuels. The varying irradiance and varying temperature in photovoltaic system affects the power generation and minimizes the efficiency. In order to track maximum power different maximum power tracking techniques are proposed. In this paper, perturb and observe method is used to track maximum power under varying irradiance condition. The function of photovoltaic system is to transfer constant active power with the help of dc-dc converter.

The accurate synchronization of grid-integrated power converter with utility is important to be consider. The extraction of positive sequence component from voltage at fundamental frequency is necessary for UPS, FACT systems [5], power line conditioners. As we know that non-linear loads introduces harmonics in grid current which affects performance of control and protection devices and may cause unbalances. Thus, it is essential to design a control which is able to filter out the noise and distortion in order to enhance power quality.

Commonly PLL is used for synchronization. Various PLL topologies are given in literature from which zero crossing detection method is the easiest one [6]. The main drawback of these method is its sensitivity. These paper gives a possible choice for the detection of phase and amplitude of positive sequence component for unbalance condition, that is to say 3ph-M PLL.

The proposed control i.e. 3ph-M PLL is utilize to detect and estimate accurate phase angle by extracting positive sequence voltage from applied three phase voltage and attenuate negative sequence and zero sequence component. As compared to conventional PLL, 3ph-M PLL is robust and utilizes linear feedback loop and mitigated the unbalance under unsymmetrical utility condition.

As the grid connected is close loop system, switching for voltage source inverter is done by using current controller. In this paper, hysteresis current controller is utilize with unit templates which generate necessary signal for switching of inverter. Hysteresis current controller consist of two bands i.e. upper band and lower band. The actual current is compared with reference current in order to produce error. If this error current reaches upper hysteresis band, lower switches turn ON and if the error current reaches lower hysteresis band, upper switches turn ON.
2. Configuration of Grid Connected Photovoltaic System

Fig. 1: Structure of grid connected photovoltaic system

Fig. 2: Control scheme for grid connected photovoltaic system
The block diagram of experimental paradigm of 3Ph-4W grid integrated PvECS is given in Fig (1). The model consist of PV array, three phase VSI, DC link capacitor (Vdc) between PV and VSI, 3Ph-4W grid with impedance (Zs), interfacing inductance (Lf), ripple filter, three phase load. The use of ripple filter is to filter out the high switching noise which are cause due to VSI. The interfacing inductance is utilize in order to mitigate the ripples the inverter current.

The control scheme of PvECS integrated to 3Ph-4W grid is shown in Fig (2). In order to enhanced the efficiency and reduce losses due to number of component single stage PvECS is utilize. Conventional solar system only provide active power to the load and grid. The PvECS can fulfil the need of active power for single phase and three phase load simultaneously. The control scheme for PvECS integrated to distribution grid is divided into two controls, in which one is perturb and observe based MPPT control for tracking the maximum power under varying irradiance and temperature condition. Second is PLL with 3ph-M PLL. SPWM modulation technique is utilized for producing switching pulses for three phase VSI.

A. MPPT control

As the solar irradiance and temperature varies point to point, it become necessary to track maximum power in order to give constant supply to the load and grid during day time. There are various MPPT techniques which are explain in articles [4, 5]. Among all of these techniques, perturb and observe method is used to track the maximum power. The objective of this block is to calculate PV power (Ppv) and reference DC link voltage (Vdc*).

B. Proposed 3ph-M PLL

The proposed PLL, is shown in Fig.4. It is nothing but the extension of conventional PLL. Consider a signal)(tu.

\[
\begin{align*}
    u(t) &= u_0(t) + d(t) + n(t) \quad (1) \\
    u_0(t) &= \begin{bmatrix}
        u_{a0}(t) \\
        u_{b0}(t) \\
        u_{c0}(t)
    \end{bmatrix} = \begin{bmatrix}
        V_0 \sin(\omega_0 t + \delta_0) \\
        V_0 \sin(\omega_0 t + \delta_0 - \frac{2\pi}{3}) \\
        V_0 \sin(\omega_0 t + \delta_0 + \frac{2\pi}{3})
    \end{bmatrix} \quad (2)
\end{align*}
\]

In expression (1), d(t) and n(t) is an unwanted part of signal and extraction of fundamental signal is the main function.

The constants i.e. uv , ud , and uw affects system operation as uv controls system settling time and amplitude of response and slow or oscillatory response are made by ud and uw. [6]

A. Computation of unit template

The phase voltages are obtained from the equations [7],

\[
\begin{align*}
    v_{sa} &= \frac{2v_{sab} + v_{sbc}}{3} \quad (3) \\
    v_{sb} &= -\frac{v_{sab} + v_{sbc}}{3} \quad (4) \\
    v_{sc} &= -\frac{v_{sab} - v_{sbc}}{3} \quad (5)
\end{align*}
\]

The peak magnitude of terminal voltage (Vt ) is computed as,

\[
V_t = \sqrt{\frac{2}{3}(v_{sa}^2 + v_{sb}^2 + v_{sc}^2)} \quad (6)
\]

The in-phase unit templates are estimated as,

\[
\begin{align*}
    u_{pa} &= \frac{v_{sa}}{V_t} \\
    u_{pb} &= \frac{v_{sb}}{V_t} \\
    u_{pc} &= \frac{v_{sc}}{V_t}
\end{align*} \quad (7)
\]

The quadrature unit templates are estimated as,

\[
\begin{align*}
    u_{qa} &= -\frac{u_{pb}}{\sqrt{3}} + \frac{u_{pc}}{\sqrt{3}} \\
    u_{qb} &= \frac{u_{pb} - u_{pc}}{2\sqrt{3}} \\
    u_{qc} &= \frac{\sqrt{3}u_{pa} + (u_{pb} - u_{pc})}{2\sqrt{3}} \quad (8)
\end{align*}
\]
### D. Interpretation of reference current

The total in-phase term can be obtained as,

\[ I_{sp} = I_{LpA} + I_{cp} - I_{spv} \]  \hspace{1cm} (9)

Where, \( I_{sp} \) is the average amplitude of in-phase load component detected for load balancing as,

\[ I_{LpA} = \frac{(I_{pa} + I_{pb} + I_{pc})}{3} \]  \hspace{1cm} (10)

The actual current is compared with the reference current and eight switching pulses are generated for VSI [8].

### 3. Simulation Parameters

#### Table 1

| Name                                | Simulated values |
|-------------------------------------|------------------|
| Open circuit voltage \((\text{Voc})\) \(V\) | 32.9 V           |
| Short circuit current \((\text{Isc})\) \(A\) | 9.88 A           |
| Number of panels in series \((\text{Ns})\) | 22               |
| Number of panels in parallel \((\text{Np})\) | 4                |
| PV array power \(P_{pv}\)          | 2.5 kW           |
| PV voltage, \((V_{pv})\) and current, \((I_{pv})\) | 715 V, 40.6 A |
| Interfacing inductor, \(L_{f}\)    | 2.5 mh           |
| Grid voltage, \(v_{ab}\)           | 230 V (rms)      |
| DC bus voltage, \(V_{dc}\)         | 669 V            |
| Sampling time, \(T_{s}\)           | 1 \(\mu\)s       |
| Non-linear load                    | \(R=10\) ohm and \(L=100\) mH |

### 4. Simulation Results

In order to verify the control scheme design for 3Ph-4W grid connected photovoltaic system, simulation has been done on MATLAB. The network is subjected to various conditions such as unbalance or changing irradiance. The system parameters are mentioned in Table I.

#### A. Operation subjected to varying solar irradiance

The PV panel is modelled in MATLAB software where, 22 panels are connected in series and 4 panels are connected in parallel. Fig (3) shows simple P-V and I-V characteristic of PV panel. The irradiance is varied for 850 W/m², 1300 W/m² and 600 W/m² as shown in fig (4) and corresponding P-V and I-V curve is shown in fig (5). Under varying irradiance condition, perturb and observe technique is implemented with the help of dc-dc converter. The output of converter after applying MPPT method is shown in Fig (6).

Fig. 3 : I-V and P-V characteristics

Fig. 4 : Irradiance level

Fig. 5 : I-V and P-V characteristics

Fig. 6 (a) : Input-output voltage waveform of boost converter
B. Operation under unbalance condition

Fig. 7 and 8 shows the results under unbalance condition where load voltage and current is shown in Fig. 7 (a) and (b) and grid voltage and current is shown in Fig. 8 (a) and (b). The unbalance is made by disconnecting phase c for a time period of 0.002 to 0.1 sec on load side. The function of control is to maintain grid current balance. The grid neutral current is maintain almost zero in order to provide neutral current compensation. It also reduces the THD from 37.4% to 1.21% given in table II.

| Phase | Load Current (THD %) | Grid Current (THD %) |
|-------|----------------------|----------------------|
| Phase a | 2.84                | 2.30                 |
| Phase b | 3.02                | 2.55                 |
| Phase c | 2.90                 | 1.21                 |
5. Conclusion

The 3Ph-4W grid connected to photovoltaic system is proposed in this paper. The photovoltaic system provide active power to load and grid. The 3ph-M PLL is utilized for maintaining balance condition on grid side in case of unbalance occurred due to fault on load side and to mitigate the harmonic distortion occurred due to non-linear load. This control maintains balance current and voltage on grid side and inverter side under unbalance condition on utility side. It also reduces THD below a limit i.e. less than 5%. Also the power factor is maintained to unity.

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