Reliability oriented thermal management of aircraft power converters

Jayakrishnan Harikumaran1, Giampaolo Buticchi1,2, Giovanni Migliazza3, Patrick Wheeler1, and Michael Galea1,2
1Department of Power Electronics Machines and Control, University of Nottingham
2Zhejiang Key Laboratory on the More Electric Aircraft Technology, University of Nottingham Ningbo China
3Department of Sciences and Methods for Engineering, University of Modena and Reggio Emilia

Abstract—On board electrical power generation continue to increase as more aircraft systems are being electrified. Increasingly power converters are the interface of power generation and electrical loads in aircrafts. Reliability of aircraft power electronics is essential for high reliability and mission critical applications. Along with wear-out failure mechanisms, random failure modes must also be addressed to realize high reliability aerospace power converters. A dominant random failure mode in aerospace environment is due to cosmic rays (CR). In this work, a thermal management approach is proposed to maximize reliability of the converters considering CR failure mode. The proposed thermal controller is validated with an experimental setup. In the discussion, the trade-off involved between wear-out and CR induced failure mode is addressed.

Index Terms—Cosmic ray failure, Reliability, Thermal control, Power electronics

I. INTRODUCTION

Aircraft designers are increasingly incorporating electrical sub-systems in order to meet pollution reduction targets set by governmental and regulatory bodies [1]. Substituting hydraulic systems, pneumatic air system etc. with electrical counter parts simplify aircraft architecture while achieving overall better aircraft fuel efficiency [2]. In the latest generation aircrafts, on board power generation exceeds 1 MW [3].

As described in [4], the on-board electrical architecture of more and all electric aircraft includes electrical generation and electric propulsion. The eFanX demonstration project from Airbus-Rolls Royce consortium proposes using this architecture. Critical role of drive converters for future electric aircrafts is highlighted as they are part of mission critical propulsion system. Hence it is vital to achieve high reliability of aviation drive converters.

Prior works have focused on wear-out reliability estimation of power converters - railway traction converters [5], electric vehicle traction [6]. Reliability of converter system would be over-estimated if random failures are not considered [7]. Random failures cannot be ignored for power converters operating in high reliability applications. Under aerospace operating conditions, CR induced failures is a dominating failure mode in power converters.

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Thermal control approaches have been proposed in literature to minimize thermal ageing of power components [8], [9]. A typical design aim of thermal control for reliability management is to minimize junction temperature cycles of semiconductors or equalize junction temperature of semiconductors and/or capacitors to maximize system lifetime.

Due to semiconductor properties CR induced failure rate accelerate, when junction temperature decreases. Hence there is a requirement to control junction temperature of semiconductors in an optimal range to balance failure rates due to wear-out failure and CR failures. The main contribution of this work is the proposed active thermal controller. Converter thermal management is performed taking into account estimated CR flux at altitude. An experimental setup validates the proposed approach. In discussion section, impact of thermal control on CR failure rate is quantified and compared.

II. SINGLE EVENT BURNOUT (SEB) OF SEMICONDUCTORS DUE TO COSMIC RAYS

Impact of atmospheric radiations were initially observed in semiconductor memory chips. Random bit flips in Dynamic Random Access Memory (DRAM) chips were eventually related to background atmospheric nucleon flux. The classic work from IBM on terrestrial cosmic rays [10] gives a comprehensive review on SEE (single event error) mechanisms due to nucleon flux in semiconductor chips. SEE is not a breakdown event as there is not enough energy at low voltages.

In case of high voltage semiconductors (above 300V), a high electrical field is present in the bulk of semiconductors, during reverse blocking mode. SEB in power semiconductors are mainly caused by high energy neutrons. The capture of neutrons by atoms in the semiconductors (Si in case of IGBTs, Si or C in case of SiC devices) causes nuclear reactions. The resultant nuclear fragments deposit a charge in semiconductor bulk and acts as seed for CR induced effects. The deposited charge may develop into a streamer of electrons and holes due to electrical field. This leads to localized electric field spikes in semiconductor. A runaway process ensues causing avalanche multiplication of charge carriers. Eventually a short circuit develops leading to sudden device destruction [11], [12].
A. Atmospheric nucleon flux at aircraft altitudes

Interaction of "Cosmic rays" with earth’s atmosphere (mainly nitrogen and oxygen molecules) generates secondary ionizing particles [13]. The most damaging of secondary particles for avionic systems is found to be neutrons [14]. Hess in his pioneering work in 1959 quantified neutron flux profile in atmosphere [15]. Boeing published neutron and other particle flux density and energy spectra variations due to altitude and latitude in [16]. From these prior works, neutron flux is found to increase with altitude and reach its maximum around 60000 feet (named Pfotzer maximum). CR intensity is also dependent on latitude - with flux intensity almost double near poles compared to intensity at equator.

It has been reported in literature that the CR intensity at aircraft altitudes are up to 300 times that at sea level. The atmospheric neutron flux profile is summarized in [10] based on numerous studies. This work along with the IEC standard 62396 provides guidelines for avionics system designers [17]. The neutron flux at any point in the atmosphere can be quantified if the following quantities are known - the latitude and longitude of the location, atmospheric rigidity of that location and altitude of interest. IEC62396 recommends a scaling procedure to estimate neutron flux density at any point in atmosphere by scaling the reference value at a known location and altitude. A neutron flux of \( \text{n} / \text{cm}^2 \text{h} \) is reported for neutrons with energy > 10 MeV at an altitude of 40000 ft. at 45°latitude [18].

B. SEB failure rate estimation

As CR failure is a complex phenomenon, analytical models are not readily available. Neutron flux and reverse blocking voltage and device physical properties determine CR failure rate. The relation to neutron flux is linear while relation to blocking voltage and device characteristics is highly non-linear.

An analytical expression was proposed by ABB to model rate of IGBT failure depending on blocking voltage, temperature, altitude, (1) [19]. The temperature dependency arises as semiconductor blocking voltage reduces with temperature. Electron charge cloud density in semiconductor bulk increases when temperature is reduced. Consequentially, avalanche breakdown is induced at lower voltages as there is higher chance of impact ionization. As reported by ABB, the temperature and altitude dependency is module independent for Si devices while voltage dependency is experimentally curve fitted for particular modules.

IEC-62396-4 specifically deals with SEB failure rate characterization of high voltage power devices in aircraft applications. According to IEC, experimental measurements of CR failures reported in FIT (failure in time) per unit area at different reversing blocking voltage levels at a reference neutron flux level can be scaled with actual neutron flux to obtain FIT figures at aerospace operating conditions. 1 FIT is equivalent to 1 failure every \( 10^9 \) hours of operation.

This method would be adopted in this work to obtain the FIT rate without accounting for temperature effects. CR failure rates for selected modules are reported in [20], [21].

III. Case study - Cosmic Ray Failure Rate estimation

In this section, case study results of CR SEB rate estimation for a particular aerospace starter generator drive converter [22], is presented. Drive converter in above work is a 3L-NPC converter. Atmospheric model is described followed by estimated cosmic ray failure rate for the converter system. Only CR failure rate is discussed here. A detailed reliability study including wear-out failures is being carried out for future publication.

A. Atmospheric model

As mentioned before, neutron flux intensity is key parameter necessary for CR failure rate calculation once system voltage is known. Altitude profile is an indirect measure of neutron flux at actual operating conditions. Temperature impact on SEB rate necessitates estimation of temperature profile. In a prior work from same authors [23], atmospheric profile a typical short haul flight was modeled based on US standard atmosphere 1976 [24]. An altitude/temperature profile from above referred work is reproduced in Fig. 2. The flight profile modelled includes 1 hour of cruising at 30000 feet. Necessary mission profile data - altitude and temperature profile and operating time are thus generated.

B. CR failure rate for SG converter system

Experimental results in [21] highlight, Si IGBTs suffer from cosmic ray failures above 60% of blocking voltage rating which follow recommendations on CR failures from Semikron [12]. A DC voltage of 810V is considered here and hence 650V IGBTs (SEMIX155MLI07E4) are selected for the drive converter. Neutron flux intensity at 30000 ft. is 130 times higher than at reference conditions [18]. CR failure rate of 9.9 FIT is tabulated based on experimental FIT rates from [21] scaled to account for increased neutron flux intensity. Above FIT rate is valid for a semiconductor in blocking mode during entire operation. Due to modulation, scaling factors
\[
\lambda(V_{DC}, T_j, h) = C_3 \cdot \exp\left(\frac{C_2}{C_1 - V_{DC}}\right) \cdot \exp\left(\frac{25 - T_j}{47.6}\right) \cdot \exp\left(1 - \left(1 - \frac{h}{44300}\right)^{5.26}\right)
\]  

(1)

corresponding to percentage of time spent in blocking mode are applied for the switches:

- 75% for the outer switches in 3L-NPC;
- 25% for the inner switches in 3L-NPC.

Cumulative CR failure rate of 3L-NPC converter is 0.0013% /year.

1) Motivation for thermal controller: The CR failure rate from section III-B is derived at a reference junction temperature of 25 °C. The temperature dependence from (1) shows that the cosmic ray failure rate at a junction temperature of 70 °C reduces by 39%, while the failure rate increases by 209% when junction temperature is -10 °C. Considering ambient temperature reduces with altitude, temperature dependence must also be accounted for in reliability estimations.

IV. THERMAL CONTROLLER FOR AEROSPACE CONDITIONS

A thermal controller is proposed with the aim of maintaining junction temperature of semiconductors in an optimum range. There are many handles to vary junction temperature in a switching power converter - switching frequency, cooling system design, device selection etc. Switching frequency variation is utilized in this work. It must be noted that other methods are also feasible.

A. Thermal controller structure

The thermal controller is implemented as a simple P-controller with saturation for the maximum and minimum switching frequency values. The temperature set point could be generated in multiple ways - based on measured atmospheric flux at altitude stored in lookup tables being one of the simplest methods. The thermal controller structure is shown in Fig. 3.

B. Experimental setup

Performance of thermal controller is experimentally validated using a 2L SiC drive converter. The test setup is shown in Fig. 4. The setup includes a DC power source to drive SiC inverter, an RL load with an inductance of 0.5 mH and a variable resistor bank, Hioki PW3390 power analyzer, oscilloscope and SiC inverter. SiC module used is CCS505M12CM2 from CREE rated at 1.2 kV and 87A and DC link is composed of 20 capacitors from TDK Epcos (B32652A4474J000) rated at 470 nF,400V. The fundamental frequency of AC output is 100 Hz and on chip NTC is used to measure case temperature of the module.

C. Experimental results

The controller test result is captured in Fig. 5 and Fig. 6. The module temperature is measured and the switching frequency is varied by the controller to maintain the module temperature in the control range. The switching frequency limits are set to 10 kHz and 50 kHz.

The on-chip NTC is characterised to obtain resistance variation relationship with temperature. On-chip ADC samples temperature of the module to be used in thermal controller.

The system unreliability for 3L NPC converter is shown and a detailed reliability analysis would be published in a future work. It can be seen that change in failure rate have significant impact on the Bxx life as prescribed in aerospace safety requirements.
D. Discussion

The impact on failure rate due to active thermal control is visualized in Fig. 7. A temperature variation of 10 °C with thermal control is modelled.

Under aerospace operating conditions, CR failures tend to dominate as the ambient temperatures are very low resulting in low wear-out damage. Such a combined optimization is necessary to maximize converter reliability in CR failure dominated systems. It can also be concluded that voltage derating for aerospace applications is particularly significant for semiconductors.

V. Conclusion

Cosmic ray failures is an important failure mode to consider for aerospace power converters. Temperature dependence of CR failure rate accentuates CR failures at high altitude conditions. A thermal controller is proposed to maintain junction temperature of semiconductors in an optimum range to maximize converter reliability.

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