Abstract—This paper investigates distributed computing systems where computations are split into “Map” and “Reduce” functions. A new coded scheme, called distributed computing and coded communication (D3C), is proposed, and its communication load is analyzed as a function of the available storage space and the number of intermediate values (IVA) to be computed. D3C achieves the smallest possible communication load for a given storage space, while a smaller number of IVAs need to be computed compared to Li et al.’s coded distributed computing (CDC) scheme. More generally, our scheme can flexibly trade the storage space and the number of IVAs to be computed. Communication load is then analyzed for any given tradeoff.

I. INTRODUCTION

Distributed computing has emerged as one of the most important approaches to big data analysis, where data-parallel computations are executed across clusters consisting of many machines. Platforms like MapReduce [1] or Dryad [2], for example, can perform computation tasks that involve data sets with tens of terabytes and more. In these systems, computations are distributed in the following way. Each cluster (referred to as node in the sequel) \( k \in \{1, \ldots, K\} \) is assigned the computation of a specific output function

\[
\phi_k(w_1, \cdots, w_N) = h_k(g_{k,1}(w_1), \cdots, g_{k,N}(w_N)),
\]

which can depend on all data elements \( w_1, \ldots, w_N \) but decomposes into smaller map functions \( g_{k,1}, \ldots, g_{k,N} \) each depending only on a single data element. The computation of the output functions \( \phi_1, \ldots, \phi_K \) is carried out in three phases. During the first map phase, each node \( k \) locally stores a subset of the input data \( M_k \subseteq \{w_1, \ldots, w_N\} \) and calculates all intermediate values (IVAs) that depend on the stored data:

\[
\{g_{k,i}(w_i) : \ell \in \{1, \ldots, K\}, i \in M_k\}.
\]

In the subsequent shuffling phase, the nodes exchange the IVAs computed during the map phase. The goal is that at the end of this second phase, each node \( k \) is aware of all IVAs \( g_{k,1}(w_1), \ldots, g_{k,N}(w_N) \) required to calculate its output function \( \phi_k \). During the final reduce phase, each node \( k \) then calculates the output function \( \phi_k \) as in (1) based on the previously collected IVAs and the reduce function \( \phi_k \).

Li et al. [3] showed that the communication cost of such a distributed computation system can significantly be reduced if the nodes exploit multicast coding opportunities during the shuffling phase. More specifically, they designed a coding scheme for the map and shuffling phases, termed coded distributed computation (CDC), and proved that it has smallest communication load among all distributed computation schemes with same storage requirements. Subsequently, various works extended the results in [3]. For example, the works in [4], [5] account for straggling nodes; [6] studies optimal allocation of computation resources; and [7] extends the works to wireless networks.

In this work, we propose to refine the performance measure and the coding scheme by Li et al. [3]. Specifically, we account also for the number of intermediate values (IVAs) that each of the nodes has to calculate. In this sense, we extend the storage-communication tradeoff studied in [3] to a storage-computation-communication tradeoff where we wish to characterize the smallest communication load required in the shuffling phase for a given storage space and a given number of IVAs totally calculated during the map phase. Notice that Li et al. [3] termed their tradeoff computation-communication tradeoff because they assume that each node calculates all the IVAs that can be obtained from the data stored at that node, irrespective of whether these IVAs are used in the sequel or not. In this sense, the total number of calculated IVAs is really a measure of the total storage space available at the nodes, this is why we refer to it as storage-computation tradeoff. The main result of this present paper is a new coding scheme, that we term distributed computing and coded communication (D3C). Our scheme in particular achieves the storage-computation tradeoff in [3] but with a reduced number of calculated IVAs compared to [3].

Notations: Let \( \mathbb{N}^+ \) be the set of positive integers. For \( m, n \in \mathbb{N}^+ \), denote the \( n \) dimensional vector space over the finite field with cardinality \( 2^m \) by \( \mathbb{F}_{2^m}^n \), and the integer set \( \{1, 2, \ldots, n\} \) by \([n]\). Scalars are denoted by upper or lower case letters. Sets or subsets are denoted by calligraphic font, and a collection (set of set) will be denoted by bold font. The cardinality of a set \( S \) is denoted by \(|S|\). The bitwise Exclusive OR (XOR) operation is denoted by \( \oplus \).

II. SYSTEM MODEL

We consider a system consisting of \( K \) distributed computing nodes and \( N \) input files each of \( F \) bits, where \( K, N, F \in \mathbb{N}^+ \).
Specifically, given any $N$ files
\[ W := \{ w_1, \ldots, w_N \}, \quad w_n \in \mathbb{F}_{2^P}, \forall \ n \in [N], \]
the goal of Node $k$ ($k \in [K]$) is to compute an output function
\[ \phi_k : \mathbb{F}_{2^P}^N \rightarrow \mathbb{F}_{2^P}, \]
which maps all the files to a bit stream
\[ u_k = \phi_k(w_1, \ldots, w_N) \in \mathbb{F}_{2^B} \text{ of length } B \text{ for some } B \in \mathbb{N}^+. \]

Following the conventions of [3, 7], we assume that the computation of the output functions $\phi_k$ can be decomposed as:
\[ \phi_k(w_1, \ldots, w_N) = h_k(g_{k,1}(w_1), \ldots, g_{k,N}(w_N)), \]
where we define the functions $g$ and $h$ as follows.

- The “map” function
  \[ g_{k,n} : \mathbb{F}_{2^P} \rightarrow \mathbb{F}_{2^T}, \quad k \in [K], \quad n \in [N], \]
maps the file $w_n$ into a binary intermediate value (IVA) of $T$ bits, i.e., $v_{k,n} \triangleq g_{k,n}(w_n) \in \mathbb{F}_{2^T}$ for some $T \in \mathbb{N}^+$.

- The “reduce” function
  \[ h_k : \mathbb{F}_{2^T} \rightarrow \mathbb{F}_{2^P}, \quad k \in [K], \]
maps the intermediate values
\[ \mathcal{V}_k \triangleq \{ v_{k,n} : n \in [N] \} \]
of the output function $\phi_k$ into the output stream $u_k = h_k(v_{k,1}, \ldots, v_{k,N})$.

Note that, such a decomposition always exists. For example, one trivial decomposition is setting the map functions be identity functions and the reduce functions be the output functions, i.e., $g_{k,n}(w_n) = w_n$, and $h_k = \phi_k$, $\forall \ n \in [N], \ k \in [K]$.

1) Map Phase: A central controller assigns a subset of files $M_k \subset W$ to Node $k$, for all $k \in [K]$. Having access to the files in $M_k$, Node $k$ computes a subset of IVAs $C_{k,n} = \{ v_{q,n} : q \in \Lambda_{k,n} \}$, where $\Lambda_{k,n} \subset [K]$ for each file $w_n \in M_k$. Denote the set of IVAs computed at Node $k$ by $C_k$, i.e.,
\[ C_k \triangleq \cup_{n,w_n \in M_k} C_{k,n}. \]

To measure the storage and computation cost of the system, we introduce the following two definitions.

**Definition 1** (Storage Space). We define the storage space, denoted by $r$, as the total number of files stored across the $K$ nodes, normalized by the total number of files $N$, i.e.,
\[ r \triangleq \sum_{k=1}^{K} |M_k| \frac{|\mathcal{M}_k|}{N}. \tag{3} \]

**Definition 2** (Computation Load). We define the computation load, denoted by $c$, as the total number of map functions computed across the $K$ nodes, normalized by the total number of map functions $NK$, i.e.,
\[ c = \sum_{k=1}^{K} |C_k| \frac{|\mathcal{C}_k|}{NK}. \tag{4} \]

2) Shuffle Phase: To compute the output function $\phi_k$, Node $k$ needs to collect the IVAs required by $\phi_k$ but not computed locally in the map phase, i.e., $\mathcal{V}_k \setminus C_k$. After the map phase, the $K$ nodes proceed to exchange the required IVAs.

To be precise, each node $k$ creates a signal $X_k \in \mathbb{F}_{2^l_k}$ for some $l_k \in \mathbb{N}$, as a function of the IVAs computed in the map phase, i.e., for some encoding function
\[ \varphi_k : \mathbb{F}_{2^l_k} \rightarrow \mathbb{F}_{2^l_k}. \]

Node $k$ multicasts the signal
\[ X_k = \varphi_k(C_k), \]
to all the other nodes. Then we assume that each node $k$ receives the signals $\{ X_i \}_{i \in [K] \setminus \{ k \}}$ without error.

**Definition 3** (Communication Load). We define the communication load $L$, as the total length of the bits transmitted by the $K$ nodes during the shuffle phase normalized by the total length of all intermediate values $NK$, i.e.,
\[ L \triangleq \sum_{k=1}^{K} \frac{L_k}{NK}. \]

3) Reduce Phase: With the signals $\{ X_i \}_{i \in [K]}$, exchanged in the shuffle phase and the IVAs $C_k$ computed locally, Node $k$ constructs all the IVAs $\mathcal{V}_k$ using some decoding function
\[ \psi_k : \mathbb{F}_{2^l_1} \times \mathbb{F}_{2^l_2} \times \cdots \times \mathbb{F}_{2^l_K} \rightarrow \mathbb{F}_{2^T}, \]
So Node $k$ computes
\[ (v_{k,1}, \ldots, v_{k,N}) = \psi_k(X_1, \ldots, X_K, C_k), \]
followed by the reduce function
\[ u_k = h_k(v_{k,1}, \ldots, v_{k,N}). \tag{5} \]

**Definition 4.** A distributed computing system achieves a storage-computation-communication (SCC) triple $(r, c, L)$ if, for any $\epsilon > 0$, when $N$ is sufficiently large, there exists a map-shuffle-reduce procedure such that the storage space, computation and communication loads do not exceed $r + c + \epsilon$, and $L + \epsilon$, respectively. In particular, define the optimal communication load as
\[ L^*(r, c) \triangleq \inf \{ L : (r, c, L) \text{ is achievable} \}. \tag{6} \]

Note that it is without loss of generality to consider the case
\[ 1 \leq c \leq r < K. \]
Indeed, as $|C_k| \leq |M_k|K$ by (2), thus $c \leq r$ from (3) and (4). Further, since each IVA needs to be computed at least once somewhere in the system, we have $c \geq 1$. Finally, if $r \geq K$, each node stores all files and can locally compute all the IVAs required for its output function.

**Remark 1.** If each Node $k$ computes the IVAs required by all output functions from all the files that it stores, i.e., $C_k = \{ v_{q,n} : q \in [K], w_n \in M_k \}$, then $|C_k| = |M_k| \cdot K$, and
\[ c = \sum_{k=1}^{K} \frac{|C_k|}{NK} = \sum_{k=1}^{K} \frac{|M_k| \cdot K}{NK} = r. \]
This is exactly the case investigated in [3], in which the authors established the fundamental storage-computation tradeoff. The optimal tradeoff curve is given by the lower
convex envelope of \( \{ (r, L^*(r)) : r = 1, \cdots, K \} \), where
\[
L^*(r) = \frac{1}{r} \left( 1 - \frac{r}{K} \right).
\]
In other words, the optimal computation load in (6) is known for the case \( c = r \) where \( L^*(r, r) = L^*(r) \). Intuitively, however, computing the IV As for all other nodes may be highly redundant across the whole system. This has motivated our investigation in the more general case where the computation load \( c \) can be strictly smaller than the storage space \( r \).

### III. MAIN RESULT

**Theorem 1.** In a distributed computing system with \( K \) nodes, for any storage space \( r \in [1, K) \), and
\[
c \in \left\{ \frac{r}{K} + \left( 1 - \frac{r}{K} \right) g : g = 1, 2, \cdots, [r] \right\},
\]
the following communication load \( L(r, c) \) is achievable:
\[
L(r, c) = \frac{1}{c - r/\lfloor r \rfloor} \left( 1 - \frac{r}{K} \right)^2.
\]
Define
\[
c^*(r) = \frac{r}{K} + \left( 1 - \frac{r}{K} \right) \cdot g_r,
\]
where
\[
g_r = \lfloor r \rfloor + \frac{(r - \lfloor r \rfloor)(K - \lfloor r \rfloor)}{K - r}.
\]
For \( c^*(r) \leq c \leq r \), \( L^*(r) \) in (7) is achievable.

For general \( c \in [1, c^*(r)] \), the lower convex envelope of (8) and the point \( (c^*(r), L^*(r)) \) is achievable, where \( L^*(r) \) is given by (7).

Notice that for any fixed \( r \in [1, K) \), the achievable communication load \( L(r, c) \) is a piecewise linear function over \( c \in [0, r] \). Fig. 1 illustrates the case \( K = 10, r = 4.5 \). When \( c \) increases from 1 to \( c^*(r) \), the communication load decreases from 1 to \( L^*(r) \). The communication load becomes constant when \( c \geq c^*(r) \). When \( c \in [c^*(r), r] \), from Theorem 1 we have \( L^*(r, c) \leq L^*(r) = L^*(r, r) \), and thus
\[
L^*(r, r) \geq L^*(r, c) \geq L^*(r, r),
\]
where the last inequality holds because \( L^*(r, c) \) is (non-strictly) decreasing respect to \( c \). Hence, we establish the following optimality.

**Corollary 1.** In a distributed computing system with \( K \) nodes and storage space \( r \in [1, K) \), for \( c \in [c^*(r), r] \),
\[
L^*(r, c) = L^*(r).
\]

Thus, for fixed \( r \in [1, K) \), to achieve the optimal communication load \( L^*(r) \), computation load \( c^*(r) \) suffices. This computation load is significantly lower than when each node computes IV As for all output functions from all its stored files (Remark 1). The following example illustrates this saving.

**Example 1.** Let \( K = 2, r = 2 \), and \( N = 6 \). Consider Fig. 2. The top-most line in each of the three boxes indicates the files stored at the node. Below this line, the computed IV As are depicted, where red circles indicate IV As \( \{v_{1,1}, \cdots, v_{1,6}\} \), green squares IV As \( \{v_{2,1}, \cdots, v_{2,6}\} \), and blue triangles IV As \( \{v_{3,1}, \cdots, v_{3,6}\} \). Nodes 1, 2 and 3 need to collect the IV As denoted by the circles, squares and triangles respectively. The last line of each box indicates the IV As that the node needs to learn during the shuffling phase.

The dashed circles/squares/triangles stand for the IV As that would be computed in addition in the CDC scheme, see [8]. Example 1]. In other words, they represent the saving in computation load. In fact for this example, D3C and CDC have computation loads \( \frac{2}{3} \) and 2, respectively.

To visualize savings in computation load, we plot \( c^*(r) \) as a function of the storage space \( r \) as well as the line \( c = r \) in Fig. 3. It implies that when the storage space exceeds a certain threshold, the necessary computation load is decreasing in the storage space. This is because with larger storage space, more IV As can be locally computed by the nodes requesting them, thus each node computes less IV As for coded communication, which consumes more computation.

We will prove Theorem 1 by proposing a distributed computing scheme, termed **distributed computing and coded communication (D3C) scheme**. For \( c = c^*(r) \), the shuffle phase of D3C scheme specializes to the shuffle phase of the CDC scheme. But the map phase of our D3C scheme only computes those IV As that are necessary for communication and decoding. This special case has been investigated in [8].
and similar conclusions are obtained there.

IV. DISTRIBUTED COMPUTING AND CODED COMMUNICATION SCHEME

In this section, we describe the proposed scheme that achieves the result in Theorem \[ \square \]
Define

\[ g = \frac{c - r}{1 - r/K}. \]

We will first present the scheme for the case \( r \in \{1, \cdots, K\} \), and \( g \in \{1, \cdots, r\} \), which will be referred to as the basic D3C scheme hereafter. Then we will extend the basic D3C scheme to obtain the achievability for other cases.

For the case \( r \in \{1, \cdots, K\} \), \( g \in \{1, \cdots, r\} \), the \( N \) input files are partitioned into \( \binom{K}{r} \binom{r}{g} \) batches, each containing

\[ \eta_g := \frac{N}{\binom{K}{r} \binom{r}{g}}. \]  \hspace{1cm} (11)

files. Associate each batch with an element \((S, T)\) of the set

\[ \Omega := \{(S, T): T \subset S \subset [K], \ |S| = r, \ |T| = g\}, \]

and let \( W_{S, T} \) denote the batch of \( \eta_g \) files associated to a pair \((S, T)\). Notice that

\[ W := \{w_1, \cdots, w_N\} = \bigcup_{(S, T) \in \Omega} W_{S, T}. \]

Let further \( U_{k, S, T} \) be the set of IVAs for output function \( \phi_k \) that can be computed from the files in \( W_{S, T} \):

\[ U_{k, S, T} := \{v_{k,n}: n \in [N] \text{ so that } w_n \in W_{S, T}\}. \]

We now describe the three distributed-computing phases.

1) **Map phase**: Node \( k \) stores the files in \( W_{S, T} \) if and only if \( k \in S \). Thus:

\[ M_k = \bigcup_{(S, T) \in \Omega: k \in S} W_{S, T}. \]

Node \( k \) further computes the IVAs in

\[ C_k = C_k^1 \cup C_k^2, \]  \hspace{1cm} (12)

where

\[ C_k^1 := \bigcup_{(S, T) \in \Omega: k \in S} U_{k, S, T}, \]  \hspace{1cm} (13)

\[ C_k^2 := \bigcup_{(S, T) \in \Omega: q \in \{K\} \setminus S} U_{q, S, T}. \]  \hspace{1cm} (14)

It’s easy to verify that all the IVAs in \( C_k \) can be computed from \( M_k \), i.e., the files stored at Node \( k \).

2) **Shuffling Phase**: For each element \((S, T) \in \Omega \) and each index \( j \in [K] \setminus S \), we partition the set \( U_{j, S, T} \) into \( g \) equal smaller subsets

\[ U_{j, S, T} = \{U_{j, S, T}^k: k \in T\}. \]

Define now the set

\[ \Pi := \{(I, J): J \subset I \subset [K], \ |I| = r + 1, \ |J| = g + 1\}. \]

For each \((I, J) \in \Pi \) and \( k \in J \), by (14), Node \( k \) can compute the signal

\[ X_{k, I, J}^k := \bigoplus_{i \in J \setminus \{k\}} U_{k, I \setminus \{i\}, J \setminus \{i\}} \]

from the IVAs calculated during the map phase. A given Node \( k \) thus sends (shuffles) the multicast signal

\[ X_k = \{X_{k, I, J}^k: (I, J) \in \Pi \text{ such that } k \in J\}. \]

3) **Reduce Phase**: Notice that \( C_k^2 \) only contains IVAs \( v_{q,n} \) where \( q \neq k \). Thus, by (12)–(13), during the shuffling phase each node \( k \) needs to learn all IVAs in

\[ \bigcup_{(S, T) \in \Omega: k \notin S} U_{k, S, T}. \]

Fix an arbitrary pair \((S, T) \in \Omega \) such that \( k \notin S \) and an element \( j \in T \). From the received multicast message \( X_{S \cup \{k\}, T \cup \{k\}}^j \) during the shuffling phase and its locally calculated IVAs during the map phase

\[ \{U_{i, S \cup \{k\} \setminus \{i\}, T \cup \{k\} \setminus \{i\}}: i \in T \setminus \{j\}\}, \]

Node \( k \) can recover the missing IVAs \( U_{k, S, T}^j \) through a simple XOR operation. After collecting all missing IVAs, Node \( k \) can proceed to compute the reduce function \( [\square] \).

4) **Analysis**: We analyze the performance of the scheme.

1) **Storage Space**: The number of batches in \( M_k \) is

\[ \binom{K-1}{r-1} \binom{r}{g}, \]

each consisting of \( \eta_g \) files. Thus, the storage space is

\[ \frac{1}{N} \cdot K \cdot \binom{K - 1}{r - 1} \binom{r}{g} \cdot \eta_g = r. \]

2) **Computation Load**: Since \( C_k^1 \cap C_k^2 = \emptyset \):

\[ |C_k| = |C_k^1| + |C_k^2|. \]  \hspace{1cm} (15)
By (11), (13), (14), we have
\[ |C_k^1| = \frac{(K-1)}{r-1} \cdot g \cdot \eta_g = \frac{rN}{K}, \]
(16)
\[ |C_k^2| = \frac{(K-1)}{r-1} \cdot g - 1 \cdot (K-r) \cdot \eta_g \]
\[ = \left(1 - \frac{r}{K}\right) \cdot g \cdot N. \]  
(17)

Thus, by (15), (16) and (17), the computation load is
\[ c = \frac{\sum_{k=1}^{K} |C_k|}{NK} = \frac{r}{K} \left(1 - \frac{r}{K}\right) g. \]
(18)

3) Communication Load: The number of signals that each Node $k$ transmits is $\binom{K-1}{r} \cdot \binom{r}{g}$, each of size $\frac{gNT}{g}$ bits. Thus, the communication load is
\[ L = \frac{\sum_{k=1}^{K} |X_k|}{NK} = \frac{1}{NK} \cdot K \left(\frac{K-1}{r}\right) \left(\frac{r}{g}\right) \cdot \left(\frac{gNT}{g}\right) \cdot \frac{1}{g} \]
\[ = \frac{1}{g} \cdot \left(1 - \frac{r}{K}\right) \]
\[ = \frac{1}{c - r/K} \left(1 - \frac{r}{K}\right)^2, \]
where $c$ follows from (18).

This concludes the achievability proof for $r \in \{1, \cdots, K\}$, $g \in \{1, \cdots, r\}$. We complete the proof by proposing several extensions of the basic D3C scheme:

E1. For any $r \in [1, K]$, and $g \in [1, \cdots, r]$, there exist an unique $\alpha \in [0, 1)$ such that
\[ r = (1 - \alpha)\lceil r \rceil + \alpha \lfloor r \rfloor. \]

We partition the $N$ files into two groups with cardinalities $(1 - \alpha)N$ and $\alpha N$ respectively. For the first group, implement the basic D3C scheme with a storage space $\lceil r \rceil$, and computing load $c_1 \triangleq \frac{r}{K} + (1 - \frac{r}{K})g$. For the second group, implement the basic D3C scheme with a storage space $\lfloor r \rfloor$, and computing load $c_2 \triangleq \frac{r}{K} + (1 - \frac{r}{K})g$. This results the achievability of (9), when $r \in [1, K]$, $g \in \{1, \cdots, r\}$.

E2. For any $r \in [1, K]$ and for $g \in [1, \lfloor r \rfloor]$, there exists an unique $\beta \in [0, 1)$ such that
\[ g = (1 - \beta)\lfloor g \rfloor + \beta \lceil g \rceil. \]

We partition the $N$ files into two groups with cardinalities $(1 - \beta)N$ and $\beta N$ respectively. For the first group, implement the scheme in E1 with a storage space $r$, and computing load $c_1' \triangleq \frac{r}{K} + (1 - \frac{r}{K})\lceil g \rceil$. For the second group, implement the scheme in E1 with a storage space $r$, and computing load $c_2' \triangleq \frac{r}{K} + (1 - \frac{r}{K})\lfloor g \rfloor$. Notice that, $\beta$ satisfies $c = (1 - \beta)c_1' + \beta c_2'$. Thus, this results to the achievability of the lower convex envelop in (9) of Theorem 1.

E3. When $r \notin \mathbb{N}$, $g \in [\lceil r \rceil, \lfloor r \rfloor]$, then there exists an unique $\theta \in (0, r - \lceil r \rceil]$ such that
\[ c = \frac{r}{K} + \left(1 - \frac{r}{K}\right) \left(\lceil r \rceil + \theta \cdot K - \lfloor r \rfloor\right). \]

Define $r'(\theta) \triangleq \frac{r - \theta}{1 - \theta}$, $\lfloor r' \rfloor$, or equivalently,
\[ r = (1 - \theta) \cdot r'(\theta) + \theta \cdot \lfloor r \rfloor. \]

Notice that $r'(\theta)$ decreases with $\theta$, so $r'(\theta) \geq r'(\theta)|_{\theta=r-\lfloor r \rfloor} = \lfloor r \rfloor$.

We partition the $N$ files into two groups with cardinalities $(1 - \theta)N$ and $\theta N$ respectively. For the first group, implement the scheme in E1 with a storage space $r'(\theta)$, and computation load $\frac{r}{K} + \left(1 - \frac{r}{K}\right)\lceil r \rceil$. For the second group, implement the basic D3C scheme with a storage space $\lfloor r \rfloor$, and computation load $\frac{r}{K} + (1 - \frac{r}{K})\lfloor r \rfloor$. This results the achievability when $r \notin \mathbb{N}$, $\lfloor r \rfloor < g \leq \lfloor r \rfloor$.

Finally, when $g > g_r$, the load $L'(r)$ can be achieved with the scheme at $g = g_r$.

V. CONCLUSION

We proposed a new scheme (the D3C scheme) for distributed computing. It leverages a flexible tradeoff between the three elementary resources storage space, computation load, and communication load. In fact, the D3C scheme achieves the best communication load for any feasible storage space and computation load, since a matching converse has been established in a following-up work [9].

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