Extended Product and Integrated Interleaved Codes

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Abstract

A new class of codes, Extended Product (EPC) Codes, consisting of a product code with a number of extra parities added, is presented and applications for erasure decoding are discussed. An upper bound on the minimum distance of EPC codes is given, as well as constructions meeting the bound for some relevant cases. A special case of EPC codes, Extended Integrated Interleaved (EI) codes, which naturally unify Integrated Interleaved (II) codes and product codes, is defined and studied in detail. It is shown that EI codes often improve the minimum distance of II codes with the same rate, and they enhance the decoding algorithm by allowing decoding on columns as well as on rows. It is also shown that EI codes allow for encoding II codes with an uniform distribution of the parity symbols.

Index Terms

Erasure-correcting codes, product codes, Reed-Solomon (RS) codes, generalized concatenated codes, integrated interleaving, MDS codes, PMDS codes, maximally recoverable codes, local and global parities, heavy parities, locally recoverable (LRC) codes.

I. INTRODUCTION

There has been considerable research in recent literature on codes with local and global properties for erasure correction (see for instance [1], [2], [4], [14], [21], [25], [33]–[39], [41] and references within). In general, data symbols are divided into sets and parity symbols (i.e., local parities) are added to each set (often using an MDS code). This way, when a number of erasures not exceeding the number of parity symbols occurs in a set, such erasures are rapidly recovered. In addition to the local parities, a number of global parities (also called heavy parities) are added. Those global parities involve all of the data symbols and may include the local parity symbols as well. The goal of the global parities is to correct situations in which the erasure-correcting power of the local parities has been exceeded.

The interest in erasure correcting codes with local and global properties arises mainly from two applications. One of them is the cloud. A cloud configuration may consist of many storage devices, of which some of them may even be in different geographical locations, and the data is distributed across them. If one or more of those devices fails, it is desirable to recover its contents “locally,” that is, using a few parity devices within a set of limited size in order to affect performance as little as possible. However, the local parities may not suffice. Extra protection is needed in case the erasure-correcting capability of a local set is exceeded. To address this situation, some devices consisting of global parities are incorporated, and when the local correction power is exceeded, the global parity devices are invoked and correction is attempted. If such a situation occurs, although there will be an impact on performance, data loss may be averted. It is expected that the cases in which the local parity is exceeded are relatively rare events, so the aforementioned impact on performance does not occur frequently. As an example of this type of application, we refer the reader to the description of the Azure system [22] or to the Xorbas code presented in [37].

A second application occurs in the context of Redundant Arrays of Independent Disks (RAID) architectures [10]. In this case, a RAID architecture protects against one or more storage device failures. For example, RAID 5 adds one extra parity device, allowing for the recovery of the contents of one device failed device, while RAID 6 protects against up to two device failures. In particular, if those devices are Solid State Drives (SSDs), like flash memories, their reliability decays with time and with the number of writes and reads [29]. The information in SSDs is generally divided into pages, each page containing its own internal Error-Correction Code (ECC). It may happen that a particular page degrades and its ECC is exceeded. However, the user may not become aware of this situation until the page is accessed (what is known as a silent failure). Assuming an SSD has failed in a RAID 5 scheme, if during reconstruction a silent page failure is encountered in one of the surviving SSDs, then data loss will occur. A method around this situation is using RAID 6. However, this method is costly, since it requires two whole SSDs as parity. It is more desirable to divide the information in a RAID type of architecture into \( m \times n \) stripes: \( m \) represents the size of a stripe, and \( n \) is the number of SSDs. The RAID architecture may be viewed as consisting of a large number of stripes, each stripe encoded and decoded independently. Certainly, codes like the ones used in cloud applications may be used as well for RAID applications. In practice, the choice of code depends on the statistics of errors and on the
frequency of silent page failures. RAID systems, however, may behave differently than a cloud array of devices, in the sense that each column represents a whole storage device. When a device fails, then the whole column is lost, a correlation that may not occur in cloud applications. For that reason, RAID architectures may benefit from a special class of codes with local and global properties, the so-called Sector-Disk (SD) codes, which take into account such correlations\cite{20}, \cite{27}, \cite{31}, \cite{32}.

From now on, we will call the entries of the codes considered in the paper “symbols”. Such symbols can be whole devices (for example, in the case of cloud applications) or pages (in the case of RAID applications for SSDs). Each symbol may be protected by one local group, but a natural extension is to consider multiple localities\cite{36}, \cite{39}, \cite{43}. Product codes\cite{28} represent a special case of multiple localities: any symbol is protected by both horizontal and vertical parities.

Product codes by themselves may also be used in RAID-type of architectures: the horizontal parities protect a number of devices from failure. The vertical parities allow for rapid recovery of a page or sector within a device (a first responder type of approach). However, if the number of silent failures exceeds the correcting capability of the vertical code, and the horizontal code is unusable due to device failure, data loss will occur. For that reason, it may be convenient to incorporate a number of extra global parities to the product code. In general, we will simply call extra parities these extra global parities in order to avoid confusion, since in a product code the parities on the parities by affecting all of the symbols, can also be considered as global parities.

In effect, consider a product code consisting of \(m \times n\) arrays such that each column has \(v\) parity symbols and each row has \(h\) parity symbols. If in addition to the horizontal and vertical parities there are \(g\) extra parities, we say that the code is an Extended Product (EPC) code and we denote it by \(EP(m,v;n,h;g)\). Notice that, in particular, \(EP(m,v;n,h;0)\) is a regular product code, while \(EP(m,0;n,h;g)\) is a Locally Recoverable (LRC) code\cite{13}, \cite{39}.

Constructions of LRC codes involve different issues and tradeoffs, like the size of the field and optimality criteria. The same is true for EPC codes, of which, as we have seen above, LRC codes are a special case. In particular, one goal is to keep the size of the required finite field small, since operations over a small field have less complexity than ones over a larger field due to the smaller look-up tables involved. For example, Integrated Interleaved (II) codes\cite{13}, \cite{40} over \(GF(q)\), where \(q > \max\{m,n\}\), were proposed in \cite{2} as LRC codes (II codes are closely related to Generalized Concatenated Codes\cite{6}, \cite{45}). Let us mention also the construction in \cite{20} (STAIR codes), which reduces field size when failures are correlated. Similarly, we propose a new family of codes that we call Extended Integrated Interleaved (EII) codes, to be defined in Section III of which both product codes and II codes are special cases. In earlier versions, we called such codes Generalized Product Codes\cite{3}. However, there are several ways of generalizing product codes, and the term Generalized Product Code usually refers to graph theoretic constructions\cite{15}, \cite{17}. The new denomination avoids confusion.

As is the case with LRC codes, construction of EPC codes involves optimality issues. For example, LRC codes optimizing the minimum distance were presented in \cite{39}. Except for special cases, II codes are not optimal as LRC codes, but the codes in \cite{39} require a field of size at least \(mn\), so there is a tradeoff. The same happens with EII codes: except for special cases to be presented in Section III they do not optimize the minimum distance.

There are stronger criteria for optimization than the minimum distance in LRC codes. For example, PMDS codes\cite{1}, \cite{4}, \cite{13}, \cite{20}, \cite{22} satisfy the Maximally Recoverable (MR) property\cite{13}, \cite{15}. The definition of the MR property is extended for EPC codes in \cite{15}, but it turns out that EPC codes with the MR property are difficult to obtain. For example, in \cite{15} it was proven that an EPC code \(EP(n,1;n,1;1)\) (i.e., one vertical and one horizontal parity per column and row and one extra parity) with the MR property requires a field whose size is superlinear on \(n\). We do not address EPC codes with the MR property in this paper.

Although the constructions we present can be extended to finite fields of any characteristic, for simplicity, we assume that they have characteristic 2.

The paper is structured as follows: in Section II we present the definition of EII codes and give their properties, like their erasure-correcting capability, their minimum distance and encoding and decoding algorithms. We also show that EII codes effectively enhance the decoding power of regular II codes, by allowing decoding on rows as well as on columns. As another application of EII codes, we show that II codes admit a balanced distribution of parity symbols. In Section III, we present an upper bound on the minimum distance of EII codes. We show that this bound generalizes the known bound on the minimum distance of LRC codes. We also present some constructions of EPC codes optimizing the minimum distance for \(E(m,1;n,1;g)\) codes. We end the paper by drawing some conclusions.

II. \textsc{Extended Integrated Interleaved (EII) Codes}

This section is divided into subsections as follows: in Subsection II-A we give the definition of EII codes and we illustrate it with several examples. In Subsection II-B we present the main (erasure) decoding algorithm of EII codes consisting of a triangulation process. In Subsection II-C we give the dimension and the minimum distance of EII codes, as well as an encoding algorithm. In Subsection II-D we show that the transpose arrays of the arrays in an EII code also constitute an EII code, and this property allows for an enhancement of the decoding algorithm, since arrays can now be iteratively decoded on rows as well as on columns, a process that generalizes the well known row-column iterative decoding of product codes. As a second application of this property, we show that EII codes allow for an uniform distribution of the parity symbols. In Subsection II-E we show how to extend the erasure decoding algorithm to errors together with erasures.
A. Definition of Extended Integrated Interleaved (EII) Codes

We start by defining EII codes, which unify product codes and II codes. II codes may be interpreted as \( m \times n \) arrays such that each row belongs in a code \( C_0 \), and certain linear combinations of the rows belong in nested subcodes of \( C_0 \) \([2], [40], [42], [44]\). In addition, we assume that each column is also in a (vertical) code, making the arrays a subcode of a product code. We assume that the individual codes are Reed-Solomon \([28]\) (RS) type of codes. Explicitly,

**Definition 1.** Take \( t + 1 \) integers

\[
0 \leq u_0 < u_1 < \ldots < u_{t-1} < u_t = n
\]

and let \( \underline{u} \) be the following vector of length \( m = s_0 + s_1 + \cdots + s_{t-1} + s_t \), where \( s_i \geq 1 \) for \( 0 \leq i \leq t - 1 \) and \( s_t \geq 0 \):

\[
\underline{u} = \left( s_0, u_0, u_1, \ldots, u_{t-1}, u_{t-1}, \ldots, u_1, u_0, s_t \right). \tag{1}
\]

Consider a set \( \{ C_i \} \) of \( t \) nested \( [n, n - u_i, u_i + 1] \), \( 0 \leq i \leq t - 1 \), RS codes with elements in a finite field \( GF(q) \), \( q > \max\{m, n\} \), such that a parity-check matrix for \( C_i \) is given by

\[
H_{u_i} = \begin{pmatrix}
1 & 1 & 1 & \cdots & 1 \\
1 & \alpha & \alpha^2 & \cdots & \alpha^{n-1} \\
1 & \alpha^2 & \alpha^4 & \cdots & \alpha^{2(n-1)} \\
\vdots & \vdots & \vdots & \ddots & \vdots \\
1 & \alpha^{u_i-1} & \alpha^{2(u_i-1)} & \cdots & \alpha^{(u_i-1)(n-1)}
\end{pmatrix} \tag{2}
\]

where \( \alpha \) is an element of order \( O(\alpha) = \max\{m, n\} \) in \( GF(q) \). Assume also that \( C_t = \{ \emptyset \} \).

Let \( C(n, \underline{u}) \) be the code consisting of \( m \times n \) arrays over \( GF(q) \) such that, for each array in the code with rows \( \underline{c}_0, \underline{c}_1, \ldots, \underline{c}_{n-1} \), \( \underline{c}_j \in C_0 \) for \( 0 \leq j \leq m - 1 \) and, if

\[
\hat{s}_i = \sum_{j=i}^{t} s_j \quad \text{for} \quad 0 \leq i \leq t,
\]

then

\[
\bigoplus_{j=0}^{m-1} \alpha^{\hat{s}_j} \underline{c}_j \in C_{t-i} \quad \text{for} \quad 0 \leq i \leq t - 1 \quad \text{and} \quad 0 \leq r \leq \hat{s}_{t-i} - 1. \tag{4}
\]

Then we say that \( C(n, \underline{u}) \) is a \( t \)-level Extended Integrated Interleaved (EII) code.

In Definition 1 notice that, if \( u_0 = 0 \), then the code \( C_0 \) is an \( [n, n, 1] \) code, that is, the whole space, with no erasure-correcting capabilities. Let us mention the recent work in \([44]\) on II codes, in which the definition is modified to improve the locality when only one row has erasures, but the number of erasures in such row exceeds \( u_0 \).

Before giving the properties of \( t \)-level EII codes, we present some examples.

**Example 2.** Assume that \( s_t = 0 \) in Definition 1 then, in \([44]\), \( i \geq 1 \) and \( C(n, \underline{u}) \) is a \( t \)-level II code \([2], [40], [42]\). So, \( t \)-level II codes can be viewed as a special case of \( t \)-level EII codes.

In \([40], [42]\), when \( t > 2 \), II codes are called Generalized Integrated Interleaved (GII) codes, while II codes refer to the case \( t = 2 \). The reason for this denomination is historical, since the first paper on II codes \([18]\) describes the case \( t = 2 \) only.

**Example 3.** Assume that \( t = 1 \), then 1 gives \( \underline{u} = \left( s_0, u_0, \ldots, u_0, u_1, \ldots, n, n, \ldots, n \right) \). If \( s_1 > 0 \), \( C(n, \underline{u}) \) is a regular product code \([28]\) such that each row is in an \( [n, n - u_0] \) code and each column in an \( [m, m - s_1] \) code. Thus, product codes can be viewed as a special case of \( t \)-level EII codes.

**Example 4.** Assume that \( t = 2 \). Then, \( C_1 \subset C_0 \),

\[
\underline{u} = \left( s_0, u_0, \ldots, u_0, s_1, u_1, \ldots, n, n, \ldots, n, s_2, u_2, \ldots, n \right),
\]
Moreover, (5) and (6) give the minimum distance up from 4 to 6. For instance, if \( m \) presented below. It consists of a product code (which has minimum distance 4) plus one extra parity. This extra parity brings (hence, \( s \))

\[
\begin{align*}
\bigoplus_{j=0}^{m-1} \alpha^{ij} \mathbf{c}_j &= 0 \text{ for } 0 \leq r \leq s_2 - 1 \\
\bigoplus_{j=0}^{m-1} \alpha^{ij} \mathbf{c}_j &\in \mathcal{C}_1 \text{ for } 0 \leq r \leq s_1 + s_2 - 1
\end{align*}
\]

The 2-level II codes presented in [18] correspond to \( s_2 = 0 \) in this example, i.e., only equations (6) are taken into account. As a special case of 2-level EII codes, take

\[
\mathbf{u} = \begin{pmatrix} m-2 \\ 1, 1, \ldots, 1, n \end{pmatrix}
\]

(hence, \( s_0 = m - 2, s_1 = s_2 = 1 \)). The rows \( \mathbf{c}_0, \mathbf{c}_1, \ldots, \mathbf{c}_{n-1} \) of \( \mathcal{C}(n, \mathbf{u}) \) constitute a 2-level II code. Each column is in an \([m, m - 1, 2]\) code and each row is in an \([n, n - 1, 2]\) code (single parity). The \( \mathcal{C}_0 \) code is the \([n, n - 1, 2]\) code, and the \( \mathcal{C}_1 \) code is the \([n, n - 2, 3]\) code given, according to [2], by the parity-check matrix

\[
H_1 = \begin{pmatrix} 1 & 1 & 1 & \ldots & 1 \\ 1 & \alpha & \alpha^2 & \ldots & \alpha^{n-1} \end{pmatrix}
\]

Moreover, (5) and (6) give

\[
\begin{align*}
\bigoplus_{i=0}^{m-1} \mathbf{c}_i &= 0 \\
\bigoplus_{i=0}^{m-1} \alpha^i \mathbf{c}_i &\in \mathcal{C}_1.
\end{align*}
\]

It is not hard to prove directly that this code can correct any 5 erasures, but this will be a consequence of Theorem [18] to be presented below. It consists of a product code (which has minimum distance 4) plus one extra parity. This extra parity brings the minimum distance up from 4 to 6. For instance, if \( m = 4 \) and \( n = 5 \), erasure patterns like the following (vertices of a rectangle)

\[
\begin{array}{ccc}
E & E & E \\
E & & E
\end{array}
\]

are uncorrectable by the product code but can be corrected by \( \mathcal{C}(5, (1, 1, 2, 5)) \). An extra erasure in addition to the four depicted above can be corrected by either the horizontal or the vertical code.

\[\square\]

**Example 5.** Assume that \( t = 3 \). Then, \( \mathcal{C}_2 \subset \mathcal{C}_1 \subset \mathcal{C}_0 \).

\[
\mathbf{u} = \begin{pmatrix} s_0 \\ u_0, u_0, \ldots, u_0, u_1, u_1, \ldots, u_1, u_2, u_2, \ldots, u_2, u_3, u_3, \ldots, u_3 \end{pmatrix}
\]

\( s_0 + s_1 + s_2 + s_3 = m \), and consider the 3-level EII code \( \mathcal{C}(n, \mathbf{u}) \). Let \( \mathbf{c} = (\mathbf{c}_0, \mathbf{c}_1, \ldots, \mathbf{c}_{n-1}) \) be an \( m \times n \) array in \( \mathcal{C}(n, \mathbf{u}) \). Then, \( \mathbf{c}_j \in \mathcal{C}_0 \) for each \( 0 \leq j \leq m - 1 \), and (3) gives

\[
\begin{align*}
\bigoplus_{j=0}^{m-1} \alpha^{ij} \mathbf{c}_j &= 0 \text{ for } 0 \leq r \leq s_3 - 1 \\
\bigoplus_{j=0}^{m-1} \alpha^{ij} \mathbf{c}_j &\in \mathcal{C}_2 \text{ for } 0 \leq r \leq s_2 + s_3 - 1 \\
\bigoplus_{j=0}^{m-1} \alpha^{ij} \mathbf{c}_j &\in \mathcal{C}_1 \text{ for } 0 \leq r \leq s_1 + s_2 + s_3 - 1
\end{align*}
\]

satisfied by the vertices of a rectangle.
In Definition 1 of EII codes, we have assumed that the nested codes are RS codes. In [42], the construction of II codes was adapted to include binary BCH codes. In order to replace the component codes by binary BCH codes, the $\gamma$'s cannot be multiplied by powers of $a$ in (4), since doing so would take us out of the binary field. This problem is overcome in [42] by replacing the powers of $a$ by powers of $x$ modulo the primitive polynomial that defines the finite field $GF(q)$. In future work, we will show how to adapt EII codes to any arbitrary set of nested codes.

Although the nested codes in Definition 1 are RS codes, they can be other types of MDS codes as well, like Extended RS codes [28] or Blum-Roth (BR) [5] codes. For simplicity, we concentrate on RS codes.

B. Erasure Decoding of EII Codes

We are now ready to state the main result regarding EII codes.

**Theorem 6.** Consider an $m \times n$ array corresponding to a $C(n, \mu)$ $t$-level EII code as given by Definition 1. Then, the code can correct up to $u_0$ erasures in any row and up to $u_i$ erasures in any $s_i$ rows, where $1 \leq i \leq t$.

**Proof:** We may assume that the rows with erasures contain more than $u_0$ erasures, since each row is in $C_0$, which is an $[n, n - u_0, u_0 + 1]$ code, hence, rows with up to $u_0$ erasures can be corrected.

Assume that there are $\ell$ rows with more than $u_0$ erasures such that there are up to $u_i$ erasures in any up to $s_i$ rows, $1 \leq i \leq t$. We do induction on $\ell$.

If $\ell = 0$, there is nothing to prove, so, assume that there are $\ell \geq 1$ rows with more than $u_0$ erasures each such that there are up to $u_i$ erasures in any up to $s_i$ rows, $1 \leq i \leq t$. In particular, $\ell \leq s_1 + s_2 + \ldots + s_t = \hat{s}_1$. By induction, up to $\ell - 1$ rows with this property are correctable.

Let $i_0, i_1, \ldots, i_{m-1}$ be an ordering of the rows according to a non-increasing number of erasures such that:

1) Row $i_j$ for $0 \leq j \leq \ell - 1$ has $v_j$ erasures, where $n \geq v_0 \geq v_1 \geq \ldots \geq v_{\ell-1} > u_0$.

2) Rows $i_{t}, i_{t+1}, \ldots, i_{m-1}$ have no erasures.

It suffices to prove that the $v_{\ell-1}$ erasures in row $i_{\ell-1}$ can be corrected. Then we will be left with $\ell - 1$ rows with more than $s_0$ erasures each such that there are up to $u_i$ erasures in any up to $s_i$ rows, $1 \leq i \leq t$, and the result follows by induction.

In effect, define $w$, $1 \leq w \leq \ell - 1$, such that

$$\hat{s}_{w+1} = \sum_{i=1}^{w} s_{w+i} < \ell \leq \sum_{i=0}^{w} s_{w+i} = \hat{s}_w$$

and consider the code $C_w$ from the nested set of codes $C_i$ in Definition 1 which can correct up to $u_w$ erasures. Since there are up to $u_w$ erasures in any up to $s_w$ rows, given that the $v_j$'s are non-increasing and $0 < \ell - \hat{s}_{w+1} = s_w$, then $v_j \leq u_w$ for $\hat{s}_{w+1} \leq j \leq \ell - 1$. In particular, $v_{\ell-1} \leq u_w$.

Rearranging the order of the elements of the sums in (4), and since $C_{t} \subset C_{t-1} \subset \cdots \subset C_w$, from (4), in particular, we have

$$\bigoplus_{j=0}^{m-1} a^{rj} \mathcal{E}_j \in C_w \quad \text{for} \quad 0 \leq r \leq \ell - 1. \quad \text{(11)}$$

Since the $\ell \times m$ matrix corresponding to the coefficients of the $\mathcal{E}_j$'s in (11) is a Vandermonde type of matrix and $O(a) \geq \max\{m, n\}$, this matrix can be triangulated, giving

$$\mathcal{E}_r \oplus \left( \bigoplus_{j=r+1}^{m-1} \gamma_{r,j} \mathcal{E}_j \right) \in C_w \quad \text{for} \quad 0 \leq r \leq \ell - 1, \quad \text{(12)}$$

where the coefficients $\gamma_{r,j}$ are a result of the triangulation. In particular, taking $r = \ell - 1$ in (12), we obtain

$$\mathcal{E}_{\ell-1} \oplus \left( \bigoplus_{j=\ell}^{m-1} \gamma_{\ell-1,j} \mathcal{E}_j \right) \in C_w. \quad \text{(13)}$$

Since $\mathcal{E}_{\ell-1}$ has $v_{\ell-1}$ erasures and $\mathcal{E}_j$ has no erasures for $\ell \leq j \leq m - 1$, then $\mathcal{E}_{\ell-1} \oplus \left( \bigoplus_{j=\ell}^{m-1} \gamma_{\ell-1,j} \mathcal{E}_j \right)$ has $v_{\ell-1}$ erasures. Since the vector is in $C_w$ and $v_{\ell-1} \leq u_w$, the erasures can be corrected. Once $\mathcal{E}_{\ell-1} \oplus \left( \bigoplus_{j=\ell}^{m-1} \gamma_{\ell-1,j} \mathcal{E}_j \right)$ is corrected, $\mathcal{E}_{\ell-1}$ is obtained as
Consider the 3-level EII code \( C(7, 1, 3, 4, 7, 7) \) according to Definition \( \text{I} \) and Example \( \text{S} \). We have four codes \( C_3 \subset C_2 \subset C_1 \subset C_0 \), where \( C_0 \) is a \( [7, 6, 2] \) code, \( C_1 \) is a \( [7, 4, 4] \) code, \( C_2 \) is a \( [7, 3, 5] \) code and \( C_3 = \{ \} \). We may assume that the entries of these codes are in \( GF(8) \) and that \( \alpha \) is a primitive element in \( GF(8) \).

Consider the following 6 × 7 array with erasures denoted by \( E \):

| \( \ell-1 \) | \( \ell-1 \) | \( E \) | \( E \) | \( E \) | \( E \) | \( E \) |
|---|---|---|---|---|---|---|
| \( \ell-1 \) | \( E \) | \( E \) | \( E \) | \( E \) | \( E \) | \( E \) |
| \( \ell-1 \) | \( E \) | \( E \) | \( E \) | \( E \) | \( E \) | \( E \) |
| \( \ell-1 \) | \( E \) | \( E \) | \( E \) | \( E \) | \( E \) | \( E \) |
| \( \ell-1 \) | \( E \) | \( E \) | \( E \) | \( E \) | \( E \) | \( E \) |

The first step is correcting the single erasures in \( \ell_0 \) and in \( \ell_0 \). An ordering of the remaining rows in non-increasing number of erasures is \( \{i_0, i_1, i_2, i_3\} = \{1, 4, 2, 3\} \) (\( \ell = 4 \)). In particular, \( \ell_1 \) has three erasures. According to (7), (8) and (9),

\[
\ell_0 + \ell_0 + \ell_0 + \ell_0 + \ell_0 + \ell_0 + \ell_0 = 0
\]

Notice that \( C_1 \) can correct three erasures, i.e., \( w = 1 \) in (10). Rearranging the \( \ell_0 \)s above in non-increasing number of erasures, we obtain

\[
\ell_1 + \ell_1 + \ell_1 + \ell_1 + \ell_1 + \ell_1 + \ell_1 = 0
\]

\[
\alpha \ell_1 + \alpha \ell_1 + \alpha \ell_1 + \alpha \ell_1 + \alpha \ell_1 + \alpha \ell_1 + \alpha \ell_1 = 0
\]

which corresponds to (11) in the proof of Theorem \( \text{S} \). Triangulating this linear system in \( GF(8) \), where \( 1 \oplus \alpha \oplus \alpha^3 = 0 \), and since \( C_2 \subset C_1 \), we obtain the following triangulated system:

\[
\ell_1 + \ell_1 + \ell_1 + \ell_1 + \ell_1 + \ell_1 + \ell_1 = 0
\]

\[
\ell_1 + \ell_1 + \ell_1 + \ell_1 + \ell_1 + \ell_1 + \ell_1 = 0
\]

Since \( \ell_3 \) has 3 erasures and \( \ell_0 \) and \( \ell_5 \) have no erasures, \( \ell_3 + \alpha^3 \ell_0 + \alpha^5 \ell_5 \) has 3 erasures, which can be corrected in \( C_1 \). Then,

\[
\ell_3 = (\ell_3 + \alpha^3 \ell_0 + \alpha^5 \ell_5) = (\ell_3 + \alpha^3 \ell_0 + \alpha^5 \ell_5).
\]

Similarly, \( \ell_2 + \alpha \ell_3 + \alpha \ell_3 + \alpha \ell_5 \) has 4 erasures, which can be corrected in \( C_2 \), and

\[
\ell_2 = (\ell_2 + \alpha \ell_3 + \alpha \ell_3 + \alpha \ell_5) = (\ell_2 + \alpha \ell_3 + \alpha \ell_5).
\]

Finally, since the first two rows of the triangulated system are equal to zero, we obtain

\[
\ell_4 = \ell_4 + \ell_2 + \ell_2 + \ell_2 + \ell_2
\]

completing the decoding. \( \Box \)

From the proof of Theorem \( \text{S} \) even if the decoding algorithm cannot correct all the erasures, it is often possible to correct a few rows. Specifically, consider a \( C(n, m) \) \( t \)-level EII code as given by Definition \( \text{I} \) and assume that, given a received
array in $C(n, \underline{u})$, row $i$ of the array has $x_i$ erasures. Let $x_{i_0} \leq x_{i_1} \leq \cdots \leq x_{i_n}$ and consider $\underline{u}$ as given by (1). Let $\underline{u} = (v_0, v_1, \ldots, v_{m-1})$, where, by (1), $v_0 \leq v_1 \leq \cdots \leq v_{m-1}$. Define $y$, $0 \leq y \leq m-1$, such that $x_{i_j} \leq v_j$ for $0 \leq j \leq y$, and $x_{y+1} > v_{y+1}$. If $y = m-1$, then, by Theorem 6, all the erasures in the array are correctable. However, if $y < m-1$, then rows $i_0, i_1, \ldots, i_y$ are still correctable by the triangulation algorithm, but rows $i_j$ for $y+1 \leq j \leq m-1$ are not. We illustrate this fact in the following example.

**Example 8.** Consider the 4-level EII code $C(7, 1, 2, 3, 5)$ according to Definition 1. We have four codes $C_3 \subset C_2 \subset C_1 \subset C_0$, where $C_0$ is a $[7,6,2]$ code, $C_1$ is a $[7,5,3]$ code, $C_2$ is a $[7,4,4]$ code and $C_3$ is a $[7,2,6]$ code. As in Example 7, we assume that the entries of these codes are in $GF(8)$ and that $a$ is a primitive element in $GF(8)$.

Consider the following $4 \times 7$ array with erasures denoted by $E$:

\[
\begin{array}{cccccc}
\underline{\epsilon_0} & E & E & E & E & E & E \\
\underline{\epsilon_1} & E & E & E & E & E & E \\
\underline{\epsilon_2} & E & E & E & E & E & E \\
\underline{\epsilon_3} & E & E & E & E & E & E \\
\end{array}
\]

Then, according to the notation above, $x_0 = 4$, $x_1 = 2$, $x_2 = 1$ and $x_3 = 4$. Writing this in non-decreasing order, we have $x_2 \leq x_1 \leq x_0 \leq x_3$. Since $x_2 \leq v_0$, $x_1 \leq v_1$ and $x_0 > v_2$, $y = 1$. Since $C_2$ has a single erasure, we may assume that this erasure is corrected in $C_0$, so now $\underline{\epsilon_1}$ is erasure free. According to Definition 1 we have the following system:

\[
\begin{align*}
\underline{\epsilon_0} & \oplus \underline{\epsilon_3} \oplus \underline{\epsilon_1} \oplus \underline{\epsilon_2} \in C_3 \\
\underline{\epsilon_0} & \oplus a^3 \underline{\epsilon_3} \oplus a \underline{\epsilon_1} \oplus a^2 \underline{\epsilon_2} \in C_2 \\
\underline{\epsilon_0} & \oplus a^6 \underline{\epsilon_3} \oplus a^2 \underline{\epsilon_1} \oplus a^4 \underline{\epsilon_2} \in C_1
\end{align*}
\]

Triangulating this system, we obtain

\[
\begin{align*}
\underline{\epsilon_0} & \oplus \underline{\epsilon_3} \oplus \underline{\epsilon_1} \oplus \underline{\epsilon_2} \in C_3 \\
\underline{\epsilon_3} & \oplus a^2 \underline{\epsilon_1} \oplus a^5 \underline{\epsilon_2} \in C_2 \\
\underline{\epsilon_1} & \oplus a \underline{\epsilon_2} \in C_1
\end{align*}
\]

Since $\underline{\epsilon_1} \oplus a \underline{\epsilon_2}$ has two erasures, they can be corrected in $C_1$. Then, $\underline{\epsilon_0}$ is obtained as $\underline{\epsilon_0} = (\underline{\epsilon_3} \oplus a \underline{\epsilon_2}) \oplus a \underline{\epsilon_2}$. However, with this procedure, $\underline{\epsilon_3} \oplus a^2 \underline{\epsilon_1} \oplus a^5 \underline{\epsilon_2}$ cannot be obtained, since 4 erasures are uncorrectable in $C_2$, so after correcting $\underline{\epsilon_1}$ and $\underline{\epsilon_0}$, we are left with the uncorrectable array

\[
\begin{array}{cccccc}
\underline{\epsilon_0} & E & E & E & E & E & E \\
\underline{\epsilon_1} & E & E & E & E & E & E \\
\underline{\epsilon_2} & E & E & E & E & E & E \\
\underline{\epsilon_3} & E & E & E & E & E & E \\
\end{array}
\]

Although the erasure pattern in Example 8 is only partially correctable, we will see after Theorem 15 that it can be fully correctable when expanding the correction to columns.

**C. Dimension, Encoding and Minimum Distance of EII Codes**

Before discussing the dimension, the encoding and the minimum distance of $t$-level EII codes, let us state and prove the following lemma.

**Lemma 9.** Consider the $t$-level EII code $C(n, \underline{u})$ as given by Definition 1. Then, for each $j$ such that $0 \leq j \leq t-1$, given $u_0, u_1, \ldots, u_j$ fixed column indices in $\hat{s}_{j+1} + 1$ different rows, there is an array in $C(n, \underline{u})$ that is non-zero in such $(\hat{s}_{j+1} + 1) \cdot (u_j + 1)$ locations and 0 elsewhere.

**Proof:** Since $C_j$ is an $[n, n-u_j, u_j + 1]$ MDS code for each $j$ such that $0 \leq j \leq t-1$, given $u_j + 1$ fixed indices in a vector of length $n$, there is a codeword $\underline{v}$ in $C_j$ whose non-zero entries are in such $u_j + 1$ fixed locations. Assume that the $\hat{s}_{j+1} + 1$ rows selected are $i_0, i_1, \ldots, i_{\hat{s}_{j+1}}$, where

\[
0 \leq i_0 < i_1 < \ldots < i_{\hat{s}_{j+1}} \leq m - 1.
\]

Let $\underline{v} = (v_0, v_1, \ldots, v_{\hat{s}_{j+1}})$ be a vector of weight $\hat{s}_{j+1} + 1$ such that

\[
\bigoplus_{s=0}^{\hat{s}_{j+1}} a^{r_s} v_s = 0 \text{ for } 0 \leq r \leq \hat{s}_{j+1} - 1.
\]
Theorem 12. Consider the code $C(n, u)$ in the $(n, k)$ linear code. Assume that the zero array is stored, and a received array $W$ equals $\hat{v}_s w$. Then, if $\hat{v}_s s \in C_{t-i}$ for $0 \leq i \leq t-1$ and $0 \leq r \leq \hat{s}_t-1$, so \([15]\) follows.

If $0 \leq j \leq t-i-1$, then, $\hat{s}_t \geq j$ and, for $0 \leq r \leq \hat{s}_t-1$, by \([14]\),

$$\bigoplus_{s=0}^{\hat{s}_t} \alpha r_i (v_s w) = \bigoplus_{s=0}^{\hat{s}_t} \alpha r_i \hat{v}_s v_s w = 0,$$

so, in particular, \([15]\) follows.

If $t-i \leq j \leq t-1$, then $C_{t-i} \subseteq C_{t-i}$ and $w \in C_{t-i}$, so \([15]\) also follows in this case.

Example 10. Consider the 3-level EI code $C(7, (1, 1, 3, 4, 7, 7))$ of Example 7. According to Lemma 9, the locations denoted by $E$ in the following arrays correspond to the non-zero entries of arrays in $C(7, (1, 1, 3, 4, 7, 7))$ for $j = 0, 1$ and 2 respectively:

The arrays with erasures in locations $E$ above are uncorrectable since, provided the zero array was stored, the decoding cannot decide between the zero array and the arrays with non-zero entries in the locations $E$. $\square$

Next we give an auxiliary general lemma.

Lemma 11. Consider an $[n, k]$ linear code, and let $S = \{i_0, i_1, \ldots, i_{k-1}\}$, where $0 \leq i_0 < i_1 < \cdots < i_{k-1} \leq n-1$. Assume that, given a codeword with erasures in $S$, the code can correct such erasures, while, for any $i \notin S$, erasures in $S \cup \{i\}$ are not correctable. Then, $n-k = s$.

Proof: Since the erasures in $S$ are correctable, there are at least $s$ linearly independent parity equations, so $n-k \geq s$.

Assume that $n-k > s$. Let $H$ be an $(n-k) \times n$ parity-check matrix of the code such that the first $s$ rows of $H$ are used to correct the $s$ erasures in $S$, thus, the $s \times s$ submatrix consisting of those first $s$ rows and columns $i_0, i_1, \ldots, i_{k-1}$ is invertible.

Consider next the matrix consisting of the first $s+1$ rows in $H$. By row operations, we can make the entries $i_0, i_1, \ldots, i_{k-1}$ in the $(s+1)$-th row equal to zero. Since the first $s+1$ rows of $H$ have rank $s+1$, then there is a non-zero location $i$, $i \notin S$, in the $(s+1)$-th row. Thus, columns $S \cup \{i\}$ in the first $s+1$ rows of $H$ are linearly independent and hence erasures in $S \cup \{i\}$ are correctable, a contradiction, so $n-k = s$. $\square$

Theorem 12. Consider the $t$-level EI code $C(n, u)$ as given by Definition 1. Then, $C(n, u)$ is an $[mn, k]$ code, where

$$k = mn - \left( \sum_{i=0}^{t} \hat{s}_i u_i \right) \quad (16)$$

Proof: Assume that the zero array is stored, and a received array $W$ has erasures in the last $u_i$ entries of rows $m - \hat{s}_i$ to $m - \hat{s}_{i+1} - 1$ for $0 \leq i \leq t-1$, and in all the entries of rows $m - s_i$ to $m - 1$. Thus, $W$ has a total of $\sum_{i=0}^{t} s_i u_i$ erasures, and by Theorem 6 it will be correctly decoded as the zero codeword.
Consider an array \( V \) which coincides with \( W \), except in one location in which it has an extra erasure. If we show that any such \( V \) is uncorrectable, by Lemma 11, \( mn - k = \sum_{i=0}^{t} s_i t_i \), which is equivalent to \( \text{(16)} \).

For each \((u', v')\) that is not in the set of erasures of \( W \), define \( i' \), \( 0 \leq i' \leq t - 1 \), such that \( m - s_{i'} \leq u' \leq m - s_{i'+1} - 1 \), and let \( Y^{(u', v')} = (y_{a,b}^{(u', v')})_{0 \leq a \leq m - 1, 0 \leq b \leq n - 1} \) be an array in \( C(n, u) \) whose non-zero coordinates are in the intersection of rows \( u', u' + 1, \ldots, u' + s_{i'+1} \) and columns \( v', n - u' + 1, \ldots, n - u' - 1, \ldots, n - 1 \). Such a non-zero array exists due to Lemma 9.

Assume that the extra erasure in \( V \) is in location \((u, v)\) and consider \( j, 1 \leq j \leq t \), such that \( n - u_j \leq v \leq n - u_{j-1} - 1 \). Take the arrays \( Y^{(u, v)} \), where \( u \leq u' \leq m - s_j - 1 \). For each \( u', u < u' \leq m - s_j - 1 \), choose constants \( c_{u'} \) such that

\[
y_{a,b}^{(u', v)} = \sum_{z=\text{un}-1}^{u'} c_{u'} y_{a,b}^{(z,v)} = 0.
\]

Then, if \( Y = \bigoplus_{z=\text{un}-1}^{m} Y^{(z,v)} \), by (17), \( Y \) has a non-zero entry in \((u, v)\), while its remaining non-zero entries are contained in the locations of the erasures of \( W \). So, array \( V \) is uncorrectable, since it can be decoded either as the zero array or as \( Y \). \( \square \)

Theorem II.1 in [40], which corresponds to Corollary 2 in [2], is a special case of Theorem 12.

**Example 13.** We illustrate the proof of Theorem 12 with the 3-level EII code \( C(7, (1,1,3,4,7,7)) \) of Examples 7 and 10. By Theorem 12 this code is a [42, 19] code. Following the proof of Theorem 12 denote by \( E \) the erased locations in an array \( W \):

\[
W = \begin{array}{cccccc}
\hline
& & & & & E \\
& & & & E & \\
& & & E & E & E \\
& & E & E & E & E \\
& E & E & E & E & E \\
E & E & E & E & E & E \\
\hline
\end{array}
\]

If the non-erased locations of \( W \) are zero, by Theorem 6, the array will be decoded as the zero array. Consider the array \( V \) which has an extra erasure in location \((u, v) = (0, 1)\) (hence, \( i = 0 \) and \( j = 3 \) in the proof of Theorem 12), rendering

\[
V = \begin{array}{cccccc}
\hline
& & & & & E \\
& & & & E & \\
& & & E & E & E \\
& & E & E & E & E \\
& E & E & E & E & E \\
E & E & E & E & E & E \\
\hline
\end{array}
\]

Consider the following arrays \( Y^{(u', 1)}, 0 \leq u' \leq 3 \), defined as in Theorem 12 whose non-zero entries are denoted \( y_{a,b}^{(u',1)} \) below:

\[
Y^{(0,1)} = \begin{array}{cccccc}
0 & y_{0,1}^{(0,1)} & 0 & 0 & 0 & 0 \\
0 & y_{1,1}^{(0,1)} & 0 & 0 & 0 & 0 \\
0 & y_{2,1}^{(0,1)} & 0 & 0 & 0 & 0 \\
0 & y_{3,1}^{(0,1)} & 0 & 0 & 0 & 0 \\
0 & y_{4,1}^{(0,1)} & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 \\
\end{array}
\]

\[
Y^{(1,1)} = \begin{array}{cccccc}
0 & y_{1,1}^{(1,1)} & 0 & 0 & 0 & 0 \\
0 & y_{2,1}^{(1,1)} & 0 & 0 & 0 & 0 \\
0 & y_{3,1}^{(1,1)} & 0 & 0 & 0 & 0 \\
0 & y_{4,1}^{(1,1)} & 0 & 0 & 0 & 0 \\
0 & y_{5,1}^{(1,1)} & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 \\
\end{array}
\]
so, by Lemma 11, the number of parity symbols is 23 and the dimension of the code is 19.

Take the 3-level EII code precomputed. The next example illustrates this encoding process.

simplification of the decoding algorithm. For example, the triangulated matrix corresponding to the coefficients of (12) may be precomputed. The next example illustrates this encoding process.

Then, defining \( Y = Y^{(0,1)} \oplus c_1 Y^{(1,1)} \oplus c_2 Y^{(2,1)} \oplus c_3 Y^{(3,1)} \), we see that

\[
Y = \begin{pmatrix}
0 & 0 & 0 & 0 & 0 & 0 & X \\
0 & 0 & 0 & 0 & 0 & X & X \\
0 & 0 & 0 & X & X & X & X \\
0 & X & 0 & X & X & X & X \\
0 & X & 0 & X & X & X & X \\
0 & X & 0 & X & X & X & X \\
0 & X & 0 & X & X & X & X \\
\end{pmatrix}
\]

where entries denoted by X may take any value. Array Y is non-zero since \( y_{0,1}^{(0,1)} \neq 0 \). Array V may be decoded as the zero array or as Y, so it is uncorrectable. We can make the same argument for any entry \((u, v)\) not contained in the erasures of W, so, by Lemma 11, the number of parity symbols is 23 and the dimension of the code is 19.

The encoding is a special case of the decoding. For example, we may place the parities at the end of the array in increasing order of parities, as shown in Theorem 12 and in Example 13. The parities are considered as erasures and may be obtained using the triangulation method described in Theorem 6. The fact that the locations of the erasures are known allows for a simplification of the decoding algorithm. For example, the triangulated matrix corresponding to the coefficients of (12) may be precomputed. The next example illustrates this encoding process.

Example 14. Take the 3-level EII code \( C(7, (1, 1, 3, 4, 7, 7)) \) of Examples 7, 10 and 13. We have to solve the erasures in array W of Example 13 proceeding by triangulation like in Example 7.

The first step is encoding rows \( \xi_0 \) and \( \xi_1 \) (single parity). An ordering of the remaining rows in non-increasing number of erasures is \( \{i_0, i_1, i_2, i_3\} = \{5, 4, 3, 2\} \).

According to (7), (8) and (9), and rearranging the \( \xi \)s in non-increasing number of erasures, we obtain

\[
\begin{align*}
\xi_5 & \oplus \xi_4 \oplus \xi_3 \oplus \xi_2 \oplus \xi_1 \oplus \xi_0 = 0 \\
\alpha^6 \xi_5 & \oplus \alpha^3 \xi_4 \oplus \alpha^3 \xi_3 \oplus \alpha^3 \xi_2 \oplus \alpha^3 \xi_1 \oplus \xi_0 = 0 \\
\alpha^{12} \xi_5 & \oplus \alpha^{12} \xi_4 \oplus \alpha^6 \xi_3 \oplus \alpha^6 \xi_2 \oplus \alpha^3 \xi_1 \oplus \xi_0 \in C_2 \\
\alpha^{15} \xi_5 & \oplus \alpha^{15} \xi_4 \oplus \alpha^{12} \xi_3 \oplus \alpha^6 \xi_2 \oplus \alpha^3 \xi_1 \oplus \xi_0 \in C_1
\end{align*}
\]

Triangulating this linear system in \( GF(8) \), since \( C_2 \subset C_1 \) and \( 1 \oplus \alpha \oplus \alpha^3 = 0 \), we obtain the following triangulated system:

\[
\begin{array}{cccccccc}
0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
\end{array}
\]
Let $\mathcal{G} = \mathcal{C}_5 \oplus \mathcal{C}_4 \oplus \mathcal{C}_3 \oplus \mathcal{C}_2 \oplus \mathcal{C}_1 \oplus \mathcal{C}_0 = 0$
\[ \mathcal{G}_4 \oplus \mathcal{A}_2 \mathcal{C}_3 \oplus \mathcal{A}_1 \mathcal{C}_2 \oplus \mathcal{A}_0 \mathcal{C}_1 \oplus \mathcal{A}_0 \mathcal{C}_0 = 0 \]
\[ \mathcal{C}_3 \oplus \mathcal{A}_2 \mathcal{C}_2 \oplus \mathcal{A}_1 \mathcal{C}_1 \oplus \mathcal{A}_0 \mathcal{C}_0 \in \mathcal{C}_2 \]
\[ \mathcal{C}_2 \oplus \mathcal{A}_2 \mathcal{C}_1 \oplus \mathcal{A}_0 \mathcal{C}_0 \in \mathcal{C}_1. \]

This triangulated system is precomputed, so it is not necessary to do Gaussian elimination when encoding.

Then, $\mathcal{C}_2 \oplus \mathcal{A}_2 \mathcal{C}_1 \oplus \mathcal{A}_0 \mathcal{C}_0$ is encoded in $\mathcal{C}_1$ and $\mathcal{C}_3$ is obtained as $\mathcal{C}_3 = (\mathcal{C}_2 \oplus \mathcal{A}_2 \mathcal{C}_1 \oplus \mathcal{A}_0 \mathcal{C}_0) \oplus (\mathcal{A}^2 \mathcal{C}_2 \oplus \mathcal{A}_1 \mathcal{C}_1 \oplus \mathcal{A}_0 \mathcal{C}_0)$. Similarly, $\mathcal{C}_3 \oplus \mathcal{A}_2 \mathcal{C}_2 \oplus \mathcal{C}_1 \oplus \mathcal{A}_0 \mathcal{C}_0$ is encoded in $\mathcal{C}_2$, and $\mathcal{C}_3$ is obtained as $\mathcal{C}_3 = (\mathcal{C}_2 \oplus \mathcal{A}_2 \mathcal{C}_2 \oplus \mathcal{C}_1 \oplus \mathcal{A}_0 \mathcal{C}_0) \oplus (\mathcal{A}_2 \mathcal{C}_2 \oplus \mathcal{A}_1 \mathcal{C}_1 \oplus \mathcal{A}_0 \mathcal{C}_0)$. Finally, we obtain
\[ \mathcal{C}_4 = \mathcal{A}^2 \mathcal{C}_3 \oplus \mathcal{A}^3 \mathcal{C}_2 \oplus \mathcal{A}^6 \mathcal{C}_1 \oplus \mathcal{A}^4 \mathcal{C}_0 \]
\[ \mathcal{C}_5 = \mathcal{C}_4 \oplus \mathcal{C}_3 \oplus \mathcal{C}_2 \oplus \mathcal{C}_1 \oplus \mathcal{C}_0. \]

At every step, we are encoding RS codes.

Another possibility for encoding EII codes is to use an existing encoding algorithm for II codes. In effect, if $\mathcal{U}$ is given by (1) and $\mathcal{U}' = \left( \overline{u_0, \ldots, u_0, u_1, \ldots, u_1, \ldots, u_{l-1}, \ldots, u_{l-1}} \right)$, then an EII code $\mathcal{C}(n, \mathcal{U})$, in particular, is contained in an II code $\mathcal{C}(n, \mathcal{U}')$, both codes as given by Definition 11. Then, by again setting the parities in the locations described in Theorem 12 and in Example 13, we obtain the vertical parities in locations $(a, b)$, $m - s_i \leq a \leq m - 1$, $0 \leq b \leq n - u_i - 1 - 1$ using (4) with $i = 0$. The remaining parities are computed by encoding the data and the vertical parities obtained in the previous step into the II code $\mathcal{C}(n, \mathcal{U}')$. Any encoding algorithm for II codes can be used, like, for example, the one described in [42].

The following theorem extends Theorem 12 on t-level II codes as stated in [40] and proven as Corollary 3 in [2] (see also [42]). It also generalizes the well-known result that the minimum distance of a product code is the product of the minimum distances of the two component codes.

**Theorem 15.** Consider the t-level EII code $\mathcal{C}(n, \mathcal{U})$ as given by Definition 11. Then, the minimum distance of $\mathcal{C}(n, \mathcal{U})$ is

\[ d = \min \left\{ (s_{i+1} + 1) (u_i + 1) \mid 0 \leq i \leq t - 1 \right\} \]

**Proof:** For each $i$ such that $0 \leq i \leq t - 1$, consider an array in $\mathcal{C}(n, \mathcal{U})$ that has $s_{i+1}$ rows with $u_i + 1$ erasures each, one row with $u_t$ erasures, and all the other entries are zero. By Theorem 10, such arrays will be corrected by the code $\mathcal{C}(n, \mathcal{U})$ as the zero codeword, thus

\[ d \geq \min \left\{ (s_{i+1} + 1) (u_i + 1) \mid 0 \leq i \leq t - 1 \right\}. \]

On the other hand, by Lemma 13 for each $0 \leq i \leq t - 1$, there is an array in $\mathcal{C}(n, \mathcal{U})$ of weight $(s_{i+1} + 1) (u_i + 1)$, so

\[ d \leq \min \left\{ (s_{i+1} + 1) (u_i + 1) \mid 0 \leq i \leq t - 1 \right\} \]

and (18) follows.

**Example 16.** Consider the 3-level EII code $\mathcal{C}(7, (1,1,3,4,7,7))$ of Examples 7, 10, 13, and 14. According to Theorem 13 since $\mathcal{C}_6 = 6$, $\mathcal{C}_5 = 1$, $\mathcal{C}_4 = 3$, $\mathcal{C}_3 = 4$, $\mathcal{C}_2 = 2$, $\mathcal{C}_1 = 1$, $\mathcal{C}_0 = 2$ and hence, $s_3 = 3$, $s_2 = s_1 + s_3 = 4$, $s_1 = s_2 + s_3 = 4$. By (18), the minimum distance of this code is $d = \min \left\{ \left( \overline{5} \right)(2), \left( \overline{4} \right)(4), \left( \overline{3} \right)(5) \right\} = 10$. \( \square \)

Although the minimum distance is not the only criterion to determine the correction power of an II code [2], see also Subsection III-C, given different II codes as $m \times n$ arrays with the same rate, there is one that has the largest minimum distance. A natural question is, if we include EII codes, is there any EII code whose minimum distance is larger than the one of any II code with the same rate? The answer depends on the parameters chosen, but the following example shows that indeed this may be the case.

**Example 17.** Consider the 4-level EII code $\mathcal{C}(7, (1,3,4,6,7))$. According to Theorem 13, the minimum distance of this code is $d = 10$. An II code with the same rate is a code $\mathcal{C}(7, (v_0, v_1, v_2, v_3, v_4))$, where $0 \leq v_0 \leq v_1 \leq v_2 \leq v_3 \leq v_4 < 7$ and $v_0 + v_1 + v_2 + v_3 + v_4 = 21$. Again by Theorem 13 the minimum distance of this code is $d \leq v_4 + 1 \leq 7$, so the EII code has larger minimum distance than any II code also consisting of $5 \times 7$ arrays and with the same rate. \( \square \)
D. Transpose Arrays, Iterative Decoding and Uniform Distribution of Parity Symbols

Definition 1 states that a $t$-level EII code $C(n, \mathbf{u})$ consists of $m \times n$ arrays such that each row in the array is in a code $C_0$, and that certain linear combinations of the rows belong in nested codes $C_i$. If we take the columns in an array in $C(n, \mathbf{u})$, they would be the rows in an $n \times m$ transpose array. A natural question is, are the rows in these transpose arrays also related by certain nested codes?

Before answering this question, we consider the simple example of a product code such that the vertical code is an $[m, k_0, m-k_0+1]$ code and the horizontal code is an $[n, k_1, n-k_1+1]$ code. We have seen in Example 3 that this product code is a $1$-level EII code $C(n, \mathbf{u})$. Now consider the $m \times m$ transpose array $C(m, \mathbf{u}')$, where $\mathbf{u}'$ is the vector $(m-k_0, m-k_0, \ldots, m-k_0, m, m, \ldots, m)$. The following theorem generalizes this argument for $t$-level EII codes.

**Theorem 18.** Consider a $t$-level EII code $C(n, \mathbf{u})$ as given by Definition 1 and take the set of $n \times m$ transpose arrays corresponding to the $m \times n$ arrays in $C(n, \mathbf{u})$. Then, this set of $n \times m$ transpose arrays constitute a $t$-level EII code $C(m, \mathbf{u}')$ such that, assuming $u_{-1} = 0$,

$$
\mathbf{u}' = \begin{pmatrix}
    s'_0 \\
    u'_0, u'_0, \ldots, u'_0, u'_1, u'_1, \ldots, u'_1, u'_{t-1}, u'_{t-1}, \ldots, u'_{t-1}, u'_t, u'_t, \ldots, u'_t
\end{pmatrix},
$$

(19)

where

$$
u'_{t-1} = s_i \quad \text{and} \quad s'_i = u_{t-i} - u_{t-i-1} \quad \text{for} \quad 0 \leq i \leq t.
$$

(20)

**Proof:** Denote by $C_j^{(H)}$, $0 \leq i \leq m-1$, the rows of an array in $C(n, \mathbf{u})$, and by $C_j^{(V)}$, $0 \leq j \leq n-1$, the columns (that is, the rows of the $n \times m$ transpose array). Specifically, if the array consists of symbols $(c_{ij})_{0 \leq i \leq m-1}$, then $C_j^{(H)} = \{c_{i,0}, c_{i,1}, \ldots, c_{i,n-1}\}$ for $0 \leq j \leq m-1$ and $C_j^{(V)} = \{c_{0,j}, c_{1,j}, \ldots, c_{m-1,j}\}$ for $0 \leq j \leq n-1$.

Consider the $t+1$ nested codes (on columns) $\{0\} = C'_0 \subseteq C'_{t-1} \subseteq C'_{t-2} \subseteq \cdots \subseteq C'_0$, where $C'_i$ is an $\lfloor m, m-u'_i, u'_i+1 \rfloor$ code and $u'_i$ is given by (20). A parity-check matrix of $C'_i$ is $H_{u'_i}$ as given by (2).

In order to prove the theorem, according to (4), we have to prove that each $C_j^{(V)} \in C'_0$ for $0 \leq j \leq n-1$, and that

$$
\bigoplus_{j=0}^{n-1} \alpha^{r} C_j^{(V)} \in C'_{t-i} \quad \text{for} \quad 0 \leq i \leq t-1
$$

and $0 \leq r \leq s'_{t-i} - 1$

(21)

$C'_0$ is an $\lfloor m, m-u'_0, u'_0+1 \rfloor$ code and by (20), $u'_0 = s_t$, so from (4), taking $i = 0$, $C_j^{(V)} \in C'_0$.

Next we have to prove (21). In effect, (21) holds if and only if, by (2),

$$
\bigoplus_{r=0}^{m-1} \bigoplus_{j=0}^{n-1} \alpha^{r} C_r C_j^{(V)} = 0
$$

for $0 \leq i \leq t-1$, $0 \leq u \leq u'_{t-i} - 1$ and $0 \leq r \leq s'_{t-i} - 1$, if and only if, changing the summation order,

$$
\bigoplus_{j=0}^{n-1} \bigoplus_{r=0}^{m-1} \alpha^{r} C_r C_j^{(V)} = 0
$$

for $0 \leq i \leq t-1$, $0 \leq u \leq u'_{t-i} - 1$ and $0 \leq r \leq s'_{t-i} - 1$, if and only if, since, by (3) and (20), $s'_{t-i} = \sum_{z=t-i}^{t} s_z' = \sum_{z=t-i}^{t} (u_{t-z} - u_{t-z-1}) = u_i$ and $u'_{t-i} = s_i$, by (2),

$$
\bigoplus_{r=0}^{m-1} \bigoplus_{v=0}^{n-1} \alpha^{rv} C_r^{(H)} \in C_i \quad \text{for} \quad 0 \leq i \leq t-1 \text{ and } 0 \leq u \leq s_i - 1,
$$
which is true by (4) and thus (21) is also true.

Theorem 18 is the most important result in this section. One application is an enhancement of the decoding algorithm by extending the iterative decoding algorithm of product codes, in which rows and columns are decoded iteratively until either all the erasures are corrected or an uncorrectable pattern remains. In order to illustrate this process, let us revisit Example 8.

Example 19. According to Theorem 18 the transpose $7 \times 4$ arrays of the $4 \times 7$ arrays in the 4-level II code $C(7,(1,2,3,5))$ are in a 4-level EII code $C(4,(0,0,1,2,3,4))$. After (partially) decoding the rows of the array with erasures in Example 8 we were left with the uncorrectable array in $C(7,(1,2,3,5))$

\[
\begin{array}{cccc}
E & E & E & E \\
E & E & E & E \\
E & E & E & E \\
\end{array}
\]

Notice that this array has two columns with no erasures, two columns with one erasure each and three columns with two erasures each. By Theorem 6 the array is correctable in $C(4,(0,0,1,2,3,4))$. Hence, after two iterations the erasures are corrected.

Example 20. Consider the 5-level II code $C(10,(1,3,6,8,9))$. The transpose arrays of $C(10,(1,3,6,8,9))$ are in the 5-level EII code $C(5,(0,1,2,2,3,3,3,4,4,5))$ by Theorem 18. Assume that the following array is received:

\[
\begin{array}{cccccccccc}
E & E & E & E & E & E & E & E & E & E \\
E & E & E & E & E & E & E & E & E & E \\
E & E & E & E & E & E & E & E & E & E \\
\end{array}
\]

Applying the decoding algorithm on rows, only the third row can be corrected, since it has exactly one erasure. After correction of the third row, we have the array

\[
\begin{array}{cccccccccc}
E & E & E & E & E & E & E & E & E & E \\
E & E & E & E & E & E & E & E & E & E \\
E & E & E & E & E & E & E & E & E & E \\
\end{array}
\]

This array has one column with no erasures, one column with one erasure and one column with two erasures, while the remaining columns contain more than two erasures. The decoding algorithm on columns (i.e., on the code $C(5,(0,1,2,2,3,3,3,4,4,5))$) allows for correction of the column with one erasure and the column with two erasures, giving the array

\[
\begin{array}{cccccccccc}
E & E & E & E & E & E & E & E & E & E \\
E & E & E & E & E & E & E & E & E & E \\
E & E & E & E & E & E & E & E & E & E \\
\end{array}
\]

This last array is decodable on rows (i.e., on the code $C(10,(1,3,6,8,9))$), hence, the erasures are corrected after three iterations.

A second application of Theorem 18 is allowing for a balanced distribution of the parity symbols in EII codes. In effect, given an $[mn,k]$ code consisting of $m \times n$ arrays, if $mn - k = qm + r$, where $0 \leq r < m$, we say that the code has a balanced distribution of parity symbols if $m - r$ of the rows contain $q$ parity symbols, while the remaining $r$ rows contain $q + 1$ parity symbols. Codes somewhat similar to II codes with a balanced distribution of parity symbols were presented in [7]. Actually, in [7] only cases for which $r = 0$ are considered, i.e., $m$ divides $mn - k$ and hence each row contains the same number $q$ of parity symbols.

Given a $t$-level EII code $C(n,u)$, so far we have placed the parity symbols as in Theorem 12 and in examples 13 and 14, that is, at the end of each row in non-decreasing order of the $u_i$s. However, this distribution of symbols in general is not balanced. If it can be shown that there is an uniform distribution of erasures that can be corrected by the code $C(m,u')$ (i.e., the code on columns as given by Theorem 13), then we can use those erasures as the locations for the parity symbols. The following theorem shows that, using Theorem 18 we can easily obtain a balanced distribution of the parity symbols for a $t$-level EII code $C(n,u)$.
Theorem 21. Consider a $t$-level EII code $\mathcal{C}(n, \mu)$ as given by Definition 11. Then $\mathcal{C}(n, \mu)$ admits a balanced distribution of the parity symbols.

Proof: We need to find $s = \sum_{i=0}^t s_i \mu_i$ erasures such that, if $s = qm + r$ with $0 \leq r < m$, then there are $m - r$ rows with $q$ erasures each and $r$ rows with $q + 1$ erasures each, and the erasures are correctable by the $t$-level EII code $\mathcal{C}(m, \mu')$ on columns as given by Theorem 18. Then such erasures can be used to place the parity symbols.

In effect, let $v_0 \geq v_1 \geq \ldots \geq v_{z-1}$ be the non-zero elements of $\mu'$ in non-increasing order. In particular, $s = \sum_{i=0}^{z-1} v_i$. We will select the first $z$ columns in an $m \times n$ array such that column $j$ has exactly $v_j$ erasures for $0 \leq j \leq z - 1$. Then, by Theorem 18 such erasures are correctable. In addition, we will show that the selection of erasures is balanced. We proceed by induction.

If $z = 1$, we have only one column and we place the erasures in the top $v_0$ positions of that column. In particular, the distribution is balanced. So assume that $z > 1$.

Consider the first $z - 1$ columns and let $s' = \sum_{i=0}^{z-2} v_i$. By induction, if $s' = q'm + r'$, we can place $s_j$ erasures in column $j$ for $0 \leq j \leq z - 2$, such that the first $r'$ rows contain $q' + 1$ erasures and the last $m - r'$ rows contain $q'$ erasures.

If $v_{z-1} \leq m - r'$, then in column $z - 1$ we place the $v_{z-1}$ erasures in locations $r', r' + 1, \ldots, r' + v_{z-1} - 1$. Then the first $r' + v_{z-1}$ rows contain $q' + 1$ erasures and the last $m - (r' + v_{z-1})$ rows contain $q'$ erasures, giving a balanced distribution of the erasures.

If $v_{z-1} > m - r'$, then in column $z - 1$ we place the $v_{z-1}$ erasures in locations $0, 1, \ldots, v_{z-1} - (m - r') - 1, r', r' + 1, \ldots, m - 1$.

Then the first $v_{z-1} - (m - r')$ rows contain $q' + 1$ erasures and the remaining rows $q'$ erasures, also giving a balanced distribution of the erasures. \hfill \Box

Of course the balanced distribution of parity symbols is not unique. We illustrate the method described in Theorem 21 in the next two examples.

Example 22. Consider a product code consisting of $5 \times 7$ arrays such that each row has one parity and each column two parities. We have seen in Example 10 that such a code can be viewed as a 1-level EII code $\mathcal{C}(7, (1, 1, 1, 7, 7))$. The distribution of parities given in the proof of Theorem 21 in this case is the following:

\[
\begin{array}{ccccc}
E & E & E & E & E \\
E & E & E & E & E \\
E & E & E & & \\
E & E & E & E & \\
E & E & E & & \\
\end{array}
\]

\hfill \Box

Certainly it is not necessary to invoke Theorem 21 to obtain a balanced distribution of the parities in a product code. The next example is more representative.

Example 23. Consider the 3-level EII code $\mathcal{C}(7, (1, 1, 3, 4, 7, 7))$ of Examples 10, 13, 14, and 16. According to Theorem 18 the $7 \times 6$ transpose arrays of this code constitute a 3-level EII code $\mathcal{C}(6, (2, 2, 2, 3, 4, 4, 6))$. The balanced distribution of parities given by Theorem 21 is

\[
\begin{array}{ccccc}
E & E & E & E & \\
E & E & E & E & \\
E & E & E & & \\
E & E & E & & \\
E & E & E & & \\
\end{array}
\]

\hfill \Box

For encoding using a balanced distribution of parities, we apply the decoding algorithm by triangulation on the $t$-level EII code $\mathcal{C}(m, \mu')$ on columns given by Theorem 18. Again, the fact that the erasures are known a priori allows for precomputing the coefficients arising from the triangulation. Obviously, any $t$-level EII code $\mathcal{C}(n, \mu)$ also admits a balanced distribution of symbols on columns.
E. Error and Erasure Decoding of EII Codes

Although in this paper we concentrate on the erasure model, the decoding algorithm can be adapted to handle errors together with erasures. Specifically:

**Algorithm 24.** Consider a $t$-level EII code $\mathcal{C}(n, \underline{w})$ as given by Definition 1 and assume that a received $m \times n$ array contains both errors and erasures. Then proceed as follows:

1) Attempt to correct in $C_0$ rows with up to $i$ errors together with up to $j$ erasures, where $2i + j \leq u_0$.
2) Consider the $\ell$ rows for which the decoding has failed. If $\ell = 0$, then correction has been successful and exit the algorithm.
3) If $\ell > \delta$, then declare that the algorithm has failed. Otherwise, as in Theorem 5, let $i_0, i_1, \ldots, i_{m-1}$ be an ordering of the rows according to a non-increasing number of erasures such that rows $i_0, i_1, \ldots, i_{\ell-1}$ correspond to the $\ell$ rows for which the decoding in $C_0$ has failed.
4) Define $w$ as in (10), i.e., $\delta_{w+1} < \ell \leq \delta_w$ and consider the code $C_w$ from the nested set of codes in Definition 1 which can correct up to $i$ errors together with up to $j$ erasures for $2i + j \leq u_w$.
5) Proceeding as in Theorem 5 after triangulation, obtain (13). Then attempt to correct up to $i$ errors together with up to $j$ erasures, where $2i + j \leq u_w$. If the decoding is successful, continue by induction with the remaining $\ell - 1$ rows. If the decoding is unsuccessful, change the order of the $\ell$ uncorrected rows (for example, by rotating them) and repeat the procedure until $C_{\ell-1}$ is decoded successfully and then proceed by induction. If none of the $\ell$ rows is decoded successfully after this procedure, declare failure.

If the algorithm fails, then correction is attempted on columns.

Contrary to the case of erasures only, there is now a probability of miscorrection each time individual decoding or errors together with erasures is attempted: if the error-erasure correcting power of the codes is exceeded, the decoder may miscorrect and give the wrong codeword. However, if the finite field is fairly large and the codes can correct a substantial number of erasures, such probability is small [8], [9], [30] and we assume that miscorrection does not occur (otherwise, the decoding algorithm gets more complicated). A similar assumption was made in [42]. Certainly, also the decoding algorithms of [40] and [42] can be adapted for $t$-level EII codes.

We illustrate Algorithm 24 with an example.

**Example 25.** Consider $\mathcal{C}(15, (3, 3, 5, 8, 8, 15))$ as a 3-level EII code over the field $GF(16)$, hence, $C_0$ is a [15, 12, 4] code, $C_1$ is a [15, 10, 6] code, $C_2$ is a [15, 7, 9] code and $C_3 = \{0\}$. Denoting errors by $X$ and erasures by $E$, assume that the following array has been received:

$$
\begin{array}{cccccccc}
\ell_0 & X & X & X & E & X & X & X \\
\ell_1 & X & X & E & X & X & X & X \\
\ell_2 & X & X & E & X & X & X & X \\
\ell_3 & X & X & E & X & X & X & X \\
\ell_4 & X & X & E & X & X & X & X \\
\end{array}
$$

Following Algorithm 24, correction of up to one error together with one erasure is attempted using code $C_0$. This correction succeeds for rows $\ell_1$ and $\ell_4$, and fails in the remaining 4 rows (i.e., $\ell = 4$). Ordering the rows in non-decreasing number of erasures and according to (7), (8) and (9) as in Example 7, we obtain (now rows $\ell_1$ and $\ell_4$ are error-free)

$$
\begin{align*}
&c_5 \oplus c_0 \oplus c_2 \oplus c_4 \oplus c_1 \oplus c_4 = 0 \\
&c_3 \oplus c_0 \oplus a^2 c_2 \oplus a^3 c_3 \oplus a^5 c_1 \oplus a^4 c_4 \in C_2 \\
&c_10 c_5 \oplus c_0 \oplus a^4 c_2 \oplus a^5 c_3 \oplus a^2 c_1 \oplus a^8 c_4 \in C_2 \\
&c_15 c_5 \oplus c_0 \oplus a^6 c_2 \oplus a^9 c_3 \oplus a^3 c_1 \oplus a^{12} c_4 \in C_1.
\end{align*}
$$

Triangulating this linear system in $GF(16)$ and assuming that $1 \oplus a \oplus a^4 = 0$, we obtain

$$
\begin{align*}
&c_5 \oplus c_0 \oplus c_2 \oplus c_3 \oplus c_4 \oplus c_1 \oplus c_4 = 0 \\
&c_0 \oplus a^6 c_2 \oplus a^3 c_3 \oplus a^7 c_1 \oplus a^{13} c_4 \in C_2 \\
&c_5 \oplus a^6 c_2 \oplus a^{12} c_1 \oplus c_4 \in C_2 \\
&c_0 \oplus a^5 c_2 \oplus a^{10} c_1 \oplus a^3 c_4 \in C_1.
\end{align*}
$$

Since $c_5 \oplus a^{10} c_1 \oplus a^3 c_4$ has two errors, they can be corrected in $C_1$. So, we get

$$
c_5 = \left(c_5 \oplus a^{10} c_1 \oplus a^3 c_4\right) \oplus \left(a^{10} c_1 \oplus a^3 c_4\right).
$$

Next we attempt to decode $c_5 \oplus a^5 c_2 \oplus a^{12} c_1 \oplus c_4$ in $C_2$. But this vector has five errors, which are uncorrectable in $C_2$. Making a rotation of the 3 uncorrected vectors, we have
Theorem 26. Let \( g \) be the minimum distance of an EP \((m,v,n,h;g)\) code. Given \( a \) such that \( 1 \leq a \leq g + 1 \), \( b = \lceil (g + 1)/a \rceil \) and \( r = g + 1 - ab \), let

\[
\begin{align*}
&c_2 + c_5 + c_9 + c_{10} + c_{13} + c_1 + c_4 = 0 \\
&c_2 + a c_5 + c_9 + c_3 + a c_1 + a^4 c_4 \in C_2 \\
&c_2 + a c_5 + a c_3 + a c_1 + a^3 c_4 \in C_2 \\
&c_2 + a c_3 + a c_4 \in C_2.
\end{align*}
\]

Triangulating this linear system, we obtain

\[
\begin{align*}
&c_2 + c_5 + c_9 + c_3 + c_1 + c_4 = 0 \\
&c_5 + a c_3 + a c_4 \in C_2 \\
&c_9 + a c_3 + a c_4 \in C_2.
\end{align*}
\]

Now, \( c_0 + a^{14} c_3 + a^4 c_4 \) has four errors, that are correctable in \( C_2 \), so \( c_0 \) is obtained as

\[
c_0 = (c_0 + a^{14} c_3 + a^4 c_4) + (a^{14} c_3 + a^4 c_4).
\]

Next, \( c_5 + a^7 c_3 + a^5 c_4 \) has two errors and four erasures, which are also correctable in \( C_2 \), so

\[
c_5 = (c_5 + a^7 c_3 + a^5 c_4) + (a^7 c_3 + a^5 c_4).
\]

Finally,

\[
c_2 = (c_2 + c_5 + c_9 + c_3 + c_1 + c_4) + (c_5 + c_0 + c_3 + c_1 + c_4),
\]

completing the decoding.

\[\Box\]

III. EXTENDED PRODUCT CODES AND OPTIMALITY ISSUES

This section is structured as follows: in Subsection III-A, we present an upper bound on the minimum distance of EPC codes, we illustrate it with examples and we show that other bounds, like the bound on LRC codes, are special cases of this bound. In Subsection III-B we present some constructions of codes meeting this upper bound for important special cases. In Subsection III-C we briefly discuss tradeoffs between different codes by giving Monte Carlo simulations for some specific parameters.

A. Upper Bound on the Minimum Distance of Extended Product Codes

The \( t \)-level EII codes \( C(n_0, m) \) described in Section II are a special case of product codes with some extra parities defined in Section II where we called these codes extended product (EPC) codes and we denoted them by \( EP(m,v,n,h;g) \). \( v \) the number of vertical parities in each column, \( h \) the number of horizontal parities in each row, and \( g \) the number of extra parities. From Definition II it is easy to determine \( v, h \) and \( g \) for \( t \)-level EII codes \( C(n_0, m) \). In effect, since \( v = s_t \) and \( h = u_0 \), the extra parities consist of all the remaining parities, i.e., \( g = \{ \sum_{i=0}^{t-1} u_i s_i \} - u_0 m - s_t (n - u_0) \). For example, the 3-level EII code \( C(7,\{1,3,4,7\}) \) of Examples II and III is an \( EP(6,2,7;1,5) \) code.

The next theorem gives an upper bound on the minimum distance of an \( EP(m,v,n,h;g) \) code.

Theorem 26. Let \( d(m,v,n,h;g) \) be the minimum distance of an \( EP(m,v;n,h;g) \) code. Given \( a \) such that \( 1 \leq a \leq g + 1 \), \( b = \lceil (g + 1)/a \rceil \) and \( r = g + 1 - ab \), let

\[
\begin{align*}
d(v,h,g; a) &= (v + b)(h + a) \text{ if } r = 0 \\
d(v,h,g; a) &= (v + b)(h + a) + h + r \text{ if } r \neq 0
\end{align*}
\]

Then,

\[
d(m,v;n,h;g) \leq \min\{d(v,h,g; a) : \lceil (g + 1)/(m - v) \rceil \leq a \leq \min\{g + 1, n - h\}\}
\]

Proof: Assume first that \( r = 0 \), the zero array is stored, and the received array has the \((v + b)(h + a)\) locations \((i,j)\) erased, where \( 0 \leq i \leq v + b - 1 \) and \( 0 \leq j \leq h + a - 1 \). Notice that, since \( \lceil (g + 1)/(m - v) \rceil \leq a \leq \min\{g + 1, n - h\} \),

\[
0 \leq i \leq v + b - 1 \leq v + \frac{g + 1}{(g + 1)/(m - v)} - 1 = m - 1
\]

and \( 0 \leq j \leq h + a - 1 \leq h + (n - h) - 1 = n - 1 \), so all the erasures are within the array. The erasures are covered by \( h(v + b) \) horizontal parities, \( v(h + a) \) vertical parities and \( g \) extra parities, but \( hv \) of such parities are dependent. Since \( ab = g + 1 \), there are only \((v + b)(h + a) - 1\) independent parities covering the \((v + b)(h + a)\) erasures, insufficient to correct them.
Similarly, assume that \( r \neq 0 \), the zero array is stored, and the received array has the \((v+b)(h+a)+h+r\) locations \((i,j)\) erased, where either \(0 \leq i < v+b-1\) and \(0 \leq j < h+a-1 \leq n-1\), or \(i = v+b\) and \(0 \leq j \leq h+r-1\). Observe that all the erasures are within the array. In effect, since \( a \) does not divide \( g+1 \),

\[
v + b = v + \left\lfloor \frac{g+1}{a} \right\rfloor \leq v + \frac{g+1}{a} \leq v + \frac{g+1}{(g+1)/(m-v)} \leq v + (m-v) = m.
\]

The erasures are covered by \( h(v+b+1) \) horizontal parities, \( v(h+a) \) vertical parities and \( g \) extra parities. Since \( hv \) of such parities are dependent and \( g+1 = ab+r \), this gives a total of \((v+b)(h+a)+h+r-1\) independent parities covering the \((v+b)(h+a)+h+r\) erasures, insufficient to correct them.

\[\square\]

**Example 27.** Consider an \( EP(5,2;8,3;3) \) code and let \( d(5,2;8,3;3) \) be its minimum distance. According to \[24\], we have \( d(5,2;8,3;3) \leq \min\{d(2,3,3;2) \: 2 \leq a \leq 4\} \), where, by \[22\] and \[24\], \( d(2,3,3;2) = 20 \), \( d(2,3,3;3) = 22 \) and \( d(2,3,3;4) = 21 \), so \( d(5,2;8,3;3) \leq 20\).

\[\square\]

**Example 28.** Consider \( EP(m,1;n,1;g) \) codes such that \( g+1 < \min\{m,n\} \). The following table gives the upper bound, according to Theorem \[26\] for \( d(m,1;n,1;g) \), where \( 0 \leq g \leq 13 \):

| \( g \) | \( d(m,1;n,1;g) \leq \) | \( g \) | \( d(m,1;n,1;g) \leq \) |
|---|---|---|---|
| 0 | 4 | 7 | 15 |
| 1 | 6 | 8 | 16 |
| 2 | 8 | 9 | 18 |
| 3 | 9 | 10 | 19 |
| 4 | 11 | 11 | 20 |
| 5 | 12 | 12 | 22 |
| 6 | 14 | 13 | 23 |

\[\square\]

The special case \( v = 0 \) in Theorem \[26\] corresponds to LRC codes \[14\], \[34\], \[39\], i.e., there is no vertical code. Let us state explicitly the result for this case.

**Corollary 29** Consider an \( EP(m,0;n,h;g) \) code. Then,

\[
d(m,0;n,h;g) \leq \left\lfloor \frac{g+1}{n-h} \right\rfloor h + g + 1
\]

**Proof:** Taking \( a = \min\{g+1, n-h\} \) in \[24\] gives for this case

\[
d(m,0;n,h;g) \leq d(0,h,g;\min\{g+1, n-h\})
\]

If \( g+1 < n-h \), then \( b = 1 \), so \[22\] gives \( d(0,h,g;g+1) = h + g + 1 \) and \[25\] follows from \[26\].

If \( g+1 \geq n-h \) and \( n-h \) divides \( g+1 \), by \[22\],

\[
d(0,h,g;n-h) = \left( \frac{g+1}{n-h} \right) (h+n-h) = \left( \frac{g+1}{n-h} \right) h + g + 1,
\]

and \[25\] follows from \[26\]. If \( g+1 > n-h \) and \( n-h \) does not divide \( g+1 \), by \[24\],

\[
d(0,h,g;n-h) = \left\lfloor \frac{g+1}{n-h} \right\rfloor n+h+g+1-(n-h) \left\lfloor \frac{g+1}{n-h} \right\rfloor = \left( \left\lfloor \frac{g+1}{n-h} \right\rfloor + 1 \right) h + g + 1 = \left\lfloor \frac{g+1}{n-h} \right\rfloor h + g + 1,
\]

and also in this case, \[25\] follows from \[26\].

\[\square\]

Bound \[25\] is well known, albeit it is usually given in a slightly different form \[14\], \[34\] as a function of the dimension of the code, while bound \[25\] is given as a function of the redundancy. It was also shown that bound \[25\] can be achieved with efficient constructions \[39\] over a field \( GF(q) \), where \( q \geq mn \) and in general \( q \) is minimal with this property.
B. Some Optimal Extended Product Codes

We say that an EP($m, v; n, h; g$) code is optimal if it meets bound (24) with equality. We believe that there are optimal EP($m, v; n, h; g$) codes for any choice of parameters, but the subject requires further research.

The next theorem shows that there is a range of parameters for which 2-level EII codes are optimal extended product codes.

**Theorem 30.** Consider the 2-level EII code $C(n, u)$ as given by Definition 1 where

$$u = \left(\begin{array}{c}
\frac{m-v-1}{h}, h, \ldots, h, h + g, n, n, \ldots, n
\end{array}\right),$$

(27)

$0 \leq v < m - 1, v \leq h, h + g < n$ and $1 \leq g \leq \left\lfloor \frac{h-v+1}{v+1} \right\rfloor$. Then, $C(n, u)$ is an optimal EP($m, v; n, h; g$) code with minimum distance

$$d = (h + g + 1)(v + 1)$$

(28)

**Proof:** Applying Theorem 15 to (27), the minimum distance of $C(n, u)$ is $d = \min \{(h + g + 1)(v + 1), (h + 1)(v + 2)\}$. In fact, we will show that $(h + g + 1)(v + 1) \leq (h + 1)(v + 2)$ and hence (28) follows.

In effect, $(h + g + 1)(v + 1) \leq (h + 1)(v + 2)$ if and only if $g \leq (h + 1)/(v + 1)$. By the conditions on $g$, it suffices to prove that

$$\left\lfloor \frac{h-v+1}{v+1} \right\rfloor \leq \frac{h+1}{v+1}.$$  

(29)

If $\left\lfloor \frac{h-v+1}{v+1} \right\rfloor = \frac{h-v+1}{v+1}$, (29) is immediate, so assume that $\left\lfloor \frac{h-v+1}{v+1} \right\rfloor > \frac{h-v+1}{v+1}$. Then, there is a $j, 1 \leq j \leq v$, such that

$$\left\lfloor \frac{h-v+1}{v+1} \right\rfloor = \frac{h-v+j+1}{v+1}.$$  

But $\frac{h-v+j+1}{v+1} \leq \frac{h+1}{v+1}$ since $j \leq v$, so (29) follows also in this case.

Next we have to prove that this minimum distance $d$ given by (28) meets bound (24) of Theorem 26. It suffices to observe that $d = d(v, h, g; g + 1)$ by (22). □

Notice that, in particular, if $v = 0$, we have an LRC code, as in Corollary 29. In this case, Theorem 30 asserts that when $1 \leq g \leq h + 1$, then $C(n, u)$ is an optimal LRC code with minimum distance $d = h + g + 1$. This result was also observed in (2), Corollary 2.3.

Let us examine now the case of EP($m, 1; n, 1; 2$) codes, where $m, n \geq 3$. In this case, bound (24) gives $d(m, 1; n, 1; 2) \leq 8$. Consider for example a 2-level EII code $C(n, (1, 1, 1, 3, n))$ or a 2-level EII code $C(n, (1, 1, \ldots, 2, 2, n))$. These are the only cases of EII codes that are $EP(m, 1; n, 1; 2)$ codes. In both cases, according to Theorem 15 the minimum distance is 6, so bound (24) is not met. We present next an optimal $EP(m, 1; n, 1; 2)$ code. The construction is related to the PMDS constructions in (2). The tradeoff is that the finite field is larger than the required one for EII codes.

Let $GF(2^b)$ be a finite field and $\alpha$ an element in $GF(2^b)$ such that $mn \leq O(\alpha)$ (remember, $O(\alpha)$ denotes the order of $\alpha$). Consider the parity-check matrix $H_2$ given by

$$H_2 = \begin{pmatrix}
I_m \otimes (1, 1, \ldots, 1) \\
\alpha \alpha^2 \ldots \alpha^{mn-1} \\
1 \alpha^{-1} \alpha^{-2} \ldots \alpha^{-(mn-1)}
\end{pmatrix},$$

(30)

where $I_m$ denotes the $m \times m$ identity matrix and $\otimes$ the Kronecker product (28) of two matrices. Notice that the first $m + n$ rows of $H_2$ in (30) correspond to the parity-check matrix of the product code with single parity in rows and columns. We denote the matrix in (30) $H_2$ to indicate that two extra parities are added to the product code.

The following theorem shows that the code whose parity-check matrix is $H_2$ is an optimal $E(m, 1; n, 1; 2)$ code.

**Theorem 31.** Consider the $EP(m, 1; n, 1; 2)$ code whose parity-check matrix $H_2$ is given by (30), $m, n \geq 3$ and $mn \leq O(\alpha)$. Then, the code has minimum distance 8.

**Proof:** We have to prove that any 7 erasures can be corrected.

First assume that there are six erasures in locations $(i_0, j_0), (i_0, j_1), (i_0, j_2), (i_1, j_0), (i_1, j_1)$ and $(i_1, j_2)$, where $0 \leq i_0 < i_1 \leq m - 1$ and $0 \leq j_0 < j_1 < j_2 \leq n - 1$ or $(i_0, j_0), (i_0, j_1), (i_1, j_0), (i_1, j_1), (i_2, j_0)$ and $(i_2, j_1)$, where $0 \leq i_0 < i_1 < i_2 \leq m - 1$ and $0 \leq j_0 < j_1 < j_2 \leq n - 1$.
and 0 ≤ j_0 < j_1 ≤ n − 1, and a seventh erasure in any other location. This seventh erasure can be corrected using either horizontal or vertical parities, thus, it is enough to prove that the two situations of six erasures described above are correctable.

Consider the first case. It suffices to prove, using the parity-check matrix as given by (30), that the 6 × 6 matrix

\[
\begin{pmatrix}
1 & 1 & 1 & 0 & 0 & 0 \\
0 & 0 & 0 & 1 & 1 & 1 \\
0 & 1 & 0 & 0 & 1 & 0 \\
0 & 0 & 1 & 0 & 0 & 1 \\
1 & a^i_1 & a^i_2 & a^{in+j_0} & a^{in+j_1} & a^{in+j_2} \\
1 & a^{-i_1} & a^{-i_2} & a^{-in+j_0} & a^{-in+j_1} & a^{-in+j_2}
\end{pmatrix}
\]

is invertible. Redefining \( i \leftarrow i_1 - i_0, j_1 \leftarrow j_1 - j_0 \) and \( j_2 \leftarrow j_2 - j_0 \), where now \( 1 ≤ i ≤ m − 1 \) and \( 1 ≤ j_1 < j_2 ≤ n − 1 \), this matrix is invertible if and only if matrix

\[
\begin{pmatrix}
1 & 1 & 1 & 0 & 0 & 0 \\
0 & 0 & 0 & 1 & 1 & 1 \\
0 & 1 & 0 & 0 & 1 & 0 \\
0 & 0 & 1 & 0 & 0 & 1 \\
1 & a^i_1 & a^i_2 & a^{in} & a^{in+j_1} & a^{in+j_2} \\
1 & a^{-i_1} & a^{-i_2} & a^{-in} & a^{-in+j_1} & a^{-in+j_2}
\end{pmatrix}
\]

is invertible. By Gaussian elimination, this 6 × 6 matrix is invertible if and only if the 2 × 2 matrix

\[
\begin{pmatrix}
(1 \oplus a^i_1)(1 \oplus a^{in}) & (1 \oplus a^{i_1})(1 \oplus a^{in}) \\
(1 \oplus a^{-i_1})(1 \oplus a^{-in}) & (1 \oplus a^{-j_1})(1 \oplus a^{-in})
\end{pmatrix}
\]

is invertible, if and only if, since \( 1 \oplus a^{in} \neq 0 \),

\[
\begin{pmatrix}
1 \oplus a^i_1 & 1 \oplus a^{i_2} \\
1 \oplus a^{-i_1} & 1 \oplus a^{-i_2}
\end{pmatrix}
= \begin{pmatrix}
1 \oplus a^i_1 & 1 \oplus a^{i_2} \\
1 \oplus a^{-i_1} & 1 \oplus a^{-i_2}
\end{pmatrix}
\]

is invertible, if and only if, since \( 1 \oplus a^i_1 \neq 0 \) and \( 1 \oplus a^{i_2} \neq 0 \), \( a^i_1 \neq a^{i_2} \), which is the case since \( 1 ≤ j_1 < j_2 ≤ n − 1 \). The second case is proven similarly.

Next, assume that there are seven erasures, such that each row and column has at least two erasures. This can only happen if one row (column) has three erasures and two rows (columns) have two erasures.

Let \( i_0 \) be the row with three erasures, and \( j_0 \) the column with three erasures, while \( j_1 < j_2 \) and \( i_1 \) is such that erasures are in \((i_1, j_0)\) and \((i_1, j_1)\) so the remaining two erasures are in \((i_2, j_0)\) and \((i_2, j_2)\). Redefining \( i_1 \leftarrow i_1 - i_0, i_2 \leftarrow i_2 - i_0, j_1 \leftarrow j_1 - j_0 \) and \( j_2 \leftarrow j_2 - j_0 \), it suffices to prove, using the parity-check matrix \( H_2 \) as given by (30), that the 7 × 7 matrix

\[
\begin{pmatrix}
1 & 1 & 1 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 1 & 1 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 1 & 1 \\
0 & 1 & 0 & 0 & 0 & 1 & 0 \\
0 & 0 & 1 & 0 & 0 & 0 & 1 \\
1 & a^i_1 & a^i_2 & a^{i_1+j_0} & a^{i_1+j_1} & a^{i_2+n+j_2} & a^{i_2+n+j_1} \\
1 & a^{-i_1} & a^{-i_2} & a^{-i_1+n-j_0} & a^{-i_1+n-j_1} & a^{-i_2+n-j_2} & a^{-i_2+n-j_1}
\end{pmatrix}
\]

is invertible, if and only if, doing Gaussian elimination like in the other two cases, the 2 × 2 matrix

\[
\begin{pmatrix}
(1 \oplus a^i_1)(1 \oplus a^{i_1+j_0}) & (1 \oplus a^{i_1})(1 \oplus a^{i_1+j_1}) \\
(1 \oplus a^{-i_1})(1 \oplus a^{-i_1+n-j_0}) & (1 \oplus a^{-i_1})(1 \oplus a^{-i_1+n-j_1})
\end{pmatrix}
= \begin{pmatrix}
(1 \oplus a^i_1)(1 \oplus a^{i_1+j_0}) & (1 \oplus a^{i_1})(1 \oplus a^{i_1+j_1}) \\
(1 \oplus a^{-i_1})(1 \oplus a^{-i_1+n-j_0}) & (1 \oplus a^{-i_1})(1 \oplus a^{-i_1+n-j_1})
\end{pmatrix}
\]

is invertible, if and only if, since \( 1 \oplus a^i_1, 1 \oplus a^{i_1+j_0}, 1 \oplus a^{i_1+j_1} \) and \( 1 \oplus a^{i_1+n-j_0} \) are non-zero, \( a^{i_1+j_0} \neq a^{i_1+j_1} \) which is the case since \( mn ≤ O(\alpha) \), thus \((i_2 - i_1)n + j_2 - j_1 \neq 0 \ (mod \ O(\alpha))\). For complete details, see [3]. □

Consider next the 3-level EII code \( C(n, \underline{u}) \), where \( \underline{u} = \left( \frac{m-3}{4}, 1, \ldots, 1, 2, 3, n \right) \). This is an EP(m, 1; n; 1; 3) code. According to Theorem [15] \( C(n, \underline{u}) \) has minimum distance 8, the same as the code given by parity-check matrix \( H_2 \), at the cost of an extra parity. However, there is a tradeoff: the size of the field required by \( C(n, \underline{u}) \) is greater than \( \max\{m; n\} \), while the field required by the code whose parity-check matrix is \( H_2 \) must have size greater than \( mn \). Also, by Theorem [5] \( C(n, \underline{u}) \) can correct any 8 erasures involving two rows with 3 erasures and one row with two erasures, like for example a pattern with
The degree of this polynomial is
\[ \sum d \]
where \( d \) is a polynomial of degree \( \sum \). The code generated by \( \mathcal{H}_2 \) is unable to correct such pattern since it does not have enough parities; so, even if both codes have the same minimum distance, \( \mathcal{C}(n, \mathbf{u}) \) can correct more erasure patterns. These tradeoffs need to be evaluated when implementation is considered.

We end this section with a construction of \( EP(m, 1; n, 1; g) \) codes. Let \( f(x) \) be a binary irreducible polynomial of degree \( b \), \( GF(2^b) \) the field of polynomials modulo \( f(x) \) and \( a \) an element in \( GF(2^b) \) such that \( f(a) = 0 \). Consider the following parity-check matrix of a code consisting of \( m \times n \) arrays:

\[
\mathcal{H}(m, n; g) = \begin{pmatrix}
I_m \otimes (1, 1, \ldots, 1) \\
(1, 1, \ldots, 1) \otimes I_n \\
1 & a & a^2 & \ldots & a^{mn-1} \\
1 & a^2 & a^4 & \ldots & a^{2(mn-1)} \\
\vdots & \vdots & \vdots & \ddots & \vdots \\
1 & a^g & a^{2g} & \ldots & a^{g(mn-1)}
\end{pmatrix}
\] (31)

Notice that the first \( m + n \) rows of \( \mathcal{H}(m, n; g) \) correspond to the parity-check matrix of a product code with single horizontal and vertical parities, while the last \( g \) rows correspond to the parity-check matrix of an \([mn, mn - g, g + 1] \) RS code over \( GF(2^b) \). Assume that \( b \geq \sum_{i=0}^{g-1} (i + 1)(mn - g + i) \). We will show that the code \( \mathcal{C}(m, n; g) \) whose parity-check matrix is \( \mathcal{H}(m, n; g) \) as given by (31) is an optimal \( E(m, 1; n, 1; g) \) code. Before proving this result, we need the following lemma:

**Lemma 32.** Let

\[
\Delta_{j_0,j_1,\ldots,j_{g-1}}(x) = \begin{pmatrix}
x^{j_0} & x^{j_1} & x^{j_2} & \ldots & x^{j_{g-1}} \\
x^{2j_0} & x^{2j_1} & x^{2j_2} & \ldots & x^{2j_{g-1}} \\
x^{3j_0} & x^{3j_1} & x^{3j_2} & \ldots & x^{3j_{g-1}} \\
\vdots & \vdots & \vdots & \ddots & \vdots \\
x^{gj_0} & x^{gj_1} & x^{gj_2} & \ldots & x^{gj_{g-1}}
\end{pmatrix}
\] (32)

be a \( g \times g \) matrix of powers of \( x \), where the \( j_i \)s are integers such that \( 0 \leq j_0 < j_1 < \cdots < j_{g-1} \). Then the binary polynomial \( \det(\Delta_{j_0,j_1,\ldots,j_{g-1}}(x)) \) has degree \( \sum_{i=0}^{g-1} (i + 1)j_i \).

**Proof:** By properties of Vandermonde determinants,

\[
\det(\Delta_{j_0,j_1,\ldots,j_{g-1}}(x)) = \left(x^{\sum_{i=0}^{g-1} j_i}\right) \prod_{0 \leq u < v \leq g-1} (x^u \oplus x^v).
\]

The degree of this polynomial is \( \sum_{i=0}^{g-1} (i + 1)j_i \) by induction on \( g \). \( \square \)

**Theorem 33.** Consider the code \( \mathcal{C}(m, n; g) \) whose parity-check matrix is \( \mathcal{H}(m, n; g) \) as given by (31), where \( GF(2^b) \) is the field of polynomials modulo the binary irreducible polynomial \( f(x) \), \( f(a) = 0 \) and \( b \geq \sum_{i=0}^{g-1} (i + 1)(mn - g + i) \). Then, \( \mathcal{C}(m, n; g) \) is an optimal \( E(m, 1; n, 1; g) \) code.

**Proof:** Assume that \( d \) is the upper bound on the minimum distance of a code \( E(m, 1; n, 1; g) \) given by Theorem 26. We will prove that any \( d - 1 \) erasures can be corrected by code \( \mathcal{C}(m, n; g) \). So, assume that we have \( d - 1 \) erasures, say, in locations \( 0 \leq j_0 < j_1 < \cdots < j_{d-2} \leq mn - 1 \). From Theorem 26 there are \( d - 1 - g \) horizontal and vertical parities covering the erasures that are linearly independent (otherwise, we would be violating the bound). Consider the \((d - 1) \times (d - 1)\) submatrix of \( \mathcal{H}(m, n; g) \) whose entries are given by the intersection of columns \( j_0, j_1, \ldots, j_{d-2} \) with the rows corresponding to the aforementioned \( d - 1 - g \) linearly independent horizontal and vertical parities, followed by the last \( g \) rows of \( \mathcal{H}(m, n; g) \).

We have to prove that this matrix is invertible. The matrix looks as follows:

\[
H_{d-1} = \begin{pmatrix}
V \\
W
\end{pmatrix}
\]

where \( V \) is a \((d - 1 - g) \times (d - 1)\) matrix of rank \( d - 1 - g \) whose entries are 0s and 1s and
\[
W = \begin{pmatrix}
\alpha^0 & a^1_0 & \ldots & a^{i-2}_0 \\
\alpha^2 & a^2_1 & \ldots & a^{2i-2}_2 \\
\vdots & \vdots & \ddots & \vdots \\
\alpha^8 & a^8 & \ldots & a^{i-2}_8
\end{pmatrix}.
\]

If \( U \subseteq \{0, 1, \ldots, d - 2\} \) and \( U = \{0, 1, \ldots, d - 2\} - U \), denote by \( V[U] \) the columns of \( V \) in locations \( U \) and by \( W[U] \) the columns of \( W \) in \( U \). By properties of determinants and since \( V \) is a binary matrix,

\[
\det H_{d-1} = \bigoplus_{U \subseteq \{0, 1, \ldots, d - 2\}} (\det V[U]) (\det W[U]) = \bigoplus_{U \subseteq \{0, 1, \ldots, d - 2\}} \det W[U]. \tag{33}
\]

Since \( V \) has rank \( d - 1 - g \), let \( U_0 \) be the first subset of \( \{0, 1, \ldots, d - 2\} \) in alphabetical order such that \( |U_0| = d - 1 - g \) and \( \det V[U_0] = 1 \). Then, \( U_0 \) is the last subset in alphabetical order such that \( \det V[U_0] = 1 \), i.e., if \( U \neq U_0 \), \( \det V[U] = 1 \). \( U = \{u_0, u_1, \ldots, u_{g-1}\} \) and \( U_0 = \{w_0, w_1, \ldots, w_{g-1}\} \), where \( u_0 < u_1 < \cdots < u_{g-1} \) and \( w_0 < w_1 < \cdots < w_{g-1} \), then \( u_i \leq w_i \) for \( 0 \leq i \leq g - 1 \). By Lemma 32 \( \det W[U_0] \) has degree (as a polynomial in \( \alpha \)) \( \sum_{i=0}^{g-1} (i+1)w_i \), which is the degree of \( \det W[U_0] \). This means, \( \alpha^{\sum_{i=0}^{g-1} (i+1)w_i} \) cannot be canceled by any other power of \( \alpha \) in (33) and \( \det H_{d-1} \) is a polynomial in \( \alpha \) of degree \( \sum_{i=0}^{g-1} (i+1)w_i \). Since \( \sum_{i=0}^{g-1} (i+1)w_i < \sum_{i=0}^{g-1} (i+1)(m - g + i) \leq b \), \( \det H_{d-1} \neq 0 \) and \( H_{d-1} \) is invertible.

We illustrate the proof of Theorem 33 in the following example:

**Example 34.** Consider the code \( \mathcal{C}(4, 9; 3) \) whose parity-check matrix is \( \mathcal{H}(4, 6; 3) \) as given by (31), and assume that \( b \geq 33 + (2)(34) + (3)(35) = 206 \). For instance, we may take \( \mathcal{C}(4, 9; 3) \) over the field \( \text{GF}(2^{206}) \) with \( f(x) \) an irreducible polynomial of degree 206 and \( f(\alpha) = 0 \). According to Theorems 26 and 33 the minimum distance of this code is \( d = 9 \), i.e., any 8 erasures can be corrected. In effect, assume that we have the following array with 8 erasures:

| E | E | E | E | E | E | E | E |
|---|---|---|---|---|---|---|---|
| E | E | E | E | E | E | E | E |
| E | E | E | E | E | E | E | E |

Following the proof of Theorem 33 erasures have occurred in locations

\[ \{j_0, j_1, j_2, j_3, j_4, j_5, j_6, j_7\} = \{1, 4, 18, 19, 22, 23, 27, 32\}. \]

Using the parity-check matrix \( \mathcal{H}(4, 9; 3) \) given by (31), it suffices to prove that the following \( 8 \times 8 \) determinant in \( \alpha \) is non-zero:

\[
g(\alpha) = \begin{vmatrix}
1 & 1 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & 1 & 1 & 1 & 1 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & 1 & 0 & 0 & 0 & 0 & 0 & 0 \\
1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
\alpha & \alpha^2 & \alpha^3 & \alpha^4 & \alpha^5 & \alpha^6 & \alpha^7 & \alpha^8 \\
\alpha^2 & \alpha^3 & \alpha^4 & \alpha^5 & \alpha^6 & \alpha^7 & \alpha^8 & \alpha^9
\end{vmatrix}. \tag{34}
\]

The first invertible \( 5 \times 5 \) submatrix of the first 5 rows is

\[
\begin{pmatrix}
1 & 1 & 0 & 0 & 0 \\
0 & 0 & 1 & 1 & 0 \\
0 & 0 & 0 & 0 & 0 \\
1 & 0 & 0 & 0 & 0 \\
0 & 0 & 1 & 0 & 1
\end{pmatrix},
\]

which corresponds to the subset of columns \( U_0 = \{0, 1, 2, 3, 6\} \). The complement of this set of columns is \( \overline{U}_0 = \{4, 5, 7\} \), so, since \( \{j_4, j_5, j_7\} = \{22, 23, 32\} \), the degree of \( g(\alpha) \) in (34) corresponds to the degree of \( \Delta_{22,23,32}(\alpha) \) which, by Lemma 32 is \( 22 + (2)(23) + (3)(32) = 164 \). Since \( b = 206 \), \( g(\alpha) \) is non-zero. \( \square \)
Theorem 33 provides an infinite family of $E(m, 1; n, 1; g)$ codes. It is sufficient to use a code $C(m, n; g)$ over a field $GF(2^b)$ with $b \geq \sum_{i=0}^{g-1} (i+1)(mn-g+i)$. However, from a practical point of view, this process requires a very large finite field with the corresponding increase in complexity even for relatively small values of $m$, $n$, and $g$, as we have seen in Example 33.

A way to overcome this problem and make the codes practical for implementation is to use the field $GF(2^p-1)$, $p$ a prime number, such that $GF(2^p-1)$ is generated by the irreducible polynomial $M_p(x) = 1 + x + x^2 + \cdots + x^{p-1}$. This field was often used in array codes requiring symbols of large size [5]. The polynomial $M_p(x)$ is not irreducible for every prime number $p$. For example, $M_5(x)$ is irreducible but $M_7(x) = (1+x+x^3)(1+x^2+x^3)$. For an irreducible $M_p(x)$, if $M_p(\alpha) = 0$, then $\alpha^p = 1$. If we choose $M_p(x)$ and $p$ is a prime number large enough, then we can apply Theorem 33 and the code will be optimal. We state this result as a corollary.

**Corollary 35** Consider the $EP(m, 1; n, 1; g)$ code whose parity-check matrix is given by (31) with $\alpha$ in (31) a zero of $M_p(x)$, $p$ a prime number, $M_p(x)$ irreducible and $\sum_{i=0}^{g-1} (i+1)(mn-g+i) \leq p-1$. Then the code is an optimal $EP(m, 1; n, 1; g)$ code.

Although the field of polynomials modulo $M_p(x)$ has size $2^p-1$ (possibly a very large number), no look-up tables are necessary in implementation, since most operations reduce to XORs and rotations [5], but we omit the details here. Strictly speaking, it is not proven that the number of primes such that $M_p(x)$ is irreducible is infinite, but from a practical point of view, it is always possible to find such a large enough prime number.

**C. Some Performance Considerations**

It is known that in product codes, the row-column iterative decoding algorithm does not necessarily correct all the correctable erasure patterns [15], [23]. Similarly, there may be correctable erasure patterns in a $t$-level EII code that cannot be corrected by the row-column iterative decoding algorithm. In effect, if after applying the iterative decoding algorithm there are still erasures left, sometimes such erasures may be corrected by solving a linear system using the parity-check matrix of the code. However, we do not deal here with this residual erasure correcting capability. A natural question is, how much does the row-column iterative decoding algorithm enhance the individual row or column decoding algorithms? The answer depends on the particular parameters considered. For example, take a 4-level II code $C(7, (1, 2, 3, 6, 6))$. By Theorem 15 the minimum distance of this code is $d = 7$. Assume that the erasures, which may correspond to failures of whole storage devices, occur randomly, one after the other. An important parameter is the average number of erasures that will produce an uncorrectable pattern [2]. If we decode by rows only, a Monte Carlo simulation for this example gives that this average number is 14.1. The column code, by Theorem 13 is a 4-level EII code $C(5, (0, 2, 2, 2, 3, 4, 5))$. Decoding by columns, the Monte Carlo simulation gives that the average number of erasures producing an uncorrectable pattern is 13.3. The iterative row-column decoding algorithm gives an average of 15.3, better than the other two algorithms taken separately. Another way of looking at the performance of the three algorithms is as follows: assume that a (random) number of erasures greater than the minimum distance has occurred. What is the probability that each of the algorithms will correct such a pattern? For example, taking the same code $C(7, (1, 2, 3, 6, 6))$, assume that 13 erasures have occurred. Again by Monte Carlo simulation, we found out that the row decoding algorithm corrects 64% of such patterns, the column decoding algorithm corrects 49%, while the iterative row-column decoding algorithm corrects 84% of them.

The average number of erasures causing an uncorrectable erasure pattern in a $t$-level EII code is very related to the mean time to data loss (MTTDL) [10]–[12] in RAID type of architectures, specially when failures occur following a Poisson model [12], and to birthday surprise type of problems [24]. For example, assume that we have a RAID 5 type of architecture, where each row of an $m \times n$ array is in an $[n, n-1, 2]$ code. Using the notation of $t$-level EII codes, this scheme corresponds to a 1-level II code $C(n, \underbrace{1, 1, \ldots, 1}_{m})$. When erasures start occurring, there will be an uncorrectable pattern when one row has two erasures. What is the average number of erasures causing this uncorrectable pattern? This question is equivalent to the birthday surprise problem: assuming that people arrive at random in a planet whose year has $m$ days, what is the average number of people that arrive until two of them have the same birthday? An exact formula for this number is well known, mainly, it is $m \int_0^m e^{-mx}(1+x)^m dx$ [24]. On Earth, $m = 365$ and this average gives 24.6, the birthday surprise number. It is possible to obtain exact formulae for the average of general $t$-level EII codes, but such formulae become too complicated. The Monte Carlo simulations provide good approximations though.

We end this subsection with an example comparing an EII code with other types of codes. Consider a code of length 64 and rate 1/2. Certainly, an MDS code has minimum distance 33, but an (extended) RS code requires the code to be at least over the field $GF(64)$. If we want a smaller field like $GF(16)$, we can use, for instance, Algebraic Geometry (AG) codes. In [19], page 23, a $[64, 32, 27]$ AG code over $GF(16)$ is presented. Consider a 5-level II code $C(8, (2, 3, 3, 4, 5, 5, 5, 6))$, also over $GF(16)$. This code, by Theorem 13 is a $[64, 32, 7]$ code, so its minimum distance is considerably smaller than the one of the AG code. However, the average number of erasures that are uncorrectable, by Monte Carlo simulation, is 30.1. If the AG does not correct erasures beyond its minimum distance, the average number of uncorrectable erasures is precisely the minimum distance 27, meaning that on average the AG code corrects less erasures than $C(8, (2, 3, 3, 4, 5, 5, 5, 6))$. Also, the
AG code, as well as the RS code, have no locality properties. In particular, $C(8,(2,3,3,4,4,5,5,6))$ is an LRC code of length 64 and locality 6. In addition to the 16 local parities, there are 16 extra (global) parities. By \((\ref{eq:7})\), an upper bound on the minimum distance of such a code is 23. Assuming a code meeting the bound (i.e., optimal) is used and there is no correction beyond the minimum distance once the local erasures have been corrected, a Monte Carlo simulation gives that the average number of uncorrectable erasures is 27, which is below 30.1 as given by the EII code. Another way of looking at the problem is the following: assuming that a number of erasures have occurred, what are the probabilities that an optimal LRC code or the row-column decoding algorithm of $C(8,(2,3,3,4,4,5,5,6))$ will decode such a pattern? For example, assuming that 27 erasures have occurred, a Monte Carlo simulation gives that an optimal LRC code can correct roughly 50% of such patterns, while $C((2,3,3,4,4,5,5,6))$ can correct 88% of them. The best constructions of optimal LRC codes would require a field of size at least the length of the code \((\ref{eq:39})\), 64 in this case.

The examples given above show that there are tradeoffs to be considered when choosing an EII or an optimal LRC code in applications.

IV. Conclusions

We have studied extended product (EPC) codes, which consist of a product code with some extra parities added in order to increase the minimum distance. We presented an upper bound on the minimum distance of EPC codes and we gave constructions of codes achieving this upper bound for the case in which the product code consists of single parity on rows and columns. We also studied in detail a special case of EPC codes: Extended Integrated Interleaved (EII) codes, which in general do not meet the bound on the minimum distance, but require a small finite field and allow for a large variety of possible parameters, making them an attractive alternative for implementation in practical cases. We showed that EII codes naturally unify product codes and Integrated Interleaved (II) codes. We provided the distance, the dimension and encoding and (erasure) decoding algorithms for any EII code. We showed that EII codes often have better minimum distance than II codes with the same rate, they allow for decoding on columns as well as on rows (enhancing the correction capability of the decoding algorithm) and they permit an uniform distribution of the parity in the array.

ACKNOWLEDGMENT

We want to thank Prof. Yuval Cassuto for pointing out the problem of balanced distribution of parity in Integrated Interleaved codes.

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