Controlling the entanglement between qubits is central to the development of every quantum computing architecture. Early efforts with superconducting quantum circuits relied on quantum interference for this purpose. Programmed sequences of one- and few-qubit gates fitting within qubit coherence times have allowed the generation of two- and three-qubit entanglement [1–4] and the implementation of elementary quantum algorithms [5–9] and games [10].

Recently, focus has shifted toward generating and preserving entanglement by nondemolition measurement of multiqubit observables and their use in feedback loops as required for quantum error correction [11]. Of particular interest is the parity measurement [12–14] that discriminates between states in a multiqubit register with even or odd total excitation number. Parity measurement on four data qubits at the corners of every square tile on a lattice is needed to realize surface codes, offering the highest fault-tolerance thresholds to date [15,16].

A convenient approach to implementing a parity measurement is a two-step indirect scheme involving coherent interaction of the data qubits with an ancillary qubit and subsequent strong measurement of this ancilla. To date, indirect four-qubit parity measurements have been achieved only in trapped-ion systems [17]. In the solid state, parity measurement using an ancillary electron spin has been used to generate probabilistic entanglement between two nuclear spins in nitrogen-vacancy centers in diamond [18]. More recently, parity measurement of two transmon qubits using a dispersively coupled 3D cavity has been used in a digital feedback loop to generate entanglement deterministically [19]. An important next step is the realization of parity measurements in an architecture amenable to surface coding.

In this Letter, we present an ancilla-based two-qubit parity measurement in a planar circuit QED (cQED) architecture [20]. Tomographic characterization shows that dephasing within even and odd parity subspaces is due to intrinsic qubit decoherence during interaction and measurement steps, making the parity meter intrinsically quantum nondemolition (QND). As a further demonstration of this nondemolition character, we generate entanglement by parity measurement on a maximal superposition state. Performing all tomographic data-qubit operations after the ancilla measurement, we achieve a concurrence of 0.46 (0.38) in the even (odd) measurement outcome using a single threshold for conditioning on the ancilla readout, matching the open-loop performance of the recent implementation based on continuous measurement [19]. A distinct architectural advantage of our two-step scheme is the possibility to continue operations on the data qubits while the ancilla measurement is performed. Performing the entanglement-by-measurement protocol using such parallel timing instead, the concurrence in the even (odd) parity outcome improves to 0.74 (0.63).

Our quantum processor, shown in Fig. 1(a), combines four transmon qubits (data qubits $D_1$ and $D_2$, ancilla $A$, and a fourth unused qubit) and five resonators, expanding the architecture introduced in Ref. [21]. A high-$Q$ resonator bus ($B$) couples to every qubit and mediates all interactions. Dedicated resonators, each dispersively coupled to one qubit, allow frequency-multiplexed individual qubit readouts via a common feedline [22,23]. Finally, flux-bias lines allow individual tuning of qubit transition frequencies with 1 ns resolution [5].

The interaction step of the parity meter involves two controlled-phase (C-PHASE) gates between $A$ and the data qubits. We compile these gates using a toolbox of resonant qubit-bus interactions proposed in Ref. [24] and first realized with phase qubits [25]. A map of coherent qubit-bus interactions in the one- and two-excitation manifolds is obtained by varying the duration and amplitude of a flux pulse on $D_1$ starting from $|e_1,0\rangle$ and $|e_1,1\rangle$, respectively [Fig. 1(b)]. We use $|g_i\rangle$, $|e_i\rangle$, and $|f_i\rangle$ to denote the ground, first, and second excited states of transmon $i$, respectively, and $|n\rangle$ to refer to the $n$-photon state of the bus. A half-period of oscillation at the $|e_1,0\rangle \leftrightarrow |g_1,1\rangle$ resonance [Fig. 1(c)] implements an iSWAP gate [26] between $D_1$ and $B$. A full period at the $|f_1,0\rangle \leftrightarrow |e_1,1\rangle$
C-PHASE gates in the interaction step of an cQED processor with four transmons (A, D1, D2, and an unused one at top right) coupled to a bus resonator. Each transmon has a dedicated readout resonator that is addressed through the shared feedline (ports 1–8–4–5) and a local flux-bias line (ports 2, 3, 6, 7) that allows tuning of the transition frequencies with ~1 ns resolution [5]. A coaxial cable connects ports 4 and 8 off the chip. (b) Gate sequence for coherent swapping of excitations between D1 and the bus by nonadiabatic qubit tuning. (c),(d) Measured average qubit populations at the end of the sequence for the one- and two-excitation manifolds, respectively. An excitation can be swapped from D1 to the bus (or vice versa) in 13.1 ns when the \( g_1-e_1 \) transition is resonant with B. (d) In the two-excitation manifold, population transfer occurs when either the \( g_1-e_1 \) or the \( e_1-f_1 \) transition is resonant with B with a half-period of 9.3 ns. The ratio of the measured periods agrees with the \( \sqrt{2} \) enhancement of the effective coupling predicted by theory.

We implement C-PHASE gates between A and D1 using three qubit-bus primitives: iSWAP\(_{A,B} \) C-PHASE\(_{B,D1} \) and iSWAP\(_{A,B} \). Note that the \( n \) C-PHASE gates in the interaction step of an \( n \)-qubit ancilla-based parity measurement can be realized with only \( n+2 \) qubit-bus primitives, since back-to-back \( A-B \) swaps can be compiled away (\( n=2 \) here).

The ideal projective ancilla measurement comprising the second step of the parity meter is high fidelity, fast relative to intrinsic qubit decoherence, and does not impose any additional backaction on data qubits. We probe the ancilla-state-dependent transmission of a dedicated, dispersively coupled resonator [28] with a microwave pulse applied to the feedline near the resonator’s fundamental (7.366 GHz). Following increasingly standard practice in cQED [29], we use a Josephson parametric amplifier (JPA) at the front end of the amplification chain to boost the readout fidelity and reduce the pulse duration [30]. Histograms of the integrated homodyne voltage \( V_{H,A} \) with an ancilla prepared in \( |e_A \rangle \) and \( |g_A \rangle \) reveal an optimal single-shot fidelity of 89% at measurement pulse duration \( \tau_{MA} = 450 \) ns while probing with ~400 intraresonator steady-state photons [Figs. 2(a) and 2(b)]. Crucially, the ancilla measurement does not induce any significant dephasing on data qubits, despite the high level of measurement power used. To show this, we embed ancilla readout pulses in the first half of standard.
echo experiments on $A$, $D_1$, and $D_2$ [Figs. 2(c)–2(f)]. For $A$, the expected coherence loss due to measurement is observed at all readout powers. For $D_1$ ($D_2$), only 2.5% ± 1% (2.3% ± 1%) of contrast is lost at the chosen measurement strength [28]. On $D_1$, whose readout resonator is closest in frequency to that of $A$, we observe power-dependent qubit detuning consistent with the ac Stark shift [31]. We correct the induced deterministic phase either with a 5 ns detuning flux pulse or in postprocessing. To completely test the QND character of ancilla measurement, we perform quantum process tomography on the data qubits undergoing 326 ns of idling, with and without an applied 300 ns ancilla readout pulse [28]. The 0.97 process fidelity between these two processes, after correcting for the phase accrued by $D_1$ with measurement on, confirms the low level of backaction.

We now combine the interaction and measurement steps described into the full parity measurement protocol shown in Fig. 3(a). We first quantify the parity measurement fidelity by analyzing the correlation between measurement results $M_p = ±1$ for data-qubit input states of definite parity, namely, the four computational states. The optimal digitizing threshold maximizes the parity readout fidelity at $F_p = 69\%$ [Fig. 3(b)]. To test the meter’s ability to preserve (suppress) coherence within (across) parity subspaces, we apply parity measurement to the maximal but separable superposition state $\frac{1}{\sqrt{2}}(|g_1g_2⟩ + |e_1e_2⟩ + |e_1e_2⟩ + |e_1e_2⟩)$ created using two $\pi/2$ pulses. State tomography of the data qubits at the end of the interaction step [parallel timing, Fig. 3(c)] shows that the average absolute coherence between states of different parity $[(|p_{ge,gg}⟩ + |p_{ge,ee}⟩ + |p_{ge,eg}⟩ + |p_{ge,eg}⟩)/4$, where $p_{ij,kl} = ⟨i|j⟩⟨k|l⟩]$ is suppressed by 90% ± 1%, while the average intraparity absolute coherence $⟨p_{ge,eg}⟩$ decreases only 10% ± 1%. Similarly, state tomography at the end of the measurement step (serial timing) shows a total intraparity coherence loss of 32% ± 1%, consistent with intrinsic qubit decoherence during $\tau_{MA}$. For parallel timing, conditioning on $M_p = +1(−1)$ unveils highly entangled states with concurrence 0.74 (0.63) and Bell-state fidelity 87% (81%). The corresponding density matrices are shown in Figs. 3(d) and 3(e), respectively. For serial timing, these values reduce to 0.46 (0.38) and 73% (67%), respectively.

Quantum process tomography of the data qubits with and without conditioning on the $M_p$ outcome [28] provides the most complete characterization of the parity measurement. For parallel timing, the fidelities to the corresponding ideal process are 0.91, 0.84, and 0.79 for no $M_p$ conditioning, conditioning on $M_p = +1$, and conditioning on $M_p = −1$, respectively. For serial timing, the respective process fidelities are 0.77, 0.70, and 0.65. From the process tomograms, we determined that the dominant error in the coherent interaction step was the 89% population transfer efficiency of the iSWAP gate between ancilla and bus [32].

![FIG. 3](color online). (a) Gate sequence realizing an indirect parity measurement of $D_1$ and $D_2$ using coherent interactions with $A$ and a subsequent projective measurement of $A$. (b) Histograms of the integrated voltage $V_{HA}$ ($\tau_{MA} = 450$ ns) for computational-basis-state inputs prepared using initial rotations $R_{0}^0, R_{1}^1 \in \{I, X\}$. The digitizing threshold indicated by the dashed line produces the maximum parity fidelity $F_p = 69\%$. (c)–(e) Manhattan-style plots of data-qubit density matrices after the parity measurement for the separable superposition input state prepared with $R_0^0 = R_1^0 = R_0^1/2$. In the unconditioned tomogram, the density matrix elements indicative of coherences between the parity subspaces have been suppressed by 90% in magnitude. The spurious residual probability amplitudes can be attributed almost entirely to the infidelity of the two iSWAP operations. (d), (e) The conditioned tomograms demonstrate probabilistic entanglement by measurement, reaching 87% (81%) fidelity to the even (odd) Bell state and 0.74 (0.63) concurrence for the even (odd) outcome. The higher fidelity of the even projection is in accordance with the error model of ancilla readout, where the dominating error mechanism is relaxation of $A$ during readout. For (c)–(e), the timing of tomographic prerotations and measurements corresponds to the parallel variation with $\tau_{MD} = 0$, as illustrated in Fig. 4(b).

Finally, we study the competition between parity readout fidelity and intrinsic qubit decoherence in the entanglement-by-measurement protocol. We vary the idling time $\tau_{MD}$ between the end of the interaction step and the beginning of the data-qubit readout pulse for both serial and parallel timings [Figs. 4(a) and 4(b), respectively]. For serial timing [Fig. 4(c), open markers], we use $\tau_{MA} = \tau_{MD} − 50$ ns, resulting in a steep initial increase in concurrence owing to rapidly improving ancilla readout fidelity followed by a decay due to intrinsic data-qubit decoherence. To quantify the evolution from a product to an entangled state of data qubits, we consider Wootters’s $\Lambda$ [33] used to define concurrence $C(\rho) = \max \{\Lambda(\rho), 0\}$. Even though the initial maximal superposition state lies at the boundary between separable and entangled two-qubit states, decoherence in the data qubits pulls the state away from the boundary, as manifested by the negative
A observed without conditioning on the ancilla measurement [Fig. 4(c), black diamonds]. This pull imposes a minimum threshold in the ancilla readout fidelity to generate entanglement by conditioning on the measurement outcome. Entanglement is established after $t_{MA} \approx 100$ ns. This time is quantitatively matched by a model including the calibrated ancilla readout errors. For parallel timing [Fig. 4(c), filled markers], in which we use the optimal integration time $t_{MA} = 450$ ns for all $t_{MD}$, entanglement decreases monotonically and consistently with the intrinsic qubit decoherence. For $t_{MD} > 450$ ns, the parallel and serial timings perform similarly because the ancilla readout fidelity is nearly constant.

The above entanglement by measurement is a discretized version of the continuous-time scheme investigated theoretically in Ref. [34]. The finite time to entanglement observed in serial timing is reminiscent of the entanglement genesis time required under continuous parity measurement. However, while the continuous scheme produces entanglement even starting from a maximally mixed state owing to the interplay of simultaneous Hamiltonian and measurement dynamics, entanglement by a discrete, projective parity measurement necessitates an initial superposition state of the data qubits. Instead, performing two parity measurements with single-qubit rotations in between would realize a QND Bell-state measurement [35], producing entanglement for any input two-qubit state. This protocol could be conveniently implemented with this processor in parallel timing by employing the unused qubit as a second ancilla.

In conclusion, we have realized a two-qubit parity meter in 2D cQED using a two-step scheme involving interaction of the data qubits with an ancilla and subsequent ancilla projection. The interaction step, employing resonant interactions at the raw speed set by qubit-bus coupling, can be efficiently compiled into $n + 2$ primitives for $n$-qubit parity measurement. Detailed characterization of the ancilla readout performed via a dedicated dispersively coupled resonator demonstrates minimal measurement-induced dephasing of data qubits (97% of single-qubit coherence retained), low measurement cross talk (2% during simultaneous three-qubit readout) [28], and high single-shot fidelity (89%). Applying the parity measurement on an unentangled superposition state of the two data qubits generates entanglement for both measurement outcomes, in both serial and parallel timings. In the former, we observe entanglement genesis after a 100 ns ancilla measurement. As a possible follow-up experiment, coupling a fifth qubit to the bus would allow the implementation of the four-qubit parity measurements necessary for quantum error correction using surface codes. We anticipate that the enhanced 2D+ connectivity offered by recent fabrication developments [36,37] will also allow the implementation of larger fragments of error-correcting lattices using this architecture.

O.-P. S. designed and fabricated the processor based on earlier devices by J. P. G. and M. M., did the measurements and data analysis, and wrote the manuscript with L. D. C. J. C. and J. P. G. devised the two-qubit C-PHASE tuneup. G. d. L. designed the mechanical parts used in the low-temperature setup. L. D. C. initiated and supervised the project. We thank W. Kindel and K. W. Lehnert for the JPA, D. Thoen and T. M. Klapwijk for NbTiN thin films, D. Risté for assistance with measurements, and A. N. Jordan for discussions. We acknowledge funding from the Netherlands Organization for Scientific Research (NWO, VIDI scheme) and the EU FP7 Projects SOLID and SCALEQIT.
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