High Frame-Rate Phase Camera for High-Resolution Wavefront Sensing in Gravitational-Wave Detectors

Erik Muñiz, Varun Srivastava, Subham Vidyant, and Stefan W. Ballmer

Department of Physics, Syracuse University, Syracuse, New York 13244, USA

(Dated: May 18, 2021)

We present a novel way of wavefront sensing using a commercially available, continuous-wave time-of-flight camera with QVGA-resolution. This CMOS phase camera is capable of sensing externally modulated light sources with frequencies up to 100 MHz. The high-spatial-resolution of the sensor, combined with our integrated control electronics, allows the camera to image power modulation index as low as -62 dBc/second/pixel. The phase camera is applicable to problems where alignment and mode-mismatch sensing is needed and suited for diagnostic and control applications in gravitational-wave detectors. Specifically, we explore the use of the phase camera in sensing the beat signals due to thermal distortions from point-like heat absorbers on the test masses in the Advanced LIGO detectors. The camera is capable of sensing optical path distortions greater than about two nanometers in the Advanced LIGO input mirrors, limited by the phase resolution. In homodyne readout, the performance can reach up to 0.1 nm, limited by the modulation amplitude sensitivity.

I. INTRODUCTION

The second generation of gravitational-wave detectors Advanced LIGO [1] and Advanced VIRGO [2] have been observing the compact binary mergers for over five years. The first direct detection of gravitational waves from the inspiral and merger of two binary black-holes happened on September 14, 2015, opening up the field of gravitational wave astronomy [3]. Since then there have been two sets of upgrades to improve the sensitivity of the Advanced LIGO and VIRGO detectors [4] and the three observational science runs have confirmed over 50 gravitational wave signals from binary collisions [5, 6].

The Advanced LIGO and VIRGO detectors are dual-recycled Fabry-Perot Michelson interferometers capable of measuring relative peak displacement between the two interferometer arms to less than one attometer [1, 2]. To enable this precise measurement, the core and auxiliary optics are seismically isolated and the feedback loops are set up to actuate on translational and angular degrees of freedom [1]. The error signals to control the optics are derived using Pound-Drever-Hall (PDH) technique [7, 8] from 9 MHz and 45 MHz sidebands modulating the carrier laser beam. These error signals are acquired using radio-frequency (RF) photodiodes - single-segment diodes for length sensing, and four-segment quadrant photodiodes for angular sensing. Similarly, beam waist position and size sensing can be done using bulls eye segment diodes [9], mode converters [10] and aperture-based schemes [11]. Alternatives to this readout scheme using jitter modulation techniques have also been proposed [12].

Since any degradation in the optical wave front will degrade these signals, significant effort has gone into ways to image the RF optical beat pattern of the readout beam for diagnostic purposes. Previously developed sensors for the LIGO and Virgo detectors include scanning-type [13–15] and optical lock-in [16, 17] phase cameras, all of which require additional optical elements ahead of the photo sensor.

In our work, we redesign the traditional time-of-flight camera to directly record these amplitude-modulated laser beat signals in QVGA resolution (320 × 240 pixels). We achieve this by phase locking the OPT8241 time-of-flight camera to the external reference oscillator that modulates the laser. This approach has the benefit of requiring minimal optical components and beam shaping, making the CMOS phase camera an excellent diagnostic tool for gravitational-wave detectors that can be easily deployed at any optical port of the interferometer. We also demonstrate that this phase camera can be used for generic wavefront sensing.

The sensitivity of interferometric gravitational wave detectors such as Advanced LIGO scales with the amount of power circulating in the arm cavities. Following the O3 upgrade, the Advanced LIGO detectors reached 200 kW of power in the arm cavities [4] for the first time. The high power in the arm cavities exposes point defects in the coatings of the test masses. Under high power these point defects, or point absorbers, burn into the coating of the Advanced LIGO test masses [13], locally heating and deforming the optic. This has proven to be problematic, limiting power buildup in the arm cavities and dark port contrast, both of which limits the sensitivity of the Advanced LIGO detectors. Specifically, the thermo-refractive and thermo-elastic surface deformations due to the point absorbers induce optical path distortions affecting the phasing of the carrier and the modulating sidebands [13]. For the carrier, this detuning results in higher-order spatial modes being resonant in the arm cavity, causing a loss in the detector sensitivity. Additionally, the non-resonant RF sidebands couple into higher-order spatial modes and degrade the alignment and control error signals error.

We aim to use the CMOS phase camera to image the deformations in the laser beat signals that arise from these point defects. In the paper, we briefly...
describe the principle of operation of time-of-flight cameras in section II. Next, in section III we describe the hardware and software changes made to reconfigure the time-of-flight camera into a CMOS phase camera. The experimental layout to test the sensitivity of the phase camera and the corresponding noise model to measure amplitude modulated signals is described in section IV A. We model the point absorber to estimate the change in beat signal and correlate it with the sensitivity of the camera in section V. Lastly, we discuss the applicability of the CMOS phase camera in Advanced LIGO and A+ detectors in section VI.

II. PRINCIPLE OF OPERATION OF TIME-OF-FLIGHT CAMERAS

FIG. 1. The prototype CMOS phase camera based on the commercially-available OPT8241 time-of-flight sensor. The custom-built enclosure ensures shielding from interference from unwanted RF signals.

Time-of-flight cameras employ a 2D array of pixels with depth sensing capabilities to independently measure the distance of a particular object in the field of view. Commercially available time-of-flight cameras typically have integrated illumination sources. They rely on either pulse-modulation, directly measuring the pulse travel time, or on continuous-wave amplitude-modulation, measuring a phase delay via demodulation with a local reference. The measured phase delay provides depth information. Since we intend to use the sensor to record the beat map of laser fields we selected the second type sensor for our research.

The amplitude $A$, phase $\phi$ and DC value $I_0$ of the amplitude modulated beam can be found by measuring the gated photo current $C$ at four demodulation phases ($\Phi = 0, \pi/2, \pi, 3\pi/2$) [19]. We have

$$A = \sqrt{(C(0) - C(\pi))^2 + (C(\pi/2) - C(3\pi/2))^2}$$
$$\phi = \angle[(C(0) - C(\pi)) + i(C(\pi/2) - C(3\pi/2))]$$
$$I_0 = \frac{C(0) + C(\pi/2) + C(\pi) + C(3\pi/2)}{4}$$

III. MOTIVATION AND DESIGN

For application as a wavefront sensor in gravitational-wave interferometers the time-of-flight camera needs to accept the interferometer RF local oscillator signal as an external reference oscillator source for demodulation. In continuous-wave amplitude-modulated time-of-flight cameras, both the illumination source and pixels are typically driven using the same internal, radio-frequency (RF) oscillator source. Shrestha et al. [20] show that the time-of-flight camera OPT8221 offers flexibility for operation as a standalone demodulation camera with an external signal driving the illumination source and the demodulation within the camera.

The design proposed here uses the OPT8221 camera evaluation board (OPT8241-CDK-EVM), which consists of two primary components: the sensor (OPT8241) and a programmable controller (OPT9221). The OPT9221 is a companion chip to the OPT8241 and is responsible for setting register functions and processing raw data. The sensor is a standard 320 $\times$ 240 pixel array (QVGA format) capable of operating at frame rates up to 150 frames per second (fps), although live-streaming with Voxelviewer software [21] is limited to 60 fps. Neighboring pixels are separated by 15 $\mu$m and are capable of demodulation up to 100 MHz. In each pixel the charge carriers are sorted into two separate charge storage wells, depending on the state of the local oscillator [22, 23]. The two wells are simultaneously read out and their difference is digitized, removing the DC component from the RF readout. Charge separation becomes less efficient at higher frequencies [24], leading to a reduction in the demodulation amplitude at higher demodulation frequencies.

The camera board can be programmed to provide 12-bit amplitude and phase, as well as 4-bit ambient values in QVGA format. The OPT8241 also features a 850 nm NIR band-pass filter covering, which has a small transmission of about 5% at 1064 nm for normal
FIG. 2. An overview of the phase camera signal chain and design components. The microcontroller acts as the host device controlling waveform generation and image frame capture. The microcontroller initializes the direct digital synthesizer board. Quadrature phase-stepped signals are generated on the four analog output channels of the direct digital synthesizer board. These signals are sent to an RF switch, which passes the appropriate signal via CMOS converter and gating switch to the camera. This gating switch removes the RF signal during the sensor readout to reduce the electronics noise. Every frame is initiated by a frame initialization trigger sent by the microcontroller to the camera board. The camera responds by sending a series of quadrature pulses back to the microcontroller, which are used in an interrupt sequence to set the control signals for the RF switch.

OPT9221 controller, the camera board responds by sending a series of quadrature (quad) exposure pulses, which trigger an interrupt sequence on the microcontroller. This interrupt sequence is used to set the logic levels on the RF switch and/or the synchronization flip-flop to output the appropriately phase-stepped signal corresponding to the quad exposure pulse.

- **Direct Digital Synthesizer (DDS) board**: Waveform generation is handled by the Analog Devices Direct Digital Synthesizer board (AD9959). The AD9959 has four independent synchronized output channels with tunable amplitude, frequency and phase. The external reference local oscillator used to modulate the interferometer input laser serves as the reference clock signal to the DDS board for synchronization. The microcontroller sets frequency, phase and amplitude registers on the DDS board for each channel and activates the settings via a separate I/O-update line. For normal operation the four channels are set to a phase of \( \Phi = 0^\circ, 90^\circ, 180^\circ, 270^\circ \) with respect to the reference clock at the desired demodulation frequency during initialization.

- **RF switch**: A single-pole, four throw RF switch from Mini-Circuits (JSW-4-272DR+) is used to switch between the four phased channels from the DDS board, selecting the camera demodulation phase. The RF switch is controlled by the microcontroller. The DDS board and RF switch together can provide an externally referenced and phase-stepped oscillator between 5 MHz to 500 MHz.

- **Synchronization Flip-Flop**: The DDS board register update is only synchronized to the internal reference clock (SYNC CLK), which is running at a higher frequency than the external reference signal. To avoid random phase jumps on update, a flip-flop is used to trigger the update of the DDS registers synchronized with the rising edge of the external reference signal. There are two schemes to operate the phase camera. First, the four channels of the DDS board are set up during camera initialization, and the flip-flop synchronization to the reference oscillator guarantees the same relative phase on every camera startup. The RF switch is then used to select the channel with the desired demodulation phase. Since this method does not rely on a synchronized register update during operations it is more robust, but also limited to four demodulation phases and a single frequency. In the second approach, we use the flip-flop to actively update the registers and phase-step the output of one of the DDS channels in accordance with the quad pulses from the camera. This scheme, discussed in-depth in section [VIII A], allows the camera to image the

---

| 25 MHz Clock Input | Direct Digital Synthesizer | Camera Extension Board |
|-------------------|---------------------------|-----------------------|
| SPI lines         | DCLK, SDO_2               | CMOS Converter        |
| PA6               | SDO_0                     | AD9513                |
| PA5               | GRO_0                     | Phase Stepped In      |
| PDA               | CSB                        | Squared CMOS Output   |
| PBD               | 90º                        | In Sync               |
| PBB               | 180º                       | Inverted              |
| PBA               | 270º                       | Gating Switch         |
| PBC               | GND                        | Gating Switch         |
| PCB               | GND                        | RF Switch             |

Microcontroller

- **Microcontroller**: We use the ARM Cortex-M4-based STM32F407 as a master device. It is programmed to control waveform generation on the Direct Digital Synthesis (DDS) board. The microcontroller transmits a data sequence to initialize each of the individual channels on the DDS board via SPI protocol. The microcontroller also initiates the frame capture sequence by sending an initial trigger pulse to the camera board. Depending on the register settings of the microcontroller, the camera board responds by sending a series of quadrature (quad) exposure pulses, which trigger an interrupt sequence on the microcontroller. This interrupt sequence is used to set the logic levels on the RF switch and/or the synchronization flip-flop to output the appropriately phase-stepped signal corresponding to the quad exposure pulse.

For application as an active wavefront sensor the camera board must be reconfigured. First, the internal modulation block of the camera board is disabled via the appropriate configuration of register settings in the OPT9221, allowing it to accept an external reference signal. This reference signal needs to be appropriately stepped in quadrature phase, with \( \Phi = 0^\circ, 90^\circ, 180^\circ, 270^\circ \). The frequency of the reference signal can also be dynamically changed between video frames, permitting imaging beat signals from multiple sidebands simultaneously. Thus, the CMOS phase camera is capable of measuring both the spatial and temporal characteristics of the illuminating beam in real-time.

In this section, we present the schematic design for the CMOS phase camera. The various electronic components, along with their functionality, are listed below and illustrated in Fig. 2.

- **Microcontroller**: We use the ARM Cortex-M4-based STM32F407 as a master device. It is programmed to control waveform generation on the Direct Digital Synthesis (DDS) board. The microcontroller transmits a data sequence to initialize each of the individual channels on the DDS board via SPI protocol. The microcontroller also initiates the frame capture sequence by sending an initial trigger pulse to the camera board. Depending on the register settings of the microcontroller, the camera board responds by sending a series of quadrature (quad) exposure pulses, which trigger an interrupt sequence on the microcontroller. This interrupt sequence is used to set the logic levels on the RF switch and/or the synchronization flip-flop to output the appropriately phase-stepped signal corresponding to the quad exposure pulse.

In section [VIII A], allows the camera to image the...
amplitude-modulated beat at different frequencies in real-time, and permits camera operation with six quads, reducing potential cross-talk from harmonics of the modulation frequencies.

- **CMOS Converter**: To convert the sinusoidal reference signal to a CMOS logic level clock, i.e. ‘squaring the clock’, we use a high-speed clock distribution integrated circuit (Analog Devices AD9513).

- **Gating Switch**: We use an additional high-speed buffer chip (Texas Instrument’s SN74LVC126A) in the signal chain to gate the output demodulation waveform during each quad exposure. Turning the RF signal off during the readout phase between camera exposures significantly reduces the camera electronics noise.

All firmware was developed within the Keil μVision IDE and flashed to the microcontroller via a USB interface. This allows the microcontroller to run as the master device after an initial reset event.

**IV. PERFORMANCE CHARACTERIZATION**

**A. Experimental Setup**

To characterize the performance of the CMOS phase camera we used the experimental setup shown in Fig. 3. A reference triangular cavity, previously used as pre-mode cleaner cavity (PMC) in the initial LIGO interferometers until 2010, is used to stabilize the frequency of a 1064 nm Nd:YAG laser via Pound-Drever-Hall (PDH) locking. The cavity has a finesse of 165 and features a piezoelectric-actuated mirror at the apex which allows for tuning of the cavity resonance. The carrier field, \( E_c = E_0 e^{i \omega_c t} \), is phase-modulated using a resonant electro-optic modulator referenced to a 25-MHz local oscillator. This adds sidebands to the optical carrier field offset at the modulation frequency \( \Omega/2\pi \). The peak-to-peak drive voltage of the local oscillator determines the amplitude of the phase modulation index \( \Gamma \). The field incident on the cavity is given by

\[
E_{in} = E_c e^{i \Gamma \cos \Omega t} \approx E_c \left( 1 + \frac{i \Gamma}{2} e^{i \Omega t} + \frac{i \Gamma}{2} e^{-i \Omega t} \right) \tag{4}
\]

To produce a significant amount of amplitude beat signal between carrier and sideband the cavity length was tuned to resonate on a single sideband. Under the simplifying assumption that one sideband passes the cavity 100%, while the other light gets reflected 100%, the reflected field becomes to first order

\[
E_{ref} \approx E_c \left( 1 + \frac{i \Gamma}{2} e^{-i \Omega t} \right), \tag{5}
\]

which is then passed through a 50/50 beam splitter where half of the laser light is reflected onto a single-element broadband RF photodiode (bbPD1) for PDH locking and the other half is directed to the camera. From here, the beam power is split again with 90% of the light going to the camera and 10% incident on a second photodiode (bbPD2) used for calibration – discussed more in section IV B. The power incident on the camera is thus amplitude modulated with power modulation index \( \Gamma \):

\[
I_{PC} = |E_{ref}|^2 \approx |E_c|^2 \left( 1 + \Gamma \sin \Omega t \right) \tag{6}
\]

Frequency terms greater than the modulation frequency are filtered by the bandwidth of the camera. In this single-sideband scenario power modulation index \( \Gamma \) is also related to the sideband-to-carrier ratio SCR via

\[
SCR \text{ [dBc]} = 20 \log_{10} \left( \frac{\Gamma}{2} \right), \tag{7}
\]

where we use the IEEE’s definition [27]. Unlike typical photodetectors, the measured photocurrent is demodulated at \( \Omega \) in each pixel on the sensor array. The corresponding amplitude and phase maps are then constructed using the measured values of I and Q for each frame. The CMOS phase camera in our experiment was set to capture at a frame rate of 7 Hz with total exposure time of 32 ms, although frame rates of up to 60 Hz are supported.

**B. Calibration**

For each frame of the 320 × 240-pixel array, the respective amplitude, phase, and ambient readout...
follows:

The calibration procedure is as follows:

1. We use the broadband photodiode (bbPD2, see Fig. 3), to determine the modulation index of the beam incident on the camera. We measured its DC transimpedance $Z_{dc}$ to be 1975 Ω and its RF transimpedance $Z_{rf}$ at 25 MHz, the frequency of the local oscillator, to be 4750 Ω. Then, the power modulation index ($\Gamma_m$) of an arbitrary beam incident on bbPD2 can be calculated via

$$\Gamma_m = \frac{1}{2} \cdot \frac{V_{rp}}{Z_{rf}} \cdot \frac{V_{dc}}{Z_{dc}},$$

where the peak-to-peak RF signal ($V_{rp}$) and the mean DC signal ($V_{dc}$) measured by bbPD2 are read on an oscilloscope. The factor of 1/2 converts peak-to-peak to amplitude.

2. To quantify the number of photons incident on the camera we measure the incident power ($P_{in}$) using a calibrated Thorlabs power meter. Since the NIR filter on the camera has a 5% transmission at 1064 nm at normal incidence [25], the total power incident on the camera after the filter ($P_{in}^{sensor}$) can be calculated. The ambient channel of the camera measures the profile of the incident beam. We fit a Gaussian profile normalized to the $P_{in}^{sensor}$ to get the Gaussian beam parameters and calculate the power density of the beam incident on the sensor, $p_{in}^{sensor}(x, y)$, see Fig. 4. The number of photons per area incident on the sensor during the exposure time $T_{exp}$ can then be estimated by

$$n_p(x, y) = \frac{p_{in}^{sensor}(x, y) \cdot T_{exp}}{h \nu}$$

The exposure time is the sum of each of the quadrature exposure time during which the signal is integrated.

3. The number of photoelectrons generated in each pixel is related to the number of photons via the quantum efficiency $\eta$, which for the OPT8241 sensor is approximately 2% for 1064 nm light [24]. Thus we have

$$N_e = \int n_e(x, y) \, dx \, dy = \int \eta \cdot n_p(x, y) \, dx \, dy$$

4. Finally, the calibration factor $\kappa$ is given by the ratio of the total number of photoelectrons generated to the sum of digital numbers reported in a region of interest (ROI):

$$\kappa_{DC}(e^-/DN) = \frac{\Sigma_{ROI}(N_e)}{\Sigma_{ROI}(DN_{DC})}$$

Similarly, the AC calibration is given by

$$\kappa_{AC}(e^-/DN) = \frac{\Sigma_{ROI}(N_e)}{\Sigma_{ROI}(DN_{AC})},$$

where $\Gamma_m$ is the modulation index from Eq. 8 and we divide by the camera output in the AC readout, $DN_{AC}$. We estimate a calibration factor of $\kappa_{DC}$ of $1.9 \cdot 10^5 \, e^-/DN$ and $\kappa_{AC}$ of $1.0 \cdot 10^5 \, e^-/DN$.

C. Noise Sources

The CMOS phase camera offers both the AC and DC readout channels, which are susceptible to noise sources typical to CMOS image sensors. These noise sources can either be temporal or spatial. Temporal noise arises due to the electronic noise from resistive components (i.e. pixel reset noise), dark noise, photon shot noise and other electronics noise related to the image sensor. Spatial noise is primarily due to the pixel-to-pixel imperfections, that is gain and threshold variations in the sensor array.

For the DC readout, the 4-bit digitization of the ambient channel results in large analog-to-digital rounding errors. As a result, the ADC quantization noise is the limiting source of noise for the DC readout. In contrast, the AC readout provides a 12-bit resolution, sufficient for resolving small fluctuations of the measured signal. For the AC readout, the different noise sources of the phase camera are presented below:

FIG. 4. The DC intensity profile, which is used in calibration, is plotted as a function of sensor position. The residual of the Gaussian fit to the DC profile is shown in the plane on the bottom. The dominant 4-bit digitization noise in the DC output is noticeable. In contrast, the RF output is digitized with 12-bit resolution.
1. **Shot Noise** in the camera translates to an effective fluctuation in the measured number of photoelectrons generated in the AC readout channel. The shot noise scales as the square root of the number of electrons generated during the exposure time. The AC shot noise can be estimated as

$$\sigma_{SN} (e^-) \approx \sqrt{2N_e}$$  \hspace{1cm} (13)

The factor of $\sqrt{2}$ is due to the demodulation. Shot noise is a fundamental limitation, but our CMOS phase camera is not shot noise limited.

2. **Electronic Noise** includes all noise sources involved in charge conversion and signal processing within the image sensor. Two prominent noise sources in the CMOS-based sensors are the amplifier noise and the pixel reset noise. Both arise due to the Johnson noise associated with the reset transistor in each pixel. Therefore, the electronic noise (in DN) can be estimated from non-illuminated regions in the frames, using the temporal standard deviation across multiple frames. The effective electronic noise can be reduced by frame- and/or pixel-averaging (i.e. temporal or spatial averaging).

3. **Fixed Pattern Noise** (FPN) describes the spatial noise associated with non-homogeneity between neighboring pixels on the sensor. Fixed pattern noise is dependent on the signal on the sensor and for a given signal can be expressed as [25]

$$\sigma_{FPN} = \alpha \tilde{C} + \beta$$  \hspace{1cm} (14)

where $\alpha$ is the gain and $\beta$ is a column or row offset in the readout. $\tilde{C}$ represents the true DC signal with additive Gaussian noise, which represents the temporal fluctuations of the signal contributing to the fixed pattern noise due to the gain. Under uniform illumination, one can estimate the fixed pixel pattern noise (in DN) as the spatial standard deviation from the mean. Using the calibration factor calculated in Section IV B, we can express the fixed pattern noise in terms of the number of electrons. As $\sigma_{FPN}$ varies as a function of the signal intensity, trivial background frame subtraction will not work to improve the SNR. Spatial averaging of frames can reduce the noise at the cost of signal resolution. Technique to subtract fixed pattern noise from phase and amplitude readouts is developed by [25].

Ideally, the limiting noise sources for the CMOS phase camera have a Gaussian distribution [29, 30]. Under this assumption, the variance of each of the quadrature measurements is constant (this assumption, the variance of each of the quadrature camera have a Gaussian distribution [29, 30]. Under ideally, the limiting noise sources for the CMOS phase camera is not shot noise limited.

D. **Quantitative Noise Measurement and Sensitivity**

Here we quantify the temporal and spatial noise of the CMOS phase camera. The noise performance is characterized using the experimental layout described in Section IV A. To characterize the DC power saturation levels, we vary the intensity of the 1064 nm laser beam incident on the CMOS sensor. We observe pixel saturation at 6μW of incident power with beam radius of 0.25 mm. Additional camera performance parameters are summarized in Table I.

To determine the RF sensing capabilities and noise limitations of the phase camera the DC intensity of the beam incident on the sensor is held constant while the power modulation index is varied by sweeping the cavity, as illustrated in Fig. 3. Using this method, we report performance measurements for power modulation index values from zero to 0.046. The AC calibration factor, calculated in section IV B, is used to convert the measured noise into equivalent number of photoelectrons. The total noise in an individual pixel in a single frame is estimated by adding in quadrature photon shot noise, fixed pattern noise, and electronics noise, all of which are measured with an integration time of 32 ms. The total amplitude and phase noise are measured as the standard deviation of the corresponding image obtained by subtracting two independent illuminated frames and dividing the result by $\sqrt{2}$. The results in Fig. 5 show a close agreement between measured and estimated noise. We find that the shot noise limit is a factor of 8 below the total measured noise and the camera sensitivity is limited by background electronic noise and fixed pattern noise.

The demodulation pixels suppress the DC contribution of the illuminating beam using correlated balance sampling [24, 61]. However, a sufficiently high carrier field intensity will saturate the pixel of the sensor. The dynamic range is the ratio of the saturation point to the noise floor defined as [24]

$$D/R = 20 \log_{10} \left( \frac{A_{sat}}{\sigma_{dark}} \right)$$  \hspace{1cm} (15)

where $A_{sat}$ is the pixel saturation value, i.e. maximum digital number and $\sigma_{dark}$ is the dark noise of the pixels. We estimate an operating dynamic range of 75 dB for the phase camera. However, due to the low modulation index of the illuminating beam in our test setup, the camera saturates in DC before reaching the full dynamic range of the amplitude and the phase readouts.

Camera performance for each measurement can also be quantified by calculating the signal-to-noise ratio (SNR). We define the SNR as the ratio of amplitude of the demodulated output $A$ (estimated using spatial averaging) to the measured amplitude noise $\sigma_A$.

$$SNR = \frac{A}{\sigma_A}$$  \hspace{1cm} (16)
We can define the SNR for individual pixels on a single frame, as well as after spatial and/or temporal averaging, Fig. 5. The figure shows the SNR as a function of the power modulation index \( \Gamma \) after averaging over a number of frames. The y-axis is scaled by \( 1/\sqrt{N_{\text{frames}}} \), such that the data points line up if the noise in the frames are independent. We find the SNR improves with the square root of the number of frames and number of pixels, consistent with temporally and spatially independent pixel noise. Pixel averaging can be implemented to improve the SNR at the cost of spatial resolution. The blue line represents a linear fit through the data. Using the fit, we estimate with 50 averages, the CMOS phase camera is capable of sensing RF signals in each pixel with \( \Gamma \) as low as 0.0009.

Bottom figures: Phase images – single frame (top row) and with 50 averages (bottom row), for incident beams with low (left) and high (right) power modulation index. The phase resolution improves linearly with SNR.

sideband-to-carrier ratio SCR in dB per frame per pixel:

\[
\text{SCR}_{\text{LIM}} = 20 \log_{10} \left( \frac{1}{\Gamma_1 \text{fr,1pix,SNR=}1} \right) = -50 \text{ dBc/fr/pix} \quad (17)
\]

Since the frame rate for the test data was 7 Hz, and we only used about 50% of the maximum quad integration time per frame (see Table 1) we find approximately

\[
\text{SCR}_{\text{LIM}} = -62 \text{ dBc/sec/pix}. \quad (18)
\]
| Capture Settings          | Minimum | Typical | Maximum | Comments                                                                                     |
|--------------------------|---------|---------|---------|----------------------------------------------------------------------------------------------|
| Frame Rate (fps)         |         | 7       | 60 (150)| Performance characterization was done at 7 fps. We tested the camera up to 60 fps (live-streaming limit). The camera supports up to 150 fps. Four quad readout is measured with π/2 phase-stepped demodulation; Six quad readout is measured with π/3 phase-stepped demodulation. |
| Quads                    | 4       | 4       | 6       | The camera allows the capture of 1-4 sub-frames to construct a single frame. Each sub-frame is constructed with the readout of all the quads above. |
| Sub-frames               | 1       | 1       | 4       |                                                                                               |
| Quad Integration time (ms)| 1% DCExp | 6.4 (15%) | 30% DCExp | Dependent on the frame rate, number of sub-quads and sub-frame and the readout time. The quad integration time cannot exceed 30% of the DC exposure due to the readout and dead time of the CMOS phase camera. |
| Pixel Resolution (µm)    |         | 15      |         | This defines the average size of each pixel on the sensor array.                                                                                     |
| Sensing Area (mm)        |         | 4.8 x 3.6 |         | The total sensing area of the CMOS sensor.                                                                                                         |
| Demodulation Frequency (MHz) | 5       | 25      | 100     | The performance characterization was done at 25 MHz. The camera supports demodulation frequency up to 100 MHz. The 5 MHz lower limit is due to the RF switch. |

| Illumination Characteristics |         |         |         |                                                                                     |
|------------------------------|---------|---------|---------|----------------------------------------------------------------------------------------------|
| DC Power (nW) [per pixel], estimated with beam size \(w_0 = 0.25\) mm | 1.5     | -       | 14.6    | The minimum input power for the pixels to sense the input beam and the maximum power before the pixels saturate. As the spot size of the incident beam increases, both the minimum and maximum DC power can be re-scaled in accordance with per pixel DC power limits above. We caution that this parameter is exposure time dependent. |
| DC Power (µW) [sensor; for incident beam of size \(w_0 = 0.25\) mm] | 0.6     | -       | 6.4     |                                                                                               |

| Frame Characterization (at 7 Hz frame rate and 15% quad integration time) |         |         |         |                                                                                     |
|------------------------------------------------------------------------|---------|---------|---------|----------------------------------------------------------------------------------------------|
| Dynamic Range (dB)                                                     |         |         | 75      | The dynamic range of the camera as estimated from the dark noise level of the camera. The CMOS phase camera can typically sense as low as 0.0063. SCR down to -50 dBc/pixel can be resolved in a single frame. With 50 frames averaged, the camera can sense incident RF beams as low as 0.0009. With 50 frames averaged we can resolve SCR down to -67 dBc/pixel. |
| \(\Gamma_m\): Single Frame                                            | 6.3^{+2.2}_{-2.2} \cdot 10^{-3} | -       | 1       |                                                                                               |
| SCR: Single frame                                                      | -50^{+3}_{-3} dBc/pixel        | -       | 0 dBc/pixel                                   |
| \(\Gamma_m\): 50 frames averaged                                      | 0.9^{+0.3}_{-0.3} \cdot 10^{-3} | -       | 1       |                                                                                               |
| SCR: 50 frames averaged                                                | -67^{+3}_{-3} dBc/pixel        | -       | 0 dBc/pixel                                   |
| Mean AC Noise (\(e^-\))                                               | 10^8                             |         |         |                                                                                               |

**TABLE I.** The table summarizes the range of camera capture setting and the absolute rating for the illumination signals.

V. SIGNAL MODELING FOR POINT ABSORBERS

One of the critical issues limiting the sensitivity of Advanced LIGO is the presence of small (few tens of um diameter) absorptive defects on the test masses, referred to as point absorbers. Exposed to the laser field in the interferometer arms, these test mass defects cause local heating and result in local optical path length distortions for the laser field. These optical
To simplify the simulation we do not include the effects of the power and the signal recycling cavities. Instead, we model the input power to be equal to the power at the beamsplitter during the Advanced LIGO O3 run [4]. The other parameters of the interferometer are summarized in Table II.

The optical path distortions due to the point absorbers are modeled with a Lorentzian profile for each of the input test masses. The carrier and the sideband fields are calculated under plane beam/paraxial approximation and the corresponding beat signals are calculated at the anti-symmetric port of the beamsplitter.

To get an approximate estimate of the phase camera’s ability to sense the effect of point absorbers LIGO’s input test masses, we present a simplified model without considering the coupled cavity layout of LIGO, shown in Fig. 7. We initially also ignore Gouy phase shifts because beamsplitter and input test masses are essentially in the same Gouy phase. We will get back to the effect of the output beam Gouy phase shift. While these are oversimplifications, they still allow us the estimate the required sensitivity to pick up the point absorber phase distortions in a phase camera image taken at the interferometer anti-symmetric port. We consider two scenarios, the current Advanced LIGO, which uses DC readout, and the A+ upgrade, which uses homodyne readout without differential arm DC offset. We choose the model parameters in accordance with the existing Advanced LIGO facilities. We assume the carrier and the 9 MHz and 45 MHz sidebands (with modulation index \( \Gamma \)) are incident at the 50/50 beamsplitter. Considering the approximate power-recycling gain of 40 for the carrier and 2 for the 45MHz sidebands we can estimate the power modulation index of the beam incident on the beamsplitter [4, 32]. The beam then propagates from the beamsplitter to the input test mass in each of the arm cavities. We model the response of the point absorbers to first-order, which affects the phase of the sidebands, but leaves the carrier unperturbed. The carrier experiences only a phase shift due to the DARM offset between the two arm cavities:

\[
\begin{align*}
\rho^\text{car}_x &= +1 \cdot e^{i\Phi_{\text{DARM}}/2} \\
\rho^\text{car}_y &= +1 \cdot e^{-i\Phi_{\text{DARM}}/2}
\end{align*}
\]

\[
\begin{align*}
\rho^\text{sb}_x &= -1 \cdot e^{i2kX(x,y)} \\
\rho^\text{sb}_y &= -1 \cdot e^{i2kY(x,y)}
\end{align*}
\]

where \( X(x,y) \) and \( Y(x,y) \) are the one-way transmission maps encoding the optical path distortions. Under these simplified assumptions one can calculate the fields of the carrier and sidebands at the anti-symmetric port of the beamsplitter. The beat map between the carrier or the reference beam with the sideband in \( \mathcal{I} \) and \( \mathcal{Q} \) is given by

\[
\begin{align*}
\mathcal{I}(x, y) &= \Re \{s^+ b^* c + c^* s b^-\} \\
\mathcal{Q}(x, y) &= \Im \{s^+ b^* c + c^* s b^-\}
\end{align*}
\]

One can analytically show that in the Gouy phase of the beamsplitter (\( \phi = 0 \)) the beat in \( \mathcal{I}(x, y) \) and \( \mathcal{Q}(x, y) \) between the carrier and the sidebands at the AS port is first-order independent of the optical path distortions due to the point absorber. We recover the usual DC readout terms:

\[
\begin{align*}
\mathcal{I}(x, y) &= 0 \\
\mathcal{Q}(x, y) &= \Gamma_p m (x, y) \sin\left(\frac{\Phi_{\text{DC}}}{2}\right) \sin \left(\frac{\omega_s L_x}{c}\right)
\end{align*}
\]

However, this does not remain true at every Gouy phase in the readout beam. The small point absorber distortion evolves differently with Gouy phase. For simplicity, we can assume that the distortion fields \( X(x, y) \) and \( X'(x, y) \) contains only one higher-order mode of order \( N = 1 + m \).
During the A+ upgrade Advanced LIGO will switch to a homodyne readout scheme, reducing the DC offset to zero, and instead using a separate carrier reference beam as local oscillator. We assume that the reference beam has about the same amplitude as the current carrier due to the DC offset, as this permits using the same sensing and readout electronics. Thus, the expression for sensing the effect from the point absorbers remains essentially the same, with one difference: For diagnostic purposes we now have control over the phase \( \alpha_{\text{Hom}} \) of the reference beam. The quadrature thus becomes

\[
\mathcal{I}^\phi = k \sqrt{P_{\text{ref}} P_{\text{in}}} \cos\left(\omega_s L_2 \frac{c}{
abla_p}\right) \sin(N\phi - \alpha_{\text{Hom}})(X - Y) \tag{27}
\]

Thus we can pick the beamsplitter Gouy phase \( \phi = 0 \) a 90 deg rotated reference beam, \( \alpha_{\text{Hom}} = \pi/2 \), removing the large beat signal in the \( \mathcal{Q} \) quadrature and avoiding the phase resolution limitation of the phase camera.

The power modulation index of the beat signal is given by

\[
\Gamma_{\mathcal{I}} = \frac{2T_{\text{DC}}}{2Q_{\text{DC}}} \quad \Gamma_{\mathcal{Q}} = \frac{2Q_{\text{DC}}}{2Q_{\text{DC}}} \tag{28}
\]

Figure Eq. 8 shows the size of the expected power modulation index signal as a function of the optical path distortion (OPD). The red horizontal dashed line represents the approximate power modulation index sensitivity limit of the phase camera for each pixel. The vertical black dashed line corresponds to the phase resolution limit of the camera, and is relevant in the presence of a large signal in the orthogonal quadrature due to the interferometer differential arm fringe offset. We expect the phase camera to be sensitive enough to pickup optical path distortions greater than about \( \sim 2 \text{ nm} \) where the readout is limited by phase resolution of the phase camera. Otherwise, the phase camera is capable of sensing OPD due to point absorbers as low as \( \sim 0.1 \text{ nm} \). Typically, the OPD caused due to point absorbers ranges between tens up to a few hundreds of nanometers. Currently Hartmann wavefront sensors are used to image point absorbers in aLIGO directly. These sensors map the point absorbers onto the surface of the test masses, but do not measure their impact on the interferometer. The CMOS phase camera does not directly image the point distortions, but instead measures the change in the interferometer phase front at the AS port caused due to these point defects.

### A. Implications for Gravitational Wave Detectors

The simple interferometer model presented above leads us to conclude that the CMOS phase camera developed in our group is capable of diagnosing the effect of point absorbers on the LIGO input test masses when installed at the interferometer anti-symmetric port. The exact Gouy phase of the camera will matter though, as there

\[
\mathcal{I}^\phi = k \Gamma_{\mathcal{I}} \sin\left(\frac{\Phi_{\text{DC}}}{2}\right) \cos\left(\omega_s L_2 \frac{c}{
abla_p}\right) \sin(N\phi)(X - Y) \tag{25}
\]

where the spatial shape is given by the beat of the fundamental and the \( N \)-th order mode, \( \Psi_N(\vec{x}, \vec{y}) \cdot \Psi_N(\vec{x}, \vec{y}) \). Equation 25 can be generalized by expanding \( X(\vec{x}, \vec{y}) \) and \( X(\vec{x}, \vec{y}) \) in terms of higher order Gaussian modes, which is straightforward, a little complicated, and not necessary if we are only interested in the camera sensitivity limitation.

The presence of a large beat signal in the \( \mathcal{Q} \) quadrature from the DC readout scheme means that we also have to worry about the camera phase resolution. The beat map phase rotation is given by

\[
\frac{\mathcal{I}^\phi}{\mathcal{Q}} = k \cot\left(\omega_s L_2 \frac{c}{
abla_p}\right) \sin(N\phi)(X - Y) \tag{26}
\]
is no signal in the beamsplitter Gouy phase. Having a separate local oscillator reference beam, either as part of the homodyne readout or as a separate local oscillator for the phase camera, will simplify the image analysis. Lastly, we note that the model presented above does not include the signal recycling cavity and the power recycling cavity of the Advanced LIGO detector. While the power recycling cavity only filters the beam incident on the beamsplitter, the signal recycling cavity will spatially filter the effect of the point absorber, cleaning up the mode. However, with a signal recycling mirror transmission around 32%, the signal recycling cavity has an extremely low finesse, preserving the distortion signal, but also making modeling rather complicated.

VI. DISCUSSION

We demonstrate a CMOS phase camera that is capable of imaging externally modulated RF beat signals incident on the sensor with high spatial resolution. The noise levels of the camera allow sensing of RF beat signals with a power modulation index as low as 0.0009 with 50 frame averages. The phase camera also has the capability to measure the beat signals at different frequencies and is sensitive to very low incident beam power levels. Lastly, the low latency image acquisition, design compactness, and relatively low cost of the phase camera make it suitable for numerous applications in wavefront diagnostics and sensing.

The primary application of a phase camera in gravitational-wave detectors is for diagnostic purpose, imaging any unexpected phase front distortions, such as for example those induced by point absorbers on test masses (see section V). However the phase camera can also be useful for controlling alignment and mode-matching in an interferometer. While quadrant-photodiode and bullseye-photodiode-based schemes, with [10] and without [11] optical mode converters, have the advantage of higher signal-to-noise, they offer only four “pixels” across the beam. In particular, the 320×240 pixel resolution of the CMOS phase camera provides the sensing capabilities to operate and control interferometers with higher-order Laguerre-Gauss or Hermite-Gauss modes as the operating resonant mode, a scheme that was proposed to reduce the coupling of thermal noise to the gravitational readout [31,32]. Additionally, it has been shown that these cameras can be operated synchronously [20], which allows for multiple cameras to simultaneously record the beam in separate Gouy phases.

In summary, we expect that the CMOS phase camera will be an excellent tool for commissioning Advanced LIGO, A+, and future gravitational wave detectors such as Cosmic Explorer [36] or Einstein Telescope [37], and might also have control applications.

VII. ACKNOWLEDGMENT

This research was funded by the National Science Foundation through the awards NSF PHY-1352511 and NSF PHY-1912536. We also would like thank numerous colleagues in the LIGO, Virgo and KAGRA scientific collaborations for many fruitful discussions.

A. Imaging of Multiple Modulation Frequencies with the CMOS phase camera

As discussed in section §III a flip-flop (FF) can be integrated with the CMOS phase camera to support four or six quad exposures. The flip-flop also allows the camera to subsequently image beat signals at different modulation frequencies. The electronic setup for this operation is shown in figure 9. An RF-switch is used to switch between the modulating frequencies of the local oscillators following the frame capture sequence governed by the microcontroller. The frequency and phase stepped output of the DDS are phase-locked by ensuring the updates are triggered on the rising edge of the local-oscillator by the flip-flop. The functionality of other elements in this scheme is the same as discussed in section §III. This setup is particularly useful for application in Advanced LIGO, which allows the CMOS phase camera to image beat signals at 9 MHz and 45 MHz in real time. Using this technique, the CMOS phase camera can image beat signals at different modulation frequencies with a low-latency of 1 Hz.

---

[1] J. Aasi, B. Abbott, R. Abbott, T. Abbott, M. Abernathy, K. Ackley, C. Adams, T. Adams, P. Addesso, R. Adhikari, et al., Classical and quantum gravity 32, 074001 (2015).
[2] F. Acernese, M. Agathos, K. Agatsuma, D. Aisa, N. Allemandou, A. Allocca, J. Amarni, P. Astone, G. Balestri, G. Ballardin, et al., Classical and Quantum Gravity 32, 024001 (2014).
[3] B. P. Abbott, R. Abbott, T. Abbott, M. Abernathy, F. Acernese, K. Ackley, C. Adams, T. Adams, P. Addesso, R. Adhikari, et al., Physical review letters 116, 061102 (2016).
[4] A. Buikema, C. Cahillane, G. Mansell, C. Blair, R. Abbott, C. Adams, R. Adhikari, A. Ananyeva, S. Appert, K. Arai, et al., Physical Review D 102, 062003 (2020).
[5] B. Abbott, R. Abbott, T. Abbott, S. Abraham, F. Acernese, K. Ackley, C. Adams, R. Adhikari, V. Adya, C. Affeldt, et al., Physical Review X 9, 031040 (2019).
FIG. 9. Alternate capture scheme mentioned in section III. It allows simultaneous imaging of RF signals with different modulation frequencies by selecting a separate modulation frequencies for every frame. In this scheme, the microcontroller controls an additional RF switch at the input off the DDS board. A 2-way splitter is used to provide a clock signal input to a flip-flop circuit. The flip-flop ensures a phase-locked output from the DDS by triggering the DDS clock signal input to a flip-flop circuit. The rest of the design block functionality is the same as discussed in Fig. [2]. The RF switch at the output of the DDS can be discarded if six sub-quads are desired for image capture. In this scenario, the DDS is configured to phase-step one particular channel in synchronization with the quadrature pulses received by the microcontroller.