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An Ising Hamiltonian Solver using Stochastic Phase-Transition Nano-Oscillators

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Computationally hard problems, including combinatorial optimization, can be mapped into the problem of finding the ground-state of an Ising Hamiltonian. Building physical systems with collective computational ability and distributed parallel processing capability can accelerate the ground-state search. Here, we present a continuous-time dynamical system (CTDS) approach where the ground-state solution appears as stable points or attractor states of the CTDS. We harness the emergent dynamics of a network of phase-transition nano-oscillators (PTNO) to build an Ising Hamiltonian solver. The hardware fabric comprises of electrically coupled injection-locked stochastic PTNOs with bi-stable phases emulating artificial Ising spins. We demonstrate the ability of the stochastic PTNO-CTDS to progressively find more optimal solution by increasing the strength of the injection-locking signal – akin to performing classical annealing. We demonstrate in silico that the PTNO-CTDS prototype solves a benchmark non-deterministic polynomial time (NP)-hard Max-Cut problem with high probability of success. Using experimentally calibrated numerical simulations, we investigate the performance of the hardware with increasing problem size. We show the best-in-class energy-efficiency of 3.26x10^7 solutions/sec/Watt which translates to over five orders of magnitude improvement when compared with digital CMOS, superconducting qubit and photonic Ising solver approaches. We also demonstrate an order of magnitude improvement over a discrete-time memristor-based Hopfield network approach. Such an energy efficient CTDS hardware exhibiting high solution-throughput/Watt can find application in industrial planning and manufacturing, defense and cyber-security, bioinformatics and drug discovery.

Combinatorial optimization is ubiquitous in various fields such as artificial intelligence, bioinformatics, drug discovery, cryptography, quantitative finance, operations research, resource allocation, trajectory and route planning. Such problems belong to the NP-hard or NP-complete complexity class, requiring computational resources (time and/or energy) that scale exponentially with the problem size. Interestingly, many combinatorial optimization problems can be translated into another fundamental physics problem of finding the ground state of an Ising model (1) (or its equivalent Quadratic Unconstrained Binary Optimization (QUBO) problem (2)). The Ising model, describing the property of spin glass, was put forward as a tool of statistical physics to explain the phenomenon of ferromagnetism. The Ising Hamiltonian with N discrete spins \( \sigma_{1 \leq i \leq N} \in \{-1, +1\}^N \)
and a symmetric coupling matrix $J$ in the absence of an external magnetic field is given by $H = - \sum_{i=1}^{N} J_{ij} \sigma_i \sigma_j$.

Finding the ground state of Ising model belongs to the NP-hard complexity class (3) and can be extended to other NP-hard and NP-complete problems including all of Karp's twenty-one NP-complete problems through polynomial time mapping (4). Solving the Ising model using exact methods such as the branch-and-bound algorithm are often limited to problem sizes of only a few hundred variables. Alternatively, approximate algorithms or heuristics and stochastic approaches such as semidefinite program (5), breakout local search (6), metropolis algorithm (7) and simulated annealing (8) are widely used in digital computers to find an optimal or near-optimal solution. Even for moderately sized problem instances, the time to find a near-optimal solution can become prohibitively large. Hence, there is a growing interest towards finding hardware approaches, beyond digital CMOS, that can solve large-scale constrained optimization problems efficiently. Recently, various schemes for building annealing-inspired non-von Neumann processors, called Ising machines, have been devised on a variety of technology platforms. These include superconducting qubit-based adiabatic quantum computing (AQC) and quantum annealing (9, 10), digital and mixed-signal complementary metal-oxide-semiconductor (CMOS) annealers (11–13) and coherent networks of degenerate optical parametric oscillators (14, 15). Qubit-based quantum annealers incur high cost and complexity arising from operation in cryogenic environment. The optical coherent Ising machine has shown competitive performance compared to the quantum annealer (16), but requires kilometer long fiber ring cavity for implementing 2,000 spins using temporal multiplexing and require extremely fast (and power hungry) field-programmable gate array (FPGA) for implementing coupling in a measure-and-feedback scheme (15). Digital CMOS annealers (11–13) rely on an external source for random number generation for introducing stochasticity and find it technologically challenging to maintain true randomness in CMOS implementation, while requiring significant post-processing.

One alternative pathway is to utilize a continuous-time dynamical system (CDTS) approach. Dynamical systems are defined by the change in the state of the system (say $x(t)$) with respect to time ($t$). In particular, a continuous-time dynamical system can be described by the time evolution of state, $x(t)$, as $\frac{dx(t)}{dt} = f(x)$, where $f$ is a linear or non-linear function describing the change in the dependence of the state. Recently, a vast repertoire of emergent dynamics exhibited by CTDS has been exploited across a wide range of fields from understanding the neural activity in the human brain (17, 18) to synchronized locomotion control (19, 20). Using a CTDS approach, it is possible to solve a combinatorial optimization problem that involves setting up the system appropriately such the stable point or the attractor state represents the solution of the problem. As the system evolves in time, it can dynamically find the global minima through energy minimization. Because of the inherent distributed nature and parallel processing capability, dynamical systems can find the ground state or near-optimum solution with immense speed-up compared to a sequentially working digital computer. In this work, we report an electronic phase-transition nano-oscillator (PTNO)-based CTDS that acts as an Ising Hamiltonian solver. The optimization problem, i.e. the Ising energy to be minimized is encoded as the inherent “energy” or Lyapunov function of the PTNO-CTDS. Through emergent synchronized dynamics, the PTNO-CTDS performs collective computing and rapidly attempts to find the global minima. Additionally, the PTNOs operate at room temperature and consume extremely low power which translates to several orders of magnitude improvement in energy-efficiency (solution-throughput/Watt) when
compared with digital CMOS, quantum and photonic approaches and a discrete-time memristor-based Hopfield network approach (21).

**Overview of PTNO-based CTDS as an Ising Hamiltonian solver**

The combinatorial optimization problem is reformulated in terms of the Ising Hamiltonian $H$ defined by the spin vector $\boldsymbol{\sigma}$, and the symmetric coupling matrix $J$ and mapped onto the Ising solver as shown in Fig. 1(a) and (b). Each Ising spin, representing a node in the graph is emulated by an insulator-to-metal phase-transition (IMT) nano-oscillator. A PTNO comprises a two-terminal phase-transition hysteretic device in series with a transistor as shown in Fig. 1(c). We use Vanadium Dioxide (VO$_2$) as a prototypical IMT material in our experiments (22). A false-colored SEM image of a two-terminal VO$_2$ of length = 1 $\mu$m used in our experiments is shown in Fig. 1(c).

Below the phase-transition temperature and under zero external electric field or current, VO$_2$ shows insulating behavior. Upon application of an electric field across the two terminals of the device that forces current to flow through, the material undergoes an abrupt phase transition from insulating to metallic state. The hysteretic phase transition is reflected as an abrupt hysteretic current-voltage (I-V) characteristic of the device. Pairing a conventional n-type metal–oxide–semiconductor (NMOS) transistor in series with the VO$_2$ such that the load line passes through the unstable region of the I-V curve results in self-sustained oscillations as shown in fig. 1(c). We used $V_{DD} = 2$V and a gate voltage $V_{GS} = 0.9$V in our experiments. We create a highly interconnected PTNO-network where the coupling matrix $W$ is derived from the adjacency or coupling matrix $J$ of the Ising model as shown in Fig. 1(a) and (b). In this example of Ising model with antiferromagnetic interactions, the presence of an edge between two nodes is denoted as $J = -1$ and is represented by a coupling capacitance $C_C$.

Fig.1(d) shows an overview of the experimental setup of the PTNO-based CTDS. The main computing kernel comprises eight PTNOs connected using coupling capacitance following the coupling matrix $W$. The fabricated VO$_2$ device array (labeled as 1-8 in Fig. 1(d)) are connected with eight NMOS transistors in series to create eight PTNOs. To emulate artificial Ising spins $\boldsymbol{\sigma}$, an external injection locking signal $S_{INJ}$ is applied to all the oscillators using injection capacitances $C_{INJ}$. As explained later, this creates bi-stable oscillator phases $\bar{\theta}$ that are used as state variables for computing. The continuous-time dynamics of the network is dictated by an “energy” or the Lyapunov function $E(\bar{\theta})$ that closely resembles the Ising Hamiltonian $H$. The dynamics of the network evolves continuously in time, so as to naturally minimize $E(\bar{\theta})$, and in the process minimizes the Ising Hamiltonian to reach the ground state as shown in Fig. 1(e). Once the network reaches an energy minima state, the output of the network is read out in terms of the state variables $\bar{\theta}$ and subsequently reformulated to provide the optimal solution of the original optimization problem. One formidable challenge in solving combinatorial optimization problem is the inevitable increase in the complexity of the energy landscape with the problem size. Specifically, the presence of a large number of local minima degrades the probability of reaching a global optimum. Stochasticity is a well-known technique utilized in Boltzmann machines and simulated annealing, to overcome the issue of getting trapped in local minima. In the latter case, a stochastic noise is used to perturb the state of the system and the magnitude of noise is reduced slowly over time (replicated as a change in the temperature parameter in the algorithm) as the system approaches the global optimum. In our system, we exploit the inherent stochasticity present in the
IMT material (25) to escape the local minima as well as introduce a new way of gradually reducing the temporal fluctuations in the oscillator phases by increasing the strength of the injection locking signal $S_{\text{INJ}}$. This allows us to perform classical annealing operation and obtain progressively better solutions as illustrated schematically in Fig. 1(e).

**Artificial Ising spin**

The binary degrees of freedom in the phase space of PTNOs arises from the phenomenon of second harmonic injection locking. We apply a sinusoidal injection locking signal $S_{\text{INJ}} = V_{\text{inj}} \sin(2\pi f_{\text{inj}} t)$ with $f_{\text{inj}} = 2f_0$ as an external input to the oscillators across the capacitor $C_{\text{inj}} = 20 \text{ pF}$ as shown in Fig. 1(d). When $V_{\text{inj}} = 0$, the oscillator is free running. Fig. 2(a, i) shows the voltage output waveform $V_{\text{out}}$ measured over multiple runs. The corresponding phase of the oscillator, measured with respect to a reference sinusoidal signal of same frequency $f_0$, shows a constantly varying phase with a uniform probability distribution over the entire phase space as seen Fig. 2(b, i). In contrast, when the oscillator is perturbed with $S_{\text{inj}}$ at the first harmonic, i.e. $f_{\text{inj}} \approx f_0$ also referred to as first harmonic injection locking (FHIL), $V_{\text{out}}$ shows a constant 80° phase locking configuration with $S_{\text{inj}}$ as seen in Fig. 2(a, ii). The probability distribution of the phase, measured over multiple runs, shows a single gaussian distribution as shown in Fig. 2(b, ii). Interestingly, when $f_{\text{inj}} \approx 2f_0$, the oscillator waveform shows both in-phase (40°) and out-of-phase (220°) injection locking configuration when measured over multiple runs as seen in Fig. 2(a, iii). The corresponding probability distribution shown in Fig. 2(b, iii) exhibits a double gaussian distribution highlighting an equiprobable and bi-stable phase portrait. This bi-stability provides an ideal means to encode the Ising spin in the electrical domain, where phase 40° represents up-spin, i.e., $\sigma = +1$, and phase 220° represents down-spin, i.e., $\sigma = -1$. The continuous-time dynamics of the phase difference $\theta$ between the oscillator and the injection locking signal can be described by a generalized version of Adler’s equation (Gen-Adler) given by

$$\frac{d\theta(t)}{dt} = -(f_{\text{inj}} - n_H f_0) + K_{\text{inj}}^H \int_0^{2\pi} \xi(\theta(t) + \phi) \cos(\phi) \, d\phi$$

where $n_H$ is the $n^{th}$ harmonic of the IMT nano-oscillator and $K_{\text{inj}}^H = 2\pi n_H f_0 f_{\text{inj}} C_{\text{inj}} V_{\text{inj}}$. The first term describes the frequency mismatch between $V_{\text{out}}$ and $S_{\text{INJ}}$, which contributes to phase slipping. The second term depends on the phase delay incurred due to the perturbation caused by $S_{\text{inj}}$ and is described in terms of the perturbation-projection-vector (PPV), $\xi$ (see Supplementary information section S1 and S2 for details). The corresponding “energy” function or Lyapunov function of the oscillator is given by

$$E(\theta) = (f_{\text{inj}} - n_H f_0)\theta - K_{\text{inj}}^H \int_0^{\theta} \int_0^{2\pi} \xi(\phi + \theta) \cos(\phi) \, d\phi \, d\phi$$

The first energy term comes from the contribution of the frequency mismatch that creates an overall bias in the energy landscape. However, this being a linear term does not introduce any additional valleys or peaks in the energy landscape. The second term describes the interaction between the injection locking signal and the oscillator. The corresponding probability distribution of the oscillator’s phase can be calculated as $P(\theta) = \frac{e^{-E(\theta)/\eta}}{Z}$, where $Z = \sum_i e^{-E(\theta_i)/\eta}$ is the
partition function and \( \eta \) is analogous to the \( kT \) term in the Boltzmann distribution and can be interpreted as a measure of the stochastic noise in the IMT oscillator. Expectedly, with zero injection locking, the “energy” function of the oscillator stays flat as shown in Fig. 2(b) indicating a uniform distribution over the phase space as is experimentally obtained in Fig. 2(a). For first harmonic injection locking (FHIL), as long as the relative frequency difference \( f_{\text{inj}} - f_0 \) is small compared to the second term in equation, \( \frac{d\theta(t)}{dt} = 0 \) exhibits one stable point at \( \theta \approx 0.4\pi \), i.e. one injection-locked equilibrium phase and the calculated \( E(\theta) \) (assuming zero frequency mismatch) displays a single energy minima. This results in a single gaussian peak in the probability distribution of the oscillator’s phase as verified experimentally. In the case of SHIL, \( \frac{d\theta(t)}{dt} \) exhibits two stable points at \( \theta \approx 0.2\pi \) and \( \theta \approx 1.2\pi \), i.e. two equilibrium phases. The calculated \( E(\theta) \) (assuming zero frequency mismatch) evolves into a double well energy landscape that results in a double gaussian distribution in the phase space as reproduced faithfully in the measurements (see Supplementary information section S1 and S2 for further details).

The steady-state analysis of the Gen-Adler equation in the case of SHIL also predicts that, in the presence of a frequency mismatch between the oscillator and the injection locking signal, there exists a critical amplitude of the injection signal \( V_{\text{inj}} \) below which no stable solution exists. In our experiments, this critical amplitude is close to 1V for a frequency mismatch of 3% as observed in the measurements. Above this threshold, the bi-stability in the phase space begins to appear. Fig. 2(c) shows the measured oscillator phase as a function of the amplitude of the injection signal, \( V_{\text{inj}} \). For very low \( V_{\text{inj}} \) close to the critical value, the phase of the oscillator measured over multiple runs shows a wide distribution in the phase space, with the distribution narrowing as \( V_{\text{inj}} \) increases. This behavior can be understood by considering the perturbation in the energy landscape as shown in Fig. 2(e) for \( V_{\text{inj}} = 1V, 3V \) and 5V. For \( V_{\text{inj}} = 1V \), the energy barrier \( E_B \) separating the two stable equilibrium phases is low (around \( E_B = 0.006 \) calculated from Eq. 3). Hence, in the presence of stochastic noise, the oscillator’s phase constantly fluctuates between the two stable phases as seen in the Fig. 2(d). An increase in \( V_{\text{inj}} \) to 3V and 5V increases the barrier height \( E_B \) to 0.012 and 0.02, respectively. This reduces the fluctuations in the measured oscillator’s phase. This is reflected in the experimentally measured mean time between each phase flip, referred to as the dwell time \( \tau_{\text{dwell}} \) (analogous to Neel relaxation time for magnetization) as a function of \( V_{\text{inj}} \) as shown in Fig. 2(f). The increase in \( \tau_{\text{dwell}} \) with increasing \( V_{\text{inj}} \) accurately follows the Arrhenius’s relation \( \tau_{\text{dwell}} = \frac{E_B}{kT_0} e^{\eta} \), where \( T_0 = \frac{1}{f_0} \) is the characteristic or attempt time (equal to the time period of the oscillator), \( \alpha \) is the fitting parameter and \( \eta \) is the stochastic noise in the IMT oscillator. This characteristic of reduction in the temporal fluctuations of the oscillator’s phase with increasing amplitude of injection locking signal proves to be key knob towards performing classical annealing in our hardware.

**Replicating the interaction term in the Ising Hamiltonian**

To implement an asynchronous PTNO-CTDS that can replicate an artificial Ising spin system, the oscillators need to be connected to one another using coupling elements that emulate the ferromagnetic and antiferromagnetic nature of interaction. We first study the nature of interaction in a pairwise coupled oscillator system as shown schematically in Fig. 3(a) using capacitance \( C_c \) as the coupling element. Fig. 3(b) shows the experimentally measured phase distribution of the
two oscillators using an injection locking capacitance $C_{inj} = 20 \, pF$ and coupling capacitance $C_c = 56 \, pF$. The oscillators remain out-of-phase with each other and the two configurations: $(40^0, 220^0)$ and $(220^0, 40^0)$ are two equally probable states. We measured the probability of out-of-phase configuration for varying coupling strength as shown in Fig. 3(c). The probability remains close to 0.5 for low coupling capacitance, meaning both in-phase and out-of-phase configurations are equally probable, and increases close to 1 for stronger coupling. We also compare our experimental results with experimentally calibrated PPV-based numerical simulations (methodology described in the Materials and Methods section later) as shown in Fig. 3(c) showing excellent agreement. To understand the exact nature of capacitive coupling, we compare it with a 2-spin Ising model with antiferromagnetic interaction (negative $J$) where the individual spins prefer to remain anti-parallel with one another. In such a system, the probability of one of the possible configurations - $(\uparrow\uparrow, \downarrow\downarrow)$ is determined by the Boltzmann distribution $P(\sigma_1, \sigma_2) = e^{-H(\sigma_1, \sigma_2)/kT}/Z$, where $T$ is temperature and $Z = \sum_{\sigma_1, \sigma_2} e^{-H(\sigma_1, \sigma_2)/kT}$ is the partition function. With antiferromagnetic interaction (negative $J$), the probability for anti-parallel configuration $(\uparrow\downarrow, \downarrow\uparrow)$ increases upon varying the interaction strength from $J = 0$ to $-2$. Thus, the antiferromagnetic interaction in an Ising Hamiltonian can be faithfully replicated using capacitive coupling in this IMT nano-oscillator-based system.

Mathematically, the continuous-time dynamics of such a PTNO network can be further described by extending Eq. 2 and Eq. 3 to incorporate an additional coupling term and is given by

$$
\frac{d\theta_i(t)}{dt} = -(f_{inj} - n_H f_{0,i}) + K^H_{inj} \int_0^{2\pi} \xi_i(\theta_i(t) + \vartheta) \cos(\vartheta) \, d\vartheta + f_0 \sum_{j=1, j \neq i}^{N} \int_0^{2\pi} \xi_i(\theta(t) + \vartheta) l_{osc,j} \, d\vartheta
$$

(3)

where $l_{osc,j} = C_{c,j} \frac{dv_{osc,j}}{dt}$. The additional third term describes the coupling interaction energy between pairs of oscillators. The corresponding “total energy” function or the global Lyapunov function of the PTNO-CTDS is then given by

$$
E(\theta) = \sum_{i=1}^{N} (f_{inj} - n_H f_0) \theta_i - K^H_{inj} \sum_{i=1}^{N} \int_0^{2\pi} \xi_i(\phi + \vartheta) \cos(\vartheta) \, d\vartheta \, d\phi - f_0 \sum_{i=1, i \neq j}^{N} \int_0^{\theta_i} \int_0^{2\pi} \xi_i(\phi + \vartheta) l_{osc,j} \, d\vartheta \, d\phi
$$

(4)

We use Eq. 4 to calculate the two-dimensional energy landscape for pairwise capacitively coupled IMT nano-oscillators as shown in Fig. 3(b). The calculated energy landscape exhibits four stable points or attractor states – two degenerate global minima at the out-of-phase configuration and two degenerate local minima at in-phase configuration. By increasing the strength of the capacitive coupling, the attractor states for the out-of-phase configuration becomes more prominent.

A similar investigation is performed for pairwise resistively coupled oscillators as shown schematically in Fig. 3(d). Contrary to the previous case, the measured oscillator phases as well as numerical simulations reveal a higher probability to be in-phase with each other in either $(40^0, 40^0)$ or $(220^0, 220^0)$ configuration as shown in Fig. 3(e) for a coupling resistance of $R_c = 40 \, k\Omega$. To establish the nature of resistive coupling, the probability of in-phase configuration is measured as a function of varying $R_c$ and compared to a 2-spin Ising model with ferromagnetic interaction.
(positive $J$). Note that a lower $R_C$ represent a higher coupling strength and hence a higher $J$. The increase in the probability of in-phase configuration with decreasing $R_C$, i.e. increasing coupling strength, agrees well with the theoretical Ising model that predicts an increase in probability of parallel configuration ($\uparrow\uparrow$, $\downarrow\downarrow$) for increasing $J$ as shown in Fig. 3(f). We also use Eq. 4 with $I_{\text{osc},j} = -\frac{V_{\text{osc},j}}{R_{C,j}}$ to calculate the two-dimensional energy landscape as shown in Fig. 3(e), revealing two global and two local energy minima for in-phase and out-of-phase configurations, respectively. This establishes that a ferromagnetic interaction can be replicated using resistive coupling in out PTNO-CTDS based Ising solver. By increasing the strength of the resistive coupling, the attractor states for the in-phase configuration becomes more prominent.

**Experimental Demonstration of Max-Cut and Performance Enhancement with Annealing**

We investigate the performance of the PTNO-CTDS-based Ising Hamiltonian solver on a NP-hard graph problem of Max-Cut for an undirected and unweighted graph. The Max-Cut problem statement is defined as: Given an undirected graph $G = (V, E)$ with $V$ nodes and $E$ non-negative weights on its edges, the problem requires partitioning $G$ into two subsets $W$ and $X$ such that the total weight on the edges connecting the two subsets is maximized. The Max-Cut problem can be formulated into an equivalent Ising problem using antiferromagnetic interaction ($J = -1$) and we assume the linear Zeeman term to be zero (See Methods section for details). The cut size for a given spin configuration $\sigma$ has a direct mapping to the Ising Hamiltonian $H(\sigma)$, given by $C(\sigma) = -\frac{1}{2}\sum_{1 \leq i < j \leq N} I_{ij} - \frac{1}{2}H(\sigma)$. As such, minimizing the Ising Hamiltonian $H$ maximizes the cut-set $C$.

We chose an undirected and unweighted Mobius Ladder graph with 8 nodes as shown in Fig. 4(a) for our experiment. The phases of the oscillators $\theta$ are converted to the Ising spins $\sigma$ using discretization windows in the phase space (see Methods section for detail). The PTNOs are connected following the adjacency (or connectivity) matrix of the given graph $G$ using coupling capacitances of equal magnitude. The sinusoidal injection locking signal at twice the oscillator frequency $f_0$ is applied across the injection capacitance $s$. We implement a linear annealing schedule where the amplitude of the injection locking signal $V_{\text{inj}}$ is linearly ramped from zero to a maximum of 10V peak-to-peak over an annealing time $T_{\text{anneal}}$. This is followed by a phase readout time $T_{\text{readout}}$. Thus, the total computation time $T_{\text{comp}} = T_{\text{anneal}} + T_{\text{readout}}$. To experimentally investigate to the efficacy of annealing, we vary the annealing time $T_{\text{anneal}}$ as shown schematically in Fig. 4(b). For all the cases we keep $T_{\text{readout}}$ fixed at 100 oscillation cycles, while $T_{\text{anneal}}$ is varied from 0 (representing no anneal scenario) to 660 oscillation cycles.

Fig. 4(c) shows, for a single run, the evolution of the phases of the PTNOs for $T_{\text{anneal}} = 3.7 \, ms$. The equivalent number of oscillations is calculated as $N_{\text{osc}} = 250 \, cycles$. The temporal evolution of the Ising energy and the resultant cut set $C$ is shown in Fig. 4(d) for the case of no anneal and 250 cycle anneal. For the case of no anneal, the application of a high $V_{\text{inj}}$ immediately binarizes the phases of the PTNOs. Hence, the corresponding temporal evolution of the Ising energy shows a steep descent. However, as highlighted in Fig. 2(f), the high $V_{\text{inj}}$ results in a high dwell time that significantly reduces the temporal fluctuations in the oscillator phases. Thus, with very little freedom to escape the local minima, the network converges to a sub-optimal solution with a higher energy. On the other hand, when we linearly increase $V_{\text{inj}}$ over 250 cycles, the dwell time exponentially increases as highlighted in Fig. 2(f) and the temporal fluctuations in the oscillator phases gradually reduce. The network slowly performs energy minimization with more freedom.
to escape the local minima and converges to the optimal solution with a higher probability as shown in Fig. 4(d). Thus, we can perform classical annealing in our PTNO-CTDS by controlling the temporal fluctuation in the oscillator phases which is very similar to that of simulated annealing with a decaying noise (or temperature). We also perform numerical simulations using same annealing schemes which show very good agreement with our experimental results.

To quantify the performance of the PTNO-CTDS for varying annealing conditions, we run the network multiple times to calculate the success probability for finding the Max-Cut on this graph instance. The success probability is defined as the ratio of the number of trials that returned the true ground-state energy to the total number of trials. To obtain the true ground-state energy for comparison, we run the same graph instance using the BiqMac solver that executes an exact algorithm (branch-and-bound) on a digital CPU (26) and using a QUBO software called qbsolv (27) developed by D-Wave. Fig. 4(e) shows the success probability increasing with the annealing cycles for both experiments as well as numerical simulations. For example, no anneal scenario resulted in a success probability of 30% obtained experimentally while increasing $T_{anneal}$ to over 600 cycles resulted in a success probability of 96%. These experimental results as well numerical simulations validate our proposed methodology of progressively obtaining better solutions through annealing. The excellent agreement of our numerical simulations with experimental results also enable us to use the simulation framework to predict the performance of the PTNO-CTDS for larger problem size.

**Scalability**

We start by investigating the performance of the PTNO-CTDS-based Ising solver on a 200 node Moebius Ladder graphs shown in Fig. 5(a). We use a linear anneal scheme over 1,000 cycles. Fig. 5(b) shows the temporal evolution of the oscillator phases governed by the process of energy minimization. Fig. 5(c) shows the increase in the cut size accompanied by a decrease in the equivalent Ising energy as the system evolves towards the ground state configuration. We run the simulation 100 times to calculate a success probability of 95%. Next, we extend the investigation for different problem sizes ranging from 20 to 200 nodes. For each problem size, we consider 20 randomly generated cubic graphs (all nodes have degree 3). Fig. 5(d) shows the ground state energy obtained from our PTNO-CTDS-based Ising solver compared against that calculated by qbsolv (27). The different clusters in the plot represent different problem sizes from 20 to 200 nodes while each data point within a cluster represent 20 randomly generated problems of same size and each problem ran for 100 trials. Fig. 5(d) confirms that our solver is always able to reach the ground state energy for problems up to 200 nodes. However, as anticipated, the distribution of the obtained minimum energy state widens with increasing problem size. This translates to a reduction in the success probability with problem size as shown in Fig. 5(e). We compare simulation results for three different anneal cycles – 100, 500 and 1000. It is seen that, for 200 node problems, the success probability reduces to 1.6% for a short anneal time of 100 cycles. Increasing the anneal time to 500 and 1000 cycles boosts the success probability to 15% and 50%, respectively. Note that, here we report the success probability for obtaining the absolute ground state or the optimum Max-Cut value. Relaxing the solution accuracy to 99% or 95% of the optimum Max-Cut value will incur a much higher success probability. This means that the PTNO-CTDS is capable of finding near-optimal solutions with high probability of success, which has immense implication for tackling real-world industrial problems.
A key metric for benchmarking the performance of any Ising solver is the total computation time required to obtain at least one ground state solution. The total time to solution is calculated directly from Fig. 5(e) as \( T_{\text{solution}} = T_{\text{comp}} \left[ \frac{\log(1 - 0.99)}{\log(1 - P_{\text{success}})} \right] \) where the total computation time \( T_{\text{comp}} = T_{\text{anneal}} + T_{\text{readout}} \). We assume that, the PTNOs are running at 100MHz in our simulations. With increasing graph size, the success probability of a single run decreases exponentially, necessitating the solver to re-run the problem several times (either serially on the same solver or in a batch mode over multiple solvers) for ensuring a 99% cumulative success probability of obtaining at least one ground state solution. As shown in Fig. 5(f), the total time-to-solution as a function of the graph size \( N \) also follows an exponential nature (\( a e^{bN} \)) highlighting the NP-hard complexity of the problem. The different time-to-solution curves for different anneal times intersect each other for increasing problem size, thus highlighting a non-trivial dependence of time to solution on the anneal time. Optimizing \( T_{\text{solution}} \) reveals a tradeoff between the anneal time for a single run and the success probability. It is seen that shorter anneal time of 100 cycles is preferred for small problems up to 50 nodes where the success probability remains very high and is therefore insensitive to the anneal time. However, longer anneal of 100 cycles is preferred for larger problems where the success probability dominates. Thus, the optimum anneal time required increases with the problem size.

We also investigate another key metric for benchmarking the performance - the energy-to-solution for solving such graph problems. For the PTNO-CTDS, the average power consumption for the main compute kernel, i.e. the coupled oscillator network, is estimated to be around 10\( \mu \)W per oscillator as obtained from circuit simulations (19). We incorporate an additional energy overhead of 20% that will be incurred from the peripheral circuits such as for reading out the phases of the oscillators. The total consumed energy-to-solution is then estimated considering the total time-to-solution from Fig. 5(f). Fig. 5(g) shows the energy-to-solution for increasing problem size and for different anneal schemes. Similar to time-to-solution, we see that shorter anneal times are preferred for small problems where the success probability remains close to unity and is therefore insensitive to the anneal time. However, longer anneal cycles are preferred for larger problems where the success probability dominates.

**Performance Comparison with Other Approaches**

Table 1 shows the performance of the PTNO-CTDS-based Ising solver compared with other approaches in solving Max-Cut on 100 nodes cubic graphs. We highlight the relevant metrics for comparison such as time-to-solution, energy-to-solution, power dissipated and energy-efficiency (calculated as solutions per second per Watt). For the comparative study, we include five different approaches - (a) well-known simulated annealing algorithm (8) running on an iMac computer with four 3.5 GHz Intel Core i5 processors, (b) a noisy mean-field annealing algorithm running on an NVIDIA GeForce GTX 1080 Ti GPU (28), (c) D-Wave’s 2000Q quantum annealer containing 2,048 qubits (16), (d) coherent Ising machine (CIM) based on optical parametric oscillator with an FPGA feedback loop (14, 15) and (e) a discrete-time memristor-based hybrid analog-digital accelerator implementing Hopfield neural network (mem-HNN) (21). Note that mem-HNN belongs to the class of discrete-time dynamical system while ours is a CTDS. On the other hand, CIM is a hybrid electronic-optical accelerator while CPU and GPU are fully digital hardware running heuristic algorithms.
We see a 2x improvement in time-to-solution and 13x lower energy-to-solution for the PTNO-CTDS compared to the discrete-time mem-HNN approach which results in an order of magnitude improvement in energy-efficiency (solution per second per watt). This stems from the fact that the PTNO-CTDS is fully asynchronous and operates in continuous-time while the mem-HNN implementing a Hopfield neural network requires discrete-time updates. We also gain an added benefit in terms of energy dissipation due to the ultra-low power operation of the VO$_2$-based PTNOs. Compared to D-Wave’s 2000Q quantum annealer, we see a 4x improvement in time-to-solution and over seven orders of magnitude improvement in energy-to-solution. Note that the time-to-solution for D-Wave is highly dependent on the connectivity of the graph and can go up to $10^4$'s for dense 100 node graphs with 50% connectivity (16). We do not see such strong dependence of the performance of our PTNO-CTDS-based Ising solver on the sparsity of the graph (23). The huge energy penalty for D-Wave comes from their cryogenic cooling need that requires around 25kW of power. We expect this overhead from cooling to be constant with scaling up to thousands of nodes. The coherent Ising machine (CIM) based on degenerate optical parametric oscillator (DOPO) requires kilometer long fiber cavity to accommodate the DOPO pulses. This incurs a cavity round trip time of microseconds and puts an upper limit on the time-to-solution. For 100 node cubic graphs, we see a 130x improvement in time-to-solution compared to CIM. Also note that although the DOPOs in CIM operate in continuous-time, it requires discrete-time updates measurement-and-feedback schemes for coupling the spins, requiring a 12-bit analog-to-digital converter (ADC), multiple field-programmable-gate-arrays (FPGAs), and high speed transceivers to connect the FPGAs (15) and is inherently serial. We outperform digital CMOS based CPU in terms of time-to-solution and both CPU and GPU in terms of energy-to-solution by orders of magnitude. For GPU, we see a smaller gain of 4x improvement in time-to-solution since GPU also harnesses parallel computation.

In summary, the demonstrated PTNO-CTDS-based Ising solver outperforms all the contemporary approaches towards solving combinatorial optimization problems including fully digital, quantum and optical techniques. We report the best-in-class energy-efficiency of $3.26\times10^7$ solutions/sec/Watt exhibiting five orders of magnitude improvement when compared with digital CMOS, quantum and photonic approaches and one order of magnitude improvement over the discrete-time memristor-based Hopfield neural network approach. Such a performance gain can be attributed to (a) inherent advantage provided by the CTDS approach harnessing the power of collective-computing with massive parallelism, (b) low-power dissipation of PTNOs, (c) inherent stochasticity of the PTNOs that allows the system to escape local minima and (d) tunable temporal fluctuation of bi-stable oscillator phases allowing implementation of classical annealing in the hardware and progressive evolution towards a globally optimum solution. While the success probability is dependent on the problem size increases for this specific annealing schedule as seen in Fig. 5(e), further improvement is possible in terms of exploring better annealing methodologies or modifying the energy function to avoid non-solution attractor states that trap the system in local minima (29, 30). Hybrid approaches can also be adopted to improve the quality of solution, such as augmenting the search of an Ising solver in the first phase with other metaheuristic local-neighborhood search algorithms such as Tabu search (31) in the second phase. While the concept of utilizing coupled oscillator-based networks for performing computation such as solving optimization problems have been recently explored (32–34), they involve bulky LC oscillators and ring oscillators with latch-based coupling. In comparison, we demonstrate a compact hardware using capacitively coupled one-transistor and one-resistor (1T-1R) PTNOs which provides marked
area and energy benefit (see Supplementary information section S6 for details). Finally, it is to be noted that, while an all-to-all connected oscillator network may not be practically feasible to map a large-scale real-life problem, various avenues of research are being currently pursued that aim at decomposing large-scale problems into smaller Ising/QUBO problems that are might be tractable with practically achievable oscillator networks (27, 35).

**Conclusion**

The notion of solving hard optimization problems using the continuous-time dynamics of a physical system reveals new avenues of exploration of dynamical systems for compute applications. There is much enthusiasm in building special purpose machines (or accelerators) for solving graph problems belonging to the NP-hard and NP-complete complexity class as part of a strong push towards a heterogenous compute platform. There is a rapidly growing demand to analyze and uncover hidden relationships between similar or diverse datasets in real-time and service applications such as customer analytics, risk and compliance management, recommendation engines, route optimization, fraud detection, asset allocation and risk management. We are witnessing a resurgence in building dedicated optimization processing units (such as Ising Hamiltonian solvers) that can complement general-purpose CPU and GPU. Specialized hardware or accelerators such as Ising solvers are gaining attraction in real-life as many relevant NP-hard and NP-complete problems can be reformulated into the problem of finding the ground state of an Ising model (4). Here, we showcase that exploiting the vast repertoire of emergent complex dynamics exhibited by CTDS enables us to design special purpose hardware that are most appropriate for solving computationally hard optimization problems belonging to the NP-hard or NP-complete complexity class. We believe that the immense benefit of such a CTDS hardware in terms of operating speed and energy dissipation comes from the inherent capability of the system to perform collective-computing in a distributed and highly parallel fashion.

**Methods**

**Sample preparation**

10nm think Vanadium dioxide (VO$_2$) is grown on a substrate of (001) TiO$_2$ substrate using Veeco Gen10 molecular beam epitaxy (MBE) system. The widths of the two terminals are defined by dry etching with CF$_4$. The device length is defined by depositing Pd/Au metal contacts using electron beam evaporation. The fabricated VO$_2$ devices varied in length from 100nm to 1um with resulting insulator-to-metal transition threshold voltages ranging from 0.7V to 4V. All our experiments have been performed on device lengths of 1$\mu$m.

**Experimental setup**

Fig. 1(c) shows the schematic of a VO$_2$-based PTNO realized by connecting an n-channel MOSFET (ALD1103) transistor in series with the two-terminal VO$_2$ device. A $V_{DD}$ of 2V is applied and the amplitude of the relaxation oscillations is $\sim$1.7V. A gate voltage $V_{GS} = 0.9$V is applied to the series transistor that set the oscillation frequency $f_0 \approx 100 \ kHz$. Note that when the PTNOs are capacitively coupled, due to loading effect the frequency gets reduced to around 60 – 70 $kHz$. A schematic of the full experimental setup for the PTNO-based Ising solver is shown in Fig. 1(d). Eight VO$_2$ devices placed in the Keithley 4200-SCS probe station were connected using multi-contact probes. The $V_{DD}$ and the analog gate voltages $V_{GS}$ of the 8 series transistors are applied using a multichannel analog voltage card connected to a computer. The injection locking
signal was applied to the 8 oscillators across injection locking capacitances \( C_{\text{inj}} = 20pF \) using an external voltage generator. \( C_{\text{inj}} \) was realized using discrete off-chip capacitors connected on a breadboard. The output voltage waveforms of the oscillators were measured using a multichannel digital oscilloscope. The coupling among the oscillators was realized using discrete off-chip coupling capacitances \( C_c = 56pF \) connected on the breadboard.

**Data-processing**

The output voltage waveform of the oscillators was measured using a multichannel digital oscilloscope and subsequently analyzed and processed in MATLAB on a digital computer. The phase of an oscillator is calculated with reference to a reference sinusoidal signal with the same frequency \( f_0 \) as the oscillator and half of the injection locking signal. The phase is defined as the time difference the minima point of the discharging phase of the IMT oscillator and the minima of the sinusoidal signal divided by the time period of the oscillator (see Supplementary Figure Fig. S4). The measured oscillator phases \( \theta \) were converted to Ising spins \( \sigma \) using a discretization window in the phase space such that if \( 90^0 < \theta < 270^0 \), \( \sigma = 1 \), else \( \sigma = 0 \). Subsequently, the Ising energy and the cut set are calculated. The final energy state of the oscillator network and the corresponding final cut set is calculated during the readout phase as mentioned earlier. We readout the oscillator phases over 100 oscillation cycles.

**Numerical Simulation of PTNO network**

The numerical simulations for our PTNO network are based on the dynamical system theory as explained in the Supplementary Information section S1. We use a PPV-based numerical simulation methodology (32). For obtaining the PPV function \( \xi \), we use a SPICE compatible macro-model of the IMT nano-oscillator (36) to quantitatively match the dynamics of the oscillators under injection locking conditions, and perform cycle accurate time domain simulations of the PTNO using the Cadence Spectre circuit simulation framework (37). The details are described in the Supplementary Information section S2. The stochastic differential equations describing the PTNO network was numerically solved using the Euler-Maruyama method. The stochasticity of the PTNO is implemented by adding a Gaussian phase noise twice every oscillation cycle. This replicates the inherent stochasticity in abrupt transition from insulator to metallic phase and vice versa occurring twice in every oscillation cycle. In our simulations, the insulator-to-metal transition voltage \( V_{\text{IMT}} \) was considered as 0.7V and a \( V_{\text{DD}} = 1V \) was used in our simulations. The total capacitance between the \( \text{VO}_2 \) device and ground was assumed to be \( C_s = 200fF \), while the metallic and insulating resistance of the device was 2k\( \Omega \) and 100k\( \Omega \), respectively. The injection locking capacitance \( C_{\text{inj}} = 1fF \) was used. The resultant operating frequency of the IMT oscillator in our simulation was \( f_0 = 100 \text{ MHz} \). Note that with the current experimental setup being dominated by large parasitic capacitances, the experimentally measured frequency is in kHz. Upon building integrated PTNOs with on-chip coupling, higher operation frequencies up to 100 MHz or above can be practically achieved (22). The time step of our simulation was fixed at 100 ps used. Each simulation run was performed for various anneal times as mentioned in Fig. 5 of the main text. The simulation was further re-run 100 times to calculate the success probability. We found that the success probability of our Ising solver for finding the Max-Cut is sensitive to parameters like the coupling strength and the stochasticity or noise in the system. As such, we varied the strength of the capacitive coupling and the oscillator jitter to find an optimal coupling value and the oscillator jitter that can maximize the success probability (see supplementary information...
For the rest of the scaling simulations reported in Fig 5, we use an optimized coupling capacitance $C_C = 30fF$ and oscillator jitter of 0.5%.

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Author contributions
S. Dutta and S. Datta conceived the idea. S. Dutta and A.K. performed the measurements, analyzed the data and performed simulations. A.K. fabricated the devices. H.P. and D.S. did MBE growth of the VO$_2$ samples. A.R. and Z.T. participated in useful discussions. S. Dutta, A.K., A.R., Z.T. and S. Datta participated in the writing of the manuscript.

Competing interests
The authors declare no competing interests.
Fig. 1. Overview of PTNO-based CTDS as an Ising Hamiltonian solver. (a, b) The combinatorial optimization problem is reformulated in terms of the Ising Hamiltonian $H$ defined by the spin vector $\vec{\sigma}$, and the symmetric coupling matrix $J$ and mapped onto the Ising solver. Each Ising spin, representing a node in the graph is represented by an insulator-to-metal (IMT) phase-transition nano-oscillator (PTNO). The PTNOs are coupled to each other using passive elements such as capacitances. The coupling matrix $W$ for the PTNO network is derived from the adjacency or coupling matrix $J$ of the Ising model. (c) Schematic of a PTNO consisting of a two-terminal hysteresis device (VO$_2$) in series with a transistor. As the load line of the series transistor passes through the unstable hysteresis region of the VO$_2$ device, self-sustained oscillations are created. (d) Experimental setup of our PTNO-based CTDS. The main computing kernel comprises eight PTNOs connected using coupling capacitance following the coupling matrix $W$. The phenomenon of second harmonic injection locking (SHIL) is used to create bistable oscillator phases, emulating artificial Ising spin. (e) The inherent stochasticity present in the PTNO along with a novel technique of gradually reducing the temporal fluctuations in the oscillator phases by increasing the strength of the injection locking signal $S_{INJ}$ is utilized to perform classical annealing and obtain progressively better solutions.
Fig. 2. Creating artificial Ising spin using second harmonic injection locking (SHIL). (a) Three different injection locking scenarios are shown. No synchronization case refers to free running oscillator with uniform of oscillator phase in the phase space. For first harmonic synchronization (FHIL), the oscillator remains injection locked with input signal at in-phase configuration. For second harmonic synchronization (SHIL), the oscillator phase gets binarized into in-phase and out-of-phase configuration with equal probability. (b) Measured distribution of the oscillator phases for the three scenarios along with the equivalent Ising energy for a single PTNO is shown depicting zero, one and two energy minima for stable phase locking, respectively. (c) Increasing the amplitude ($V_{inj}$) of SHIL forces the oscillator into bi-stable phase configuration. (d) The increase in $V_{inj}$ also reduces the temporal fluctuations between the two stable phases in the presence of noise. (e) Modulation of the energy landscape with $V_{inj}$. The three curves correspond to $V_{inj} = 1V$, 3V and 5V. Increasing $V_{inj}$ increases the energy barrier between the two bi-stable energy minima states. This tightens the phase distribution as seen in (c). (f) The mean dwell time ($\tau_{Dwell}$) spent by an oscillator before hopping between the two phases is plotted against the applied injection locking amplitude and is found to follow a Arrhenius type law ($\alpha \tau_0 e^{E_B/\eta}$) with the inherent stochasticity ($\eta$) playing the role of temperature. $\tau_0 = \frac{1}{f_0}$ is the characteristic or attempt time (equal to the time period of the oscillator) and $\alpha$ is the fitting parameter.
Fig. 3. Replicating ferromagnetic and anti-ferromagnetic Ising interactions. (a) Schematic of pairwise capacitively coupled PTNOs. (b) Measured distribution of the phases of the two PTNOs, highlighting the preference of the oscillators to remain in a stable out-of-phase configuration. The calculated energy landscape for capacitive coupling also highlights the presence of global energy minima corresponding to the out-of-phase configuration. (c) The measured probability of out-of-phase configuration as a function of varying capacitive coupling strength establishes the equivalent anti-ferromagnetic nature of interaction when compared to a 2-spin Ising model. (d) Schematic of pairwise resistively coupled PTNOs. (e) The measured phase distribution for resistive coupling along with the calculated energy landscape showing the preference for in-phase configuration. (f) The measured probability of in-phase configuration vs coupling strength highlights the ferromagnetic nature of interaction for resistively coupled oscillators.
Fig. 4. Experimental demonstration of Max-Cut and performance enhancement with annealing. (a) An undirected and unweighted 8-node Mobius Ladder graph used for investigating the Max-Cut. The PTNOs are connected following the same adjacency (or connectivity) matrix of the graph using coupling capacitances. (b) Schematic of the annealing schedule used in the experiment. A sinusoidal injection locking signal at twice the oscillator frequency $f_0$ is applied with a linearly increasing amplitude over the annealing time $T_{\text{anneal}}$ that corresponds a linear annealing schedule. This is followed by a phase readout time $T_{\text{readout}}$. Thus, the total computation time $T_{\text{comp}} = T_{\text{anneal}} + T_{\text{readout}}$. The annealing time $T_{\text{anneal}}$ is varied from zero (corresponding to no anneal) to 660 oscillation cycles. (c) Evolution of the phases of the 8 oscillators, settling to either in-phase or out-of-phase with the injection locking signal. (d) The calculated temporal evolution of the Ising Hamiltonian shows an energy minimization accompanied by an increase in the graph cut size. For no annealing scheme, the network converges to a sub-optimal solution with a higher energy while annealing over 250 cycles allows the network to converge to the optimal solution with a lower Ising energy with a higher probability. Numerical simulations using same annealing schemes show very good agreement with our experimental results. (e) Experimental data and numerically simulation results for the success probability for different anneal times showing a steady increase from 30% with no anneal to 96% for over 600 cycles of linear anneal.
Figure 5

(a) Schematic of a 200 node Mobius ladder graph used for numerical simulation. (b) The evolution of the phases of the oscillators for a single run as a function of oscillation cycles. (c) Increase in the graph cut size accompanied by a decrease in the equivalent Ising energy as the system evolves towards the ground state configuration. The simulation was performed for 100 trials to calculate a success probability of 95%. (d) Comparison between the ground state energy obtained from our PTNO-CTDS-based Ising solver and that calculated by qbsolv (27) for randomly generated cubic graphs (all nodes have degree 3). Different clusters represent different problem sizes ranging from 20 to 200 nodes while each data point within a cluster represent 20 randomly generated problems of same size and each problem ran for 100 trials. (e) Success probability of finding the ground state for random cubic graphs of varying size. We compare simulation results for three different anneal cycles – 100, 500 and 1000. (f) The total time-to-solution for different anneal times and for varying problem size. A non-trivial dependence of time to solution on the anneal time is seen where shorter anneal time is preferred for smaller problems where the success probability remains insensitive to the anneal time. Longer anneal is preferred for larger problems where the success probability dominates. (g) Energy-to-solution plotted for different anneal times and for varying problem size showing a similar trade-off is seen.
Table 1. Performance comparison between PTNO-CTDS-based Ising solver and other state-of-the-art approaches. Comparison done for solving Max-Cut problem on 100 node random cubic graphs.

| Represent spins | Simulated Annealing (CPU) | Noisy mean-field annealing (GPU) | D-Wave 2000Q | Coherent Ising Machine (CIM) | Memristor-based Hopfield network (mem-HNN) | Phase Transition Nano-oscillator (PTNO) |
|-----------------|--------------------------|----------------------------------|--------------|-----------------------------|-------------------------------------------|----------------------------------------|
| Interaction     | Ising interaction        | Ising interaction                | Flux storage | FPGA                        | -                                         | Capacitance/Resistance                 |
| Update mechanism| Synchronous             | Synchronous                      | Synchronous  | Asynchronous                | Hybrid\(^{a}\)                            | Asynchronous                           |
| Connectivity    | All-to-all               | All-to-all                       | Sparse       | All-to-all                  | All-to-all                                | All-to-all                             |
| Annealing scheme| Classical annealing      | Classical annealing              | Quantum annealing | Coherent computing | Modulate intrinsic noise | Classical annealing                        |
| Scaling with problem | \(e^{-N}\)                | \(e^{-N}\)                  | \(e^{-N^2}\)  | \(e^{-N}\)                | \(e^{-N}\)                             | \(e^{-N}\)                             |
| Cryogenic cooling | No                        | No                               | Yes          | No                          | No                                       | No                                     |
| Time to solution | 246ms                     | 100\(\mu\)s                     | 100\(\mu\)s | 3.3ms                      | 40\(\mu\)s\(^*\)                  | 25.5\(\mu\)s                           |
| Energy to solution | 14.8J                    | < 25mJ                           | 2.5J         | -                          | 0.4\(\mu\)J                             | 30.6\(\mu\)J                           |
| Power           | 60W\(^{a}\)              | < 250W                           | 25kW         | -                          | 10.9mW                                  | 1.2mW                                  |
| Energy efficiency (Solutions/sec/Watt) | 6.7x10\(^{-2}\)        | > 40                             | 0.4          | -                          | 2.5x10\(^{6}\)                        | 3.26x10\(^{7}\)                       |

\(^{a}\)SA algorithm implemented on a CPU using 4 Intel Core i5 processors each running at 3.5GHz.
\(^{*}\)Hybrid scheme updates 10 nodes at a time.
\(^{*}\)Reported for 300 cycles performed sequentially.
Overview of PTNO-based CTDS as an Ising Hamiltonian solver. (a, b) The combinatorial optimization problem is reformulated in terms of the Ising Hamiltonian $J$ defined by the spin vector $\sigma$, and the symmetric coupling matrix $W$ and mapped onto the Ising solver. Each Ising spin, representing a node in the graph, is represented by an insulator-to-metal (IMT) phase-transition nano-oscillator (PTNO). The PTNOs are coupled to each other using passive elements such as capacitances. The coupling matrix $W$ for the PTNO network is derived from the adjacency or coupling matrix $J$ of the Ising model. (c) Schematic of a PTNO consisting of a two-terminal phase-transition hysteretic device (VO2) in series with a transistor. As the load line of the series transistor passes through the unstable hysteresis region of the VO2 device, self-sustained oscillations are created. (d) Experimental setup of our PTNO-based CTDS. The main computing kernel comprises eight PTNOs connected using coupling capacitance following the coupling matrix $W$. The phenomenon of second harmonic injection locking (SHIL) is used to create bi-stable oscillator
phases, emulating artificial Ising spin. (e) The inherent stochasticity present in the PTNO along with a novel technique of gradually reducing the temporal fluctuations in the oscillator phases by increasing the strength of the injection locking signal is utilized to perform classical annealing and obtain progressively better solutions.

Figure 2

Creating artificial Ising spin using second harmonic injection locking (SHIL). (a) Three different injection locking scenarios are shown. No synchronization case refers to free running oscillator with uniform of oscillator phase in the phase space. For first harmonic synchronization (FHIL), the oscillator remains injection locked with input signal at in-phase configuration. For second harmonic synchronization (SHIL), the oscillator phase gets binarized into in-phase and out-of-phase configuration with equal probability. (b) Measured distribution of the oscillator phases for the three scenarios along with the equivalent Ising energy for a single PTNO is shown depicting zero, one and two energy minima for stable phase locking, respectively. (c) Increasing the amplitude ($V_{inj}$) of SHIL forces the oscillator into bi-stable phase
configuration. (d) The increase in $V_{\text{INJ}}$ also reduces the temporal fluctuations between the two stable phases in the presence of noise. (e) Modulation of the energy landscape with $V_{\text{INJ}}$. The three curves correspond to $V_{\text{INJ}} = 1\text{V}, 3\text{V}$ and $5\text{V}$. Increasing $V_{\text{INJ}}$ increases the energy barrier between the two bi-stable energy minima states. This tightens the phase distribution as seen in (c). (f) The mean dwell time ($T_{\text{dwell}}$) spent by an oscillator before hopping between the two phases is plotted against the applied injection locking amplitude and is found to follow a Arrhenius type law with the inherent stochasticity ($\tilde{\sigma}$) playing the role of temperature. $\tilde{\sigma} = 1/\tilde{\tau}$ is the characteristic or attempt time (equal to the time period of the oscillator) and $\tilde{\sigma}$ is the fitting parameter.

Figure 3

Replicating ferromagnetic and anti-ferromagnetic Ising interactions. (a) Schematic of pairwise capacitively coupled PTNOs. (b) Measured distribution of the phases of the two PTNOs, highlighting the preference of the oscillators to remain in a stable out-of-phase configuration. The calculated energy landscape for capacitive coupling also highlights the presence of global energy minima corresponding to the out-of-phase configuration. (c) The measured probability of out-of-phase configuration as a function of varying capacitive coupling strength establishes the equivalent anti-ferromagnetic nature of interaction when compared to a 2-spin Ising model. (d) Schematic of pairwise resistively coupled PTNOs. (e) The measured phase distribution for resistive coupling along with the calculated energy landscape showing
the preference for in-phase configuration. (f) The measured probability of in-phase configuration vs coupling strength highlights the ferromagnetic nature of interaction for resistively coupled oscillators.

Figure 4

Experimental demonstration of Max-Cut and performance enhancement with annealing. (a) An undirected and unweighted 8-node Mobius Ladder graph used for investigating the Max-Cut. The PTNOs are connected following the same adjacency (or connectivity) matrix of the graph using coupling capacitances. (b) Schematic of the annealing schedule used in the experiment. A sinusoidal injection locking signal at twice the oscillator frequency $\omega_0$ is applied with a linearly increasing amplitude over the annealing time $T_{\text{anneal}}$ that corresponds a linear annealing schedule. This is followed by a phase readout time $T_{\text{readout}}$. Thus, the total computation time $T_{\text{comp}} = T_{\text{anneal}} + T_{\text{readout}}$. The annealing time $T_{\text{anneal}}$ is varied from zero (corresponding to no anneal) to 660 oscillation cycles. (c) Evolution of the phases of the 8 oscillators, settling to either in-phase or out-of-phase with the injection locking signal. (d) The calculated temporal evolution of the Ising Hamiltonian shows an energy minimization accompanied by an increase in the graph cut size. For no annealing scheme, the network converges to a sub-optimal solution with a higher energy while annealing over 250 cycles allows the network to converge to the
optimal solution with a lower Ising energy with a higher probability. Numerical simulations using same annealing schemes show very good agreement with our experimental results. (e) Experimental data and numerically simulation results for the success probability for different anneal times showing a steady increase from 30% with no anneal to 96% for over 600 cycles of linear anneal.

Figure 5

Performance of PTNO-CTDs-based Ising Hamiltonian solver and scaling with problem size. (a) Schematic of a 200 node Mobius ladder graph used for numerical simulation. (b) The evolution of the phases of the oscillators for a single run as a function of oscillation cycles. (c) Increase in the graph cut size...
accompanied by a decrease in the equivalent Ising energy as the system evolves towards the ground state configuration. The simulation was performed for 100 trials to calculate a success probability of 95%. (d) Comparison between the ground state energy obtained from our PTNO-CTDS-based Ising solver and that calculated by qbsolv (27) for randomly generated cubic graphs (all nodes have degree 3). Different clusters represent different problem sizes ranging from 20 to 200 nodes while each data point within a cluster represent 20 randomly generated problems of same size and each problem ran for 100 trials. (e) Success probability of finding the ground state for random cubic graphs of varying size. We compare simulation results for three different anneal cycles – 100, 500 and 1000. (f) The total time-to-solution for different anneal times and for varying problem size. A non-trivial dependence of time to solution on the anneal time is seen where shorter anneal time is preferred for smaller problems where the success probability remains insensitive to the anneal time. Longer anneal is preferred for larger problems where the success probability dominates. (g) Energy-to-solution plotted for different anneal times and for varying problem size showing a similar trade-off is seen.

**Supplementary Files**

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- [SupplementaryInformationNEfinal.pdf](#)