A Comprehensive Survey on Distributed Training of Graph Neural Networks

This article presents a comprehensive review of distributed training of graph neural networks by investigating its various optimization techniques.

By Haiyang Lin, Mingyu Yan, Member IEEE, Xiaochun Ye, Dongrui Fan, Senior Member IEEE, Shirui Pan, Senior Member IEEE, Wenguang Chen, and Yuan Xie, Fellow IEEE

ABSTRACT | Graph neural networks (GNNs) have been demonstrated to be a powerful algorithmic model in broad application fields for their effectiveness in learning over graphs. To scale GNN training up for large-scale and ever-growing graphs, the most promising solution is distributed training that distributes the workload of training across multiple computing nodes. At present, the volume of related research on distributed GNN training is exceptionally vast, accompanied by an extraordinarily rapid pace of publication. Moreover, the approaches reported in these studies exhibit significant divergence. This situation poses a considerable challenge for newcomers, hindering their ability to grasp a comprehensive understanding of the workflows, computational patterns, communication strategies, and optimization techniques employed in distributed GNN training. As a result, there is a pressing need for a survey to provide correct recognition, analysis, and comparisons in this field. In this article, we provide a comprehensive survey of distributed GNN training by investigating various optimization techniques used in distributed GNN training. First, distributed GNN training is classified into several categories according to their workflows. In addition, their computational patterns and communication patterns, as well as the optimization techniques proposed by recent work, are introduced. Second, the software frameworks and hardware platforms of distributed GNN training are also introduced for a deeper understanding. Third, distributed GNN training is compared with distributed training of deep neural networks (DNNs), emphasizing the uniqueness of distributed GNN training. Finally, interesting issues and opportunities in this field are discussed.

KEYWORDS | Communication pattern; computational pattern; distributed training; graph learning; graph neural network (GNN); optimization technique; software framework; workflow.

I. INTRODUCTION

Graph is a well-known data structure widely used in many critical application fields due to its powerful representation capability of data, especially in expressing the associations between objects [1], [2]. Many real-world data can be naturally represented as graphs that consist of a set of vertices and edges. Take social networks as an example [3], [4], the vertices in the graph represent people and the edges represent interactions between people on Facebook [5]. An illustration of graphs for social networks is illustrated in Fig. 1(a), where the circles represent the vertices and the arrows represent the edges. Another well-known example is knowledge graphs [6], [7], in which the vertices represent entities while the edges represent relations between the entities [8].

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Graph neural networks (GNNs) demonstrate superior performance compared to other algorithmic models in learning over graphs [9], [10], [11]. Deep neural networks (DNNs) have been widely used to analyze Euclidean data such as images [12]. However, they encounter challenges when dealing with non-Euclidean data, characterized by arbitrary size and complex topological structures that can be efficiently represented using graphs [13]. Besides, a major weakness of deep learning paradigms identified by industry is that they cannot effectively carry out causal reasoning, which greatly reduces the cognitive ability of intelligent systems [14]. To this end, GNNs have emerged as the premier paradigm for graph learning and endowed intelligent systems with cognitive ability.

An illustration of a GNN and its training procedure is shown in Fig. 1(b). GNNs consist of multiple layers. Each layer comprises two types of operations corresponding to two distinct steps: Aggregation and Combination. The Aggregation step performs graph operations, often referred to as aggregation operations. For instance, it gathers neighboring features for each vertex. The Combination step executes neural network operations, which are commonly known as combination operations. For example, it uses a multilayer perceptron to update the features of each vertex. The training process for GNNs involves forward propagation and backward propagation. During forward propagation, the input data undergo layer-by-layer processing to produce the final output. This final output is then compared to the true labels of the input data to calculate the loss. Subsequently, the loss is utilized in the backward propagation to calculate gradients, which are then used to update the model weights (W). This iterative process continues until the loss converges. Finally, the trained model can be applied to graph tasks, including vertex prediction [15] (predicting the properties of specific vertices), link prediction [16] (predicting the existence of an edge between two vertices), and graph prediction [17] (predicting the properties of the whole graph), as shown in Fig. 1(c).

Thanks to the superiority of GNNs, they have been widely used in various real-world applications in many critical fields. These real-world applications include knowledge inference [18], natural language processing [19], [20], machine translation [21], recommendation systems [22], [23], [24], visual reasoning [25], chip design [26], [27], [28], traffic prediction [29], [30], [31], ride-hailing demand forecasting [32], spam review detection [33], molecule property prediction [34], and so forth. GNNs enhance machine intelligence when processing a broad range of real-world applications, such as giving >50% accuracy improvement for real-time ETAs in Google Maps [29], generating >40% higher quality recommendations in Pinterest [22], achieving >10% improvement of ride-hailing demand forecasting in Didi [32], improving >66.90% of recall at 90% precision for spam review detection in Alibaba [33].

However, both industry and academia are still eagerly expecting the acceleration of GNN training for the following reasons [35], [36], [37], [38].

1) The scale of graph data is rapidly expanding, consuming a great deal of time for GNN training. With the explosion of information on the Internet, new graph data are constantly being generated and changed, such as the establishment and demise of interpersonal relationships in social communication and the changes in people’s preferences for goods in online shopping. The scales of vertices and edges in graphs are approaching or even outnumbering the order of billions and trillions, respectively [39], [40], [41].
Distributed training is a popular solution to speed up GNN training [35], [36], [37], [38], [40], [44], [45], [46], [47], [48], [49], [50], [51], [52], [53], [54], [55], [56], [57], [58]. It tries to accelerate the entire computing process by adding more computing resources, or “nodes,” to the computing system with parallel execution strategies, as shown in Fig. 1(d). NeuGraph [44], proposed in 2019, is the first published work of distributed GNN training. Since then, there has been a steady stream of attempts to improve the efficiency of distributed GNN training in recent years with significantly varied optimization techniques, including workload partitioning [44], [45], [46], [47], transmission planning [37], [44], [45], [46], and caching strategy [35], [51], [52].

Despite the aforementioned efforts, there is still a dearth of review of distributed GNN training. The need for management and cooperation among multiple computing nodes leads to a different workflow, resulting in complex computational and communication patterns, and making it a challenge to optimize distributed GNN training. However, regardless of whether plenty of efforts on this aspect have been or are being made, there are hardly any surveys on these challenges and solutions. Current surveys mainly focus on GNN models and hardware accelerators [9], [10], [11], [59], [60], [61], [62], but they are not intended to provide a careful taxonomy and a general overview of distributed training of GNNs, especially from the perspective of the workflows, computational patterns, communication patterns, and optimization techniques.

This article presents a comprehensive review of distributed training of GNNs by investigating its various optimization techniques. First, we summarize distributed GNN training into several categories according to their workflows. In addition, we introduce their computational patterns, communication patterns, and various optimization techniques proposed in recent works to facilitate scholars to quickly understand the principle of recent optimization techniques and the current research status. Second, we introduce the prevalent software frameworks and hardware platforms for distributed GNN training and their respective characteristics. Third, we emphasize the uniqueness of distributed GNN training by comparing it with distributed DNN training. Finally, we discuss interesting issues and opportunities in this field.

Our main goals are given as follows:

1) introducing the basic concepts of distributed GNN training;
2) analyzing the workflows, computational patterns, and communication patterns of distributed GNN training and summarizing the optimization techniques;
3) highlighting the differences between distributed GNN training and distributed DNN training;
4) discussing interesting issues and opportunities in the field of distributed GNN training.

The rest of this article is described in accordance with these goals. Its organization is shown in Table 1 and summarized as follows.

1) Section II introduces the basic concepts of graphs and GNNs, and two training methods of GNNs: full-batch training and mini-batch training.
2) Section III introduces the taxonomy of distributed GNN training and makes a comparison between them. A comparison of nondistributed GNN training and distributed GNN training is also included.
3) Section IV introduces distributed full-batch training of GNNs and further categorizes it into two types, of which the workflows, computational patterns, communication patterns, and optimization techniques are introduced in detail.
4) Section V introduces distributed mini-batch training and classifies it into two types. We also present the workflow, computational pattern, communication pattern, and optimization techniques for each type.
5) Section VI introduces software frameworks currently supporting distributed GNN training and describes their characteristics.
6) Section VII introduces hardware platforms for distributed GNN training.
7) Section VIII highlights the uniqueness of distributed GNN training by comparing it with distributed DNN training.
8) Section IX summarizes the distributed full-batch training and distributed mini-batch training, and discusses several interesting issues and opportunities in this field.
9) Section X is the conclusion of this article.

II. Background
This section provides some background concepts of graphs and GNNs, and introduces the two training methods applied in GNN training: full-batch and mini-batch.

A. Graphs
Graphs’ efficient representation of non-Euclidean data has positioned them as a popular choice for various...
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| Summary and Discussion       | (Conclusion of the Survey)                      | Section X |

learning tasks involving diverse sets of non-Euclidean data [13], [18], [63], [64], [65].

Euclidean data refer to data represented in Euclidean space where the Euclidean metric is used to measure distances and spatial relationships. This type of data includes various modalities such as images. Images are typically composed of pixels, where each pixel contains color information (such as red, green, and blue channels). These color values can be represented as points in Euclidean space. In a 2-D image, the coordinates of each pixel can be represented as points in 2-D Euclidean space. Fig. 2(a) illustrates an image and its corresponding pixel matrix.

Conversely, non-Euclidean data refer to data that cannot be represented in Euclidean space. This type of data may exist in spaces with curvature or nonlinear metrics, such as Riemannian manifolds [66]. Graphs are widely used to represent non-Euclidean data because they provide a versatile and intuitive way to model complex relationships and structures. In a graph, the connections between vertices can be arbitrary and are not constrained by spatial positions. The connection between two vertices can represent entirely different relationships, which may be immeasurable or highly complex. For example, in a social network, the connection between two people could represent friendship, collaboration, and so on—relationships that are difficult to quantify using Euclidean distance in space. Fig. 2(b) shows a social network and its corresponding graph.

Graphs are commonly defined as \( G = (V, \mathcal{E}) \), where \( V = \{v_i | i = 1, 2, \ldots, N \} \) denotes vertices, \( N \) signifies the number of vertices, and \( \mathcal{E} = \{e_{ij} | v_i, v_j \in V \} \), with \( |\mathcal{E}| \leq N^2 \), represents edges connecting vertices. Common representation methods also include utilizing an adjacency matrix \( A \) to denote the connections between vertices. The adjacency matrix \( A \) is an \( N \times N \) matrix with Boolean values, where \( A_{ij} \) is 1 if \( e_{ij} \) exists in \( \mathcal{E} \); otherwise, it is 0.
B. Graph Neural Networks

Graph Neural Networks (GNNs) have been dominated to be a promising algorithmic model for learning knowledge from graph data [18], [64], [65], [67], [68], [69]. It takes the graph data as input and learns a representation vector for each vertex in the graph. The learned representation can be used for downstream tasks, such as vertex prediction [15], link prediction [16], and graph prediction [17].

A GNN model consists of one or multiple layers, each of which includes the Aggregation step and the Combination step. An illustration is provided in Fig. 3. In the Aggregation step, the Aggregate function $\text{Aggregate}(\ )$ is used to aggregate the feature vectors of in-neighbors from the previous layer or input layer for each target vertex. For example, in Fig. 3, vertex 4 gathers the feature vectors of itself and its in-neighbors (i.e., vertexes 2, 5, and 8) using graph operations. In the Combination step, the Combine function $\text{Combine}(\ )$ transforms the aggregated feature vector of each vertex using neural network operations.

To sum up, the aforementioned computation on a graph $\mathcal{G}(\mathcal{V}, \mathcal{E})$ can be formally expressed by

$$a_v^l = \text{Aggregate}\left(\{h_u^{l-1} | u \in N(v) \cup \{v\}\}\right) \quad (1)$$

$$h_v^l = \text{Combine}(a_v^l) \quad (2)$$

where $h_v^l$ denotes the feature vector of vertex $v$ at the $l$th layer, $a_v^l$ represents the aggregation result of vertex $v$ at the $l$th layer, and $N(v)$ represents the in-neighbors of vertex $v$. Specifically, the input features of vertex $v \in \mathcal{V}$ are denoted as $h_v^0$. Note that in GNNs, every vertex in a directed graph gathers information from its in-neighbors. Throughout this article, the term “the vertex's neighbors” specifically refers to its in-neighbors. In the context of an undirected graph, it can be converted into a directed graph by splitting each edge into two directed edges, each with opposite directions.

In addition, it is crucial to clarify that the existence of cycles in a graph does not influence the computation of GNNs. In the Aggregation step of each GNN layer, every vertex gathers information from its in-neighbors. This entire computation process does not require consideration of whether the graph contains cycles. However, when cycles are present in the graph, they can potentially impact the model accuracy. The presence of cycles may result in the repetition and mixing of information after multiple rounds of GNN propagation, leading to what is known as the oversmoothing phenomenon. Oversmoothing occurs...
when vertex features become excessively similar, causing a
loss of discriminative information between vertices [70],
[71]. To address the issue, techniques such as DropEdge [72] and DropNode [73] are employed. These tech-
niques involve selectively removing edges and vertices. It
will be further discussed in Section IX-G.

C. Training Methods for GNNs

In this section, we introduce the training methods for
GNNs, which are approached in two ways including full-
batch training [15], [63] and mini-batch training [13],
[74], [75], [76], [77].

A typical training procedure of neural networks, includ-
ing GNNs, includes forward propagation and backward
propagation. In forward propagation, the input data are
passed through the layers of neural networks toward the
output. Neural networks generate differences in the output
of forward propagation by comparing it to the predefined
labels. Then, in backward propagation, these differences
are propagated through the layers of neural networks in
the opposite direction, generating gradients to update the
model parameters.

As illustrated in Fig. 4, training methods of GNNs can
be classified into full-batch training [15], [63] and mini-
batch training [13], [74], [75], [76], [77], depending
on whether the whole graph is involved in each round. Here, we define a round of full-batch training consisting of
a model computation phase, including forward propa-
gation and backward propagation, and a parameter update
phase. On the other hand, a round in mini-batch training
additionally contains a sampling phase, which samples a
small-sized workload required for the subsequent model
computation and, thus, locates prior to the other two
phases. Thus, an epoch, which is defined as an entire pass
of the data, is equivalent to a round of full-batch training,
while that in mini-batch training usually contains several
rounds. Details of these two methods are introduced in the
following.

1) Full-Batch Training: Full-batch training utilizes the
whole graph to update model parameters in each round.

A complete dataset $\mathcal{V}$ is typically divided into a training
set $\mathcal{V}_t$, a validation set $\mathcal{V}_{val}$, and a test set $\mathcal{V}_{test}$. Using
the training set $\mathcal{V}_t \subseteq \mathcal{V}$, the loss function of full-batch
where $\mathcal{L}$ is the loss, $\nabla l(\cdot)$ is a loss function, $y_i$ is the known label of vertex $v_i$, and $z_i$ is the output of GNN model when inputting features $x_i$ of $v_i$. In each epoch, the GNN model needs to aggregate representations of all neighboring vertices for each vertex in $V_t$ all at once. As a result, the model parameters are updated only once at each epoch.

2) **Mini-Batch Training:** Mini-batch training utilizes part of the vertices and edges in the graph to update model parameters in every forward propagation and backward propagation. It aims to reduce the number of vertices involved in the computation of one round to reduce the computing and memory resource requirements.

Before each round of training, a mini-batch $V_s$ is sampled from the training dataset $V_t$. By replacing the full training dataset $V_t$ in (3) with the sampled mini-batch $V_s$, we obtain the loss function of minibatch training

$$L = \frac{1}{|V_s|} \sum_{v_i \in V_s} \nabla l(y_i, z_i).$$

It indicates that for mini-batch training, the model parameters are updated multiple times at each epoch since numerous mini-batches are needed to have an entire pass of the training dataset, resulting in many rounds in an epoch. Stochastic gradient descent (SGD) [78], a variant of gradient descent that applies to mini-batch, is used to update the model parameters according to the loss $L$.

**Sampling:** Mini-batch training requires a sampling phase to generate the mini-batches. The sampling phase first samples a set of vertices, called target vertices, from the training set according to a specific sampling strategy, and then, it samples the neighboring vertices of these target vertices to generate a complete mini-batch. The sampling method can be generally categorized into three groups: nodewise sampling, layerwise sampling, and subgraph-based sampling [56], [79].

1) **Nodewise sampling** [13], [22], [80], [81] is directly applied to the neighbors of a vertex: the algorithm selects a subset of each vertex’s neighbors, as depicted in Fig. 5(a). It is typical to specify a different sampling size for each layer. For example, in GraphSAGE [13], it samples at most 25 neighbors for each vertex in the first layer and at most ten neighbors in the second layer.

2) **Layerwise sampling** [74], [75], [82] enhances nodewise sampling. It selects multiple vertices in each layer and then proceeds recursively layer by layer, as shown in Fig. 5(b).

3) **Subgraph-based sampling** [76], [83], [84], [85] first partitions the original graph into multiple subgraphs and then samples the mini-batches from one or a certain number of them, as illustrated in Fig. 5(c).

### III. TAXONOMY OF DISTRIBUTED GNN TRAINING

This section introduces the taxonomy of distributed GNN training. As shown in Fig. 6, we first categorize it into distributed full-batch training and distributed mini-batch training, according to the training method introduced in Section II-C, i.e., whether the whole graph is involved in each round, and show the key differences between the two types. Each of the two types is classified further into two detailed types, respectively, by analyzing their

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*Fig. 5. Illustrations of three groups of sampling methods. (a) Nodewise sampling (vertex 7 is the target vertex, vertices colored in yellow represent sampled one-hop in-neighbors, and vertices colored in blue represent sampled two-hop in-neighbors). (b) Layerwise sampling (vertices colored in blue, yellow, and orange, respectively, represent the vertices sampled from the first, second, and third layers). (c) Subgraph-based sampling.*
workflows. This section introduces the first-level category, that is, distributed full-batch training and distributed mini-batch training, and makes a comparison between them. The second-level category of the two types is introduced in Sections IV and V, respectively.

A. Distributed Full-Batch Training

Distributed full-batch training is the distributed implementation of GNN full-batch training, as illustrated in Fig. 4. Except for graph partition, a major difference is that multiple computing nodes need to synchronize gradients before updating model parameters so that the models across the computing nodes remain unified. Thus, a round of distributed full-batch training includes two phases: model computation (forward propagation + backward propagation) and gradient synchronization. The model parameter update is included in the gradient synchronization phase.

Since each round involves the entire raw graph data, a considerable amount of computation and a large memory footprint are required in each round [37], [47], [50]. To deal with it, distributed full-batch training mainly adopts the workload partitioning method [44], [45]: split the graph to generate small workloads and hand them over to different computing nodes.

Such a workflow leads to a lot of irregular internode communications in each round, mainly for transferring the features of vertices along the graph structure. This is because the graph data are partitioned and consequently stored in a distributed manner and the irregular connection pattern in a graph, such as the arbitrary number and location of a vertices’ neighbors. Therefore, many uncertainties exist in the communication of distributed full-batch training, including the uncertainty of the communication content, target, time, and latency, leading to challenges in the optimization of distributed full-batch training.

As shown in Fig. 6, we further classify distributed full-batch training more specifically into two categories according to whether the workload is preset in the preprocessing phase, namely, dispatch-workload-based execution and preset-workload-based execution, as shown in the second column of Table 2. Their detailed introduction and analysis are presented in Sections IV-A and IV-B.

B. Distributed Mini-Batch Training

Similar to distributed full-batch training, distributed mini-batch training is the distributed implementation of GNN mini-batch training, as shown in Fig. 4. It also needs to synchronize gradients prior to model parameter update, so a round of distributed mini-batch training includes three phases: sampling, model computation, and gradient synchronization. The model parameter update is included in the gradient synchronization phase.

Distributed mini-batch training parallelizes the training process by processing several mini-batches simultaneously, one for each computing node. The mini-batches can be sampled either by the computing node itself or by other devices, such as another node specifically for sampling. Each computing node performs forward propagation and backward propagation on its own mini-batch. Then, the nodes synchronize and accumulate the gradients and update the model parameters accordingly. Such a process can be formulated by

\[
W_{i+1} = W_i + \sum_{j=1}^{n} \nabla g_{i,j} \tag{5}
\]

where \(W_i\) is the weight parameters of the model in the \(i^{th}\) round of computation, \(\nabla g_{i,j}\) is the gradients generated...
in the backward propagation of the computing node \( j \) in the \( i \)th round of computation, and \( n \) is the number of the computing nodes.

As shown in Fig. 6, we further classify it more specifically into two categories according to whether the sampling and model computation are decoupled, namely, individual-sample-based execution and joint-sample-based execution, as shown in the second column of Table 2. Their detailed introduction and analysis are presented in Sections V-A and V-B.

### C. Comparison Between Distributed Full-Batch Training and Distributed Mini-Batch Training

This section compares distributed full-batch training with distributed mini-batch training of GNNs. The major differences are also summarized in Table 3.

The workflow of distributed full-batch training is summarized as collaborative computation with workload partition. Since the computation in each round involves the entire graph, the computing nodes need to cache it locally, leading to a high memory capacity requirement [44]. Also, the communication volume of distributed full-batch training is large [40], [47]. In every round, the Aggregate function needs to collect the features of neighbors for each vertex, causing a large quantity of internode communication requests since the graph is partitioned and stored on different nodes. Considering that the communication is based on the irregular graph structure, the communication irregularity of distributed full-batch training is high [46]. Another characteristic of communication is high uncertainty. The time of generating the communication request is indeterminate since each computing node sends communication requests according to the currently involved vertices in its own computing process. As a result, the main challenges of distributed full batch training are workload imbalance and massive transmissions [45], [46], [47].

In contrast, the workflow of distributed mini-batch training is summarized as independent computation with periodic synchronization. The major transmission content is the mini-batches, sent from the sampling node (or component) to the computing node (or component) responsible for the current mini-batch [51], [52], [86]. As a result, these transmissions have low irregularity and low uncertainty, as the direction and content of transmission are deterministic. Since the computation of each round only involves the mini-batches, it triggers less communication volume and requires less memory capacity [51]. However, the extra sampling phase may cause some new challenges. Since the computation of the sampling phase is irregular and requires access to the whole graph for neighbor information of a given vertex, it is likely to encounter the problem of insufficient sampling performance, causing the subsequent computing nodes (or components) to stall due to lack of input, resulting in a performance penalty [56], [86].

### D. Other Information of Taxonomy

Table 2 provides a summary of the current studies on distributed GNN training using our proposed taxonomy. Except for the aforementioned classifications, we also add

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**Table 2** Summary of Recent Studies on Distributed GNN Training

| Name          | Taxonomy                | Software Framework | Hardware Platform | GPU Types | Year | Code Available |
|---------------|-------------------------|--------------------|-------------------|-----------|------|----------------|
| NeoGraph [44] | Dispatch-workload-based | NeoGraph           | multi-GPU         | Tesla P100 | 2019 | -              |
| Roc [45]      | Dispatch-workload-based | Roc                | multi-GPU         | Tesla P100 | 2020 | Yes            |
| CAGNET [40]   | Preset-workload-based   | PyTorch            | multi-GPU         | Tesla V100 | 2020 | Yes            |
| DGCL [46]     | Preset-workload-based   | PyTorch            | multi-GPU         | Tesla V100 | 2021 | -              |
| DistGNN [47]  | Preset-workload-based   | DGL                | multi-CPU         | -          | 2021 | Yes            |
| FlexGraph [37]| Preset-workload-based   | FlexGraph          | multi-CPU         | -          | 2021 | -              |
| MG-GCN [48]   | Preset-workload-based   | MG-GCN             | multi-GPU         | Tesla V100/A100 | 2021 | Yes            |
| Dorylus [49]  | Preset-workload-based   | Dorylus            | multi-CPU         | -          | 2021 | Yes            |
| SAR [50]      | Preset-workload-based   | SAR                | multi-CPU         | -          | 2021 | -              |
| AliGraph [35] | Joint-sample-based      | AliGraph           | multi-CPU         | -          | 2019 | -              |
| DistDGL [38]  | Joint-sample-based      | DistDGL            | multi-CPU         | -          | 2020 | -              |
| AGL [36]      | Individual-sample-based | AGL                | multi-CPU         | -          | 2020 | -              |
| PaGraph [51]  | Joint-sample-based      | DGL                | multi-GPU         | GTX 1080Ti | 2020 | -              |
| 2PGraph [52]  | Joint-sample-based      | PyTorch            | multi-GPU         | RTX 5090  | 2021 | -              |
| LLCG [53]     | Joint-sample-based      | PyG                | multi-GPU         | RTX 8000  | 2021 | Yes            |
| GraphTheta [55]| Joint-sample-based       | GraphTheta        | multi-CPU         | -          | 2021 | -              |
| SALIBENT [56] | Individual-sample-based | PyG                | multi-GPU         | Tesla V100 | 2021 | -              |
| P3 [57]       | Joint-sample-based      | DGL                | multi-GPU         | Tesla P100 | 2021 | -              |
| DistDGLv2 [54]| Joint-sample-based      | DistDGLv2         | multi-GPU         | Tesla T4   | 2022 | -              |
| DGTP [58]     | Individual-sample-based | DGTP              | multi-GPU         | GTX 1080Ti | 2022 | -              |
some supplemental information in the table to provide a comprehensive review of them.

1) **Software Frameworks:** The software frameworks used by the various studies are shown in the third column of Table 2. PyTorch Geometric (PyG) [87] and deep graph library (DGL) are the most popular among them. In addition, there are many newly proposed software frameworks aiming at distributed training of GNNs, and many of them are the optimization version of PyG [87] or DGL [88]. A detailed introduction to the software frameworks of distributed GNN training is presented in Section VI.

2) **Hardware Platforms:** The multi-central processing unit (CPU) and multi-graphic processing unit (GPU) platforms are the most common hardware platforms of distributed GNN training, as shown in the fourth column of Table 2. The multi-CPU platform usually refers to a network with multiple servers, which uses CPUs as the only computing component. On the contrary, in multi-GPU platforms, GPUs are responsible for the major computing work, while CPU(s) handles some computationally complex tasks, such as workload partition and sampling. A detailed introduction to the hardware platforms is presented in Section VII.

3) **Year:** The contribution of distributed GNN training began to emerge in 2019 and is now showing a rapid growth trend. This is because more attention is paid to it due to the high demand from industry and academia to shorten the training time of the GNN model.

4) **Code Available:** The last column of Table 2 simply records the open source status of the corresponding study on distributed GNN training for the convenience of readers.

### E. Comparison Between Nondistributed GNN Training and Distributed GNN Training

To clearly distinguish distributed GNN training, a detailed comparison between nondistributed and distributed GNN training is presented in the following. A comprehensive summary of this comparison is also provided in Table 4.

In the context of nondistributed GNN training, its scalability is limited, rendering it challenging to accommodate the demands of larger graph datasets and larger model training requirements. The training speed is significantly constrained by the finite computational and storage resources available on a single machine. Furthermore, data preparation and storage predominantly rely on local data file systems. This approach is characterized by low communication overhead and is well-suited for computing systems that lack high-speed network infrastructure support. Moreover, nondistributed training demonstrates high data security compared with distributed training, as it exclusively involves local data processing, thereby enhancing its resistance to external security threats.

In contrast, distributed GNN training offers enhanced scalability, permitting the dynamic addition of computing nodes to accommodate larger graph datasets and models as needed. The availability of greater computational and storage resources contributes to substantially faster training speeds. Data processing and storage primarily rely on distributed file systems. However, this distributed approach results in higher communication overhead, necessitating frequent data and model parameter transfers across computing nodes. Furthermore, distributed training exhibits lower data security due to network transmission and multimachine computation.

Transitioning from nondistributed training to distributed training can enhance training speed by utilizing additional computing nodes, but it may also impact the model's accuracy. For commonly used distributed training methods, they typically retain the same fundamental computational processes, merely transitioning from single-machine computation to multimachine computation. For instance, in synchronous training methods within distributed mini-batch training [38], the computational process remains essentially equivalent to that of
single-machine computation. Consequently, the convergence behavior and final accuracy of the model remain consistent with single-machine training.

Subsequently, we delve into the reasons why some distributed training might affect model accuracy. First, distributed training often involves the use of larger batch sizes, where the batch size refers to the number of samples used for a single model parameter update during training. Larger batch sizes can potentially lead to overfitting, wherein the model excessively fits the training data, thereby affecting accuracy. To address this issue, a common approach is to adjust the learning rate. Second, in the case of some other distributed training methods, changes in model accuracy often stem from the adoption of aggressive computational strategies, such as delayed aggregation in distributed full-batch training and asynchronous training in distributed mini-batch training. These aggressive methods typically enhance parallel computing efficiency and reduce node stagnation waiting time by allowing computing nodes to utilize partial or outdated information, including vertex features or model parameters. The impact of these techniques on accuracy is discussed in corresponding sections, i.e., Sections IV-B4 and V-B4.

### IV. DISTRIBUTED FULL-BATCH TRAINING

This section describes GNN distributed full-batch training in detail. Our taxonomy classifies it into two categories according to whether the workload is preset in the preprocessing phase, namely, dispatch-workload-based execution and preset-workload-based execution, as shown in Fig. 7(a).

#### A. Dispatch-Workload-Based Execution

The dispatch-workload-based execution of distributed full-batch training is illustrated in Fig. 7(b). Its workflow, computational pattern, communication pattern, and optimization techniques are introduced in detail as follows.

1) **Workflow:** In the dispatch-workload-based execution, a leader and multiple workers are used to perform training. The leader stores the model parameters and the graph data, and is also responsible for scheduling: it splits the computing workloads into chunks, distributes them to the workers, and collects the intermediate results sent from the workers. It also processes these results and advances the computation. Note that the chunk that we use here is a unit of workload.

2) **Computational Pattern:** The computational patterns of forward propagation and backward propagation are similar in dispatch-workload-based execution: the latter can be seen as the reversed version of the former. As a result, we only introduce forward propagation’s computational pattern here for simplicity. The patterns of the two functions in forward propagation (Aggregate and Combine) differ a lot and are introduced in the following, respectively.

   a) **Aggregate function:** The computational pattern of the Aggregate function is dynamic and irregular, making workload partition for this step a challenge. In the Aggregation step, each vertex needs to aggregate the features of its own neighbors. As a result, the computation of the Aggregation step relies heavily on the graph structure, which is irregular or even changeable. Thus, the number and memory location of neighbors vary significantly among vertices, resulting in the dynamic and irregular computational pattern [89], causing poor workload predictability and aggravating the difficulty of workload partition.

   b) **Combine function:** The computational pattern of the Combine function is static and regular; thus, the workload partition for it is simple. The computation of the Combination step is to perform neural network operations on each vertex. Since the structure of neural networks is regular and these operations share the same weight parameters, the Combination step enjoys a regular computational pattern. Consequently, a simple partitioning method is sufficient to maintain workload balance, so it is relatively not a major consideration in dispatch-workload-based execution of GNN distributed full-batch training.

3) **Communication Pattern:** The majority of communication is the transmission of input data and output results between the leader and the workers [44]. Since the leader is responsible for workload distribution and intermediate result collection, it needs to communicate with multiple workers. Such a communication structure results in a one-to-many communication pattern, resulting in a possible
bottleneck in the leader’s communication path [44]. However, these communications are relatively regular since the distribution of tasks is controlled by the leader. Therefore, communication path congestion can be avoided to some extent by optimized scheduling techniques.

4) Optimization Techniques: Next, we introduce the optimization techniques used to partition workload, balance workload, reduce transmission, and exploit parallelism for dispatch-workload-based execution. We classify them into four categories: 1) vertex-centric workload partition; 2) balanced workload generation; 3) transmission planning; and 4) feature-dimension workload partition. A summary for these categories is shown in Table 5. Note that the transmission reduction in this article refers to a reduction in the amount of transmitted data or in transmission time.

a) Vertex-centric workload partition: Vertex-centric workload partition refers to the technique of generating workload chunks by partitioning the graph or matrix from the perspective of vertices. Specifically, the graph is partitioned into a list of subgraphs according to the source vertex $u$ and the destination vertex $v$ of edge $(u, v)$. Then, the subgraphs are taken as the workload chunk, and the leader distributes them to each computing node for computation. This is a very common partitioning method for processing large-scale graphs in the traditional graph application [90], [91], [92].

Fig. 8 illustrates 2-D graph partition, a typical example of a graph partition. The vertices are first partitioned into $P$ disjoint blocks. Then, we tile the edges into $P \times P$ chunks according to their source and destination vertices: in the $y$th chunk of the $x$th row, the source vertices of the edges all belong to the $x$th source vertex block, and the destination vertices all belong to the $y$th destination
vertex block. This partitioning method works well for the Aggregation step, which needs to transfer the information of the source vertex to the destination vertex along the edge.

NeuGraph [44] adopts the 2-D graph partitioning method. It chooses \( P \) as the minimum integer satisfying the requirement to fit each chunk in the device memory of GPUs. Roc [45], on the other hand, uses the graph partition strategy proposed in [92], which can also be mentioned as 1-D graph partition. In this approach, the graph, comprising \( T \) vertices, is divided into \( n \) subgraphs. To achieve this, \( n - 1 \) numbers, ranging from 1 to \( T \), are selected to split the vertices into \( n \) parts. Consequently, each subgraph contains consecutively numbered vertices stored in adjacent locations, along with their respective in-edges. This arrangement maximizes coalesced access to device memory during subsequent computations. It is important to note that the 1-D graph partitioning approach can be readily applied to directed graphs. For other graph structures, such as undirected graphs, a conversion to a directed graph format is necessary. For instance, NeuGraph [44] employs a chain-based streaming scheduling scheme. The idea is to have one GPU (which already holds the data chunk) forward the data chunk to its neighbor GPU under the same PCIe switch, which can eliminate the bandwidth contention on the upper level shared interconnection link.

b) Balanced workload generation: Workload balance is an extremely important optimization goal for dispatch-workload-based execution. Since the workloads are split and distributed to multiple computing nodes, the prerequisite of the continuation of computing is that all the computing nodes have already returned their intermediate results. If the workload is not evenly partitioned, the consequent lag of waiting will stall the training process. Therefore, it is necessary to carefully adjust the workload partition, so as to make the workload of each computing node as balanced as possible.

In response to that, Roc [45] proposes a linear regression cost model to produce balanced workloads in each round. The cost model is used to predict the computation time of a GNN layer on an arbitrary input, which could be the whole or any subset of an input graph.

c) Transmission planning: Planning data transmission is helpful in making full use of reusable data across computing components and nodes, thereby reducing data transmission. In dispatch-workload-based execution, the major transmission overhead is caused by the requirement to transmit the input data and intermediate results. Recent work focuses on harvesting two following optimization opportunities to reduce this overhead.

Avoid Repeated Transmission of Overlapped Data: The input data required by different computing tasks may overlap. As a result, caching these overlapped parts on computing nodes or components is a reasonable way to reduce transmission. Roc [45] formulates GPU memory management as a cost-minimization problem. It uses a recursive dynamic programming algorithm to find the global optimal solution to decide which part of the data should be cached in GPU memory for reuse according to the input graph, the GNN model, and device information. This minimizes data transmission between CPUs and GPUs.

Rationally Select the Source of the Transmission: The overhead of data transmission can be reduced by allowing computing components or nodes to send data, so every component or node can rationally select the source of the transmission. As a result, the design can reduce the overhead by decreasing the transmission distance. For instance, NeuGraph [44] employs a chain-based streaming scheduling scheme. The idea is to have one GPU (which already holds the data chunk) forward the data chunk to its neighbor GPU under the same PCIe switch, which can eliminate the bandwidth contention on the upper level shared interconnection link.

d) Feature-dimension workload partition: Feature-dimension partition refers to the finer partition of the workload from the dimension of the vertex feature to make full use of the parallel computing hardware in the computing node. The scales of vertices and edges in graphs are approaching or even outnumbering the order of billions and trillions, respectively [39]. Importantly, both vertices and edges can be processed independently. Consequently, in terms of parallelism in traditional graph applications, especially graph traversal (e.g., single-source shortest path), the primary focus is on vertex and edge parallel computation [91]. However, GNNs stand apart from traditional graph applications due to their representation of vertex features as extensive vectors or even large tensors, rather than mere scalars. This distinction provides an opportunity for more nuanced parallel computation, specifically focusing on the dimensions of vertex features.

| Optimization Technique | Function              | Operation Object | Focus                  | Related Work       |
|------------------------|-----------------------|------------------|------------------------|-------------------|
| 1 Vertex-centric Workload Partition | Workload Partition | Graph or Matrix | Graph or Matrix Partition Strategy | NeuGraph [44], Roc [45] |
| 2 Balanced Workload Generation | Workload Balance | Partition Operation | Partition Strategy | Roc [45] |
| 3 Transmission Planning | Transmission Reduction | Transmission Operation | Transmission Strategy | Roc [45], NeuGraph [44] |
| 4 Feature-dimension Workload Partition | Parallelism Exploitation | Vertices' Features | Feature Partition Strategy | NeuGraph [44] |
Modern computing hardware has advanced to facilitate large-scale parallel computations, including vector operations, through the implementation of hardware architectures that support single-instruction multiple-data (SIMD) parallel execution. For instance, in GPUs, each set of 32 consecutive threads forms a warp [93]. Threads within a single warp operate in parallel, following the SIMD parallel execution, ensuring efficient utilization of the underlying computing units [94]. Considering that vertex features in GNNs are high-dimensional vectors, often reaching dimensions of 1024 or more [44], [86], it is intuitive to harness the parallel capabilities of the hardware. This can be achieved by leveraging parallelism at the feature-dimension level, distributing the computation of each element of the vertex features across threads.

To capitalize on this opportunity, NeuGraph [44] adopts a clever strategy where the computations of each vertex are subdivided according to its feature dimensions. This approach effectively distributes the computations across different threads within a GPU warp. These threads duplicate neighboring vertex information, facilitating coarse-grained access to neighboring features while concurrently processing the vertex’s computations at the feature-dimension level. This strategy ensures efficient utilization of the substantial parallel computing resources offered by GPUs, helping to achieve significant improvement in performance.

B. Preset-Workload-Based Execution

The preset-workload-based execution of distributed full-batch training is illustrated in Fig. 7(c). Its workflow, computational pattern, communication pattern, and optimization techniques are introduced in detail as follows.

1) Workflow: Preset-workload-based execution involves multiple collaborative workers to perform training. The graph is first split into subgraphs through the partition operation in the preprocessing phase. Then, each worker holds one subgraph and a replica of the model parameters. During training, each worker is responsible for completing the computing tasks of all the vertices in its subgraph. As a result, a worker needs to query the information from other workers to gather information on neighboring vertices. However, the Combine function can be performed directly locally, as the model parameters are replicated, but this also means that gradient synchronization is required for each round to ensure the consistency of model parameters across the nodes.

2) Computational Pattern: The computational pattern of preset-workload-based execution differs significantly in different steps. In the Aggregation step, a node needs to query the neighbor information of vertices from other nodes, so only when each computing node cooperates efficiently, the data can be supplied to the target computing node in a timely manner. However, in the Combination step, each computing node conducts the operation on the vertices in its own subgraph independently due to the local replica of model parameters. As a result, the Aggregation step is more prone to inefficiencies such as computational stagnation, and it is the key optimization point for preset-workload-based execution.

In addition, the preset workload has the benefit that the whole graph can be loaded at a time, as it is partitioned into small subgraphs that fit in the memory of computing nodes in the preprocessing phase. This means that it is more scalable than dispatch-workload-based execution when either adding more computing resources or increasing the size of the dataset. This avoids frequent accesses to graph data from low-speed storage such as hard disks, thereby ensuring the timely provision of data for high-speed computing.

3) Communication Pattern: Communication happens mostly in the Aggregation step and gradient synchronization. Due to the distributed storage of graph data, it encounters a lot of irregular transmissions during the Aggregation step when collecting the features of its neighboring vertices that are stored in other nodes due to graph partition [46]. Since the features of vertices are vectors or even tensors, the amount of data transmitted in the Aggregation step is large. Also, the communication is irregular due to the irregularity graph structure [89], [91], [95], which brings difficulties in optimizing connection. In contrast, the communication overhead of gradient synchronization is minuscule due to the small size of model parameters and the regular communication pattern. As a result, the communication between nodes in the Aggregation step is the main concern of preset-workload-based execution in distributed full-batch training.

4) Optimization Techniques: Here, we introduce the optimization techniques used to balance workload, reduce transmission, and reduce memory pressure for preset-workload-based execution in detail. We classify them into four categories: 1) graph prepartition; 2) transmission optimization; 3) delayed aggregation; and 4) activation rematerialization. A summary for these categories is shown in Table 6.

   a) Graph prepartition: Graph prepartition refers to partitioning the whole graph into several subgraphs according to the number of computing nodes, mainly to balance workload and reduce transmission [46]. This operation is conducted in the preprocessing phase. The two key principles in designing the partitioning algorithm are listed as follows.

First, in order to pursue workload balance, the subgraphs need to be similar in size. In preset-workload-based execution, each worker performs the computation of vertices within its own subgraph. Therefore, the size of the subgraph determines the workload of the worker. The main reference parameters are the number of vertices and edges in the subgraph. Since preset-workload-based execution has a gradient synchronization barrier, workload balance is very important for computing nodes to complete...
a round of computation at a similar time. Otherwise, some computing nodes will be idle, causing performance loss.

Second, minimizing the number of edge-cuts in the graph prepartition can reduce communication overhead. It is inevitable to cut edges in the graph prepartition, meaning that the source and destination vertices of an edge might be stored in different computing nodes. When the information of neighboring vertex is required during the computation of the Aggregation step, communication between workers is introduced. Therefore, reducing the number of edges cut can reduce communication overhead in the Aggregation step.

DGCL [46] uses METIS library [96] to partition the graph for both the above two targets. Dorylus [49] also uses an edge-cut algorithm [97] for workload balance. DistGNN [47] aims at the two targets too. However, it uses a vertex-cut-based graph partition technique instead. This means distributing the edges among the partitions. Thus, each edge exists in only one partition, while a vertex can reside in multiple partitions. Any updates to such vertex must be synchronized to its replicas in other partitions. FlexGraph [37] partitions the graph in the manner of edge-cut too. Besides, it learns a cost function to estimate the training cost for the given GNN model. Using the estimated training cost, FlexGraph migrates the workload from overloaded partitions to underloaded ones to pursue workload balance.

b) Transmission optimization: Transmission optimization refers to adjusting the transmission strategy between computing nodes to reduce communication overhead. Due to the irregular nature of communication in preset-workload-based execution, the demand for transmission optimization is even stronger than above.

DGCL [46] provides a general and efficient communication library for distributed GNN training. It tailors the shortest path spanning tree algorithm to transmission planning, which jointly considers fully utilizing fast links, avoiding contention, and balancing workloads on different links.

Compared to traditional transmission planning, FlexGraph [37] takes a different approach to take advantage of the aggregation nature of the Aggregate function. FlexGraph [37] partially aggregates the features of neighboring vertices that collocate at the same partition when possible, aiming to reduce the amount of data transmission and overlap partial aggregations and communications. When each computing node receives a neighbor information request from other nodes, it first partially aggregates the neighbor information locally and then sends the partial result to the requesting computing node, instead of directly sending the initial neighbor information. The requesting computing node only needs to aggregate the received partial result with its local nodes to continue the computation. As a result, due to the reduction in data transmission and the overlap of computation and communication, the communication overhead is significantly reduced.

c) Delayed aggregation: By allowing the computing nodes to utilize old transmitted data in the Aggregation step, delayed aggregation can reduce the overhead of data transmission. Normally, there is no scope for intraepoch overlap due to the dependence between consecutive phases in an epoch. Delayed aggregation solves the problem of overlapping the computation and communication, allowing the model to use the previously transmitted data. However, we need to point out that in order to ensure convergence and final accuracy, the delayed aggregation is mainly based on bounded asynchronous training [49].

Dorylus [49] uses bounded asynchronous training on two synchronization points: the update of weight parameters in the backward propagation and the neighbor aggregation in the Aggregation step. Bounded asynchronous training of GNNs is based on bounded staleness [98], [99], [100], [101], an effective technique for mitigating the convergence problem by employing lightweight synchronization. The key policy is to restrict the number of iterations between the fastest worker and the slowest worker to not exceed more than a user-specified staleness threshold $s$, where $s$ is a natural number. As long as the policy is not violated, there is no waiting time among workers.

DistGNN [47] proposes the delayed remote partial aggregate (DRPA) algorithm to overlap the communication with local computation, which is interepoch computation-communication overlap. In the algorithm, the set of vertices that may be queried by other computing nodes is partitioned into $r$ subsets. For each epoch computation, only the data of one subset are transmitted. The transmitted data are not required to be received at this epoch but after $r$ epochs. This means that the computing nodes do not use

| Table 6 Summary of Various Categories of Optimization Techniques Used in Preset-Workload-Based Execution of Distributed Full-Batch Training |
|---------------------|---------------------|---------------------|---------------------|---------------------|
| Optimization Technique | Function | Operation Object | Focus | Related Work |
| ---------------------|---------------------|---------------------|---------------------|---------------------|
| 1. Graph Pre-partition | Workload Balance and Transmission Reduction | Entire Graph | Graph Partition Strategy | DGCL [46], Dorylus [49], FlexGraph [37], DistGNN [47] |
| 2. Transmission Optimization | Transmission Reduction | Transmission Data | Transmission Strategy | DGCL [46], FlexGraph [37] |
| 3. Delayed Aggregation | Transmission Reduction | Aggregation Operation | Aggregation Strategy | Dorylus [49], DistGNN [47] |
| 4. Activation Rematerialization | Memory Pressure Reduction | Intermediate Results | Intermediate Results | SAR [50] |
the latest global data of vertices but locally existing data of them. This algorithm allows communication to overlap with more computational processes, thereby reducing communication overhead.

Given the efforts described above involving the tradeoff between model accuracy and computational efficiency, a detailed analysis of this tradeoff is presented below to provide a deeper understanding of these endeavors. In the case of Dorylus [49], the use of delayed aggregation demands more epochs to achieve the same level of convergence accuracy. This variation depends on the asynchronous parameter settings, resulting in an increase in the number of convergence epochs by 8% and 41% for within-epoch and within-two-epoch delayed aggregations, respectively. The great improvement in computational efficiency stems from the significant reduction in time per epoch due to the asynchronous execution. Regarding DistGNN [47], under the same number of epochs, the delayed aggregation method manages to maintain the final accuracy decline within 1% compared to the original synchronous training method. Hence, it is crucial to recognize that while delayed aggregation methods lead to performance enhancements, they require additional training epochs to compensate for the accuracy loss. Therefore, maintaining precise control over hyperparameters in asynchronous training, such as the length of asynchronous strides, is essential to strike a balance between accuracy and computational efficiency.

d) Activation rematerialization: Activation rematerialization uses data retransmission and recomputation during the computation process to reduce the memory pressure of computing nodes caused by intermediate results or data. In preset-workload-based execution, the graph data are stored in each worker in a distributed manner: if there are \( n \) workers, then each worker only needs to store \( 1/n \) of raw data initially. However, during the computation, each worker needs to store the information received from other workers. In forward propagation, each worker needs to query other workers to obtain the neighbor information of its local vertices, and the information needs to be stored for later use in backward propagation. As a result, the actual data stored by each worker during the computation are much larger than its initial size. In addition, its size is difficult to estimate and may lead to memory overflow problems.

Activation rematerialization, a widely applied and mature technology in DNN, solves the problem by storing all activations during forward propagation. Activation is the output of each layer of neural networks, which means the representation of each vertex of each layer in GNNs. Its idea is to recompute or load the activations directly from disks during the backward propagation to reduce the pressure of memory [102], [103].

The sequential aggregation and rematerialization (SAR) method, as proposed in SAR [50], builds upon the concept of “activation rematerialization” and introduces SAR for distributed GNN training. The specific execution flow is given as follows. In forward propagation, each computing node only receives activation from one other computing node at a time. After the aggregation operation is completed, the activation is removed immediately. Then, the computing node receives the activation from the next computing node and continues the aggregation operation. This makes the activation of each vertex only exist in the computing node where it is located, and there are no replicas. In backward propagation, the computation is also performed sequentially as above. Each computing node transmits activation sequentially to complete the computation. Through this method, memory will not overflow as long as the memory capacity of the computing node is larger than the size of two subgraphs. This allows SAR to scale to arbitrarily large graphs by simply adding more workers.

V. DISTRIBUTED MINI-BATCH TRAINING

This section describes GNN distributed mini-batch training in detail. Our taxonomy classifies it into two categories according to whether the sampling and model computation are decoupled, namely, individual-sample-based execution and joint-sample-based execution, as illustrated in Fig. 9(a).

A. Individual-Sample-Based Execution

The individual-sample-based execution of distributed mini-batch training is illustrated in Fig. 9(b). Its workflow, computational pattern, communication pattern, and optimization techniques are introduced in detail as follows.

1) Workflow: The individual-sample-based execution involves multiple samplers and workers so that it decouples sampling from the model computation. The sampler first samples the graph data to generate a mini-batch and then sends the generated mini-batch to the workers. The worker performs the computation of the mini-batch and conducts gradient synchronization with other workers to update the model parameters. By providing enough computing resources for the samplers to prepare mini-batches for the workers, the computation can be performed without stalls.

A more detailed description of the workflow follows. First, the samplers generate mini-batches by querying the graph structure. Since each worker requires one mini-batch per round of training, the samplers need to generate enough mini-batches in time. These mini-batches are transferred to the workers for subsequent computations. The workers perform forward propagation and backward propagation on their own received mini-batch and generate gradients. After that, gradient synchronization is conducted between workers to update model parameters.

2) Computational Pattern: The computational pattern in the individual-sample-based execution is dominated by the computation of the sampler and the workers that are responsible for the sampling phase and other computational phases, respectively. The sampling phase relies
on the graph structure, which leads to irregularities in the computation. These irregularities are reflected in the uncertainty of the data, including its amount and storage address, making it difficult to estimate the computational efficiency of the samplers, as well as the efficiency of mini-batch generation. After that, the sampler sends the mini-batches to the workers, which perform model computation on the batches. In contrast to sampling, the computational efficiency of workers is easy to estimate since the amount of data and computation between mini-batches is similar. In addition, since the size of the GNN model is small, the gradient synchronization overhead is also small, which makes it generally not a bottleneck. As a result, the difference in the computational pattern between the samplers and workers makes the generation of mini-batch and the balance of consumption the focus of attention. The key optimization point is to accelerate the sampling process so that it provides enough mini-batches in time to avoid stalls.

3) Communication Pattern: The main communication of individual-sample-based execution is the mini-batch transmission between samplers and workers, which is characterized by regular but frequent. Since the mini-batch consists of a fixed number of target vertices and their limited neighbors, the amount of data is consistent and small. In addition, its transmission target is determined. This makes the transmission regular. Due to the small amount of data in the mini-batch, the amount of computation is also small and it is easy to estimate the time cost required for computation. Due to the continuous consumption of mini-batches by workers, frequent mini-batch transfers are required to maintain a timely supply of mini-batches.

4) Optimization Techniques: Here, we introduce the optimization techniques used to generate mini-batch, balance workload, reduce transmission, and exploit parallelism for individual-sample-based execution in detail. We classify them into four categories: 1) parallel mini-batch generation; 2) dynamic mini-batch allocation; 3) mini-batch transmission pipelining; and 4) parallel aggregation with edge partitioning. A summary for these categories is shown in Table 7.

a) Parallel mini-batch generation: Parallel mini-batch generation refers to parallelizing the generation of mini-batches to reduce the waiting time of the workers for mini-batches. It aims to speed up the sampling to provide the workers with sufficient mini-batches in time. As mentioned
before, the computational pattern of the sampling is irregular, which is hard to be efficiently performed by GPUs. Therefore, CPUs are generally used to perform sampling [51], [52]. A widely used solution is to parallelize the generation of mini-batches by using CPUs’ multithread design.

SALIENT [56] parallelizes mini-batch generation by using the multithread technology of CPUs. It uses C++ threads to end-to-end implement mini-batch generation rather than Python threads to avoid Python’s global interpreter lock and, thus, improves the performance of mini-batch generation.

AGL [36], on the other hand, generates $k$-hop neighborhoods of vertices through preprocessing to simplify and speed up the generation of mini-batches. Here, the $k$-hop neighborhoods are the vertices that can be visited within the maximum $k$ edges by the given vertex. AGL proposes a distributed pipeline to generate $k$-hop neighborhoods of each vertex based on message passing and implements it with MapReduce infrastructure [104]. The generated $k$-hop neighborhoods’ information is stored in the distributed file system. Since it is only necessary to collect the $k$-hop neighborhoods’ information of the target vertices when a mini-batch is required, such a design greatly accelerates the sampling process. In order to limit the size of $k$-hop neighborhoods, AGL implements a set of sampling strategies to selectively collect neighbors.

b) Dynamic mini-batch allocation: In dynamic mini-batch allocation, mini-batches are dynamically allocated to the workers to ensure that each computing node has a mini-batch supply in time and alleviate workload imbalance. Usually, a well-designed allocator is used to collect the mini-batch generated by the samplers and then supply the mini-batch in time according to the needs of the workers. In this way, the performance of sampling is determined by the efficiency of all samplers, instead of the slowest sampler. This can effectively avoid the situation that the computation is stalled due to the long sampling time of some slow samplers.

Meanwhile, dynamic mini-batch allocation helps to alleviate workload imbalance. Traditionally, the mini-batch static allocation is that each worker has one or several corresponding samplers, and its sampler is responsible for sampling and transferring mini-batches to it. Due to the irregular computational pattern of sampling, the computing time of a sampler is difficult to estimate, and it may lead to a serious workload imbalance in static allocation, as the workers corresponding to some slow samplers have to wait for their mini-batches. In contrast, dynamic mini-batch allocation uniformly manages the workload and dispatches mini-batches to workers in time, thereby avoiding workload imbalance and achieving better performance.

SALIENT [56] implements dynamic mini-batch allocation by using a lock-free input queue. CPU responsible for sampling and GPU responsible for model computation are not in a static correspondence. The number of each worker is sequentially stored in the queue. The mini-batch generated by each CPU is assigned a destination worker number according to the number stored in the queue during the generation. After the generation, the mini-batch is sent to the corresponding worker according to the destination number immediately. The lock-free input queue is a simple design that effectively avoids the problem faced by static allocation.

c) Mini-batch transmission pipelining: Mini-batch transmission pipelining refers to pipelining the transmission of mini-batches and the model computation to reduce the overhead of data transmission.

Due to the decoupled nature of sampling and model computation in individual-sample-based execution, mini-batches need to be transferred from samplers to workers. The overhead of the transmission is high and even occupies up to 35% of the per-epoch time [56]. Fortunately, the mini-batch transmission and model computation do not require strict sequential execution. Thus, the mini-batch transmission and model computation pipelining can be implemented to reduce the mini-batch transmission overhead. This reduces the occurrence of worker stagnation due to the failure of the mini-batch to be supplied in time.

In SALIENT [56], the samplers are CPUs and the workers are GPUs, so mini-batches need to be transferred from CPUs to GPUs. To pipeline mini-batch transmission and model computation, it uses different GPU threads to deal with each of them, respectively. AGL [36] uses the idea of pipelining to reduce the data transmission too.

d) Parallel aggregation with edge partitioning: Parallel aggregation with edge partitioning refers to partitioning the edges in mini-batches according to their destination...
vertices and assigning each partition to an individual thread to accomplish aggregation in parallel.

For the Aggregation operation of GNNs, each vertex needs to aggregate its in-neighbors’ features. It means that the computation is actually determined by the edges. AGL [36] proposes edge partitioning techniques to accomplish parallel aggregation according to this property. In each mini-batch, AGL partitions the edges into several partitions according to their destination vertices ensuring that the edges with the same destination vertex are in the same partition. Then, these partitions are handled by multiple threads independently. As there are no data conflicts between these threads, the Aggregation operation can be accomplished effectively in parallel. In addition, the workload balance between these threads is guaranteed. It is because the mini-batch generated by sampling ensures that each vertex has a similar number of neighboring vertex, resulting in a similar number of edges per edge partition.

B. Joint-Sample-Based Execution

The joint-sample-based execution of distributed mini-batch training is illustrated in Fig. 9(c). Its workflow, computational pattern, communication pattern, and optimization techniques are introduced in detail as follows.

1) Workflow: In the joint-sample-based execution, multiple collaborative workers are used to perform training. The graph is split into subgraphs through the partition in the preprocessing. Each worker holds one subgraph and a replica of the model parameters. The workers sample their own subgraphs to generate mini-batches and perform forward propagation and backward propagation on the mini-batches to obtain gradients. Then, they update the model synchronously through communication with other workers.

A more detailed description of the workflow follows. First, in order to make the computation of each worker more independent, a well-design graph partition algorithm is used to split the original graph into multiple subgraphs in the preprocessing. Each subgraph is assigned to a worker, and the worker samples its local subgraph, which means that the target vertex chosen for sampling is chosen only from its own subgraph. However, the worker may need to query other workers for the neighborhoods of target vertices in the sampling. Second, the worker performs model computation on the mini-batch generated by itself. Through forward propagation and backward propagation, each worker produces gradients and conducts gradient synchronization together to update model parameters. Note that the sampling and model computation are conducted on a single worker. This feature makes the computation of each computing node more independent, which greatly reduces the demand for data transmission.

2) Computational Pattern: In the joint-sample-based execution, the computation of each computing node is relatively independent. The computation of each computing node includes two parts: sampling and model computation. In addition, gradient synchronization between computing nodes is required for updating model parameters. In the sampling phase, each computing node mainly uses its own local data, and there may be a need to access remote data for neighbor information of vertices. The mini-batch generated by sampling is directly consumed by itself for model computation. For such a computing pattern, the main concern is how to make the computation of computing nodes more independent, so as to obtain better performance.

3) Communication Pattern: There are three communication requirements in joint-sample-based execution, each with a different communication pattern. The first exists in the sampling phase, and its communication pattern is irregular. In the joint-sample-based execution, each computing node selects the target vertices of the mini-batch from its own subgraph. Then, the computing node needs to sample the neighbors of these target vertices. Since the graph is partitioned, it may be necessary to query other computing nodes to obtain the neighbor information of these target vertices [38]. As the communication is determined by the graph structure and the graph itself has an irregular connection pattern, its communication pattern is irregular.

The second communication requirement is in the gradient synchronization phase. After sampling and model computation, the computing nodes need to synchronize to update the model parameters. Unlike DNNs, the size of model parameters of GNNs is much smaller since GNNs have few layers and share weights across all vertices. As a result, it is expected that the gradient synchronization phase occupies a small part of the execution time [38], [51]. However, synchronization with the nature of a fence requires all computing nodes to complete the computation before it can start, so it is highly sensitive to workload imbalance. The low performance of a single computing node causes other computing nodes to wait, which results in a large time overhead for this part of the communication [86].

The third communication requirement only arises when the sampling and model computation of a single worker are performed on different computing components, such as CPUs and GPUs on a single server [51], [52]. Its communication pattern is regular but with high redundancy. The existence of the same vertex in different mini-batches is called inter-mini-batch redundancy, which leads to wasted transmission. This redundancy is exacerbated by the fact that each computing node in the joint-sample-based execution mainly samples on its own subgraph: compared to the sampling on the entire graph, the sampling on the same subgraph exhibits a higher probability of sampling the same vertices for different mini-batches [51]. As a result, reducing transmission overhead by eliminating this redundancy is an effective optimization point.
Table 8 Summary of Various Categories of Optimization Techniques Used in Joint-Sample-Based Execution of Distributed Mini-Batch Training

| Optimization Technique                          | Function         | Operation Object | Focus                  | Related Work                  |
|------------------------------------------------|------------------|------------------|------------------------|------------------------------|
| 1. Locality-aware Partition                    | Transmission Reduction | Entire Graph   | Graph Partition Strategy | PaGraph [51], 2PGraph [52], DistDGL [38], AliGraph [35] |
| 2. Partition with Overlap                      | Transmission Reduction | Neighboring Vertices | Duplicate Strategy   | PaGraph [51], 2PGraph [52]   |
| 3. Independent Execution with Refinement       | Waiting Time Reduction | Computing Fashion | Computing Strategy     | LLCG [53]                    |
| 4. Frequently-used Data Caching                | Transmission Reduction | Frequently-used Data | Caching Strategy      | AliGraph [35], PaGraph [51], 2PGraph [52] |

4) Optimization Techniques: Here, we introduce the optimization techniques used to reduce transmission overhead and waiting overhead for joint-sample-based execution in detail. We categorize them into four categories: 1) locality-aware partition; 2) partition with overlap; 3) independent execution with refinement; and 4) frequently used data caching. A summary for these categories is shown in Table 8.

a) Locality-aware partition: Locality-aware partition refers to partitioning the graph into subgraphs with good locality; that is, vertices and their neighbors have a high probability of being in the same subgraph, so most of the data required for sampling is local to the computing node. This partition is conducted in the preprocessing phase.

Locality-aware partition aims to make the computation of the nodes more independent by reducing the communication between them. According to the workflow of mini-batch training, the model computation is restricted in using the mini-batch data and does not involve access to the entire raw graph data. This means that the access to the raw graph data is almost completely in the sampling phase. By focusing on the locality of the graph, each vertex and its neighbors are clustered into a subgraph as much as possible. This makes the workers mainly access their own subgraph during the sampling phase, reducing the remote queries of neighboring vertices, and, thus, improving the independence of each worker’s computation.

The graph partition here is very different from the one of distributed full-batch training mentioned earlier in this article, which is mainly reflected in its purpose. The graph partition in the preset-workload-based execution of distributed full-batch training focuses more on workload balance due to its significant impact on performance. Because a large amount of information in the subgraph is related to the execution time, it is necessary to take various factors into account to guide the graph partition, such as the numbers of vertices and edges. In contrast, workload balance is not the critical issue for graph partition in distributed mini-batch training.

Because the computation amount of the subgraph is mainly determined by the number of target vertices contained in it, it is only necessary to ensure that the number of target vertices in each subgraph tends to be consistent, which is simple but effective. Such characteristics cause the two graph partitions to be quite different, which, in turn, leads to different techniques used.

To implement locality-aware partition, PaGraph [51] proposes a formula to guide the graph partition. Its partition algorithm scans the whole target vertex set and iteratively assigns the scanned vertex to one of $P$ partitions according to the score computed from the following formula:

$$\text{score}_{v_i}^{(t)} = \left| TV_t \cap \mathit{IN}(V_t) \right| \cdot \frac{\left| TV_{avg} - \left| TV_t \right| \right|}{\left| PV_t \right|}$$  \hspace{1cm} (6)

where $TV_t$ denotes the train vertex set already assigned to the $i$th partition, $IN(V_t)$ denotes the $k$-hop ($k$ is the layer number of GNN model) in-neighbors of the target vertex $v_t$, and $PV_t$ denotes the total number of vertices in the $i$th partition. $TV_{avg}$ denotes the expected number of target vertices in the final $i$th partition, which is typically set as the total number of vertices $|TV_t|$ divided by the partition number $P$ for workload balance. This formula fully considers the distribution of the vertices’ neighbors when splitting them into each subgraph, thus making great use of the locality of the graph.

Other researches mainly use existing graph partition algorithms to harvest the locality of the graph. 2PGraph [52] uses the cluster-based graph partition algorithm [105], [106] in its graph partition. DistDGL [38] takes advantage of the multiconstraint mechanism in METIS [105] for graph partition. AliGraph [35] implements four graph partition algorithms in its system and suggests that the four algorithms are suitable for different circumstances. They are METIS [96] for processing sparse graphs, vertex and edge cut partitions [107] for processing dense graphs, 2-D partition [108] for systems with a fixed number of workers, and streaming-style partition strategy [109] for graphs with frequently edge updates.

b) Partition with overlap: Partition with overlap refers to duplicating the neighboring vertices of the target vertex when partitioning the graph, thereby reducing or even completely eliminating the data transmission between computing nodes in the sampling phase.

For comparison, we first review the general graph partitioning method used in traditional graph analytics, i.e., partition with nonoverlap, which means that no duplicate
method is used. An example of partition with nonoverlap is demonstrated in Fig. 10(a). The original graph is split into two subgraphs, denoted as A and B. The vertices held by each subgraph are disjoint. This has the benefit of linearly reducing the requirement of memory capacity when training large graphs. In the case of $n$ workers, each worker only needs to hold $1/n$ of the original graph data. However, partition with nonoverlap causes some vertices’ neighbors to be stored in remote workers, resulting in the need for remote queries during the sampling phase. Intensive transmissions would lead to inefficiencies in the parallel execution of distributed training.

To reduce the amount of transmission in the sampling phase, partition with overlap is proposed. An example of partition with overlap is illustrated in Fig. 10(b), whose duplicate range is one-hop neighbors. The original graph is split into two subgraphs with duplicate vertices. Vertices 2 and 7 are duplicated as they are the one-hop neighboring vertex of target vertices in subgraph A. This method takes advantage of the characteristics of GNN mini-batch training. When the mini-batch is sampled, only the $L$-hop neighboring vertices of the target vertices are needed, where $L$ is the number of GNN layers. Therefore, by duplicating the features of its neighboring vertices, the transmission overhead in the sampling phase can be reduced. These duplicate vertices are regarded as mirror vertices, which do not participate in training as target vertices [107]. If the $L$-hop neighbors are fully duplicated, the transmission requirements during the sampling phase can be completely eliminated. Note that duplicating more vertices causes an increase in the memory capacity requirements of the computing nodes, so there is a tradeoff in how many neighboring vertices are duplicated.

Both PaGraph [51] and 2PGraph [52] use this method to make the computation of each worker more independent. DGCL [46] defines a replication factor, which is computed from the total number of vertices (including the original and mirror vertices) kept by all workers divided by the number of vertices in the original graph. In its experiments, for the dense Reddit dataset [13] with 16 GPU workers and a two-hop duplicate range, the replication factor reaches 15. In contrast, for the sparser Web-Google dataset [110] with the same setting, the replication factor is much smaller, which is 2.5. It suggests that sparse graphs are more suitable for the duplicate method as they have less memory capacity overhead.

c) Independent execution with refinement: Independent execution with refinement refers to letting each worker compute and update the model parameters independently and then periodically averages the model parameters using additional refined operations to release the potential of parallel computation while ensuring high accuracy.

Although the workers of joint-sample-based execution mainly perform computations on their own local subgraph, there are still interactions between them to advance the training process, including the sampling phase and the gradient synchronization phase. The limitations of these two phases cause each worker’s execution to be affected by the state of other workers, resulting in idle wait time.

To enable more independent computations for each computing node, a method called independent execution with refinement is proposed, building upon the concept of “learn locally, correct globally” (LLCG) as outlined in LLCG [53]. In its implementation, each worker first performs sampling and corresponding model computation independently on their own subgraphs. That is to say, the data of remote workers are not accessed at all, and each worker only updates its own model parameters locally. A parameter server then periodically collects the model parameters from each worker and performs the average operation of the parameters. Finally, the parameter server refines the average result, that is, it samples the mini-batch on the whole graph and conducts model computation to update the model parameters of every worker. This is due to the fact that each worker lacks the structural information of the whole graph, which may lead to low accuracy. Through this method, the computation of each worker is more independent while the refinement of model parameters ensures the high accuracy of the GNN model.

Considering the aforementioned effort involving the balance between model accuracy and computational efficiency, an analysis of this tradeoff is presented below to
offer a more detailed understanding of this effort. LLCG’s methodology has been validated for convergence through rigorous algorithmic theory and practical experiments, as described in its paper [53]. However, given its focus on subgraph computations, the method results in a marginal decrease in final model accuracy, usually within a 1% range. Simultaneously, it achieves a remarkable reduction in communication volume by almost $100 \times$, leading to a substantial enhancement in computational efficiency.

\[ \text{Imp}^k(v) = \frac{\text{Nin}^k(v)}{\text{Nout}^k(v)} > U^k \]  

where \( \text{Imp}^k(v) \) denotes the importance score of the vertex \( v \), \( \text{Nin}^k(v) \) and \( \text{Nout}^k(v) \) represent the number of \( k \)-hop incoming and outgoing neighbors of the vertex \( v \), and \( U^k \) is a user-specified threshold. When \( \text{Imp}^k(v) \) exceeds \( U^k \), the vertex \( v \) is considered to be an important vertex. By caching the outgoing neighbors of important vertex \( v \), the transmission overhead derived from the queries of other vertices via \( v \) can be reduced.

Similarly, the frequently used data caching technique can also reduce the transmission of mini-batches between CPUs and GPUs. It is the third communication requirement of joint-sample-based execution, which occurs when CPUs and GPUs are responsible for sampling and model computation, respectively. Due to the benefit of mini-batch training, each GPU only needs to load one mini-batch of data per round of computation, and it is possible to load cached data into the free memory during model computation. Thus, by caching vertex features and combining them into mini-batches on GPUs, the amount of mini-batch transmission can be reduced.

PaGraph [51] uses a very simple caching strategy. It sorts vertices according to their degree from largest to smallest and then caches them in order, where the degree is the number of incoming edges of a vertex. The higher the degree, the higher the cache priority. This is a simple but effective method: when a vertex has a larger degree, this vertex is more likely to be a neighbor of other target vertices. As for the proportion of cached vertices, it is determined by measuring the free memory capacity of GPUs during the prerun. 2PGraph [52] proposes GNN layer-aware caching. It adopts a sampling method that uses a fixed order of target vertices, which allows it to schedule the vertices to train next. By precaching all of the target vertices’ \( L \)-hop neighbors into GPU memory before the computation, the time overhead of data transmission can be almost completely eliminated. However, fixing the order of target vertices may compromise the model accuracy, so it proposes to periodically permute the order to reduce this effect.

VI. SOFTWARE FRAMEWORKS FOR DISTRIBUTED GNN TRAINING

In this section, we introduce the software frameworks of distributed GNN training. Table 9 provides a summary of these frameworks and their supported attributes.

A. Basic Software Frameworks

PyG [87] and DGL [88] are the two most popular software frameworks in the GNN community. PyG [87] is a geometric deep learning extension library for PyTorch to enable deep learning on irregular structure data such as graphs. It supports both CPU and GPU computing, providing convenience for using GPU to accelerate the computing process. Through the message-passing application programming interface (API), it is easy to express various GNN models, as neighbor aggregation is a kind of message propagation. DGL [88] is a framework specialized for deep learning models on graphs. It abstracts the computation of GNNs into a few user-configurable message-passing primitives, thus helping users express GNNs more conveniently. It achieves good performance by exploring a wide range of parallelization strategies. It also supports both CPU and GPU computing.

Due to their open source and easy-to-use nature, more and more software frameworks are building on top of them and propose optimizations for distributed GNN training, such as DistGNN [47], SAR [50], DistDGL [38], PaGraph [51], LLCG [53], DistDGLv2 [54], SALIENT [56], and P3 [111]. Next, we introduce the software frameworks dedicated to distributed full-batch training and distributed mini-batch training.

B. Software Frameworks Dedicated to Distributed Full-Batch Training

The software frameworks dedicated to distributed full-batch training include NeuGraph [44], Roc [45], FlexGraph [37], MG-GCN [48], and Dorylus [49].

NeuGraph [44] is a distributed GNN training software framework proposed in 2019, using a multi-GPU hardware platform. It is categorized as the dispatch-workload-based execution of distributed full-batch training. It proposes SAGA-NN, an abstract model for the programming of GNN operations, which splits each layer of model computation into four stages: Scatter, ApplyEdge, Gather, and ApplyVertex. SAGA-NN is named after these stages. The Scatter stage means that the vertices scatter their features to their output edges, while the Gather stage means that the vertices gather the value from their input edges. There are two user-defined functions used in the ApplyEdge stage and...
the ApplyVertex stage, for users to declare neural network computations on edges and vertices, respectively. Through the abstraction of SAGA-NN, users can easily express various GNN models and execute them in a parallelized way. NeuGraph optimizes the training process based on this abstract model, using techniques including vertex-centric workload partition, transmission planning, and feature-dimension workload partition.

Roc [45] is a distributed multi-GPU software framework for fast GNN training and inference, proposed in 2020. It is categorized as the dispatch-workload-based execution of distributed full-batch training. Its optimization of distributed training mainly focuses on balanced workload generation and transmission planning. In terms of balanced workload generation, an online linear regression cost model is proposed to achieve efficient graph partition. The cost model is being tuned by collecting runtime data. According to this cost model, the training resources and time required for the subgraph can be estimated to guide the workload partition for workload balance. In terms of transmission planning, a recursive dynamic programming algorithm is introduced to find the global optimal solution to decide which part of the data should be cached in GPU memory for reuse.

FlexGraph [37] is a distributed multi-CPU training software framework proposed in 2021. It is categorized as the preset-workload-based execution of distributed full-batch training. In order to express more kinds of GNNs including GNNs for heterogeneous graphs, it proposes a novel programming abstraction, namely, NAU. NAU splits the computation of one GNN layer into three stages including NeighborSelection, Aggregation, and Update stages, each with a user-defined function. Based on NAU, FlexGraph optimizes the training process using techniques including graph prepartition and partial aggregation. In terms of partial aggregation, FlexGraph partially aggregates the features of vertices collocated at the same partition when possible. In addition, it overlaps partial aggregations and communication to reduce the transmission overhead.

MG-GCN [48] is a distributed multi-GPU training software framework proposed in 2021. It is categorized as the preset-workload-based execution of distributed full-batch training. It focuses on the efficient parallelization of the sparse matrix–matrix multiplication (SpMM) kernel on the multi-GPU hardware platform. It uses a matrix partitioning method to distribute raw data to multiple GPUs, and each GPU is responsible for completing the workload of its own local matrix. It involves the efficient reuse of memory buffers to reduce the memory footprint of training GNN models and overlaps communication and computation to reduce communication overhead. Specifically, the memory buffer in the computing node is used to cache the data reused by the forward propagation and backward propagation, thereby reducing data transmission. As for the communication and computation overlap, it uses two GPU streams for computation and communication, respectively.

Dorylus [49] is a distributed multi-CPU training software framework proposed in 2021. It is categorized as the preset-workload-based execution of distributed full-batch training. Its main focus is on how to train GNNs at a low cost, so it adopts serverless computing. Serverless computing refers to “cloud function” threads, such as AWS Lambda and Google Cloud Functions, which can be used massively in parallel at an extremely low price. The hardware platform of Dorylus consists of CPUs and serverless threads. CPUs mainly perform the Aggregation operation, while the serverless threads are used for the Combination operation to reduce memory overhead. As for the communication and computation overlap, it uses two GPU streams for computation and communication, respectively.

### Table 9: Software Frameworks for Distributed GNN Training

| Name          | Multi-CPU | Multi-GPU | Distributed Mini-batch Training | Distributed Full-batch Training | Homogeneous Graph | Heterogeneous Graph | Static Graph | Dynamic Graph |
|---------------|-----------|-----------|---------------------------------|---------------------------------|-------------------|---------------------|--------------|---------------|
| PyG [87]      | ✓         | ✓         | ✓                               | ✓                               | ✓                  | ✓                   | ✓            | ✓             |
| DGL [88]      | ✓         | ✓         | ✓                               | ✓                               | ✓                  | ✓                   | ✓            | ✓             |
| NeuGraph [44] | ✓         |           | ✓                               | ✓                               | ✓                  | ✓                   | ✓            | ✓             |
| Roc [45]      | ✓         |           | ✓                               | ✓                               | ✓                  | ✓                   | ✓            | ✓             |
| FlexGraph [37]| ✓         |           | ✓                               | ✓                               | ✓                  | ✓                   | ✓            | ✓             |
| MG-GCN [48]   | ✓         |           | ✓                               | ✓                               | ✓                  | ✓                   | ✓            | ✓             |
| Dorylus [49]  | ✓         |           | ✓                               | ✓                               | ✓                  | ✓                   | ✓            | ✓             |
| AliGraph [35] | ✓         |           | ✓                               | ✓                               | ✓                  | ✓                   | ✓            | ✓             |
| AGL [36]      | ✓         |           | ✓                               | ✓                               | ✓                  | ✓                   | ✓            | ✓             |
| GraphTheta [55]| ✓            |           | ✓                               | ✓                               | ✓                  | ✓                   | ✓            | ✓             |
make full use of computing resources and reduce stagnation.

C. Software Frameworks Dedicated to Distributed Mini-Batch Training

The software frameworks dedicated to distributed mini-batch training include AliGraph [35] and AGL [36].

AliGraph [35] is a distributed multi-CPU training framework proposed in 2019. It is categorized as the joint-sample-based execution of distributed mini-batch training. It supports not only GNNs for homogeneous graphs and static graphs but also GNNs for heterogeneous graphs and dynamic graphs. In terms of storage, it adopts a graph partitioning method to store graph data in a distributed manner. The structure and features of the subgraph in each computing node are stored separately. In addition, two caches are added for the features of vertices and edges. Furthermore, it proposes a caching strategy to reduce the communication overhead between computing nodes; that is, each computing node caches the outgoing neighbors of frequently used vertices.

AGL [36] is a distributed multi-CPU training software framework proposed in 2020. It is categorized as the individual-sample-based execution of distributed mini-batch training. To speed up the sampling process, it introduces a distributed pipeline to generate \( k \)-hop neighborhood in the spirit of message passing, which is implemented with MapReduce infrastructure [104]. In this way, in the sampling phase, mini-batch data can be rapidly generated by collecting the \( k \)-hop neighbors of the target vertices. In the training phase, the computing nodes are partitioned into workers and parameter servers. The workers perform the model computation on the mini-batches, while the parameter servers maintain the current version of the model parameters. It also uses the commonly used optimization techniques for better efficiency, such as the transmission pipeline.

D. Software Frameworks Dedicated to Both Distributed Full-Batch Training and Mini-Batch Training

There exists a software framework dedicated to both distributed full-batch training and distributed mini-batch training.

GraphTheta [55] is a distributed multi-CPU training software framework proposed in 2021. It supports three training methods: mini-batch, full-batch, and cluster-batch training. The cluster-batch training method was proposed by Chiang et al. [83] in 2019. It first partitions a large graph into a set of smaller clusters. Then, it generates a batch of data either based on one cluster or a combination of multiple clusters. Apparently, cluster batch restricts the neighbors of a target vertex into only one cluster, which is equivalent to conducting a globalized convolution on a cluster of vertices. There is a parameter server in GraphTheta, which is responsible for managing multiversion model parameters. The worker obtains the model parameters from the parameter server and transfers the generated gradient back to the parameter server for the update of model parameters. Multiversion parameter management makes it possible to train GNNs asynchronously and synchronously.

VII. HARDWARE PLATFORMS FOR DISTRIBUTED GNN TRAINING

In this section, we introduce the hardware platforms for distributed GNN training. At present, it is mainly categorized into two types: multi-CPU hardware platform and multi-GPU hardware platform. A summary of the hardware platform for distributed GNN training is shown in Table 10. Next, we introduce them separately.

| Hardware Platform | Main Advantages | Main Disadvantages | Suitable Training Method |
|-------------------|-----------------|--------------------|--------------------------|
| Multi-CPU        | Rich Memory Resource and Good Scalability | Limited Computing Resource | Distributed Full-batch Training |
| Multi-GPU        | Rich Computing Resource | Limited Memory Resource, Resource Contention, and High Transmission Overhead | Distributed Mini-batch Training |
nodes for storage. Each computing node is responsible for the workload of its own subgraph. This results in computing nodes using mostly local data for computation although additional communication is also involved. By further optimizing the communication overhead, multi-CPU platforms can achieve good scalability.

The main disadvantage of a multi-CPU hardware platform is the limited computing resources in each computing node. CPU is a latency-oriented architecture with limited computing resources [112]. This makes it less friendly to neural network operations that incur a lot of computation [113], [114]. This problem is even more pronounced when using distributed mini-batch training. In this case, CPUs perform both the sampling and model computation, leading to a shortage of computing resources.

B. Multi-GPU Hardware Platform

The structure diagram of the multi-GPU hardware platform is illustrated in Fig. 11(b). Graph data are generally stored in a distributed manner on multiple computing nodes. Each computing node consists of CPUs and GPUs. Computing nodes are connected through high-speed network interfaces. For example, GPUs communicate with each other over PCIe under the same computing node and over InfiniBand across different computing nodes. GPUs can also be interconnected through NVLink and NVSwitch for higher speed inter-GPU communication.

The main advantage of a multi-GPU hardware platform is the rich computing resources. Compared with CPUs, GPUs have more computing resources that can be used for large-scale parallel computing. This is because GPU is a throughput-oriented architecture that focuses on improving the throughput by using massive computing resources [112], [113], [114]. In view of the development of DNN, both sparse matrix multiplication and dense matrix multiplication in GPU have been fully developed, which facilitates the model computation of GNN. Due to its limited memory capacity, it is not suitable for distributed full-batch training. Otherwise, it will result in a large amount of data transmission. Currently, distributed mini-batch training is usually performed on a multi-GPU hardware platform. The most popular way to perform distributed mini-batch training on a multi-GPU hardware platform is that CPUs are responsible for the sampling phase and GPUs are responsible for the model computation. This is because the computational pattern of the sampling phase is irregular and CPUs are more suitable for the irregular computational pattern than GPUs [91], [95].

The main disadvantages of a multi-GPU hardware platform are resource contention issues and expensive data transmission overhead. Since there are generally multiple GPUs on a computing node, these GPUs share the computing node’s resources, including CPU sampling threads and network interface [44], [86]. The competition for these resources makes multiple GPUs hard to obtain sufficient input data in time. This causes the occurrence of computational stagnation, resulting in poor scalability, manifested by a large gap between the actual speedup and the ideal [86]. The expensive data transmission overhead is due to the tight memory resources of GPUs. When using a multi-GPU hardware platform for distributed full-batch training, it needs to continuously transmit input data and output data [44]. When performing distributed mini-batch training, it is also necessary to continuously transmit the mini-batch data sampled by CPUs [51]. This problem is further exacerbated when the graph is large.

C. Multi-CPU Hardware Platform Versus Multi-GPU Hardware Platform

The summary of the comparison between multi-CPU hardware platform and multi-GPU hardware platform is shown in Table 10. Multi-CPU and multi-GPU hardware platforms have their own advantages and are suitable for different scenarios. The different characteristics of the two also lead to different optimization directions. The multi-CPU hardware platform is more suitable for distributed full-batch training. The entire graph can be cached in the memory, which leads to good scalability. The multi-GPU hardware platform is more suitable for distributed mini-batch training given its limited memory resources.
Table 11 Summary of the Comparison Between Distributed GNN Training and Distributed DNN Training

| Similarity | Distributed Full-batch Training | DNN Model Parallelism |
|------------|----------------------------------|------------------------|
| Perspective of Parallelism Exploitation | Model                  |                        |
| Fashion of Parallelism Exploitation     | Partitioning Workload of Model Computation |
| Communication Uncertainty               | High                    | Low                    |
| Transmission Planing Complexity         | High                    | Low                    |
| Probability of Communication Congestion | High                    | Low                    |
| Probability of Computation Stagnation   | High                    | Low                    |

| Difference | Distributed Mini-batch Training | DNN Data Parallelism |
|------------|---------------------------------|----------------------|
| Perspective of Parallelism Exploitation | Input Data           |                       |
| Fashion of Parallelism Exploitation     | Partitioning Raw Data |                      |
| Source of Input Data                    | Generated by Sampling Phase | Directly Loaded From Raw Data |
| Main Phase of Inter-node Communication | Sampling             | Gradient Synchronization |
| Communication Uncertainty               | High                  | Low                   |
| Transmission Planing Complexity         | High                  | Low                   |
| Probability of Communication Congestion | High                  | Low                   |
| Probability of Computation Stagnation   | High                  | Low                   |

However, when the graph size is small and there are high-speed interconnections between GPUs, such as NVLink, distributed full-batch training using a multi-GPU hardware platform can achieve good performance [45].

VIII. COMPARISON TO DISTRIBUTED DNN TRAINING

In this section, we highlight the characteristics of distributed computing of GNNs by comparing the distributed training of GNNs with that of DNNs. We first introduce the two categories of distributed DNN training: data parallelism and model parallelism. Among them, for GNN distributed full-batch training, its operation mode is similar to the model parallelism of DNNs. GNN distributed mini-batch training is similar to the data parallelism of DNNs. The two pairs are compared separately in the following. A summary of the comparison between distributed GNN training and distributed DNN training is shown in Table 11. Next, we introduce them separately.

A. Brief Introduction to Distributed DNN Training

Distributed DNN training is categorized into model parallelism and data parallelism [115]. In model parallelism, the model is split into several parts and different computing nodes compute each part. AlexNet [116] is the first one to use model parallelism because its model size could not fit in one GPU at that time. Therefore, the model is split into two parts that are then computed by two GPUs together.

In data parallelism, each computing node holds a copy of the model parameters, and the input dataset is distributed to each computing node. Each computing node conducts the training process completely using its own local data and uses the generated gradients to update the model parameters with other computing nodes together. Data parallelism is classified into synchronous training and asynchronous training. However, compared with synchronous distributed training, asynchronous distributed training has lower final accuracy, and sometimes, non-convergence may occur [115]. In synchronous distributed training, after each computing node completes one round of training on a small piece of data, the system starts to collect gradients that are used to update the model parameters uniformly. In this way, each round of training in each computing node is carried out with the same model parameters, which is equivalent to the case of one computing node.

B. Distributed Full-Batch Training Versus DNN Model Parallelism

1) Similarities: Both of them distribute the workload of model computation to different computing nodes from the perspective of the model. Each computing node needs to transfer the intermediate results of model computation to each other and cooperate to complete a round of training. In DNN model parallelism, DNN model computation is partitioned into different parts and distributed to different computing nodes [116]. Each computing node may be responsible for executing several layers of DNN or a set of operations within each layer. Similarly, GNN distributed full-batch training also distributes the workload of model computation to different computing nodes from the perspective of the model [40], [44], [45], [48]. Since the operation of GNN model computation is to operate on vertices, including the Aggregation and Combination operations, the workload is generally distributed to different computing nodes by partitioning the graph. Each computing node is responsible for completing the Aggregation and
Combination operations of its assigned vertices and communicating with other computing nodes to jointly advance the computation [37], [46], [47].

2) Differences: DNN model parallelism has low data communication uncertainty, while GNN distributed full-batch training exhibits high data communication uncertainty. In DNN model parallelism, most of the transmitted data are intermediate data generated in the computation. Since the transmission of intermediate data directly affects the execution of other computing nodes, there is a strong execution dependence among computing nodes. Due to the high certainty of data transmission in DNN model parallelism, which is reflected in the fixed data volume and transmission target, the computation and data transmission can be overlapped by reasonable planning to reduce the communication overhead. However, in GNN distributed full-batch training, the data communication uncertainty is high. This is because the data transmitted between each computing node is mainly the features of the neighboring vertex needed in the Aggregation step. Due to the irregular connection pattern of the graph itself and the fluctuation of the computational efficiency of each computing node, there are great uncertainties in data transmission [46]. Therefore, the congestion of the communication network can easily occur, which, in turn, leads to stagnation of computation [117].

C. Distributed Mini-Batch Training Versus DNN Data Parallelism

1) Similarities: Both of them use small pieces of input data per round of training to update the model parameters. The computing nodes use different small pieces of input data for computation and then jointly update the model parameters. In DNN data parallelism, each computing node holds a portion of the original data, typically stored in its memory. In each round of computation, a small piece of data is taken from its own local data and computed. Then, using the generated gradients, the model parameters are updated asynchronously or synchronously. In GNN distributed mini-batch training, the training of each computing node also uses independent input data, that is, the mini-batch generated by sampling [51], [52].

2) Differences: The input data required for each round of GNN distributed mini-batch training need to be generated by the sampling, while DNN data parallelism is loaded directly from memory. In DNN data parallelism, each computing node performs computations on its own local data [115]. It does not need to communicate with other computing nodes except for the stage of updating model parameters. In GNN distributed mini-batch training, a large number of communication requirements are required in the sampling phase, including querying graph data and transmitting mini-batch data. As graphs generally exhibit an irregular connection pattern, the communication in the sampling phase is highly irregular, resulting in high communication uncertainty and planning complexity. This leads to the fact that distributed GNN training may not be able to provide input data in time due to the inefficiency of the sampling, resulting in computational stagnation. This makes it necessary to optimize the sampling stage to reduce the communication overhead when conducting GNN distributed mini-batch training [51], [52].

IX. SUMMARY AND DISCUSSION

This section summarizes the aforementioned details of distributed GNN training and discusses several interesting issues and opportunities in this field.

The main focus of optimizing distributed full-batch training is to reduce its communication overhead [37], [40], [44], [45], [46], [47], [49]. In this training method, each computing node inevitably needs to communicate with other nodes for information about the entire graph structure, so that it can finish operations that require graph data that are not stored in the local split, such as aggregating information of neighboring vertices that are distributed in other computing components. As a result, reducing communication overhead, mainly by reasonably splitting the graph and planning the data transmission, can greatly improve computational efficiency.

By contrast, the main focuses of optimizing distributed mini-batch training are to speed up the sampling phase and reduce the mini-batch transmission overhead [35], [36], [38], [51], [52], [56]. Currently, it is popular to perform distributed mini-batch training in which CPUs are responsible for sampling and GPUs are responsible for GNN model computation. The sampling capability of CPUs is insufficient, producing a lot of idleness and limiting the utilization of GPUs due to insufficient data supply. By accelerating sampling and optimizing data transmission, the utilization of computing components can be improved, thereby improving efficiency.

The two methods show their advantages in different aspects, respectively. In current designs of distributed training of GNNs, the adoption of the mini-batch method is gradually increasing due to its advantages of a simpler implementation and less memory pressure. Distributed full-batch training, on the other hand, requires programmers to put more effort into programming and design, so it is more difficult to optimize. However, some research shows that the final precision of distributed full-batch training can be higher [45].

Next, we discuss several interesting issues and opportunities in this field.

A. Quantitative Analysis of Performance Bottleneck

As mentioned before, the performance of distributed GNN training is primarily hindered by the workload imbalance across different computing nodes, irregular transmission between computing nodes, and so on. Although these performance bottlenecks are well-known in the GNN
community, the quantitative analysis of these bottlenecks, which is important to guide the optimizations for distributed GNN training, is still obscure.

Recently, a lot of characterization efforts for GNN training or inference have been conducted on a single computing node or a single GPU [86], [118], [119], [120], [121], [122]. For example, Yan et al. [118] quantitatively disclose the computational pattern of the inference on a single GPU. Zhang et al. [119] quantitatively characterize the training of a large portion of GNN variants concerning general-purpose and domain-specific hardware architectures. However, there are few characterization efforts on distributed GNN training [86].

Diagnosing performance issues without a quantitative understanding of the bottlenecks can cause misdiagnosis: a potentially expensive effort that involves optimizing something that is not the real problem. Although Lin et al. [86] quantitatively characterize the end-to-end execution of distributed GNN training, it is not enough for the long-term development of distributed GNN training. We believe that the quantitative analysis of performance bottleneck will be one of the main directions in the field of distributed GNN training. Here are some potential directions for further exploration.

1) **Performance modeling and measurement**: GNN distributed training methods can be subjected to mathematical modeling and analysis, allowing us to gain insights into their theoretical potential and identify primary bottlenecks. To enhance model modeling, it is beneficial to measure the performance metrics of the distributed system in real-world operation, thus improving the precision of performance evaluation.

2) **Scalability testing**: Evaluating the scalability of distributed training frameworks and techniques across a range of workload and resource conditions is crucial. This assessment should consider factors such as computing capability growth and variations in workload balancing to ensure robust scalability.

3) **Cost-benefit analysis**: Allocating additional computational resources for acceleration introduces extra computational costs. It is imperative to measure the input–output ratio, assess the cost of hardware resources, and optimize resource utilization for cost-effective scaling.

4) **Automated evaluation tools**: Developing automated evaluation tools that can perform relevant tests automatically based on provided distributed configuration scenarios streamlines the testing process.

Moreover, there are opportunities to delve into the reliability and security aspects of GNN distributed systems. In summary, quantitative analysis of GNN distributed training systems plays a pivotal role in identifying and addressing performance bottlenecks.

**B. Performance Benchmark**

Despite the large number of performance benchmarks presented to fairly evaluate and compare the performance of software frameworks and hardware platforms for DNNs, there are few performance benchmarks for distributed GNN training. Most of the recent benchmarks for GNNs mainly focus on evaluating the prediction accuracy of trained GNN models. For example, the Open Graph Benchmark (OGB) [39] provides a set of large-scale real-world benchmark datasets to facilitate graph machine learning research. However, current benchmarks focus mainly on accuracy, instead of performance. As a result, standardized performance benchmark suites with typical, representative, and widely acknowledged workloads are needed for the industry and academia to comprehensively compare the performance of distributed GNN training systems.

In the field of DNNs, MLperf [123], an industry-standard benchmark suite for machine learning performance, has made a great contribution to the development of software frameworks and systems. Therefore, we believe that performance benchmark suites for GNNs similar to MLperf are vital to drive the rapid development of GNNs.

**C. Distributed Training on Extreme-Scale Hardware Platform**

For distributed GNN training on large-scale graphs or even extreme-scale graphs, the performance can be greatly improved by using the extreme-scale hardware platform. The scale of the graph data has reached a staggering magnitude and is growing at an exaggerated rate. For example, the Sogou [124] graph dataset has 12 trillion edges, and the Kronecker2 [125] graph dataset has 70 trillion edges. The training on graphs in this order of magnitude puts a very high requirement on the computing resources and memory resources of the computing system. Only by using the extreme-scale hardware platform, the training task can be completed in a reasonable time. In this respect, the research on graph processing and distributed DNN training on the extreme-scale hardware platform can be used for reference. ShenTu [124] realizes processing graph with trillion edges in seconds by using a petascale computing system. It suggests that the extreme-scale hardware platform can be used to accelerate distributed GNN training, which needs to explore and establish more hardware platform support for distributed GNN training.

**D. Domain-Specific Distributed Hardware Platform**

There are many single-node domain-specific hardware platforms designed for GNNs. However, very few efforts are conducted on distributed hardware platforms. Recently, many single-node domain-specific hardware accelerators designed for GNNs have achieved significant improvement in performance and efficiency compared with the single-GPU platform. For example, HyGCN [89] proposes a hybrid architecture consisting of the Aggregation engine and the Combination engine for the acceleration of the Aggregation operation and the Combination operation, respectively. Compared with NVIDIA V100 GPU,
HyGCN achieves an average $6.5 \times$ speedup with $10 \times$ energy reduction, respectively. However, the single-node platform is not sufficient to handle the rapid development of GNNs since the ever-growing scale of graphs dramatically increases the training time, generating the demand for a distributed GNN hardware platform. In response to that, MultiGCN [117] proposes a distributed hardware platform for the acceleration of GNN inference, which consists of multiple computing nodes to perform the inference in parallel. MultiGCN achieves $2.5 \sim 8 \times$ speedup over the state-of-the-art multi-GPU software framework. However, only a single work is not enough. We have witnessed that TPU-Pod [126], a domain-specific supercomputer for training DNNs, greatly accelerates the deployment of large-scale DNN models. Therefore, we believe that a domain-specific distributed hardware platform for GNNs similar to TPU-Pod is time to present, which is vital to drive the rapid development and deployment of GNNs.

E. General Communication Library for Distributed GNN Training

The variety of hardware platforms and irregular communication patterns of distributed GNN training leads to a strong demand for a general communication library to ease the programming effort on transmission planning while achieving high efficiency. The communication characteristics of hardware platforms are various due to the differences in the number of computing nodes, network topology, communication bandwidth, and communication latency of the interconnection interface. In order to better adapt to the various transmission requirements of distributed GNN training on various hardware platforms, it is urgent to develop a general communication library that adapts to different network topologies, as well as various interconnection interfaces of different communication bandwidths and latency, so as to ease the implementation of novel communication patterns while ensuring efficiency.

F. Distributed Training for Dynamic GNNs

Dynamic GNNs are a specific variant of GNNs designed for dynamic graphs, which inherently evolve over time. These dynamic graphs fall into two main categories: continuous-time dynamic graphs (CTDGs) and discrete-time dynamic graphs (DTDGs) [127], [128]. CTDGs capture continuous dynamics of dynamic graphs, while DTDGs consist of a sequence of graph snapshots sampled at regular intervals.

Dynamic GNNs differ from GNNs focusing on static graphs in two crucial ways. First, they operate on dynamic graphs with changing input. Second, they incorporate temporal neural networks such as long short-term memory (LSTM) [129] to capture temporal information. As a result, dynamic GNNs pose a higher computational complexity compared to static GNNs, making distributed training for them more challenging. Applying the distributed training optimization techniques discussed earlier to dynamic GNNs necessitates specific adjustments. For instance, optimization techniques related to graph partitioning must consider how to update graphs dynamically.

To conclude, developing distributed training systems for dynamic GNNs presents several challenges.

1) **Dynamic input data**: Swift propagation of changes in graph topology and vertex features to computing nodes is crucial. Efficient network transmission support and strategies to minimize transmission overhead and reduce waiting times for computing nodes are necessary [127], [130].

2) **Real-time requirements**: Dynamic GNNs are designed for rapidly changing graph data. As a result, dynamic GNNs may require real-time output generation to satisfy predefined time constraints.

3) **Workload balancing**: The dynamic nature of input data in dynamic GNNs makes traditional static workload partitioning methods impractical, necessitating innovative workload balancing approaches.

4) **Parallelism exploitation**: Dynamic GNNs, especially in the case of processing DTDGs, perform computations across multiple graphs. Exploring parallel computations across these graphs offers opportunities for parallelism.

Current research focuses on reducing redundant data transmission by leveraging similarities between graphs [127], [130]. Two frameworks, PyG Temporal [131] and TGL [132] (based on PyG [87] and DGL [88], respectively), support distributed training of dynamic GNNs. Sven [128], an extension of TGL, has significantly accelerated distributed training by eliminating data transmission redundancy and utilizing pipeline parallelism. While still in its early stages, research on optimizing distributed training for dynamic GNNs is poised to become a central focus for future investigations.

G. Distributed Training for Deep GNNs

The issue of performance deterioration has garnered significant attention in the field of GNNs [70], [71], [72], [133], [134], [135], [136], which significantly affects the number of GNNs’ layers. Specifically, it has been observed that using deeper layer stacks can lead to a partial loss in performance, which is in contrast to the behavior of DNNs [137]. Consequently, the best-performing GNN models currently tend to have two to four layers [70]. This phenomenon can primarily be attributed to two factors: 1) oversmoothing and 2) underreaching [70], [71]. Oversmoothing refers to the distinctiveness of vertex features diminishing as information is propagated through multiple layers. Targeted solutions for this issue include techniques such as DropEdge [72] and DropNode [73], which involve selectively removing edges and vertices. Underreaching, on the other hand, signifies the challenge of propagating information across long-range paths. Specific strategies to address this problem include adding virtual edges [138] and super vertices [139]. It is addition, it is worth noting...
that EEGNN [70] introduces another contributing factor called missimplification, where excessive simplification occurs during graph building, neglecting the importance of edges and self-loops.

The impact of performance deterioration on distributed GNN training significantly influences the overall optimization direction in this domain. Because of the performance deterioration issue, current GNN models tend to have a relatively small number of layers [70], resulting in the smaller scale of model parameters. This discrepancy becomes especially apparent when comparing them to large-scale graph datasets [39]. The stark disparity between the vast volume of input data and the modest model parameters makes GNN execution memory-intensive or communication-intensive. Consequently, there is a strong emphasis on researching methods to minimize the preparation overhead of input data, particularly concerning data transmission costs. This pursuit has led to techniques such as activation rematerialization in Section IV-B, mini-batch transmission pipelining in Section V-A, and frequently used data caching in Section V-B. This also explains the popularity of current distributed mini-batch training, which is similar to data parallelism in DNNs, as partitioning GNN models into smaller pieces does not hold much significance.

Currently, there is a continuous emergence of techniques addressing performance deterioration in GNNs [70], [72], [73], [138], [139], encouraging the exploration of deeper layer structures in GNN architectures. This evolving trend is shaping the future optimization strategies for distributed GNN training. One key aspect is the necessity to accommodate the rise of deep GNN models, which may favor approaches akin to model parallelism, such as pipeline parallelism [140]. Research efforts are likely to focus on implementing the execution of deep GNNs on distributed training platforms.

**X. CONCLUSION**

In this article, we comprehensively reviewed the distributed training of GNNs. After investigating recent efforts on distributed GNN training, we classify them in more detail according to their workflow, including the dispatch-workload-based execution and preset-workload-based execution of distributed full-batch training, and the individual-sample-based execution and joint-sample-based execution of distributed mini-batch training. Each taxonomy’s workflow, computational pattern, and communication pattern are summarized, and various optimization techniques are further introduced to facilitate the understanding of recent research status and enable readers to quickly build up a big picture of distributed training of GNNs. In addition, this article introduces the training software and hardware platforms and then contrasts distributed GNN training with distributed DNN training. In the end, we provide several discussions about interesting issues and opportunities for distributed GNN training.

The emergence of distributed GNN training has successfully expanded the usage of GNNs on large-scale graphs, making it a powerful tool when learning from real-world associative relations. We optimistically look forward to new research that can further optimize this powerful tool on large-scale graph data and bring its application to the next level.

**REFERENCES**

[1] D. B. West, *Introduction to Graph Theory*, vol. 2. Upper Saddle River, N.J., USA: Prentice-Hall, 2001.

[2] J. A. Bondy and U. S. R. Murty, *Graph Theory With Applications*, vol. 290. London, U.K.: Macmillan, 1976.

[3] S. Wasserman et al., *Social Network Analysis: Methods and Applications*. Cambridge, U.K.: Cambridge Univ. Press, 1994.

[4] H. Kwak, C. Lee, H. Park, and S. Moon, “What is Twitter, a social network or a news media?” in Proc. 19th Int. Conf. World Wide Web. New York, NY, USA: Association for Computing Machinery, Apr. 2010, p. 591, doi: 10.1145/1772690.1772751.

[5] J. J. McAuley and J. Leskovec, “Learning to discover social circles in ego networks,” in Proc. Adv. Neural Inf. Process. Syst., P. L. Bartlett, F. C. N. Pereira, C. J. C. Burges, L. Bottou, and K. Q. Weinberger, Eds. Lake Tahoe, NV, USA, 2012, pp. 548–556. [Online]. Available: https://proceedings.neurips.cc/paper/2012/hash/7a614fd06c325499f167a614fd06c325499f16.html.

[6] K. Bollacker, C. Evans, P. Paritosh, T. Sturge, and J. Taylor, “Freebase: A collaboratively created graph database for structuring human knowledge,” in Proc. ACM SIGMOD Int. Conf. Manag. Data, 2008, pp. 1247–1250.

[7] M. Ashburner et al., “Gene ontology: Tool for the unification of biology,” *Nature Genet.*, vol. 25, pp. 25–29, Mar 2000.

[8] G. A. Miller, “WordNet: A lexical database for English,” *Comput. ACM*, vol. 38, no. 11, pp. 39–41, 1995.

[9] Z. Zhang, P. Cui, and W. Zhu, “Deep learning on graphs: A survey,” *IEEE Trans. Knowl. Data Eng.*, vol. 34, no. 1, pp. 249–270, Jan. 2022, doi: 10.1109/TKDE.2020.2981333.

[10] W. L. Hamilton, Z. Ying, and J. Leskovec, “Graph neural networks: A review of methods and applications,” *AI Open*, vol. 1, pp. 57–81, Jan. 2020, doi: 10.1016/j.aiopen.2021.01.001.

[11] J. Zhou et al., “Graph neural networks: A review of methods and applications,” *AI Open*, vol. 1, pp. 436–445, 2018.

[12] L. Hamilton, Z. Ying, and J. Leskovec, “Inductive representation learning on large graphs,” in *Proc. Adv. Neural Inf. Process. Syst.*, I. Guyon et al., Eds. Long Beach, CA, USA, 2017, pp. 1024–1034. [Online]. Available: https://proceedings.neurips.cc/paper/2017/hash/s0d9bb0e5033da9c6f05b3a837a7e89-Abstract.html.

[13] M. M. Bronstein, J. Bruna, Y. LeCun, A. Szlam, and P. Vandergheynst, “Geometric deep learning: Going beyond Euclidean data,” *IEEE Signal Process. Mag.*, vol. 34, no. 4, pp. 18–42, Jul. 2017.

[14] T. N. Kipf and M. Welling, “Semi-supervised classification with graph convolutional networks,” in *Proc. 5th Int. Conf. Learn. Represent.*, 2017, pp. 1–14.

[15] M. Zhang and Y. Chen, “Link prediction based on graph neural networks,” in *Proc. Adv. Neural Inf. Process. Syst.*, S. Bengio, H. M. Wallach, H. Larochelle, K. Grauman, N. Cesa-Bianchi, and R. Garnett, Eds. Montreal, QC, Canada, 2018, pp. 5171–5181. [Online]. Available: https://proceedings.neurips.cc/paper/2018/hash/539bdf753f7799638240f94d62e24263-Abstract.html.

[16] Z. Zhang, Z. Cui, M. Neumann, and Y. Chen, “An end-to-end deep learning architecture for graph classification,” in *Proc. 32nd AAAI Conf. Artif. Intell. (AAAI), 30th Innov. Appl. Artif. Intell. (IAAI), 8th AAAI Symp. Educ. Adv. Artif. Intell. (EAAI), S. A. McIlraith and K. Q. Weinberger, Eds. New Orleans, LA, USA, 2018, pp. 4438–4445. [Online]. Available: https://www.aaai.org/ocs/index.php/AAAI/AAAI18/paper/view/17164.

[17] M. Schlichtkrull, T. N. Kipf, P. Bloem, R. V. D. Berg, I. Titov, and M. Welling, “Modeling relational data with graph convolutional networks,” in *Proc. Eur. Semantic Web Conf. Cham, Switzerland: Springer, 2018*, pp. 593–607.

[18] T. Young, D. Hazarika, S. Porta, and E. Cambria, “Recent trends in deep learning based natural language processing [review article],” *IEEE Comput. Intell. Mag.*, vol. 13, no. 3, pp. 55–75, Aug. 2018, doi: 10.1109/MCI.2018.2840738.

[19] W. Mu, S. Pan, X. Zhu, C. Zhou, and L. Pan, “Domain-adversarial graph neural networks for text classification,” in *Proc. IEEE Int. Conf. Data Mining (ICDM), Beijing, China, Nov 2019*, pp. 648–657, doi: 10.1109/ICDM.2019.00075.

[20] J. Bartsch, I. Titov, W. Ariz, D. Marcheggiani, and K. Sima'an, “Graph convolutional encoders for
syntax-aware neural machine translation,” in Proc. Conf. Empirical Methods Natural Lang. Process. Copenhagen, Denmark: Association for Computational Linguistics, Sep. 2017, pp. 1957–1967. [Online]. Available: https://doi.org/10.18653/v1/D17-1209

[22] R. Ying, R. He, K. Chen, P. Eksombochtai, W. L. Hamilton, and J. Leskovec, “Graph convolutional networks for web-scale recommender systems,” in Proc. 24th ACM SIGKDD Int. Conf. Knowl. Discovery Data Mining, Y. Guo and F. Fanaaqa, Eds. Jul. 2018, pp. 974–983, doi: 10.1145/3219819.3219903.

[23] W. Pan et al., “Graph neural networks for social recommendation,” in Proc. World Wide Web Conf., L. Liu, Eds., San Francisco, CA, USA, 2019, pp. 417–426, doi: 10.1145/3308558.3313488.

[24] C. Ma, M. A. Yang, J. Sun, X. Liu, and M. Coates, “Memory augmented graph neural networks for sequential recommendation,” in Proc. AAAI Conf. Artif. Intell., Apr. 2020, vol. 34, no. 4, pp. 5045–5052. [Online]. Available: https://ojs.aaai.org/index.

[25] X. Chen, L.-J. Li, L. Fei-Fei, and A. Gupta, “Scene graph generation from images,” in Proc. IEEE/CVF Conf. Vis. Pattern Recognit., Jun. 2018, pp. 7229–7238.

[26] A. Mirhoseini et al., “A graph placement methodology for chip design,” Nature, vol. 594, no. 7862, pp. 207–212, Jun. 2021.

[27] H. Wang et al., “GCN-RL circuit designer: A graph neural network based approach,” Comput. Aided Des. Model. Eng., vol. 594, no. 7862, pp. 207–212, Jun. 2021.

[28] X. Geng et al., “Spatiotemporal multi-graph convolutional neural networks,” in Proc. 9th Int. Conf. High Perform. Comput., Netw., Storage Anal., Nov. 2020, pp. 1–14, doi: 10.1109/SC40.2020.00007.

[29] J. Huang, W. Qin, X. Wang, and W. Chen, “Survey of external memory large-scale graph processing on a multi-core system,” J. Supercomput., vol. 76, no. 1, pp. 549–573, Jan. 2020, doi: 10.11172/019.001230-0.

[30] J. Huang, H. Wang, X. Fei, X. Wang, and W. Chen, “TC-stream: Large-scale triangle graph counting on a single machine,” IEEE Trans. Parallel Distrib. Syst., vol. 33, no. 11, pp. 3067–3078, 2022, doi: 10.1109/TPDS.2021.3135329.

[31] C. Ma, L. Ma, Y. Zhang, J. Sun, X. Liu, and D. Tsirfat, Eds., R. Bento, usa, 2019, pp. 443–458. [Online]. Available: https://www.usenix.org/conference/artificial-presentation.

[32] Z. J. S. L. M. J. G. D. B., and A. Aiken, “Improving the accuracy, scalability, and performance of graph neural networks with ROC,” in Proc. 28th ACM SIGMOD Conf. Adv. Neural Inf. Process. Syst., H. Larochelle, M. Ranzato, A. Lahoti, M. Balcan, and H. Lin, Eds. 2020, pp. 2118–2123. [Online]. Available: https://proceedings.neurips.cc/paper/2020/hash/6b6041a5c5702bd2c83d52c2f6dcd6b9-Abstract.html.

[33] D. Zheng et al., “DistGNN: Distributed graph neural network training on billion-scale graphs,” in Proc. IEEE/ACM 10th Workshop Irregular Appl., Architectures Algorithms (IA), Atlanta, GA, USA, Mar. 2020, pp. 36–44, doi: 10.1109/1400.00117.

[34] W. Hu et al., “Open graph benchmarks: Datasets for machine learning on graphs,” in Proc. Adv. Neural Inf. Process. Syst., H. Larochelle, M. Ranzato, A. Lahoti, M. Balcan, and H. Lin, Eds. 2020, pp. 22118–21213. [Online]. Available: https://proceedings.neurips.cc/paper/2020/hash/1b06d41a5c5702bd2c83d52c2f6dcd6b9-Abstract.html.

[35] A. Tripathy, K. Velick, and A. Buluc, “Reducing communication in graph neural network training,” in Proc. Int. Conf. High Perform. Comput. Netw., Storage Anal., Nov. 2020, pp. 1–14, doi: 10.1109/SC40.2020.00007.

[36] X. Geng et al., “Statistical accuracy and comprehensive taxonomy,” 2020, art 2005.03675.

[37] M. Coates, “Memory augmented graph neural networks,” in Proc. 29th Int. Joint Conf. Artif. Intell., C. Biessiere, Ed. Jul. 2020, pp. 4877–4884, doi: 10.24963/ijcai.2020/679.

[38] P. Velickovic, G. Cucurull, A. Casanova, A. Romero, P. Liò, and Y. Bengio, “Graph attention networks,” 2018, arXiv:1710.03722v1.

[39] Y. Liu et al., “GraphTheta: A distributed graph neural network learning system with flexible training strategy,” 2021, arXiv:2104.10569.

[40] T. Kaler et al., “Accelerating training and inference of graph neural networks with fast sampling and pipelining,” 2021, arXiv:2110.08450.

[41] S. Gandhi and A. P. Iyer, “P3: Distributed deep graph learning at scale,” in Proc. 15th USENIX Symp. Operat. Syst. Design Implement. (OSDI), A. D. Brown and J. R. Lorch, Eds. Jul. 2021, pp. 551–568. [Online]. Available: https://www.usenix.org/conference/osdi21/presentation/gandhi.

[42] Z. Liu, V. Bao, and C. Wu, “Optimizing task placement and online scheduling for distributed GNN training acceleration,” in Proc. IEEE INFOCOM Conf. Comput. Commun., London, U.K., May 2022, pp. 890–899, doi: 10.1109/INFOCOM48880.2022.9976910.

[43] S. Abadal, A. Jain, R. Guirado, J. L. Larocca, and E. Alarcón, “Computing graph neural networks: A survey from fast execution to deployment,” 2020, arXiv:1903.37891.

[44] L. C. Lamb, A. D. Garezzo, M. Gori, M. O. Prates, P. H. A. Avelar, and M. Yardi, “Graph neural networks on modest hardware,” in Proc. 39th Int. Conf. High Perform. Comput. Archit., F. J. Ruiz, J. G. D. M. B., and J. van de Meent, Eds. Valencia, Spain, 2023, pp. 2132–2146. [Online]. Available: https://proceedings.mlr.press.

[45] R. Zhu et al., “GraphDNN: A distributed graph neural network training system with flexible training strategy,” 2021, arXiv:2111.08202.

[46] D. Zheng, X. Qin, C. Yang, J. Lin, M. L. K., and C. Wang, “DistGNN: A graph neural network system with thread-parallel training,” 2021, arXiv:2110.10569.

[47] L. C. Lamb, A. D. Garezzo, M. Gori, M. O. Prates, P. H. A. Avelar, and M. Yardi, “Graph neural networks on modest hardware,” in Proc. 39th Int. Conf. High Perform. Comput. Archit., F. J. Ruiz, J. G. D. M. B., and J. van de Meent, Eds. Valencia, Spain, 2023, pp. 2132–2146. [Online]. Available: https://proceedings.mlr.}
ABOUT THE AUTHORS

Haiyang Lin received the B.S. degree from Shanghai Jiao Tong University, Shanghai, China, in 2018. He is working toward the Ph.D. degree at the Institute of Computing Technology, Chinese Academy of Sciences, Beijing, China.
His current research interests include distributed training, graph-based hardware accelerators, and high-throughput computer architecture.

Mingyu Yan (Member, IEEE) received the Ph.D. degree from the University of Chinese Academy of Sciences, Beijing, China, in 2020.
He is currently an Associate Professor with the Institute of Computing Technology, Chinese Academy of Sciences, Beijing, China. To date, he has published over 20 research papers in top-tier journals and conferences, including the International Symposium on Microarchitecture (MICRO), High Performance Computer Architecture (HPCA), and International Joint Conference on Artificial Intelligence (IJCAI). His current research interests include graph-based machine learning, domain-specific hardware architecture for graph-based machine learning, domain-specific distributed systems for graph-based machine learning, and high-throughput computer architecture.

Xiaochun Ye received the Ph.D. degree in computer architecture from the Institute of Computing Technology, Chinese Academy of Sciences, Beijing, China, in 2010. He is currently a Professor with the Institute of Computing Technology, Chinese Academy of Sciences. To date, he has published over 90 research papers in top-tier journals and conferences, the International Symposium on Microarchitecture (MICRO), High Performance Computer Architecture (HPCA), International Joint Conference on Artificial Intelligence (IJCAI), Design Automation Conference (DAC), International Conference on Computer-Aided Design (ICCAD), IEEE TRANSACTIONS ON COMPUTERS (TC), IEEE TRANSACTIONS ON COMPUTER-AIDED DESIGN OF INTEGRATED CIRCUITS AND SYSTEMS (TCAD), IEEE/JOURNAL OF AUTOMATICA SINICA (IEEE/JAS), and IEEE JOURNAL OF SELECTED TOPICS IN SIGNAL PROCESSING (IEEE-JSTSP). His current research interests include graph-based machine learning, domain-specific hardware architecture for graph-based machine learning, domain-specific distributed systems for graph-based machine learning, and high-throughput computer architecture.
Hot Chips: A Symposium on High Performance Chips (HOT CHIPS), International Conference on Computer Vision (ICCV), IEEE TRANSACTIONS ON COMPUTERS (TC), IEEE/CAA JOURNAL OF AUTOMATICA SINICA (IEEE/CAA JAS), and IEEE Micro. His main research interests include domain-specific hardware architecture for graph-based machine learning and high-throughput computer architecture.

Dongrui Fan (Senior Member, IEEE) received the Ph.D. degree in computer architecture from the Institute of Computing Technology, Chinese Academy of Sciences, Beijing, China, in 2005. He is currently a Professor and a Ph.D. Supervisor with the Institute of Computing Technology, Chinese Academy of Sciences. To date, he has published over 140 research papers in top-tier journals and conferences, including the IEEE TRANSACTIONS ON PARALLEL AND DISTRIBUTED SYSTEMS (TPDS), IEEE TRANSACTIONS ON COMPUTERS (TC), IEEE Micro, Journal of Parallel and Distributed Computing (JPDC), International Symposium on Microarchitecture (MICRO), High Performance Computer Architecture (HPCA), ACM SIGPLAN Symposium on Principles and Practice of Parallel Programming (PPoPP), International Joint Conference on Artificial Intelligence (IJCAI), Design Automation Conference (DAC), International Conference on Computer-Aided Design (ICCAD), Design, Automation and Test in Europe (DATE), International Conference on Computer Vision (ICCV), International Conference on Parallel Architectures and Compilation Techniques (PACT), and Hot Chips: A Symposium on High Performance Chips (HOT CHIPS). His main research interests include high-throughput computer architecture, high-performance computer architecture, and low-power design.

Dr. Fan has been a member of the program committee of many important academic conferences, including MICRO and HPCA.

Shirui Pan (Senior Member, IEEE) received the Ph.D. degree in computer science from the University of Technology Sydney (UTS), Ultimo, NSW, Australia, in 2015. He was a Senior Lecturer with the Faculty of Information Technology (IT), Monash University, Melbourne, VIC, Australia. He is currently a Professor with the School of Information and Communication Technology, Griffith University, Nathan, QLD, Australia. He is an expert in graph neural networks (GNNs). To date, he has published over 150 research papers in top-tier journals and conferences, including the IEEE TRANSACTIONS ON PATTERN ANALYSIS AND MACHINE INTELLIGENCE (TPAMI), IEEE TRANSACTIONS ON KNOWLEDGE AND DATA ENGINEERING (TKDE), IEEE TRANSACTIONS ON NEURAL NETWORKS AND LEARNING SYSTEMS (TNNLS), International Conference on Machine Learning (ICML), Annual Conference on Neural Information Processing Systems (NeurIPS), ACM Knowledge Discovery and Data Mining (KDD), AAAI Conference on Artificial Intelligence (AAAI), International Joint Conference on Artificial Intelligence (IJCAI), International Conference on World Wide Web Conference (WWW), IEEE Conference on Computer Vision and Pattern Recognition (CVPR), IEEE International Conference on Data Engineering (ICDE), IEEE International Conference on Data Engineering (ICDE), and International Conference on Data Mining (ICDM). His research has attracted over 10,000 citations. His research interests include data mining and machine learning.

Dr. Pan serves as an Area Chair/Senior Program Committee (SPC) Member of ICDM, AAAI, and IJCAI and a PC Member of NeurIPS, KDD, WWW, and so on. He is an Australian Research Council (ARC) Future Fellow. He was recognized as one of the AI 2000 AAAI/IJCAI Most Influential Scholars in Australia in 2021 and 2022 and one of the World’s Top 2% Scientists in 2021. His research on GNNs received the IEEE ICDM Best Student Paper Award in 2020 and the JCDL Best Paper Honorable Mention Award in 2020. He has seven papers on GNNs recognized as the Most Influential Papers in KDD (x1), IJCAI (x1), and ACM International Conference on Information and Knowledge Management (CIKM x1) in February 2022. In particular, his survey paper “A comprehensive survey on graph neural networks” in IEEE TRANSACTIONS ON NEURAL NETWORKS AND LEARNING SYSTEMS in 2021 has been cited over 4100 times. His research on "Graph WaveNet" (582 citations) is the most cited research at IJCAI 2019 as of September 2022.

Wenguang Chen received the B.S. and Ph.D. degrees in computer science from Tsinghua University, Beijing, China, in 1995 and 2000, respectively. He was the CTO of Opportunity International Inc., Beijing, from 2000 to 2002. In January 2003, he joined Tsinghua University. He joined Ant Group, Hangzhou in 2020 as the Head of Graph Processing Technology. Since 2022, he has been the President of the Ant Technology Research Institute, Hangzhou. He is currently a Professor and an Associate Head of the Department of Computer Science and Technology, Tsinghua University. He is an expert in parallel and distributed systems for extreme-scale graph processing. To date, he has published over 100 research papers in top-tier conferences and journals, including the USENIX Symposium on Operating Systems Design and Implementations (OSDI), ACM SIGPLAN Symposium on Principles and Practice of Parallel Programming (PPoPP), International Conference for High Performance Computing, Networking, Storage, and Analysis (SC), International Conference on Architectural Support for Programming Languages and Operating Systems (ASPLOS), International Conference on Supercomputing (ICS), European Conference on Computer Systems (EuroSys), International Parallel and Distributed Processing Symposium (IPDPS), IEEE/ACM International Symposium on Microarchitecture (MICRO), USENIX Annual Technical Conference (ATC), Code Generation and Optimization (CGO), International Conference on Very Large Data Bases (VLDB), International Conference on Parallel Architectures and Compilation Techniques (PACT), Nature, IEEE TRANSACTIONS ON PARALLEL AND DISTRIBUTED SYSTEMS (TPDS), and IEEE TRANSACTIONS ON COMPUTERS (TC).

Dr. Chen has been a member of the program committee of many important academic conferences, including OSDI, ACM Symposium on Operating Systems Principles (SOSP), ACM SIGPLAN Conference on Programming Language Design and Implementation (PLDI), PPoPP, SC, ASPLOS, TPDS, TC, ATC, CGO, IPDPS, IEEE/ACM International Symposium on Cluster, Cloud and Grid Computing (CCGrid), International Conference on Parallel Processing (ICPP), PACT, EuroSys, and ICS. Their work in extreme-scale graph processing, i.e., “ShenTu: Processing Multi-Trillion Edge Graphs on Millions of Cores in Seconds,” was a finalist for the Association for Computing Machinery (ACM), Gordon Bell Prize in 2018; the Gordon Bell Prize is commonly referred to as the Nobel Prize of Supercomputing. He is the chair of the ACM China Council and the Chief Editor of the Chinese edition of Communications (ACM).
Yuan Xie (Fellow, IEEE) received the B.S. degree in electronic engineering from Tsinghua University, Beijing, China, in 1997, and the M.S. and Ph.D. degrees in electrical engineering from Princeton University, Princeton, NJ, USA, in 1999 and 2002, respectively.

He was an Advisory Engineer with the IBM Microelectronics Division, Burlington, NJ, USA, from 2002 to 2003. He was a Full Professor with Pennsylvania State University, State College, PA, USA, from 2003 to 2014. He was a Visiting Researcher with the Interuniversity Microelectronics Center (imec), Leuven, Belgium, from 2005 to 2007 and in 2010. He was a Senior Manager and a Principal Researcher with the AMD, Beijing Research China Laboratory, Beijing, from 2012 to 2013. He is currently a Chair Professor with the Department of Electronic and Computer Engineering, Hong Kong University of Science and Technology. Through extensive collaboration with industry partners, including AMD, HP, CA, USA, Honda, Tokyo, Japan, IBM, VT, USA, Intel, Google, Samsung, imec, Qualcomm, Alibaba, Seagate, CA, USA, and Toyota, Toyota City, Japan, he has helped the transition of research ideas to industry. His research interests include VLSI design, electronic design automation (EDA), computer architecture, and embedded systems.

Dr. Xie serves as a Committee Member of the IEEE Design Automation Technical Committee (DATC). He is a Fellow of American Association for the Advancement of Science (AAAS)/Association for Computing Machinery (ACM) and an expert in computer architecture who has been inducted into the International Symposium on Computer Architecture (ISCA)/International Symposium on Microarchitecture (MICRO)/High Performance Computer Architecture (HPCA) Hall of Fame. He was a recipient of the Best Paper Awards at HPCA 2015, International Conference on Computer-Aided Design (ICCAD 2014), Great Lakes Symposium on VLSI (GLSVLSI 2014), IEEE Computer Society Annual Symposium on VLSI (ISVLSI 2012), International Symposium on Low Power Electronics and Design (ISLPED 2011), Asia and South Pacific Design Automation Conference (ASPDAC 2008), and International Conference on ASIC (ASICON 2001); the Best Paper Nominations at ASPDAC 2014, MICRO 2013, Design, Automation and Test in Europe (DATE 2013), ASPDAC in 2009 and 2010, and ICCAD 2006; the 2016 IEEE Micro Top Picks Award at the 2008 IBM Faculty Award; and the 2006 NSF CAREER Award. He has served as the TPC Chair of ICCAD 2019, HPCA 2018, ASPDAC 2013, ISLPED 2013, and International Forum on Embedded MPSoC and Multicore (MPSOC 2011). He serves as the Editor-in-Chief of ACM Journal on Emerging Technologies in Computing Systems and an Associate Editor for the ACM Transactions on Design Automation of Electronic Systems, the IEEE TRANSACTIONS ON COMPUTERS, the IEEE TRANSACTIONS ON COMPUTER-AIDED DESIGN OF INTEGRATED CIRCUITS AND SYSTEMS, the IEEE TRANSACTIONS ON VERY LARGE SCALE INTEGRATION (VLSI) SYSTEMS, IEEE Design and Test, and IET Computers & Digital Techniques.