Improved model predictive control for three-phase Vienna rectifiers

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Abstract: Vienna rectifiers are unidirectional three-level boost rectifiers used in extensive industrial applications. Much effort has been devoted to study the control strategies for Vienna rectifier. This paper introduces the manipulation principles and operation steps of model predictive control strategy based on optimal switching sequence principle. In order to address several issues of the conventional methods, an improved model predictive control strategy has been proposed to regulate the ac currents and neutral point voltage deviation. The proposed strategy has been validated by experiments. The consequences show that the proposed strategy can achieve accurate prediction especially during neutral point voltage unbalance by dealing with constraints and provide excellent performance in dynamic property and stability.

Keywords: model predictive control, three-phase Vienna rectifiers, control strategies, neutral point voltage balance

Classification: Power devices and circuits

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1 Introduction

Three-phase Vienna rectifiers have been widely used in industrial applications such as telecommunication systems, aircraft, power factor correction systems and wind turbine systems [1, 2] due to the advantages of compact size, low cost, high power density, high efficiency and low current total harmonic distortion (THD) [3, 4]. A lot of research has been carried out with respect to rectifier topologies, modulation strategies and control algorithms to achieve desired performance.

Model predictive control (MPC) has become a very attractive strategy for controlling power converters. Based on the discrete model, MPC can derive the prediction of control variables for future moments within finite prediction horizon. By minimizing the cost function evaluating the difference between prediction and reference, an optimal control sequence can be obtained. Depending on the property of the system input, MPC can be divided into finite control set (FCS) MPC and continuous control set (CCS) MPC. MPC is well known for its wide application ranges, fast dynamic response and capability of handling constraints [5, 6].

FCS-MPC is an intuitive and easy-to-implement MPC algorithm using the switching actions as the system inputs directly. Due to the finiteness of the switching actions, FCS-MPC evaluates each switching action by applying them
into the cost function. Search for the optimal control action minimizing the cost function’s value [7]. The computational cost of FCS-MPC depends on the size of control set greatly. Therefore, the advantage of simplicity has been shown in Vienna rectifier for its less feasible switching patterns compared with neutral point clamped (NPC) three-level converter. However, FCS-MPC generates variable switching frequency and provides widespread harmonic spectrum [8].

The algorithm selecting the continuous control signals as the system input is referred to as CCS-MPC. Different from FCS-MPC, CCS-MPC provides constant switching frequency. Among various CCS-MPC, optimal switching sequence (OSS) MPC strategy considers the commutation instants as the system input. For the determined switching sequence, the optimal commutation instants achieving desired system behavior can be obtained by minimizing the cost function. In the strategy consisting of two vectors (a nonzero vector and a zero vector), the active vector can be selected by minimizing the cost function for all possible switching vectors similar to FCS-MPC [9, 10]. A symmetrical switching sequence based on three voltage vectors was introduced in [11] in detail. In order to compensate the performance degradation caused by inappropriate switching sequence selection, several improved algorithms were presented in the literature. [12] redefined the candidate switching sequence in case of negative values of predicted duration times. A method avoiding the use of lookup tables was introduced in [13] by minimizing the cost function based on calculated optimal commutation instants for each sector in search of the global optimal switching sequence. [14] analyzed the relationship of duty cycles in each sector and proposed an algorithm without sector information of the grid voltage and the voltage vector selection process.

This paper proposed an improved model predictive control strategy for Vienna rectifiers based on optimal switching sequence principle. The constraints can not be addressed to achieve the optimal solutions by the conventional method. The proposed strategy takes the constraints into account when dealing with the optimal issues. The dynamic model of Vienna rectifier has been reconstructed. With the modified prediction, the minimal cost function evaluating the ac currents can be obtained. Additionally, the neutral point voltage difference has been introduced into the cost function. Compared with conventional method, the proposed strategy can obtain the accurate prediction of ac currents especially during neutral point voltage unbalance and system performance has been improved.

The rest of this paper is organized as follows. In Section 2, the conventional OSS-MPC strategy is introducing. In Section 3, the proposed OSS-MPC is presented in detail. In Section 4, experimental results are presented and analyzed. Finally, conclusions are given in Section 5.

2 Conventional OSS-MPC strategy

In this part, the conventional OSS-MPC strategy has been introduced. A lot of research was carried out in the literature. In order to achieve the desired performance, several important principles of OSS-MPC consists in discretized system model, prediction for control variables, the cost function evaluating the system outputs and minimizing algorithm deriving the optimal solutions.
2.1 Model of Vienna rectifiers

The circuit diagram of three-phase Vienna rectifiers is shown in Fig. 1. The rectifier is composed of a main diode bridge and three bidirectional switches connecting the input phases to the neutral point.

The continuous-time grid current dynamic of Vienna rectifier can be given as

\[ e_i = L \frac{di_i}{dt} + Ri_i + v_{IN} + v_{NO} \quad \forall i \in \{a, b, c\} \]  

where \( e_i \) and \( i_i \) are grid voltages and input currents, respectively, \( L \) and \( R \) are the input filtering inductance and resistance, respectively, \( v_{IN} \) is the switch voltage and \( v_{NO} \) is the zero-sequence voltage.

If the bidirectional switch \( S_i \) is on, \( v_{IN} \) is clamped to zero and the corresponding phase current flows to the neutral point through bidirectional switch. If the bidirectional switch is off, \( v_{IN} \) is determined by the direction of input current.

By analyzing the flow path of input currents, the currents through the positive and negative dc-bus can be obtained as

\[
\begin{align*}
  i_p &= S_{ap}i_a + S_{bp}i_b + S_{cp}i_c \\
  i_n &= S_{an}i_a + S_{bn}i_b + S_{cn}i_c 
\end{align*}
\]

where \( S_{ap} \) and \( S_{an} \) denote the conducting state of up and down diode of each phase, respectively.

The dynamic model with respect to dc side of Vienna rectifier can be derived as

\[
\begin{align*}
  C_1 \frac{dv_1}{dt} &= i_p - \frac{v_1 + v_2}{R_L} \\
  C_2 \frac{dv_2}{dt} &= i_n - \frac{v_1 + v_2}{R_L}
\end{align*}
\]

where \( C_1 \) and \( C_2 \) are the dc side capacitances, and \( R_L \) is the load resistance.

2.2 Input currents prediction

Based on the analysis in Section 2.1, there are three switching states in each phase as for the voltages between input phases and neutral point of dc-bus. Considering the different combination of three-phase switching states, 25 feasible switching states can be derived for Vienna rectifier. It is different from three-level neutral point clamped rectifier. Assuming that the utility grid is balanced and voltages of each dc side capacitor are equal, the voltages between input phases and neutral point of the grid can be obtained for each switching states.
According to polarity of three-phase input currents, six sectors can be obtained. Due to the conducting characteristic of Vienna rectifier, the vector is feasible only when the polarity of its switching voltages is in accordance with the polarity of the corresponding phase currents. The operating ranges are constrained for Vienna rectifier. Based on the polarity of the phase current, the feasible vectors make up the hexagon in accordance with the determined sector.

Transform the dynamic model of Vienna rectifiers based on \(dq\) synchronous rotating frame, Eq. (1) can be rewritten as

\[
\begin{align*}
\frac{di_d}{dt} &= \frac{1}{L} (e_d - Ri_d - v_d + \omega Li_q) \\
\frac{di_q}{dt} &= \frac{1}{L} (e_q - Ri_q - v_q - \omega Li_d)
\end{align*}
\]

The variation of input currents can be derived once the information of grid voltages, currents and switching voltages is determined. Define the variation of input currents as

\[
\begin{align*}
fd &= \frac{di_d}{dt} \\
fq &= \frac{di_q}{dt}
\end{align*}
\]

where \(v_j\) is the switching voltages with \(i \in \{1, 2, 3\}\).

Define the application times of vectors \(v_1, v_2\) and \(v_3\) as \(t_1, t_2\) and \(t_3\), respectively. With the knowledge of current variations and their application times, the prediction of grid current at the end of the sampling interval can be performed as

\[
\begin{align*}
\hat{i}_{d,k+1} &= i_{d,k} + f_{d1}t_1 + f_{d2}t_2 + f_{d3}t_3 \\
\hat{i}_{q,k+1} &= i_{q,k} + f_{q1}t_1 + f_{q2}t_2 + f_{q3}t_3
\end{align*}
\]

### 2.3 Cost function minimization

The control objective of Vienna rectifier is to guarantee the input currents follow the reference accurately. To this end, it is essential to denote the error between the currents prediction and the reference. Thus, the quadratic cost function is introduced as

\[
J = (i_{d,k+1} - \hat{i}_{d,k+1})^2 + (i_{q,k+1} - \hat{i}_{q,k+1})^2
\]

Substitute Eq. (6) into Eq. (7), \(J\) is transformed into a quadratic function depending on \(t_1, t_2\) and \(t_3\) as

\[
J = (e_{d,k} - f_{d1}t_1 - f_{d2}t_2 - f_{d3}t_3)^2 + (e_{q,k} - f_{q1}t_1 - f_{q2}t_2 - f_{q3}t_3)^2
\]

where \(e_{d,k} = i_{d,k+1} - i_{d,k}\) and \(e_{q,k} = i_{q,k+1} - i_{q,k}\).

In order to make the input currents follow the reference as exactly as possible, an optimal time set \(\{t_1, t_2, t_3\}\) is determined to minimize cost function \(J\).

The solution is found by solving the system as

\[
\begin{align*}
\frac{\partial J}{\partial t_i} &= 0 \\
t_1 + t_2 + t_3 &= t_s
\end{align*}
\]

where \(t_s\) is the sampling interval.
The optimal time set \{t_1, t_2, t_3\} minimizing the cost function \(J\) corresponding to particular switching sequence is given as

\[
\begin{align*}
  t_1 &= \frac{e_{d,k}f_q - e_{q,k}f_p - e_{d,k}f_{q3} + e_{q,k}f_p + f_{p2}f_{q3}t_s - f_{p3}f_{q2}t_s}{f_{p1}f_q - f_{p2}f_{q1} - f_{p1}f_{q3} + f_{p3}f_{q1} + f_{p2}f_{q3} - f_{p3}f_{q2}} \\
  t_2 &= \frac{-e_{d,k}f_q + e_{q,k}f_p + e_{d,k}f_{q3} - e_{q,k}f_p - f_{p1}f_{q3}t_s + f_{p3}f_{q1}t_s}{f_{p1}f_q - f_{p2}f_{q1} - f_{p1}f_{q3} + f_{p3}f_{q1} + f_{p2}f_{q3} - f_{p3}f_{q2}} \\
  t_3 &= t_s - t_1 - t_2
\end{align*}
\]  

(10)

For each vector sequence, the optimal time set can be calculated through Eq. (10) and with the specific application times, the value of cost function is derived. Among all sequences, the one minimizing the cost function is selected as optimal sequence and the corresponding time set is applied for the system.

### 3 Proposed OSS-MPC strategy

An improved OSS-MPC strategy has been proposed. Aiming at obtaining the constrained solutions considering all the constraints for application times, modification of the conventional strategy has been presented. Besides, issues of inaccurate prediction have been analyzed and an optimized prediction process has been proposed. Finally, the control target of neutral point voltage balance has been introduced into the control strategy.

#### 3.1 Constrained solutions for minimal cost function

The research conclusion introduced in Section 2.3 is based on the assumption that the total application times are equal to the switching period. It is regarded as the constrained condition that must be observed.

Undesirably, there are situations that the optimal application times derived by Eq. (10) are negative and outside the feasible range during one sampling period.

Considering the practical operation, the application times must satisfy the following inequalities as \(0 \leq t_i \leq t_s, i \in \{1, 2, 3\}\).

The equality constraints for Vienna rectifiers can be summarized as Table I.

In order to obtain the constrained solutions, the boundary conditions have to be analyzed. The constrained solutions exist if one or more component of application times is equal to 0. Assume that the application times satisfy \(t_2 = 0\) and constrained condition can be denoted as

\[
\begin{align*}
  t_1 + t_2 &= t_s \\
  t_3 &= 0
\end{align*}
\]  

(11)

Substitute Eq. (11) into the cost function Eq. (8) and the cost function can be written as

| No. | Constraints | No. | Constraints | No. | Constraints |
|-----|-------------|-----|-------------|-----|-------------|
| 1   | \(t_1 + t_2 + t_3 = t_s\) | 5   | \(t_1 = t_s\) | 9   | \(t_3 = 0\) |
| 2   | \(t_1 + t_2 = t_s\) | 6   | \(t_2 = t_s\) | 10  | \(t_3 = 0\) |
| 3   | \(t_1 + t_3 = t_s\) | 7   | \(t_1 = t_s\) |     |             |
| 4   | \(t_2 + t_3 = t_s\) | 8   | \(t_1 = 0\)  |     |             |
Define
\[
t_n = \frac{(f_{pn} - f_{p(n+1)})(e_{p,k} - f_{p(n+1)}) + (f_{qn} - f_{q(n+1)})(e_{q,k} - f_{q(n+1)})}{(f_{pn} - f_{p(n+1)})^2 + (f_{qn} - f_{q(n+1)})^2}
\]
(13)

Make the derivative with respect to \( t_1 \) equal to 0. The constrained solution can be derived from Table II as the result corresponding to Case B. Similarly, the optimal solutions corresponding to \( \mathcal{F} \) and \( \mathcal{G} \) can be obtained.

Taking each constrained condition into consideration, the optimal application times can be summarized as Table II. There are seven cases to be considered.

The minimal value of the cost function can be obtained by applying the solutions listed in Table II. Compare the results of Case A to G and search the optimal application times \( \{t_1, t_2, t_3\} \) that have the minimal cost function.

### 3.2 Prediction strategy for neutral voltage unbalance

The prediction of \( i_{d,k+1} \) and \( i_{q,k+1} \) analyzed in Section 2.2 is based on the assumption that dc side voltages are balanced during the sampling interval, namely, \( v_1 = v_2 = v_{dc}/2 \). However, due to the introduction of neutral point current \( i_n \), the voltage error between two capacitors \( C_1 \) and \( C_2 \) is generated. It is inevitable to observe the voltage error and much effort is supposed to taken to decrease it. As a result, the variation of input currents during neutral point imbalance is different from that of balance condition. The nonzero error will make an impact on the prediction of input currents and the results made to follow the reference are not accurate compared with the actual input currents.

The current trajectories of \( i_d \) and \( i_q \) are shown in Fig. 2. The blue solid line and red solid line is the predicted currents when \( v_1 = v_2 \) and \( v_1 \neq v_2 \), respectively. \( e_{id} \) and \( e_{iq} \) are the prediction error caused by neutral point voltage imbalance if the

![Fig. 2. Current trajectories of \( i_d \) and \( i_q \)](image_url)
inaccurate predicted currents are forced to follow the reference. Modification of the prediction strategy is essential to improve the performance for Vienna rectifier.

Denoting $v_{iN}$ using $v_1$ and $v_2$ instead of $v_{dc}$ and considering system model Eq. (1), the rectifier voltages $v'_a$ and $v'_b$ can be given in Table III.

It is noticeable that the results are identical to the conventional ones when $v_1 = v_2 = v_{dc}/2$. In addition, there are two different vectors representing $V_{01}$, namely, the switching states describing small vectors provide distinct currents variations. The modified rectifier voltages can be illustrated in the $\alpha$-$\beta$ plain as Fig. 3.

Introduce the error of dc side two capacitors as

$$\Delta v = v_1 - v_2$$

(14)

The modified currents variation $F'_j = [f'_{dj} \ f'_{qj}]^T$ can be expressed as

$$F'_j = F_j + \frac{A}{L} E_j \Delta v$$

(15)

where $A$ is coordinate transformation matrix transforming $v_{dq}$ into $v_{dq}$, $L$ is input filtering inductance, $E_j$ is the error matrix expressed in Table IV and $\Delta v = v_1 - v_2$ is the error of dc side two capacitors.

From Table III, it can be observed that the switching states representing small vectors have different current variations. It is necessary to assign the application times to them separately and the time set can be written as \{ $t_1$, $t_2$, $t_{01}$, $t_{02}$ \}. $t_{01}$ and $t_{02}$ represent the application times of switching states describing small vectors.

The predicted input currents can be rewritten as

$$\begin{align*}
\hat{i}_{d,k+1} &= \hat{i}_{d,k} + f'_{d1} t_1 + f'_{d2} t_2 + f'_{d01} t_{01} + f'_{d02} t_{02} \\
\hat{i}_{q,k+1} &= \hat{i}_{q,k} + f'_{q1} t_1 + f'_{q2} t_2 + f'_{q01} t_{01} + f'_{q02} t_{02}
\end{align*}$$

(16)

Apply the modified predicted input currents to the implementation system of OSS-MPC strategy. The optimal vector sequence and application times can be obtained

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**Table III.** Rectifier voltages $v'_a$ and $v'_b$

| Vector | $v'_a$ | $v'_b$ | Vector | $v'_a$ | $v'_b$ |
|--------|--------|--------|--------|--------|--------|
| $V_1$  | $2(v_1 + v_2)/3$ | 0 | $V_{06}$ | $v_2/3$ | $-\sqrt{3}v_2/3$ |
| $V_{12}$ | $(2v_1 + v_2)/3$ | $\sqrt{3}v_2/3$ | $V_{01}$ | $(2v_1 + v_2)/3$ | $-\sqrt{3}v_2/3$ |
| $V_0$  | $v_2/3$ | $\sqrt{3}v_2/3$ | $V_{01}$ | $2v_1/3$ | 0 |
| $V_0$  | 0 | 0 | $V_{02}$ | $2v_2/3$ | 0 |

---

![Fig. 3. Modified voltage vectors for sector I](image-url)
by minimizing the cost function describing the error between prediction and reference.

3.3 Neutral point voltage balance control

Two capacitors composing the dc side of Vienna rectifier are analyzed to study the operating principle of neutral point voltage unbalance. The charge-discharge characteristics and equivalent impedances of capacitors influencing the neutral point voltage fluctuation can be determined by the manufacturing processes and stray parameters. In addition, the neutral point current containing ac fluctuation and dc offset components can lead to neutral point voltage unbalance.

Based on the circuit diagram of Vienna rectifiers, the mathematical model of two dc-link capacitors can be given as

\[
\begin{align*}
    i_{C_1} &= C_1 \frac{dv_1}{dt} \\
    i_{C_2} &= C_2 \frac{dv_2}{dt}
\end{align*}
\]  

(17)

Assume that \( C_1 = C_2 = C \) and integrate Eq. (17), the voltages of capacitors can be derived as

\[
\begin{align*}
    v_{1,t_2} &= \frac{1}{C} \int_{t_1}^{t_2} i_{C_1} dt + v_{1,t_1} \\
    v_{2,t_2} &= \frac{1}{C} \int_{t_1}^{t_2} i_{C_2} dt + v_{2,t_1}
\end{align*}
\]  

(18)

Apply \( t_1 = t_{k+1} \) and \( t_2 = t_k \) to Eq. (18). The variation of capacitor voltages \( \Delta v \) is expressed as

\[
\Delta v_{k+1} = \frac{1}{C} \int_{k}^{k+1} (i_{C_1} - i_{C_2}) dt + \Delta v_k
\]  

(19)

The current difference of two capacitors related to the input currents and switching states can be expressed as

\[
i_{C_1} - i_{C_2} = -(i_aS_a + i_bS_b + i_cS_c)
\]  

(20)

Assume the currents flowing through the capacitors are constant during the sampling interval and the variation of capacitor voltages can be derived as
\[ \Delta v_{k+1} = -\frac{1}{C} (i_a S_a + i_b S_b + i_c S_c) t_s + \Delta v_k \]  
where \( t_s \) is the sampling period.

Define a function that describes the variation of neutral point voltage difference as

\[ f_{vi} = \frac{d\Delta v}{dt} \bigg|_{S_{dc}=S_{dc}} = i_a S_a + i_b S_b + i_c S_c \]  

The prediction of neutral point voltage difference at the end of the sampling period can be computed as

\[ \Delta \hat{v}_{k+1} = \Delta v_k + f_{n1} t_1 + f_{n2} t_2 + f_{n01} t_{01} + f_{n02} t_{02} \]  

To achieve neutral point voltage balance, the voltage deviation is set equal to zero. Introducing the weighting factor representing the adjustment between input currents and neutral point voltage, the cost function containing the quadratic difference between the prediction and reference for neutral point voltage can be given as

\[ J = (i^*_{d,k+1} - \hat{i}_{d,k+1})^2 + (i^*_{q,k+1} - \hat{i}_{q,k+1})^2 + \lambda (\Delta v^*_k - \Delta \hat{v}_{k+1})^2 \]  

Transform Eq. (24) into

\[ J = (e_{d,k} - f_{d1} t_1 - f_{d2} t_2 - f_{d01} t_{01} - f_{d02} t_{02})^2 + (e_{q,k} - f_{q1} t_1 - f_{q2} t_2 - f_{q01} t_{01} - f_{q02} t_{02})^2 + \lambda (e_{u,k} - f_{n1} t_1 - f_{n2} t_2 - f_{n01} t_{01} - f_{n02} t_{02})^2 \]  

where \( e_{u,k} = \Delta v^*_k - \Delta \hat{v}_{k+1} \).

Make the partial derivative of \( J \) with respect to application times equal to zero, the optimal solution can be obtained as

\[
\begin{align*}
  t_1 &= \frac{m_{11} e_{d,k} + m_{12} e_{q,k} + m_{13} e_{u,k} + m_{14} t_s}{m_{11} f_{p1} + m_{12} f_{q1} + m_{13} f_{n1} + m_{14}} \\
  t_2 &= \frac{m_{21} e_{d,k} + m_{22} e_{q,k} + m_{23} e_{u,k} + m_{24} t_s}{m_{21} f_{p2} + m_{22} f_{q2} + m_{23} f_{n2} + m_{24}} \\
  t_{01} &= \frac{m_{31} e_{d,k} + m_{32} e_{q,k} + m_{33} e_{u,k} + m_{34} t_s}{m_{31} f_{p01} + m_{32} f_{q01} + m_{33} f_{n01} + m_{34}} \\
  t_{02} &= \frac{m_{41} e_{d,k} + m_{42} e_{q,k} + m_{43} e_{u,k} + m_{44} t_s}{m_{41} f_{p02} + m_{42} f_{q02} + m_{43} f_{n02} + m_{44}} 
\end{align*}
\]

where polynomial \( m_{ij} \) is expressed in Table V.

4 Experimental results

In order to validate the effectiveness of the theoretical derivation, experiments have been carried out based on the proposed strategy. The three-phase Vienna rectifier is shown in Fig. 1. The system parameters are listed in Table VI. The Vienna rectifier is controlled to operate in unity input power factor and the reactive power reference is set to zero. The control strategy has been coded according to the control process introduced in this paper based on the digital control platform consisting of TMS320F28335 DSP. In addition, the conventional model predictive control strategy is implemented to compare with the proposed strategy.
can be improved by using the proposed OSS-MPC. Overshoot can be achieved. As a result, the dynamic performance of load change strategy has shown its superiority in dynamic response rapidity and less current overshoot. From the ac current waveforms obtained in the consequence, the proposed OSS-MPC is depicted. The load change from no load to full load has been carried out. The system response to load change from no load to full load for (a) conventional OSS-MPC and proposed OSS-MPC is shown in Fig. 4. As a result, the dynamic performance of load change can be improved by using the proposed OSS-MPC.

**Table V.** Expression of polynomial $m_{ij}$

| $m_{ij}$ | Expression |
|---------|------------|
| $m_{11}$ | $f_{a0}(f_{p0} - f_{q0}) + f_{a0}(f_{p0} - f_{q0}) + f_{a0}(f_{p0} - f_{q0})$ |
| $m_{12}$ | $f_{a0}(f_{p0} - f_{q0}) + f_{a0}(f_{p0} - f_{q0}) + f_{a0}(f_{p0} - f_{q0})$ |
| $m_{13}$ | $f_{a0}(f_{p0} - f_{q0}) + f_{a0}(f_{p0} - f_{q0}) + f_{a0}(f_{p0} - f_{q0})$ |
| $m_{14}$ | $f_{a0}(f_{p0} - f_{q0}) + f_{a0}(f_{p0} - f_{q0}) + f_{a0}(f_{p0} - f_{q0})$ |
| $m_{21}$ | $f_{a0}(f_{p0} - f_{q0}) + f_{a0}(f_{p0} - f_{q0}) + f_{a0}(f_{p0} - f_{q0})$ |
| $m_{22}$ | $f_{a0}(f_{p0} - f_{q0}) + f_{a0}(f_{p0} - f_{q0}) + f_{a0}(f_{p0} - f_{q0})$ |
| $m_{23}$ | $f_{a0}(f_{p0} - f_{q0}) + f_{a0}(f_{p0} - f_{q0}) + f_{a0}(f_{p0} - f_{q0})$ |
| $m_{24}$ | $f_{a0}(f_{p0} - f_{q0}) + f_{a0}(f_{p0} - f_{q0}) + f_{a0}(f_{p0} - f_{q0})$ |

**Table VI.** System parameters

| Symbol | Description | Value |
|--------|-------------|-------|
| $e$    | Grid phase voltage | 110 V |
| $f$    | Grid frequency | 50 Hz |
| $L$    | Input filtering inductance | 3 mH |
| $C$    | Dc-link capacitance | 2200 uF |
| $v_{dc}$ | Dc-link voltage | 300 V |
| $P$    | Rated output power | 2 kW |
| $f_s$  | Switching frequency | 10 kHz |

In Fig. 4, the dynamic performance of conventional OSS-MPC and proposed OSS-MPC is depicted. The load change from no load to full load has been carried out. From the ac current waveforms obtained in the consequences, the proposed strategy has shown its superiority in dynamic response rapidity and less current overshoot can be achieved. As a result, the dynamic performance of load change can be improved by using the proposed OSS-MPC.

**Fig. 4.** System response to load change from no load to full load for (a) conventional OSS-MPC (b) proposed OSS-MPC

Fig. 5 presents the waveforms of grid voltage and ac current for phase A. The THD of ac current for the conventional method is 7.1%. However, the ac current
has been improved by the proposed strategy deriving the THD of 4.1%. It is noticeable that the proposed OSS-MPC can achieve desired sinusoidal phase currents and unity power factor. Fig. 6 shows the experimental waveforms for dc-link voltages during steady state. Compared the conventional OSS-MPC, dc-link voltage ripple consisting of smaller amplitude can be achieved by the proposed OSS-MPC. As a result, the proposed OSS-MPC can reduce the dc-link voltage ripple and improve the steady state performance.

5 Conclusion

Model predictive control has attracted more and more attention for its good performance. However, during the prediction and optimization process, several issues are essentially needed to be addressed. In this paper, an improved model predictive control strategy has been presented following optimal switching sequence principle. Based on the analysis of the predicted dynamic model, accurate currents prediction and minimization have been carried out to improve the performance by using the proposed strategy. The experimental results have been carried out. As a result, the proposed strategy has shown the capability in practical applications for its effectiveness and prospective research values.