Study of Interface of Ohmic Contacts to AlGaN/GaN Heterostructure

Joanna PRAZMOWSKA, Wojciech MACHERZYNNSKI, Regina PASZKIEWCZ

Department of Microelectronics and Nanotechnology, Faculty of Microsystem Electronics and Photonics, Wroclaw University of Technology, Janiszewskiego 11/17, 50-370 Wroclaw, Poland

joanna.prazmowska@pwr.edu.pl, wojciech.macherzynski@pwr.edu.pl, regina.paszkiewicz@pwr.edu.pl

DOI: 10.15598/aeee.v14i2.1584

Abstract. The paper embraces studies of the interface of ohmic contacts and AlIIIIBV-N heterostructure. The TiAl based metallization stack was investigated. The Ti/Al/Ni/Au contact to AlGaN/GaN heterostructures fabricated by metal-organic vapour phase epitaxy was examined using three methods i.e. etching of annealed contact metallization, fractures (prepared at room temperature and after a bath in liquid nitrogen) and microsections imaging. The main focus was on the estimation of reaction range on the metal-semiconductor interface of samples. In the first method, the surface of AlGaN/GaN heterostructure after etching of metallization was studied by an optical microscope, scanning electron microscope and atomic force microscope. The changes of surface morphology of heterostructure directly reflect solid state reactions range between metallization and semiconductor. The range of reactions was also observed using the small-angle microsections method while the fractures analysis did not bring valuable information.

Keywords

AlIIIIBV-N heterostructures, metal-semiconductor interface, Ti/Al/Ni/Au metallization.

1. Introduction

AlGaN/GaN heterostructure based High Electron Mobility Transistors (HEMTs) are good candidates for high-power and high-temperature application. This area of applications enforces thermal stability of applied materials, thus also ohmic/Schottky contacts have to be of good quality, appropriate morphology, low-resistance and thermal stability [1], [2]. Parameters of ohmic contacts depend on various factors as e.g.: used metal stack, thicknesses of metal layers, heterostructure properties, and thermal annealing process parameters [3]. The standard scheme of metallization consists of Ti/Al/metal/Au, where metal could be one of Ni, Pd, Pt, Mo [4]. The metallization is processed in thermal annealing system after deposition. Depending on applied temperature of annealing, improvement or deterioration of electrical parameters of metal-semiconductor system occurs.

While the mechanism of contact formation to GaN is already understood, the ohmic contact formation to AlGaN/GaN needs further studies [5]. The most common method of investigation of metal-semiconductor (m-s) interface phenomena is TEM (transmission electron microscope) analysis [4], [5], [6] and [7]. In the paper, the m-s interface of Ti/Al/Ni/Au and AlGaN/GaN was studied using three methods. First technique included deposition of metallization on the surface of AlGaN/GaN, thermal annealing, etching of annealed ohmic contact and further observation of the etched surface of semiconductor using microscope methods as Scanning Electron Microscope (SEM) and Atomic Force Microscope (AFM). That gave an information on the m-s interface and permitted for estimation of the reaction range. Second one relied on fractures preparation and observation of the interface by application of SEM (Scanning Electron Microscope). The third method required polishing of the metallization near the edge of the sample. Obtained small-angle microsection permitted for observation of semiconductor surface.

2. Technology

The investigated samples contained metallization stacks deposited on AlGaN/GaN heterostructures fabricated using metalorganic vapour phase epitaxy
method on Al₂O₃ substrates. Mesa structures were formed in chloride plasma of Reactive Ion Etching (RIE). The etching process was carried out through the oxide mask (SiO₂) deposited in Plasma-Enhanced Chemical Vapour Deposition (PECVD) system. The pattern was achieved in standard lithography process. The mask layer thickness was of about 300 nm.

Samples with Ti/Al/Ni/Au (20/100/40/150 nm) layers deposited in UHV system by electron beam (Ti, Ni) and resistance heater (Al, Au) evaporation were investigated. The test structures were fabricated using photolithography process in lift-off technique by application of LOR and SPR 700 bi-layer. The samples were coated by the layers using spin-coating method. Samples with Ti/Al/Ni/Au multilayer metallization were annealed in Rapid Thermal Annealing (RTA) system at a temperature of 820°C.

To study the influence of annealing process on the properties of heterostructure the metallization layers after annealing were selectively etched layer by layer. The etching process was carried out in:

- iodine-potassium iodide (Au),
- H₃PO₄:H₂O (1:3) at 70°C (Ni),
- H₃PO₄:HNO₃:CH₃COOH:H₂O (85:5:5:5) at 45°C (Al),
- hydrogen peroxide at 65°C (Ti).

Etching stages duration was 10 minutes each. After etching of metal layers optical microscope and SEM images of surface morphology of chosen area were recorded. This permitted to estimate the range of metal-semiconductor solid state reactions, follow the morphology changes and its correlation with m-s interface topography.

The study of application of fractures technique for the solid state reaction range on the interface of annealed metallization and AlGaN/GaN was carried out. The fractures technique was adopted from AlIBV technology [8]. The mechanical incision of samples for this investigation was made using diamond blade. Samples were incised from the Al₂O₃ side for the depth of about 180 µm. Then samples were fractured at room temperature or immediately after bath in liquid nitrogen. Also samples fractured during the technological process because of the large stress were examined. The last group were samples grinded and polished under small-angle near the edge of the sample. This method permitted for observation of the reactions range occurring on the interface in only little invasive way. In contrary to two above-mentioned methods, the sample after microsection preparation and observation can be used for further processing.

3. Results

The rapid thermal annealing process of ohmic contacts to AlGaN/GaN heterostructure based devices affects parameters of the contact. The selection of appropriate temperature value of annealing temperature was made based on presented and discussed earlier paper [9]. Application of annealing at temperature above 835°C led to the degradation of AlGaN/GaN heterostructure surface beneath the contact. Temperature equal to 805°C started reactions at the level of 2DEG position i.e. of about 25 nm from the surface. Therefore, the intermediate value of 820°C was chosen for annealing of investigated samples. During thermal annealing of the Ni-based metallization the agglomerates appear on the surface (black spots in Fig. 1(a) and Fig. 3(a)). They are a consequence of migration and coalescence of melted Ni. The mechanism of agglomeration formation was already studied and described [10]. Figure 1 presents optical images of metallization surface Fig. 1(a) before etching, and remained metallizations layers after etching of Fig. 1(b) Au, Fig. 1(c) Ni, Fig. 1(d) Al and Fig. 1(e) Ti.

Each step of etching changed the surface of ohmic contact. The remained metal films (Fig. 1(b) Fig. 1(c) Fig. 1(d) and Fig. 1(e)) had slightly different topography. After first etching mostly the largest agglomerates of the contact (remarkable as black spots in Fig. 1(a)) were still observed. That could indicate lack of gold in the agglomerates volume, which corresponded to
It was observed that the remained metallization (Fig. 1(b) and Fig. 1(c)) topographies did not reflect the m-s interface topography (Fig. 1(e)). Nevertheless, there were significant differences in topographies of surfaces beyond the ohmic contact on the mesa (Fig. 2 A) and outside of it (Fig. 2 A'). That may be explained by various topographies before metallization deposition (Fig. 2 B and C) and various materials (i.e. AlGaN and GaN).

Figure 3 presents SEM images of metallizations layers, Fig. 3(a) before etching and remained after etching of Fig. 3(b) Au, Fig. 3(c) Ni, Fig. 3(d) Al and Fig. 3(e) Ti.

SEM images confirmed gradual removal of the metallization layers in subsequent stages except of etching the Al layer. It could be explained by occurring of a metal compound that could not be etched in used solution in contrary to $\text{H}_2\text{O}_2$.

Step of Au etching caused a delamination near the agglomerates (Fig. 3(b)). The surface beneath the ohmic contact after etching of all layers of metallization changed significantly. Topography of this area is a reflection of the m-s interface. The topographies under agglomerates and outside of this area were nearly identical. The difference between surfaces of a sample before and after Al etching (Fig. 3(c) and Fig. 3(d)) was nearly unnoticeable, which corresponded to optical microscope images (Fig. 1(c) and Fig. 1(d)). It could be caused by etching of Al layer (or its compound) by solution of $\text{H}_3\text{PO}_4\cdot\text{H}_2\text{O}$, used for Ni layer removal but it requires further analysis e.g. by EDX (Energy Dispersive X-ray Spectroscopy).

The consumption of the AlGaN layer was significant (Fig. 3(f)) – represented as dark spots on the picture. Figure 4(a) presented SEM image of two areas of mesa – bare surface (A) and surface with deposited and etched metallization (B). The AFM images were taken

![Fig. 2: SEM image embracing three types of areas: metallization on etched GaN (A) and mesa (A'), mesa surface (B), etched GaN (C).](image)

![Fig. 3: SEM images of metallization surface before etching (a), and remained metallizations layers after etching of Au, Ni, Al and Ti.](image)

![Fig. 4: SEM image of semiconductor layer; mesa (A) and mesa surface after etching of ohmic contact (B), profile of the surface after metallization etching (extracted from AFM image).](image)
Fig. 5: Example SEM images of fractures of annealed Ti/Al/Ni/Au metallization and AlGaN/GaN heterostructure made at room temperature (a), (b) immediately after bath in LN$_2$ (c), (d) and fractured during technological process (e), (f).

from the region embracing A and B. Figure 4(b) consisted of profile of the surface after metallization etching (extracted from AFM image). It confirmed phenomenon of AlGaN surface consumption. The depth of solid state reaction on the m-s interface reached even 30 nm within studied area. It seemed to penetrate the 2DEG region, but in this stage of research we are not able to estimate if heterostructure beneath metallization has to be preserved for obtaining the optimal parameters of m-s contact.

The applied fracture technique did not permit for observation of the penetration of ohmic contact into AlGaN/GaN heterostructure (Fig. 5). Despite obtained sharp fractures, the m-s interface on SEM images did not bring any valuable information. Bath in liquid nitrogen improved sharpness of the fractures, but it was still insufficient to investigate the changes occurring in the range of depth equal to 30 nm. This technique permitted only for distinguishing of ohmic contact and thickening metallization layer Fig. 5(d), Fig. 5(e) and Fig. 5(f).

The samples with annealed ohmic contacts were also examined using small-angle microsections. Figure 6(a) presents the microsection embracing two areas – metallization (upper part of the SEM image) and AlGaN/GaN heterostructure (bottom of the SEM image). The spots on the surface of metallization could be agglomerates which are composed of materials in other proportions than metallization stack thus having other mechanical properties that led to more efficient polishing in those areas.

One of the disadvantages of this method is a tear off of the metallization in some areas of microsections. Nevertheless in the studies limited to the investigation of the AlGaN/GaN heterostructure surface morphology, the method does not require further optimization.

The study of the heterostructure surface beneath the ohmic contact (Fig. 6(c) and Fig. 6(d)) allows to observe the black spots among the polishing grooves that may indicate the interfacial reaction between the semiconductor and metallization. The size of the observed spots was of few tens of micron, similar as that for samples with etched ohmic metallization.

The spots were not observed on the microsection in the area outside the ohmic metallization Fig. 6(e).

4. Conclusion

The Ti/Al/Ni/Au metallizations to AlGaN/GaN heterostructures fabricated by metal-organic vapour phase epitaxy were studied. The range of reactions on the metal-semiconductor interface of samples was exam-
ined after etching of annealed ohmic contacts and using fractures.

At any stage of etching, topography of remained metallization did not reflect the topography of the m-s interface. Nevertheless, solid state reactions on the m-s interface were significant. The depth of reactions estimated on the bases of AFM images reached even 30 nm within studied area. SEM images of prepared fractures did not allow to estimate the depth of solid state reactions on the m-s interface. Application of LN$_2$ for fractures influenced slightly the sharpness of the fractures but did not enable analysis of solid state reactions on the m-s interface beneath annealed ohmic contact.

The applied method of small-angle microsections study permits for observation of range of reactions on the interface of Ti/Al/Ni/Au and AlGaN/GaN heterostructure. The reactions consequences are observable as black spots on the surface of semiconductor in the area beneath the ohmic contact.

Acknowledgment

This work was co-financed by the European Union within European Regional Development Fund, through grant Innovative Economy (POIG.01.01.02-00-008/08-05), National Science Center Poland under the grant no. DEC-2012/07/D/ST7/02583, by National Centre for Research and Development through Applied Research Program grant no. 178782, program LIDER no. 027/533/L-5/13/NCBR/2014, by Wroclaw University of Technology statutory grants and Slovak-Polish International Cooperation Program no. SK-PL-2015-0028.

References

[1] WANG, C., S.-J. CHO and N.-Y. KIM. Optimization of Ohmic Contact Metallization Process for AlGaN/GaN High Electron Mobility Transistor. *Transactions on Electrical and Electronic Materials*. 2013, vol. 14, iss. 1, pp. 32–35. ISSN 1229-7607. DOI: 10.4313/TEEM.2013.14.1.32

[2] MOKRZYCKI, K. M. Optimization of photolithography process with application of modern resists of Shipley Company. Wrocław. 2012. Master Thesis. Wrocław University of Technology. Supervisor: Bogdan Jankowski.

[3] MACHERYNSKI, W. and B. PASZKIEWICZ. Study of interface reactions between Ti/Al/Ni/Au metallization and AlGaN/GaN heterostructures. *Central European Journal of Physics*. 2013, vol. 11, iss. 2, pp. 258–263. ISSN 1644-3608. DOI: 10.2478/s11534-012-0158-0

[4] MACHERYNSKI, W., K. INDYKIEWICZ and B. PASZKIEWICZ. Chemical analysis of Ti/Al/Ni/Au ohmic contacts to AlGaN/GaN heterostructures. *Optica Applicata*. 2013, vol. 43, iss. 1, pp. 67–72. ISSN 0078-5466. DOI: 10.5277/oa130109.

[5] VAN DAELEA, B., G. VAN TENDELOO, J. DERLUYN, P. SHRIVASTAVA, A. LORENZ, M. R. LEYS and M. GERMAIN. Mechanism of Ohmic contact formation on Si$_3$N$_4$ passivated AlGaN/GaN high-electron-mobility transistors. *Applied Physics Letters*. 2006, vol. 89, iss. 20, pp. 201908-1–201908-3. ISSN 0003-6951. DOI: 10.1103/1.2388889.

[6] TAKEI, Y., M. OKAMOTO, W. SAITO, K. TSUTSUI, K. KUKUSHIMA, H. WAKABAYASHI, Y. KATAOKA and H. IWAI. Ohmic Contact Properties Depending on AlGaN Layer Thickness for AlGaN/GaN High Electron Mobility Transistor Structures. *The Electrochemical Society Transactions*. 2014, vol. 61, iss. 4, pp. 265–270. ISSN 1938-5862. DOI: 10.1149/06104.0265ecst.

[7] WANG, C. and N.-Y. KIM. Electrical characterization and nanoscale surface morphology of optimized Ti/Al/Ta/Au ohmic contact for AlGaN/GaN HEMT. *Nanoscale Research Letter*. 2012, vol. 7, iss. 1, pp. 107-1–107-8. ISSN 1556-276X. DOI: 10.1186/1556-276X-7-107

[8] JUNG, S. M., C. T. LEE and M. W. SHIN. Investigation of V-Ti/Al/Ni/Au Ohmic contact to AlGaN/GaN heterostructures with a thin GaN cap layer. *Semiconductor Science and Technology*. 2015, vol. 30, no. 7, pp. 075012–075012. ISSN 1361-6641. DOI: 10.1088/0268-1242/30/7/075012

[9] CHIU, Y.-S., T.-M. LIN, H.-Q. NGUYEN, Y.-C. WENG, C.-L. NGUYEN, Y.-C. LIN, H.-W. YU, E. Y. CHANG and C.-T. LEE. Ti/Al/Ti/Ni/Au ohmic contacts on AlGaN/GaN high electron mobility transistors with improved surface morphology and low contact resistance. *Journal of Vacuum Science & Technology B*. 2014, vol. 32, iss. 1, pp. 011216–011216. ISSN 2166-2754. DOI: 10.1116/1.4862165

[10] MACHERYNSKI, W., A. STAFINIAK, A. SZYSZKA, J. GRYGLEWICZ, B. PASZKIEWICZ, R. PASZKIEWICZ and M. TLACZALA. Effect of annealing temperature on the morphology of ohmic contact Ti/Al/Ni/Au to n-AlGaN/GaN heterostructures. *Optica Applicata*. 2009, vol. 39, iss. 3, pp. 673–679. ISSN 0078-5466.
[11] SCHMID, A., C. SCHROETER, R. OTTO, M. SCHUSTER, V. KLEMM, D. RAFAJA and J. HEITMANN. Microstructure of V-based ohmic contacts to AlGaN/GaN heterostructures at a reduced annealing temperature. *Applied Physics Letters*. 2015, vol. 106, iss. 20, pp. 201908-1–201908-3. ISSN 0003-6951. DOI: 10.1063/1.4907735.

About Authors

Joanna PRAZMOWSKA received her M.Sc. degree in Electronics from Wroclaw University of Technology, Poland in 2005 and Ph.D. degree from Wroclaw University of Technology (WrUT) in 2011. Now she is assistant professor at WrUT. Her research interest embraces technology of semiconductor devices i.e. lithography process development of electronic, optoelectronic devices as well as gas sensors.

Wojciech MACHERZYSKII received his M.Sc. degree in Electronics from Wroclaw University of Technology, Poland in 2005 and Ph.D. degree from the Wroclaw University of Technology in 2011. Now he is assistant professor at WrUT. His research is focused on the technology of semiconductors devices in particular on development of the metal-semiconductor junction.

Regina PASZKIEWICZ received her M.Sc. degree in Electrical Engineering from St. Petersburg Electrotechnical University, St. Petersburg, Russia in 1982 and Ph.D. degree from the Wroclaw University of Technology in 1997. Now she is full professor at WrUT. Her research is focused on the technology of (Ga, Al, In)N semiconductors, microwave and optoelectronic devices technological processes development.