Reduced Order Lumped Parameter Thermal Network for Dual Three-Phase Permanent Magnet Machines

P. Giangrande, V. Madonna, S. Nuzzo, C. Spagnolo, C. Gerada, M. Galea

Abstract — In recent years, electrical machines are employed in an ever-increasing number of safety-critical applications, which require high power density, along with a demanding level of reliability and/or fault-tolerance capability. Multi-phase machines are generally considered a suitable option for satisfying these constraints. Among them, the dual three-phase configuration claims the additional benefit of being operated through conventional power electronics converters and control strategies. Despite the undeniable advantages and their wide diffusion, the thermal modelling of dual three-phase machines still represents an open research topic. Thus, this paper proposes the thermal analysis of a dual three-phase permanent magnet synchronous machine, highlighting the thermal coupling between the two winding sets. An experimentally fine-tuned lumped parameter thermal network is initially presented. Then, due to its relatively high number of nodes, a reduced order thermal network using only 3 nodes is developed. Finally, both thermal networks are examined and compared under several load conditions, in terms of accuracy and computational burden.

Index Terms—Dual three-phase, Fine-tuning, Multi-phase, LPTN, PMSM, Thermal management, Thermal modelling.

I. INTRODUCTION

Dual three-phase permanent magnet synchronous machines (PMSMs) are a viable design choice for safety-critical applications [1, 2]. Indeed, they can safely operate even in the event of severe failures, such as open- and short-circuit faults [3]. Compared with other topologies of multi-phase (i.e. number of phases higher than three) electrical machines (EMs), the dual three-phase winding arrangement allows to adopt standard three-phase power electronics converters (PECs) [4-6]. This is surely an advantage over e.g. five phase machines, where custom PECs and control strategies are required [7, 8].

When designing an EM, a complete and multi-physics approach should be considered by including electromagnetic, thermal, and mechanical aspects [9-11]. In fact, in modern applications, where high-performance needs to be reached in limited dimensional constraints, there is a drive towards boosted power density, i.e. achieving a compact design while increasing the power level [12]. Such tendency is leading to push the electromagnetic and thermal boundaries to the limit [13]. In this scenario, the thermal management plays a significant role [14, 15], as well as the thermal modelling [16, 17]. Indeed, over-temperatures, caused by either temporary overloads or winding faults, are to be avoided, since they might compromise the EM insulation lifetime [18, 19]. Therefore, thermal models are used for predicting the EMs’ thermal behaviour and for identifying the hot-spots [20].

In literature, several works deal with the electromagnetic design of dual three-phase PMSMs and their performance in healthy and faulty operations [2, 4, 5, 21]. Further, a detailed thermal study is presented, in [4]. Nevertheless, it is not common to find a ‘complete’ lumped parameter thermal network (LPTN) for dual three-phase PMSMs, which simultaneously models both winding sets. A tool featuring the aforementioned capability might result useful for investigating the mutual thermal influence between the two winding sets, mainly when they work with different electric loads (i.e. asymmetric load operations). In other words, the temperature rise, occurring in one winding set and produced by the Joule losses within the other winding, could be evaluated. Conversely, a more accurate temperature estimation could be achieved, when only one winding set is supplied, whilst the second one is open (e.g. the winding set is open in response to a PEC failure) [4]. In this case, the ‘complete’ LPTN is able to account also for the part of heat generated inside the loaded slot and transferred to the unloaded one.

Considering all the above, the paper presents a thermal analysis of a dual three-phase PMSM, together with its ‘complete’ LPTN, where the thermal behaviour of both winding sets is addressed. The LPTN is experimentally fine-tuned LPTN using a custom-built PMSM prototype. The aim of the fine-tuning procedure consists in identifying the LPTN critical parameters, such as the slot equivalent thermal conductivity, the natural air convection coefficient, etc. [22]. By exploiting the geometric symmetry featured by the analysed PMSM, the nodes’ number of the obtained LPTN is then minimized leading to a reduced order LPTN (i.e. 3-node), which still includes both winding sets. The elements of the new LPTN are easily determined and its performance is assessed against the measured temperature profiles, revealing a good fitting especially at steady-state. Finally, the computational effort of the two LPTNs (i.e. high- and low-order) is appraised and, as expected, a considerable simulation time reduction is observed when the reduced order LPTN is utilized.
II. PMSM DESCRIPTION

The dual three-phase PMSM employed during the thermal investigation features a 12 slots / 10 poles combination and is equipped with a concentrated single-layer winding layout. The geometry and winding arrangement of the PMSM are shown in Fig. 1, while the main design parameters are listed in TABLE I. Both winding sets (i.e. set ‘1’ and set ‘2’) are star-connected and the shift angle between phase ‘A1’ and phase ‘A2’ axes is equal to zero degrees. The chosen winding topology complies with the fault-tolerance requirements [23], because high per-phase self-inductance and low (approximately zero) mutual inductance among phases are accomplished [3]. Additionally, the phase-to-phase short-circuit risk is reasonably mitigated, being the coils non-overlapped. The PMSM is naturally air cooled and an aluminium housing with axial fins is mounted for enhancing the heat transfer to the external ambient. For the sake of completeness, the housing engineering drawing is provided in Fig. 2.

![Fig. 1. Geometry and winding layout of the dual three-phase, 12 slots / 10 poles PMSM.](image)

### TABLE I. DUAL THREE-PHASE PMSM PARAMETERS

| Parameter               | Symbol | Value |
|-------------------------|--------|-------|
| Rated Speed [rpm]       | \( \Omega_{pm} \) | 2500  |
| Rated Torque [Nm]       | \( \tau_{pm} \)   | 1     |
| Rated Current [A rms]   | \( I \)     | 2.2   |
| Phase Resistance [\( \Omega \)] | \( R \) | 3.24  |
| d- and q-axis Inductances [mH] | \( L_d \) and \( L_q \) | 13.7  |
| PM Flux [Wb]            | \( \Psi_{pm} \) | 0.112 |
| Stack Length [mm]       | \( L_s \)     | 50    |
| Stator Outer Diameter [mm] | \( D_s \) | 50    |

III. COMPLETE HIGH-ORDER LPTN

In general, LPTNs provide a precise temperature estimation with low computational time compared to computational fluid dynamics (CFDs) [11] or finite element (FE) methods [16]. Nevertheless, some LPTN parameters, also known as critical parameters [21], are not easy to be analytically calculated, due to the uncertainty of material properties, and they strongly influence the LPTN accuracy. Hence, the LPTN critical parameters are experimentally quantified for the PMSM at hand (i.e. LPTN fine-tuning procedure). The tests are performed by feeding both winding sets via as many independent DC power supplies. It is worth to point out that, by using DC sources, the thermal contribution of the PMSM iron losses is neglected, as well as, the impact of skin and proximity effects on the winding electrical resistance. This choice (i.e. DC tests) does not compromise their validity, because the relatively low PMSM fundamental frequency (i.e. \( \approx 208 \text{ Hz} \)), combined with the small wire cross-section adopted (i.e. AWG 26), makes reasonable the assumption of neglecting skin and proximity effects. Further, the FE simulation results prove that the main share to the PMSM losses is given by the Joule losses. Therefore, the iron losses only slight affect the hot-spot temperature, thus it is appropriate to exclude them from the thermal model.

![Fig. 2. Engineering drawing of the PMSM aluminium housing (all dimensions are in mm).](image)

The DC voltages are applied as depicted in Fig. 3, where two phases (‘A1’ and ‘B2’ in the figure), each belonging to different winding sets and hosted in adjacent slots, are simultaneously loaded. The described measurement layout permits to examine the mutual thermal influence between the two winding sets. The winding temperatures are measured by means of K-type thermocouples, which are uniformly distributed within the PMSM’s windings, with the purpose of detecting the hot-spot temperature. The recorded temperatures are elaborated through an acquisition board connected to a personal computer. The whole thermal test setup is shown in Fig. 4, where the PMSM under test, both DC power supplies and the data logger are highlighted.

Knowing the materials and the geometric dimensions of the PMSM, the high-order LPTN is built, as illustrated in Fig. 5. The procedure for computing the thermal resistances and capacitances can be found in [15] and thus, it is not reported in this paper. The LPTN is developed under the assumption that no-heat transfer occurs along the axial direction, consequently only radial and circumferential resistances are modelled. In addition, the rotor is considered as thermally insulated from the ambient (i.e. no-heat transfer across shaft and bearings). This choice is justified by the convenience of having a conservative LPTN. With the aim of evaluating the thermal effect, due to different combinations of Joule losses.
generated by the currents flowing throughout the two PMSM’s stars, two slots are implemented in the LPTN, each belonging to one winding set. However, the presence of both slots inevitably increases the LPTN nodes and its computational burden. Finally, between stator and housing, a tiny air interface (i.e. $I_{th}$ thickness) is incorporated in the LPTN. Hence, the stator core is thermally connected to the finned housing via an interference thermal resistance.

![Fig. 4. Complete experimental setup for the thermal tests.](image1)

![Fig. 5. PMSM high-order LPTN modelling two slots.](image2)

Based on the outcomes of the thermal tests (i.e. experimental temperature profiles), the values of the critical parameters are estimated by means of the Simulink® Design Optimization Toolbox, allowing for the LPTN fine-tuning. In detail, for several DC current combinations (i.e. $I_{star1}$ and $I_{star2}$ pairs), the measured hot-spot temperature profiles of both winding sets ($T_{star1}$ and $T_{star2}$) are imported and pre-processed by the toolbox, which determines the critical parameters using a sequential quadratic programming (SQP) algorithm [5]. The list of these parameters is given in TABLE II, alongside with their values. Once the high-order LPTN is fine-tuned, it is used for predicting the PMSM hot-spot temperatures of each winding set, for different combinations of currents flowing into the two three-phase windings.

![Fig. 6. Hot-spot temperature profiles for different load conditions: experimental (black continuous line) and LPTN-predicted (red dashed line).](image3)

In order to assess the effectiveness of the high-order thermal model, the LPTN-predicted temperature profiles are compared to those measured, for the following loading states: 1) one winding set loaded, while the other one is open (i.e. $I_{star1}=1.5$ A and $I_{star2}=0$ A), 2) both winding sets loaded, but at different current levels (i.e. $I_{star1}=1.5$ A and $I_{star2}=1$ A) and 3) the same current flows in the winding sets (i.e. $I_{star1}=I_{star2}=0.5$ A). The obtained results are shown in Fig. 6, which reveals an excellent match between measured and estimated temperatures, during steady-state and transient conditions. Analysing the temperature profiles corresponding to cases when $I_{star1}=1.5$ A (i.e. cases 1 and 2), it is evident the thermal coupling between the winding sets. In fact, if $I_{star2}=0$ A, the resulting $T_{star1}$ is lower than the value that would be predicted by a LPTN modelling just one slot, as the heat from the loaded slot is actually transferred to the external ambient and the unloaded slot (i.e. slot 2). On the other hand, if $I_{star2}=1$ A, the ensuing $T_{star1}$ is higher than the value estimated in the previous case ($I_{star2}=0$ A), due to a lower thermal gradient between the slots (i.e. smaller amount of heat produced in slot 1 and migrating towards slot 2).

Despite the good estimation performance of the high-order LPTN, its complex structure (i.e. elevate number of nodes) makes the thermal model inconvenient for real-time applications and/or fast temperature prediction. Besides the layout complexity, the significant number of critical parameters to be identified (i.e. 10) yields to a time-costly fine-tuning. These drawbacks led to the development of a low-order LPTN, which represents one of the main outcomes of this work. The low-order LPTN is discussed in the next section.

### IV. Low-Order LPTN

The tests campaign findings have confirmed that the PMSM hot-spot temperatures are located within the windings. Therefore, relying on the PMSM geometric symmetry, the LPTN presented in Fig. 5 is simplified, focusing on the slot with the purpose of deriving a reduced order LPTN. As depicted in Fig. 7, the new LPTN consists in only 3 thermal
resistances and 2 thermal capacitances, resulting in a total number of nodes equal to 3. The choice of a 3-node LPTN is supported by the interest of giving preference to the LPTN simplicity. In particular, two thermal resistances are employed for modelling the heat transfer from each slot to the external ambient, and due to the symmetric structure, they are characterized by the same numerical value (i.e. R_k). The third thermal resistance (i.e. R_j) connects the two slots and it accounts for the heat exchanged between them (i.e. thermal coupling). Finally, the two thermal capacitances (i.e. C_k) featuring identical values (based on the PMSM symmetry) are placed in every slot.

Fig. 7. PMSM low-order LPTN modelling two slots.

Having outlined the reduced LPTN layout characterized by only 3 nodes, the procedure for determining its components is presented. Considering the steady-state conditions, the thermal capacitances can be removed from the circuit of Fig. 7 and the thermal resistances R_k and R_j can be expressed according to (1) and (2), respectively.

\[
R_k = \frac{T_1 + T_2 - 2T_{\text{ambient}}}{P_{\text{joule(1)}} + P_{\text{joule(2)}}} \\
R_j = R_k \cdot \frac{T_1 - T_2}{2T_{\text{ambient}} - R_k(P_{\text{joule(1)}} + P_{\text{joule(2)}})}
\]

where \(T_{\text{ambient}}\) is the ambient temperature, \(T_1\) and \(T_2\) are the measured steady-state hot-spot temperatures of the corresponding slots, while \(P_{\text{joule(1)}}\) and \(P_{\text{joule(2)}}\) are the relative Joule losses. As a result of the experimental tests, all the above parameters are known quantities, hence, \(R_k\) and \(R_j\) are easily calculated. It is worthy to point out that (2) can be applied only when \(I_{\text{start}} \neq I_{\text{start}}\). Further, only one experimental test, under asymmetric electric load condition (i.e. \(I_{\text{start}} \neq I_{\text{start}}\)), is required for defining \(R_k\) and \(R_j\). Once these resistances are computed, the capacitance value \(C_k\) is identified following the same approach previously adopted for the critical parameters estimation (i.e. Simulink® Design Optimization Toolbox). Nevertheless, a much lower computational time is needed at this round, due to the restrained number of unknowns (i.e. only \(C_k\)). The reduced order LPTN parameters are listed in TABLE III, alongside with their numerical values. The prediction performance of the obtained low-order LPTN is evaluated in both symmetric (\(I_{\text{start}} = I_{\text{start}}\)) and asymmetric (\(I_{\text{start}} \neq I_{\text{start}}\)) electric load operations. In Fig. 8, both experimental and low-order LPTN-predicted temperature profiles are shown for 0.5 A and 1 A symmetric load conditions. For the sake of clarity, just the temperature profiles of slot 1 (i.e. T1) are reported in Fig. 8, since T1 and T2 profiles result overlapped. As expected, the reduced order LPTN is capable of estimating the steady-state temperature with a very good accuracy (i.e. error below 3%). On the other hand, the 3-node LPTN tends to overestimate the temperature during the transients, where the mismatch between measured and predicted temperatures increases. This discrepancy can be ascribed to the implementation of only two capacitances in the reduced order LPTN. Regarding the asymmetric electric load operations, the following current combinations are considered: 1) \(I_{\text{start}} = 1.5\) A and \(I_{\text{start}} = 1\) A plus 2) \(I_{\text{start}} = 1\) A and \(I_{\text{start}} = 0.5\) A. The comparison outcome is summarized in Fig. 9, where as in the previous case, an excellent precision is achieved at steady-state, whilst a higher estimation error is observed when the temperature rises.

![Fig. 8. Hot-spot temperatures in symmetric load conditions: experimental (black continuous) and low-order LPTN-predicted (red dashed) profiles.](image)

![Fig. 9. Hot-spot temperatures in asymmetric load conditions: experimental (black continuous) and low-order LPTN-predicted (red dashed) profiles.](image)
operations, the hot-spot LPTN-predicted temperature profiles are plotted alongside with the measured values. In Fig. 10, their comparison is shown considering only T1 (for the sake of figure clarity), while the temperature estimation errors, calculated against the experimental temperature, are reported in Fig. 11, for \( I_{\text{star}}=I_{\text{star}2}=1.4\text{ A} \) (top subplot) and \( I_{\text{star}}=I_{\text{star}2}=1\text{ A} \) (bottom subplot). For highlighting the transient mismatch, a logarithmic time-scale is used in Fig. 11.

![Fig. 10. Hot-spot temperatures in symmetric load conditions: measured (black continuous), high-order LPTN-predicted (red dashed) and low-order LPTN-predicted (blue dotted).](image)

![Fig. 11. Temperature prediction errors for \( I_{\text{star}}=I_{\text{star}2}=1.4\text{ A} \) (top subplot) and \( I_{\text{star}}=I_{\text{star}2}=1\text{ A} \) (bottom subplot).](image)

![Fig. 12. Hot-spot temperatures in asymmetric load conditions: measured (black continuous), high-order LPTN-predicted (red dashed) and low-order LPTN-predicted (blue dotted).](image)

This analysis confirms that during the temperature rise, the low-order LPTN reveals a greater estimation error with respect to the high-order one. In particular, the maximum mismatch is observed in the time-window from 50 s to 400 s. In transient conditions, as previously mentioned, the poor accuracy of the reduced order LPTN is mainly due to the small number of capacitances employed. Such drawback represents the price to pay for a fast and easy-to-tune LPTN.

For completeness, the LPTNs estimation capability is also tested in asymmetric electric load operations, namely \( I_{\text{star}}=1.5\text{ A} \) and \( I_{\text{star}2}=1\text{ A} \). As depicted in Fig. 12, the LPTN-predicted temperature profiles (T1 and T2) are compared between them and versus the experimental outcomes, whilst the corresponding estimation errors are given in Fig. 13, for both slot 1 (top subplot) and slot 2 (bottom subplot). Considerations similar to those carried out for Fig. 11 can be extended to the results of Fig. 13.

![Fig. 13. Slot 1 (top subplot) and slot 2 (bottom subplot) temperature prediction errors when \( I_{\text{star}}=1.5\text{ A} \) and \( I_{\text{star}2}=1\text{ A} \).](image)

Finally, the LPTNs computational effort is investigated by implementing the LPTNs in Simulink® Simscape® environment and recording their simulation times. For this study, a personal computer equipped with an Intel® Xeon® E5-1620 3.50 GHz processor and 32GB of RAM is utilized. In order to guarantee a fair comparison, identical simulation period (i.e. 20000 s) and solver are adopted. For performing a full-transient simulation, the high-order LPTN takes 28.12 s, whereas 3.44 s are required by the low-order LPTN. According to these findings, it can be concluded that the high-order LPTN is more suitable than the low-order one for a thermal analysis at the design stage of the dual three-phase PMSM, due to its higher accuracy. However, a motorette resembling the designed PMSM should be employed for the fine-tuning of the critical parameters. On the contrary, the reduced order LPTN is advisable for on-line temperature estimation seeing its low computational burden.

VI. CONCLUSIONS

In this paper, the detailed thermal analysis of a dual three-phase PMSM was carried out, by developing two LPTNs with different levels of complexity. Aiming at modelling the thermal coupling between the two winding sets, these LPTNs have the peculiarity of including two slots (each belonging to a distinct winding set), which allows to predict the hot-spot temperatures under symmetric and asymmetric load conditions of the considered platform.

First, a high-order LPTN was developed and its performance was evaluated via experimental results. The high-order LPTN provides very accurate results at the cost of a significant computational time and a complex fine-tuning procedure for determining the LPTN critical parameters. Due to these drawbacks, a 3-node LPTN was then built exploiting...
the inherent PMSM geometric symmetry. In favour of the computational speed, the simplest possible LPTN was preferred to medium complexity architectures. The procedure for calculating the reduced order LPTN’s components was described and its simplicity was highlighted. Only a single experimental test, in asymmetric load conditions, is needed for identifying the relevant thermal resistances. Although poor estimation accuracy during transient was registered due to the reduced number of thermal capacitances adopted, the low-order LPTN has shown outstanding potential for fast estimation of the PMSM’s thermal behaviour. Further investigations are in place for improving the low-order LPTN prediction performance throughout the temperature rise, while keeping its complexity moderate.

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