DESIGN OF LQR CONTROLLER FOR GAN BASED BUCK CONVERTER

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Abstract: This paper presents a design procedure of a switched-mode power converter, the well-known synchronous buck converter: the calculating method, and how to choose the parts of the converter are presented in detail, like the inductor, the capacitor and the semiconductors as well as the design of the state feedback. During the design process the efficiency and the high switching frequency are very important: the switching semiconductors are Gallium Nitride based. Then, a linear-quadratic regulator is designed and applied to the particular case of a buck converter.

Keywords: Switch mode power supply, Buck converter, State-space equation, Switch-mode power supplies, Linear quadratic regulator

1. Introduction

Nowadays the Switch-Mode Power Converters (SMPS) getting more and more attention, which are characterized by the high efficiency (and low losses), high power density, the wide input and output range, finally the small size and light weight [1], [2]. The operation mode of these converters is based on Pulse Width Modulation (PWM), resulting an easy way to control the output voltage. In addition, the SMPS can be step-up or step-down converters. Combining these positive advantages of the new semiconductor properties very high switching frequency and efficiency can be achievable. The high switching frequency means also lower ripple, however the sudden
changes in current \( \frac{di}{dt} \) or voltage \( \frac{du}{dt} \) generate higher order harmonics and the effect of parasitic inductances and capacities are not negligible. The next generation of the semiconductor devices, like Silicon Carbide (SiC) or Gallium Nitride (GaN) gives new opportunities the design of the SMPS and control circuits.

2. Operation and design of buck converter

Buck converter is a type of non-isolated (Direct Current) DC/DC converters, which steps down the input voltage [3]. The non-isolated buck converter contains two semiconductors (transistor and diode) and energy storage elements (inductors and capacitors). The function of these elements is dual: they store the energy and have filter function. Nowadays, for the better efficiency the diodes are replaced by transistors having the same function, but better properties. This type of buck converters is called the synchronous buck converter in the literature [4].

The basic operation of the buck converter can be illustrated by looking at the two current paths represented by the states of the two switches (Fig. 1): SW1 is the high-side switch and SW2 is the low side switch (diode). In the first period the high-side switch is turned on and the energy flows from input - through the LC filter - to the output. The energy in the inductor increases. In the second period high-side switch turn off and the low-side switch turned on. The energy from the inductor flows to the output, through SW2. Relevant, that at the same time just one semiconductor conducts; another one is off-state. Between the state-change a dead time has to apply avoiding the short circuit.

In steady state, it can distinguish two operating mode, which depend on the current in the energy storage element (inductor):

- Continuous Conduction Mode (CCM);
- Discontinuous Conduction Mode (DCM).

In CCM the inductor current goes to zero in each switching period, in DCM the inductor sits with no current and no stored energy for part of the time.

The objective of this work is to design (and after that build up) a synchronous buck converter in CCM, because in the practice this solution is more common. The circuit of
buck converter is shown in Fig. 1. This modeling method takes the parasite resistance of capacitor and inductor into consideration.

In the example, the input voltage of the buck converter is 48 V, the output voltage is 12 V, the output power is 100 W, and the switching frequency is 1 MHz. The maximum allowable ripple current is 10% of the maximal output current, and the value of the voltage ripple is maximum 100 mV. Table I contains the additional parameters of the converter.

Table I

| Parameter | Value |
|-----------|-------|
| d         | 0.25  |
| L         | 10 µH |
| r_L       | 21.8 mΩ |
| C         | 10 µF |
| ESR       | 5.7 mΩ |
| R_t       | 1.2 Ω |
| T_s       | 1 µs  |

In steady-state the duty cycle is very important, which gives the ratio between the on time and the switching period and the ratio between outputs furthermore input voltage. It is denoted by \( d \), accordingly [4]:

\[
d = \frac{v_o}{v_i},
\]

where \( V_o \) is the output voltage and \( V_i \) is the input voltage of the converter. The value of the output current can be calculated by using the output power and the output voltage:

\[
I_o = \frac{P_o}{V_o}
\]

where \( I_o \) is the output current and the \( P_o \) is the output power of the converter.

Inductors can be manufactured in various materials typically with a tolerance of ±20%. Inductors have an inherent DC resistance \( (r_L) \), which impacts the performance of the output stage. Minimizing the Direct Current Resistance (DCR) improves the overall efficiency of the converter. There is a trade-off between inductance and ripple current: the lower inductance causes higher ripple through the inductor, typically 10-30% of the maximum output current. In this case the value of ripple current has been defined. The value of the inductor can be calculated [4]:

\[
L = \frac{(v_i - v_o)}{dI_L} t_{on} = \frac{|v_{in}|}{dI_L} t_{off},
\]

where \( \Delta I_L \) is the ripple current through the inductor, \( t_{on} \) is the on time of the semiconductors and the \( t_{off} \) is the off time of semiconductor.
The output capacitor $C_{OUT}$ maintains the regulated output voltage during the times when the inductor current is higher or lower than the output current, therefore the role of this part is very important. Each capacitor has an Equivalent Serial Resistance (ESR), which comes from the physical design. This parameter is very important in case of the design of regulators, because it influences the response time of the feedback loop and is also one component of the output voltage ripple. Choosing of the value of the capacitor requires great care. The minimal value of the capacitor can be calculated as [4]:

$$C_{\text{min}} = \frac{\Delta I_L}{8f_{sw}(\Delta V_{\text{out,max}} - \Delta V_{\text{ESR}})},$$

where $f_{sw}$ is the switching frequency and $\Delta V_{\text{out,max}}$ is the output voltage ripple through the output capacitor.

3. State-space equations

There are lots of modeling methods, which describe the behavior of the system e.g. the DQ-form [5], the state-space equations. The method of state-space describes the system with the state variables, which can be the voltage across the capacitor and the current through the inductor. These equations are first-order differential equations. In case of switching converters, these equations should be linearized, so they approximate the switching converter as a continuous linear system. In case of linear, causal and invariant Single-Input Single Output (SISO) system the state-space model can be described by [6]:

$$\frac{dx(t)}{dt} = Ax(t) + bs(t),$$

$$y(t) = C^T x(t),$$

where $x(t)$ is the state vector; $s(t)$ is the input; $y(t)$ is the output; $A$ is the system matrix; $b$ is the input vector; $C^T$ is the output vector.

The system matrix $A$ is $N \times N$-order quadratic matrix. According to Kirchhoff's law, the following equations can be written [7]:

$$v_C + r_{\text{ESR}} C \frac{dv_C}{dt} = V_o = R_t \left( i_L - C \frac{dv_C}{dt} \right),$$

and

$$r_L i_L + L \frac{di_L}{dt} = V_i - V_o = V_i - R_t \left( i_L - C \frac{dv_C}{dt} \right).$$

The of output voltage is the following:

$$V_o = \frac{r_{ESR}}{R_t + r_{\text{ESR}}} i_L + \frac{R_t}{R_t + r_{\text{ESR}}} v_C.$$
When SW1 is turned on (and SW2 is turned off), conducts for a ratio \(d\) of a period. The state matrix at the state \(t_{on}\) is, from (6), (7) and (8):

\[
\begin{bmatrix}
\frac{di_L}{dt} \\
\frac{dv_C}{dt}
\end{bmatrix} = \begin{bmatrix}
\left(\frac{R_Lr_L + r_{ESR}r_L + r_{ESR}R_t}{L(R_t + r_{ESR})} - \frac{R_t}{C(R_t + r_{ESR})}
\right) \\
\left(\frac{1}{C(R_t + r_{ESR})}\right)
\end{bmatrix} \begin{bmatrix} i_L \\ v_C \end{bmatrix} + \begin{bmatrix} 1 \\ 0 \end{bmatrix} V_o = A_{on}x + b_{on}S,
\]

\[V_o = \begin{bmatrix} \frac{R_t r_{ESR}}{R_t + r_{ESR}} \\ \frac{r_t}{R_t + r_{ESR}} \end{bmatrix} \begin{bmatrix} i_L \\ v_C \end{bmatrix} + 0 \\
0 \end{bmatrix} V_i = C_{on}^T x. \quad (9)
\]

When SW1 is turned off, SW2 conducts for a ratio of \((1-d)\) of a period. The state space equation can be written as (\(t_{off}\) state matrix):

\[
\begin{bmatrix}
\frac{di_L}{dt} \\
\frac{dv_C}{dt}
\end{bmatrix} = \begin{bmatrix}
\left(\frac{R_Lr_L + r_{ESR}r_L + r_{ESR}R_t}{L(R_t + r_{ESR})} - \frac{R_t}{C(R_t + r_{ESR})}
\right) \\
\left(\frac{1}{C(R_t + r_{ESR})}\right)
\end{bmatrix} \begin{bmatrix} i_L \\ v_C \end{bmatrix} + \begin{bmatrix} 1 \\ 0 \end{bmatrix} V_i
\]

\[V_o = \begin{bmatrix} \frac{R_{t} r_{ESR}}{R_{t} + r_{ESR}} \\ \frac{r_{t}}{R_{t} + r_{ESR}} \end{bmatrix} \begin{bmatrix} i_L \\ v_C \end{bmatrix} + \begin{bmatrix} 0 \\ 0 \end{bmatrix} V_i = V_{ocf}^T x. \quad (10)
\]

Averaging the state-variable description [3]:

\[
\frac{dx(t)}{dt} = \left( A_{on}d + A_{off}(1-d) \right) x + \left( b_{on}d + b_{off}(1-d) \right) s,
\]

\[y(t) = \left( C_{on}^T d + C_{off}^T (1-d) \right) x. \quad (12)
\]

Separating the variables:

\[d = D + \tilde{d}, \quad (13)\]

\[x = X + \tilde{x}, \quad (14)\]

where small signal values are represented with a tilde and the steady state component by the uppercase letters. For example, \(\tilde{x}\) is a small-signal variation in the DC or steady-state component \(X\).

The small signal state space description:

\[
\frac{d\tilde{x}(t)}{dt} = A\tilde{x} + \left( (A_{on} - A_{off})X + (b_{on} - b_{off})V_i \right) \tilde{d},
\]

\[\tilde{y}_o = C^T \tilde{x} + \left( C_{on}^T - C_{off}^T \right) X \tilde{d}. \quad (16)
\]
The linearized and simplified model of the buck converter can be written as

\[
\begin{bmatrix}
\frac{di_L}{dt} \\
\frac{dv_C}{dt}
\end{bmatrix} = \begin{bmatrix}
\frac{(R_L + \gamma_{ESR} + \gamma_{ESR}R_L)}{L(R_L + \gamma_{ESR})} & -\frac{R_L}{C(R_L + \gamma_{ESR})} \\
\frac{1}{C(R_L + \gamma_{ESR})}
\end{bmatrix} \begin{bmatrix}
i_L \\
v_C
\end{bmatrix} + \begin{bmatrix}
0 \\
1
\end{bmatrix} V_i.
\]

(17)

Substituting the values of Table I it results in

\[
A = \begin{bmatrix}
-2747.3 & -99527.2 \\
99527.2 & -82939.4
\end{bmatrix}, \quad b = \begin{bmatrix}
25000 \\
0
\end{bmatrix}.
\]

(18)

The transfer function of the system can be obtained, i.e.

\[
W(z) = \frac{141.8s + 2.488 \cdot 10^9}{s^2 + 83.569 \cdot 10^7 s + 1.013 \cdot 10^9}
\]

(19)

The step response of the system without compensator between input and output voltage is shown in Fig. 2.

**Fig. 2. Step response of the system without compensator**

### 4. Linear quadratic regulator design

For regulatory purposes the Linear Quadratic Regulator (LQR) has been used. LQR is an optimal control, and type of a state feedback, which guarantees at least 60° phase margin [8]. It can be used in case of SISO or Multiple-Input Multiple-Output (MIMO) system. For LQR design it is necessary the use the state space variable description (5). In addition, a functional with linear quadratic form can be defined by the general form:

\[
J(x, u) = \frac{1}{2} \int_0^T [x^T Q x + u^T R u] \, dt,
\]

(20)

where \(Q\) and \(R\) are the weighting matrices, \(Q \geq 0\), and \(R > 0\). The aim of an LQR strategy is to minimize the functional \(J\). Because the aim is searching the minimum value of this functional, also the LQR gives the optimum value of control \(u\).

*Pollack Periodica 15, 2020, 2*
It is very common, that the LQR system is augmented with an extra integrator [8], [9]: the derivative of this state is error, which is the difference between the output and the reference voltage. The modified block diagram of the closed loop system is shown in Fig. 3.

According to Fig. 3 the new state space matrices of the system can be describing [10] [11], [12].

\[
\frac{dx}{dt} = \bar{A}x + \bar{b}u, \quad y = \bar{c}^T x, \tag{21}
\]

where

\[
\bar{A} = \begin{bmatrix} A & 0 \\ -C^T & 0 \end{bmatrix}, \quad \bar{b} = \begin{bmatrix} b \\ 0 \end{bmatrix}, \quad \bar{c}^T = \begin{bmatrix} c^T \\ 0 \end{bmatrix}. \tag{22}
\]

It is the designer’s job to find the values of \( Q \) and the value of \( R \). There exist a lot of solutions and algorithms for this problem, which prefer different parameters of the system. To determine \( Q \) and \( R \) the energy-based design method have been used, which is described in [12] and [13]. The buck converter extended with an integrator is a third-order system therefore the matrices \( Q \) is a 3 by 3 matrix. \( R \)’s dimension is 1 because the controller can regulate just the duty cycle \((d)\). Accordingly, the matrices have been set to be:

\[
Q = \begin{bmatrix}
LI_L^2 & 0 & 0 \\
0 & CU_L^2 & 0 \\
0 & 0 & C \cdot 10^{10}
\end{bmatrix}, \quad R = 1. \tag{23}
\]

Solving the LQR problem with \([k, P, e] = lqr(\bar{A}, \bar{b}, Q, R)\) a function by Matlab the results is:

\[
K = \begin{bmatrix} K_1 & K_2 & K_3 \end{bmatrix} = \begin{bmatrix} 2 & 1 & 20833 \end{bmatrix}. \tag{24}
\]
5. Simulation results

The controller has also been tested in the frame of Matlab Simulink. The implementation of Buck converter is shown in Fig. 4, and the closed loop system with LQR controller is shown in Fig. 5 [14].

![Simulink Block model of Buck converter](image1)

**Fig. 4. Simulink Block model of Buck converter**

![Simulink model of controlled Buck converter](image2)

**Fig. 5. Simulink model of controlled Buck converter**

The converter is also tested with load disturbances: in $t=1.2$ ms the load resistance is changed to 1.09 $\Omega$ (10% higher output current), and $t=2$ ms the output resistance and

*Pollack Periodica* 15, 2020, 2
current are changed back to normal, 1.2 Ω. In Fig. 6, the first plot shows the output resistance, the second is the output voltage and the third is the output current. The voltage overshoot, in case of 1 A overcurrent, is about 660 mV, and after 66 ms the output is stabilized.

![Fig. 6. Dynamic test of Buck converter](image)

The Bode plot of the open loop system is shown in Fig. 7. The correct operation of the controller can be checked with the enough phase margins: in this case it is 86.7°.

![Fig. 7. Bode plot of Buck converter](image)
The open loop simulation results in LTSpice with semiconductor GaN E-HEMT (GS61008T) is shown in Fig. 8. The GaN based power electronics system can be exceeding the Si-system limits: reduce the energy cost, it is works on higher switching frequency (or on the same frequency it has lower loss) and it has better thermal and electrical properties [15]. To achieve good switching properties, separated turn-on and turn-off gate resistors are used. The turn on resistor is selected 10 Ω and the off resistor is 1 Ω. The dead time between the high and low side signal is 25 ns [16]. Table II contains the important simulation results and in Fig. 9 the following simulation results are presented: the first plot shows the inductor current, the second plot shows the output voltage, the third plot shows the output current, the fourth plot shows the high side gate drive signal and the fifth plot shows the low side gate drive signal.

![Buck converter open loop circuit diagram in LTSpice](image)

**Fig. 8.** Buck converter open loop circuit diagram in LTSpice

**Table II**

| Parameter | Value |
|-----------|-------|
| D         | 0.25  |
| L         | 10 μH |
| rL        | 5.4 mΩ|
| C         | 10 μF |
| ESR       | 5.7 mΩ|
| Rf        | 1.2 Ω |
| Ts        | 1 μs  |
| P_loss T1 | 4.153 W |
| P_loss T2 | 746.45 mW |
| Total Efficiency | 97.58% |

*Pollack Periodica 15, 2020, 2*
The role of electromagnetic compatibility (EMC) is very important in the design processes of SMPS especially for high frequency applications. Therefore, measuring this parameter is necessary in case of SMPS [17].

![LTSpice simulation results in steady state](image)

**Fig. 9.** LTSpice simulation results in steady state

### 6. Conclusion

A linear quadratic regulator controller has been designed for the Buck converter, which is based on the state feedback. The controller has been tested via Matlab and LTSpice. It is shown that this robust regulator can be realized easily. Next the circuit will be designed with GaN E-HEMT semiconductor and high frequency gate driver from Texas Instruments. The controller circuit will be designed in two ways: realized by passive components like operational amplifier, resistor and capacitor, and by digital controller.

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