Two-qubit silicon quantum processor with operation fidelity exceeding 99%

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Silicon spin qubits satisfy the necessary criteria for quantum information processing. However, a demonstration of high fidelity state preparation and readout combined with high fidelity single- and two-qubit gates, all of which must be present for quantum error correction, has been lacking. We use a two qubit Si/SiGe quantum processor to demonstrate state preparation and readout with fidelity over 97%, combined with both single- and two-qubit control fidelities exceeding 99%. The operation of the quantum processor is quantitatively characterized using gate set tomography and randomized benchmarking. Our results highlight the potential of silicon spin qubits to become a dominant technology in the development of intermediate-scale quantum processors.

Since the publication of the Loss-DiVincenzo proposal for spin-based quantum information processing in 1998 [1], the semiconductor quantum dot research community has worked to satisfy the DiVincenzo criteria for quantum information processing using spin qubits. Electron spins can be initialized and read out using spin-to-charge conversion [2, 3], single spin qubits can be coherently controlled using oscillating electromagnetic fields [4–6], and nearest neighbor spins can be coherently coupled via the exchange interaction [3].

Seminal results for spin qubits were obtained with GaAs quantum dots [2–4], however hyperfine coupling of the electron spin to lattice nuclei greatly limited quantum coherence [7]. A transition to silicon, which can be isotopically enriched, has led to a several order-of-magnitude increase in electron spin coherence times [8] as well as improved quantum control fidelities [9–11]. The small ~100 nm scale of quantum dot spin qubits and the significant capabilities of the silicon microelectronics industry could allow for scaling to system sizes that are capable of fault tolerant operation.

While high single- and two-qubit gate fidelities have been demonstrated in silicon [9, 11–13], state preparation and measurement (SPAM) errors have generally hovered around ~10–20%. Here we demonstrate a spin-based two qubit quantum processor with all-around high performance fidelities (readout F > 97%, simultaneous single qubit control F > 99%, and a two-qubit controlled-phase (CPhase) gate F > 99.8%). Our two-qubit gate fidelity exceeds recent reports on spin qubits [12, 13] and is competitive with superconducting qubits [14, 15].

High-fidelity quantum control and readout are achieved in the first two qubits (Q1 and Q2) of a six qubit linear array [Fig. 1A]. Quantum dot electrons are vertically confined in an isotopically enriched (800 ppm) 28Si quantum well and lateral confinement is achieved using an overlapping aluminum gate stack [16]. Figure 1B depicts the double quantum dot formed under gates P1 and P2 with the exchange interaction controlled by gate B2 [3]. An external magnetic field BE = 365 mT is applied in the plane of the quantum well to Zeeman split the spin-states. The external field adds to the magnetic field generated by a Co micromagnet resulting in electron spin resonance frequencies f1 = 18.247 GHz and f2 = 17.851 GHz.

We first demonstrate high visibility readout and single qubit control fidelities. Single qubit gates are achieved at the symmetric operating point in the (1,1) charge state using electric dipole spin resonance in the transverse field gradient created by the micromagnet [6, 17, 18]. Here (N1, N2) denotes the charge occupation of dots 1 and 2. Qubit state preparation and readout are achieved by spin dependent tunneling with the reservoir [2, 19, 20]. Figure 1C shows Rabi chevrons for each qubit, obtained by measuring the spin-up probability P↑ as a function of drive frequency f and microwave burst length τR. Rabi oscillations approaching unit visibility are achieved when driving each qubit on resonance [Figure 1D].

We perform the quantum characterization, verification, and validation protocol of gate set tomography (GST) on the single qubit gates - identity (idle qubit i for π/2 gate time 70 ns) Ii, πXY/2 rotation Xi, and πY/2 rotation Yi - and estimate single qubit control fidelities above 99.9% when driving and measuring one qubit at a time [Fig. 1C, inset] [21]. The fidelities are limited by decoherence [T2∗(T2) = 1.7(23) μs and 2.3(102) μs for Q1 and Q2] and are comparable to the highest single spin qubit gate fidelities in the literature [9, 22]. SPAM errors extracted from GST are quantified by the initialization fidelities ρ0,1 = 99.4% and ρ0,2 = 97.5% and the measurement fidelities M1 = 98.1% and M2 = 99.8%, making the overall operation fidelity high enough to support common error correction protocols.

Building to the two qubit space, we use GST to characterize the qubit control fidelities when both qubits are operated simultaneously (X1 ⊗ X2, Y1 ⊗ X2, etc.) by combining microwave control signals on the drive gate [MW gate in Fig. 1A]. GST estimates an average simultaneous single qubit control fidelity F = 99.4 ± 0.1% when
High-fidelity operation of a two-qubit quantum processor. (A) False-color scanning electron microscope image of the device. Spins are selectively driven in a field gradient of a Co micromagnet using the microwave (MW) gate. (B) Two electron spins (Q1 and Q2) are trapped beneath gates P1 and P2, and the exchange coupling between the spins is set by the barrier gate B2. (C) Spin-up probabilities, \( P_{1↑}(P_{2↑}) \), for Q1(Q2) plotted as a function of drive frequency \( f \) and microwave burst length \( \tau_R \). Insets: Single qubit gate fidelities extracted from GST. (D) Spin-up probabilities for each qubit when driven on resonance.

Operating in the two qubit space. Finally, we cross-check these GST results using the widely accepted protocol of randomized benchmarking (RB) and achieve \( F_1 = 99.50 \pm 0.02\% \), \( F_2 = 99.48 \pm 0.02\% \), and a joint fidelity of \( 99.13 \pm 0.03\% \). These results build significantly on past attempts to simultaneously drive spin qubits, where single qubit control fidelities were as low as 97\% [10].

Two qubit control is achieved by pulsing on gate B2 to turn on the exchange interaction \( J(V_{B2}) \) [3], which up to single qubit rotations yields a controlled-Z (CZ) gate \( U_{CZ} = \text{diag}(1,1,1,-1) \) in the regime where the magnetic field gradient exceeds exchange \( \Delta E_z \gg J \) [23, 24]. Figure 2A demonstrates a 3-decade variation of \( J(V_{B2}) \). Exchange is extracted in the high-\( J \) regime (\( J > 1/T_z^2 \)) by measuring time domain exchange oscillations [25], whereas a spin echo is utilized to measure residual exchange down to a \( T_z \) limit of 10 kHz [blue in Fig. 2A]. In Fig. 2B we optimize the CZ gate by preparing the target qubit (Q2 here) in a superposition state and the control qubit in either \( \uparrow \) or \( \downarrow \). Exchange is then pulsed on for 40 ns using a smoothed square pulse [20] to execute a C-Phase gate. Afterwards, a software Z rotation \( Z_\phi \) is applied to realize a CZ gate. As an initial demonstration of full two-qubit control with low SPAM errors, we perform Bell state tomography and use maximum likelihood estimation to achieve the Bell state fidelities \( |\Psi^+\rangle = 97.5\%(98.3\%) \), \( |\Psi^-\rangle = 97.0\%(98.3\%) \), \( |\Phi^+\rangle = 95.4\%(97.2\%) \), \( |\Phi^-\rangle = 96.3\%(97.4\%) \), without (with) SPAM corrections included [Fig. 2C] [20]. These results significantly improve upon past measurements with SPAM corrected fidelities of 78-90\% [11, 25-27] and are comparable to the simulated Bell state fidelities obtained by Xue et al. [12].

To demonstrate integrated control of the two qubit processor, we combine the CZ with other primitive qubit operations to create familiar two qubit gates (e.g. CNOT and SWAP). We first synthesize a CNOT gate using the Hadamard and CZ gates. Figure 3A shows the raw input-output measurement results of performing the synthesized CNOT gate on the four different input product states with Q2 as the target. To show the target qubit will follow the control qubit state we prepare Q1 in the \( \downarrow \) state and Q2 in an arbitrary superposition state using a Rabi drive pulse for time \( \tau_R \). This state preparation routine is followed by the CNOT with Q1 as the target qubit this time. The result is high visibility anti-correlated oscillations of the \( \downarrow\downarrow \) and \( \uparrow\uparrow \) joint state probabilities, Fig. 3B. Additionally, we generate synthesized SWAP gates with three alternating CNOT operations and measure the input-output SWAP table, Fig. 3C. To show a SWAP of an arbitrary superposition state \( |\psi\rangle \) we perform a Rabi
drive pulse on Q2 and then SWAP the state onto Q1, resulting in Q2 being in the \( \downarrow \) state and Q1 in the \( |\psi\rangle \) state [Fig. 3D].

We turn to two qubit interleaved RB to quantify the overall performance of our processor [28]. The two qubit Clifford group \( C_2 \) in this experiment has 576 single-qubit elements and integrates the CZ into 10944 two-qubit elements containing CNOT-, iSWAP-, and SWAP-like operations similar to those demonstrated in Fig. 3. With this technique, the CZ and synthesized CNOT fidelities can be determined by interleaving these operations in the benchmarking sequences and comparing to the reference curve. To thoroughly sample the Clifford group and obtain an accurate estimate of our CZ and CNOT fidelities we randomize 125 unique sequences for each reference and interleaved measurement going to sequence lengths as long as 65 total Clifford operations and average 160 times [Fig. 4]. The resulting two qubit RB fidelities for the CZ and CNOT are \( F_{\text{CZ}} = 99.81 \pm 0.17\% \) and \( F_{\text{CNOT}} = 98.62 \pm 0.16\% \) with error bars determined by bootstrapping [29].

We have demonstrated full two qubit control in a silicon quantum device with simultaneous single qubit control fidelities exceeding 99% and a primitive two-qubit CZ-gate fidelity exceeding 99.8%. In contrast to previous implementations [12, 13, 25, 26], SPAM errors are very low <3%. Our demonstration represents the highest total operation fidelity in a two qubit processor realized in silicon quantum dots with performance capable of fault tolerant operation [30]. These experiments demonstrate two qubit gates with silicon spin qubits at speeds exceeding trapped ions [31] and fidelities comparable with superconducting qubits [14, 15]. Given recent advances in quantum dot fabrication [16, 32] spin qubits are poised to scale-up to larger multi-qubit quantum processors.

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FIG. 2. Optimization of the CZ gate. (A) Exchange interaction measured as a function of \( \nu_{B2} \) using a combination of Ramsey and Hahn echo type sequences (see text). The quantity \( \Delta J \) indicates how far we can dynamically tune exchange. (B) The CZ is tuned by preparing the target spin in a superposition state, applying the C-Phase operation at \( J \sim 5 \) MHz for 40 ns, and then adjusting the phase \( Z_\alpha \) such that \( F_{2\nu} = 1 \) \( (F_{\nu} = 0) \) when Q1 is prepared in the spin-down (spin-up) state using an optional \( \pi \)-pulse (blue dashed box). (C) Bell state tomography of the \( |\Phi^-\rangle \) state yields the extracted density matrix (shown) with a raw state fidelity \( F_u = 96.3\% \). Correcting for SPAM errors yields a fidelity \( F_u = 97.4\% \).
FIG. 3. Synthesis of CNOT and SWAP gates using the primitive CZ gate. (A) Input-output table illustrating the effect of the synthesized CNOT gate on the four different input product states ($|↑↑⟩$, $|↑↓⟩$, $|↓↑⟩$, $|↓↓⟩$). (B) Joint state probabilities $P_{↑↑}$, $P_{↑↓}$, $P_{↓↑}$, and $P_{↓↓}$ plotted as a function of microwave burst length $\tau_R$ at frequency $f_2$ showing that the joint state $|↓↓⟩$ is anticorrelated with $|↑↑⟩$. (C) Input-output table for the synthesized SWAP gate. (D) $P_{↑↑}$ and $P_{↓↓}$ measured as a function of Q2 drive time $\tau_R$ at frequency $f_2$ following a SWAP of the Q2 state onto Q1.

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