A Novel Fault Diagnosis Method for Analog Circuits Based on Multi-Input Deep Residual Networks with an Improved Empirical Wavelet Transform

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Abstract: Analog circuits play an essential role in electronic systems. To strengthen the reliability of sophisticated electronic circuits, this paper proposes a novel analog circuit fault diagnosis method. Compared with traditional fault diagnosis, the fault diagnosis process in this paper uses a square wave as the stimulus of the circuit under test (CUT), which is beneficial for obtaining the response of the CUT with rich time and frequency domain information. The improved empirical wavelet transform (EWT), which can more accurately extract the amplitude modulated–frequency modulated (AM-FM) components, is used to preprocess the original response. Finally, based on the preprocessed data, a multi-input deep residual network (ResNet) is constructed for fault feature extraction and fault classification. The multi-input ResNet is a powerful approach for learning the fault characteristics of the CUT under different faults by learning the characteristics of the AM-FM components. The effectiveness of the method proposed in this paper is verified by comparing different fault diagnosis methods.

Keywords: analog circuit; fault diagnosis; multi-input deep residual network (ResNet); empirical wavelet transform (EWT); circuit under test (CUT)

1. Introduction

As the electronic industry advances, the volume of electronic circuits is smaller, the functions are more abundant, and the integration and complexity are absolutely higher. A previous survey showed that nearly 80% of electronic circuits are digital circuits, while approximately 80% of faults are caused by analog circuits [1]. Therefore, the analog circuit crucially influences the stability and reliability of the electronic system. If an analog circuit fails during use, it will lead to certain economic losses and even safety issues. In recent years, due to the important role played by analog circuits in electronic systems, analog circuit fault diagnosis has become a research hotspot in engineering and academia. However, due to the nonlinear nature of the analog circuit and the lack of a good fault diagnosis model, analog circuit fault diagnosis has also become a research difficulty [2]. With the increased integration and complexity of electronic circuits, vulnerability to circuit noise, component tolerances, and difficulty of obtaining test points, fault diagnosis of analog circuits has become more intricate and difficult.

Currently, two main types of fault diagnosis methods are available for analog circuits: simulation before test (SBT) and simulation after test (SAT) [2]. SBT simulates various faults prior to fault diagnosis of analog circuits by simulation tools. In practical applications, the actual measured data are compared with the dataset obtained from the simulation to complete the fault diagnosis. SAT refers to the realization of fault location through theoretical analysis and logical deduction based on the fault data of the analog
circuit collected in the actual measurement, although small parameter disturbances will affect the fault location results [1]. Compared with SAT, SBT is more widely used in analog circuit fault diagnosis due to its simpler theoretical and logical analysis and better real-time performance. The fault types of analog circuits are mainly divided into hard faults and soft faults [1,2]. Hard faults mainly refer to open and short faults. Such faults will contribute to relatively large changes in the relevant parameters of the circuit that directly cause circuit failure or destruction, and most of them can be found in the test stage before the circuit is promoted and used. Soft faults refer to failures in which the parameters of the components in the circuit deviate from the nominal value due to factors such as manufacturing, temperature, humidity, pressure, sunshine, dust, and component ageing. Furthermore, the probability of soft faults is much greater than that of hard faults, and the diagnostic demand and value of soft faults are greater than those of hard faults.

At the end of the 20th century, with the rise of artificial intelligence algorithms, a large number of fault diagnosis methods for analog circuits based on artificial intelligence algorithms were proposed, thereby realising the intelligence and automation of analog circuit fault diagnosis. From 2000 to 2007, Aminian used wavelet transform (WT) to preprocess the impulse response of the CUT and then realised the fault diagnosis of analog circuits by neural networks [3–5]. In 2008, Tan extracted features from the feasible domain based on wavelet packet transform (WPT) and finally completed the fault diagnosis of analog circuits by probabilistic neural networks [6]. In 2010, Cui used WPT to preprocess the response signal of the CUT, extracted the fault features and finally completed the fault diagnosis of the analog circuit by a support vector machine (SVM) [7]. Yuan preprocessed the responses of the CUT and input the entropy and kurtosis of the responses into the neural network to complete the fault classification, thereby realising fault diagnosis of the analog circuit in 2010 [8]. In 2013, Long combined the mean value and standard deviation of the WT components and the mean value, standard deviation, skewness, peak value, and entropy in the statistical characteristics of original responses to form a feature vector, and then used the least square support vector machine (LS-SVM) to finish the fault diagnosis of the CUT [9]. In 2016, Xiong used ensemble empirical mode decomposition (EEMD) to preprocess the response of the CUT to obtain the amplitude modulation and frequency modulation (AM-FM) components and calculate the peak value and relative entropy of each AM-FM component to form the feature vector, and then they completed fault diagnosis by an extreme learning machine (ELM) [10]. In 2020, He used a cross-wavelet transform (XWT) to obtain the spectrum map of the faulty signal and then fed the spectrum map to a generative adversarial neural network (GAN) to achieve fault diagnosis of the CUT [11]. In 2021, Ji obtained the spectrogram of the response of the CUT by short-time Fourier transform (STFT) and input it to ResNet to complete feature extraction and fault diagnosis of the CUT [12]. In 2021, Yang used WT to preprocess the signal at the output of the CUT and input the preprocessed data into an improved deep convolutional neural network to complete the diagnosis of incipient faults in analog circuits [13].

According to the above methods, it can be concluded that the general process of analog circuit fault diagnosis is data preprocessing, feature extraction, and fault diagnosis. The experimental results of a large number of studies have shown that by preprocessing the response signal of the CUT, the speed and accuracy of fault diagnosis can be effectively improved. Common signal preprocessing methods include STFT, WT, WPT, EMD, EEMD, [14] and empirical wavelet transform (EWT) [15]. Although WT can be used continuously as a time function for signal localization analysis, it can only decompose the low-frequency part of the signal and the level of detail is insufficient. WPT can process the high- and low-frequency parts of the signal, but cannot perform data-driven frequency partitioning [16]. EMD can realise the adaptive decomposition of the original signal, but it lacks a theoretical basis and is prone to modal aliasing and end effects [17]. Although EEMD can eliminate mode aliasing and end effects, the algorithm is sophisticated and the time complexity is huge. The EWT proposed based on EMD and wavelet theory is not limited by the signal frequency band and can better eliminate mode aliasing and endpoint
effects [15]. In fault diagnosis, support vector machines (SVMs) and artificial neural networks (ANNs) are the two most popular fault classification methods. SVM as a classifier requires additional feature analysis, extraction, or fusion, which can increase the complexity of analog circuit fault diagnosis. Compared with SVMs, ANNs can automatically learn the characteristics of the signal without manual intervention for feature analysis and processing.

Inspired by the above papers, a novel analog circuit fault diagnosis method is proposed in this paper, which first applies a multi-input ResNet to analog circuit fault diagnosis. In addition, a square wave is used as the stimulus of the CUT, and then responses are preprocessed by the improved EWT. The multi-input ResNet is used to complete feature extraction and fault diagnosis on the basis of the components decomposed by the improved EWT. This paper is proposed mainly based on the following ideas.

• The components of the response signal vary among different CUT fault types. The improved EWT can effectively extract each component of the response signal, which is conducive to reducing the input and layer number of the neural network and improving the efficiency of feature extraction and fault diagnosis;
• The multi-input ResNet can richly learn the characteristics of each component without extra feature analysis and processing, which is beneficial for reducing the complexity of analog circuit fault diagnosis and improving the accuracy.

The rest of this paper is organized as follows. The second section introduces the analog circuit fault diagnosis method proposed in this paper. The third section verifies the effectiveness of the proposed method by the Sallen–Key bandpass filter circuit, CSTV filter circuit, and leap-frog low-pass filter circuit. Finally, the fourth section concludes and summarises the work of this paper.

2. Methodology
2.1. Proposed Framework for Fault Diagnosis

Based on the theory of EWT and ResNet, this paper makes subtle improvements to EWT and proposes a multi-input ResNet model. Moreover, the improved EWT and multi-input ResNet are applied to analog circuit fault diagnosis. Figure 1 shows the framework of proposed method. The first step of the method is to take the square wave as the stimulus of the CUT and collect the response of the output of the circuit. Then, the improved EWT is employed to preprocess the collected data and divide the preprocessed data into a training set, validation set, and test set. The multi-input ResNet model is trained with the training set, and the optimal multi-input ResNet model corresponding to the highest verification accuracy is acquired with the validation set. Finally, the test set is input to the optimal multi-input ResNet model, and the result of fault diagnosis is obtained.

![Figure 1. Framework of the proposed method.](image-url)
2.2. Theory of EWT

EWT is a new method of adaptive wavelet decomposition proposed by Gills in 2013 [15]. The main idea is to extract different modes of a signal by constructing a set of bandpass filters. Figure 2 shows the workflow of the EWT method. The first step of the algorithm is to convert the signal from the time domain to the frequency domain by using the Fourier transform method. Subsequently, we segment the Fourier spectrum by detecting the boundaries automatically to construct a bandpass filter bank. Finally, the AM-FM components of the original signal centred on a specific frequency are obtained by filtering.

![Figure 2. EWT workflow.](image)

Suppose the Fourier axis of a signal is normalised to $[0, 2\pi]$. To follow Shannon’s theorem, the discussion scope is limited to $[0, \pi]$. The Fourier support interval $[0, \pi]$ is partitioned into $N$ consecutive segments, and each segment is denoted as $\Lambda_n = [\omega_n, \omega_{n+1}]$, where $\omega_n$ and $\omega_{n+1}$ denote the boundary of each segment, with $n = 1, 2, \ldots, N$, $\omega_0 = 0$, $\omega_N = \pi$, and $\sum_{n=1}^{N} \Lambda_n = [0, \pi]$. $N$ consecutive segments, excluding $\omega_0$ and $\omega_N$, still need $N-1$ boundaries. The remaining boundaries of the divided Fourier spectrum can be determined according to the following:

- Local maxima other than $0$ and $\pi$ should be found in the spectrum and arranged in descending order;
- If the number of local maxima $M$ is less than $N$, retain all the maxima and reset the number of Fourier segments $N$ to $M$. Otherwise, retain the first $N$ maxima;
- Then, the $N-1$ boundaries can be defined as the centre of two consecutive maxima.

As shown in Figure 3, the transition zone is defined as the grey hatched area of width $2\tau_n$, where $\tau_n = \gamma \omega_n$ and $0 < \gamma < 1$. We can ensure that there is no overlap between two consecutive transition zones when $\gamma < \min_1 \frac{\omega_{n+1} - \omega_n}{\omega_{n+1} + \omega_n}$.

![Figure 3. Segmentation of the Fourier spectrum.](image)

The empirical wavelets are equivalent to bandpass filters with band width $\Lambda_n$. The empirical scaling function $\hat{\phi}_n(\omega)$ and the empirical wavelet function $\hat{\psi}_n(\omega)$ can be expressed by (1) and (2), respectively, based on the idea of constructing Littlewood–Paley and Meyer’s wavelets.
\[ \widehat{\phi}_n(\omega) = \begin{cases} 1, & \text{if } |\omega| \leq \omega_n - \tau_n \\ \cos[\frac{\pi}{2} \beta(1 - \frac{1}{2 \tau_n}|\omega| - \omega_n + \tau_n)], & \text{if } \omega_{n+1} - \tau_{n+1} \leq \omega_n + \tau_n \\ 0, & \text{otherwise} \end{cases} \]  

(1)

\[ \widehat{\psi}_n(\omega) = \begin{cases} 1, & \text{if } \omega_n + \tau_n \leq |\omega| \leq \omega_{n+1} - \tau_{n+1} \\ \cos[\frac{\pi}{2} \beta(1 - \frac{1}{2 \tau_n}|\omega| - \omega_{n+1} + \tau_{n+1})], & \text{if } \omega_{n+1} - \tau_{n+1} \leq |\omega| \leq \omega_{n+1} + \tau_{n+1} \\ \sin[\frac{\pi}{2} \beta(1 - \frac{1}{2 \tau_n}|\omega| - \omega_n + \tau_n)], & \text{if } \omega_n - \tau_n \leq |\omega| \leq \omega_n + \tau_n \end{cases} \]  

(2)

The function \( \beta(x) \) is an arbitrary \( C^1([0,1]) \) function that meets the basic requirements, as in (3).

\[ \beta(x) = \begin{cases} 0, & \text{if } x \leq 0 \\ \beta(x) + \beta(1 - x) = 1 & \forall x \in [0,1] \\ 1, & \text{if } x \geq 1 \end{cases} \]  

(3)

It has been proven in practice that many polynomials can satisfy this condition, although the polynomial proposed by Daubechies used by Gilles is widely used, as shown in (4).

\[ \beta(x) = x^4 (35 - 84x + 70x^2 - 20x^3) \]  

(4)

EWT is analogous to the conventional WT. As shown in (5) and (6), the detail coefficients and approximation coefficients can be given by the inner product with the empirical wavelet function and empirical scaling function, respectively, where \( \wedge \) indicates Fourier transform, \( \vee \) indicates inverse Fourier transform, and \( \cong \) represents complex conjugation.

\[ W^0_j(t) = \langle f, \phi \rangle = \int f(t) \hat{\phi}(\tau - t) dt = (\hat{f}(\omega) \hat{\phi}(\omega))^\vee \]  

(5)

\[ W^n_j(t) = \langle f, \psi \rangle = \int f(t) \hat{\psi}(\tau - t) dt = (\hat{f}(\omega) \hat{\psi}(\omega))^\vee \]  

(6)

Following this formalism, we can obtain the empirical mode component \( f_k \), which is expressed as follows:

\[ f_k(t) = W^0_j(t) * \phi (t) = (\hat{W}^0_j(0,\omega) \hat{\phi}(\omega))^\vee \]  

(7)

\[ f_k(t) = \sum_{n=1}^{N} W^n_j(n,t) * \psi_s (t) = (\sum_{n=1}^{N} \hat{W}^n_j(n,\omega) \hat{\psi}_s(\omega))^\vee \]  

(8)

where \(*\) denotes convolution.

From the above, it is evident that the AM-FM components composed of \( f_k \) are directly influenced by segmentation boundaries. Thus, boundaries can determine the decomposition effect of EWT.
2.3. Stimulus-Based Improved EWT

In this section, an improved EWT method with a new boundary condition based on stimuli is proposed. In the SBT method, stimuli are very important for simulation, and they must be capable of transmitting fault information between one or more nodes [18]. At present, the two most prevalent excitation signals in analog circuit fault diagnosis are pulse signals [9,10,12,13] and sinusoidal sweep signals [8,11,19–21]. Fault information can be transmitted by pulse signals and sweep signals, and fault diagnosis can be accomplished by analysing circuit response signals. However, the impulse response only contains time domain information; therefore, it is difficult to analyse in the frequency domain. The sweep response of the circuit is a kind of unsteady signal, which increases the difficulty of frequency domain analysis.

Previous studies [22,23] combined multiple sinusoidal signals of different frequencies as the excitation of the CUT and verified the effectiveness of multifrequency analysis in analog circuit fault diagnosis. Inspired by these articles, this paper takes advantage of the characteristic that the square wave signal consists of multiple frequency components of fundamental and odd harmonics, and a square wave is used as the stimulus at the input of the CUT. Assuming that the square wave signal \( x(t) \) is of frequency \( f \) and amplitude \( E \), (9) is the Fourier series expansion of \( x(t) \), where \( \Omega = 2\pi f \). Using the square wave as the excitation of the CUT can not only transmit fault information and meet the needs of multiple frequencies but is also more convenient for the time and frequency domain analysis of the square wave response of the CUT.

\[
x(t) = \frac{2E}{\pi} \left( \sin \Omega t + \frac{1}{3} \sin 3\Omega t + \frac{1}{5} \sin 5\Omega t + \cdots \right)
\]  

The main idea of EWT is to construct a bandpass filter bank to obtain AM-FM components centred on a specific frequency. The traditional EWT arranges the local maximum values in the spectrum in descending order and takes the centre of two continuous local maximum values as the boundary of the spectrum segmentation. Thus, (1) and (2) can be used to construct a bandpass filter bank to obtain component signals.

Analog circuits are particularly complex, and most of them are nonlinear. When a square wave is used as a stimulus, the components of the circuit response are affected by different faults; therefore, the local maxima of the spectrum may be inconsistent in different faults. As a result, the centre frequency of the AM-FM components of the response of the same CUT in different faults is not necessarily the same, which will result in the inability to accurately analyse the impact of different faults on the components, thus limiting the improvement of the analog circuit fault diagnosis accuracy. Therefore, for the stimulus selected in this article, the traditional EWT used for the preprocessing of the response signal at the output of the analog circuit will no longer be applicable. To accurately reflect the effect of different analog circuit faults on the components, this paper uses a priori knowledge on the frequency of the excitation to determine the spectrum splitting boundaries in the improved EWT. Suppose that the Fourier axis of \( x(t) \) is normalised to \([0, 2\pi]\), and the fundamental frequency of the square wave can be denoted as follows:

\[
\omega_0 = \frac{\Omega}{f_c}
\]  

where \( f_c \) denotes the sampling frequency of \( x(t) \). According to (9), the square wave is mainly composed of fundamental and odd harmonics. If the boundary of the splitting spectrum is determined at even times, \( \omega_p \), the components with odd times, \( f \), as the centre frequency can be accurately extracted. Therefore, the boundaries in the improved EWT except for 0 and \( \pi \) can be expressed as follows:

\[
\omega_p = p\omega_0, \ p = 2, 4, \cdots, 2(N-1)
\]
The specific implementation steps of the improved EWT are as follows:

1. According to the characteristics of the CUT, select the square wave signal with frequency \( f \) as the excitation of the CUT;
2. Perform Fourier transform on the response signal at the output end of the CUT, obtain the spectrum and convert the signal to the frequency domain for analysis;
3. Use \( \omega_0 \) as the boundary for dividing the Fourier spectrum;
4. Obtain the AM-FM components of the signal based on Equations (1) and (2).

2.4. Construction Method for the Multi-Input ResNet

In the field of artificial intelligence, ANNs are widely used in fault diagnosis due to their strong expression ability and learning ability. However, with increasing ANN network depth, the training accuracy gradually increased to saturation and then began to degrade rapidly. The deep residual network (ResNet) proposed by He. Kaiming in 2016 can be used to resolve the problem of degradation [24]. ResNet is based on the following concepts:

- The features obtained by shallow neural networks are not sufficiently abstract. By increasing the layers of the neural network, the abstraction of features can be improved and the training effect for some specific tasks can be improved to some extent.
- With the increase in neural network layers, the training accuracy of the neural network gradually becomes saturated and then rapidly decreases. This degradation problem limits the improvement of neural network training accuracy.
- In theory, adding identity mapping to the optimal shallow neural network can not only increase the depth of the neural network, but also retain the same training errors as the shallow neural network so that the deeper network can perform better than the shallow network.

Based on these ideas, the problem of gradient degradation can be handled by adding identity mapping to the shallow neural network. The implementation of ResNet is equivalent to fitting an identity map.

The structure of the deep residual learning block is shown in Figure 4. The residual block is realised by a shortcut connection, which is equal to adding the input and output of the block directly and activating the block by the activation function. Therefore, the introduction of the identity shortcut does not increase the computational complexity, nor does it require additional parameters. In fact, identity mapping is ideal and impossible to realise. And it’s a desired underlying mapping. Therefore, the deep residual learning block can only be used to infinitely close the identity mapping by underlying mapping.

![Deep residual learning block](image)

Figure 4. Deep residual learning block.

Suppose that the underlying mapping realised by the deep residual learning block is \( H(x) \), where \( x \) represents the input of the deep residual block. \( F(x) = H(x) - x \) is defined as
a residual mapping, and then \( H(x) = F(x) + x \). The deep residual block is infinitely close to \( H(x) \), which is equivalent to stacking multiple nonlinear layers in the residual block to approximate \( F(x) \). It is easier to implement residual mapping with multiple nonlinear layers to approximate \( F(x) \) to 0 than to approximate identity mapping with multiple nonlinear layers.

Figure 5 presents the structure of the multi-input ResNet based on the deep residual learning block. The AM-FM components of the response of the CUT obtained by the improved EWT are input from different input terminals of the multi-input ResNet, and the feature vector of each AM-FM component is cascaded. Finally, the result is output through the fully connected (FC) layer and softmax function. The multi-input ResNet model is obtained by combining the input layer, multiple ResNet units, concat layer, and FC layer. There are \( N \) ResNet units with the same structure in Figure 5. The main purpose of this structure is to use a ResNet unit to learn the characteristics of an AM-FM component and sufficiently learn the characteristics of each component. Although the structure of each ResNet unit is the same, their weights and biases obtained by training are different to obtain more abundant fault characteristics under different CUT fault types.

Each ResNet unit consists of a convolution block, 4 residual blocks and an average pooling layer. The convolution block consists of a one-dimensional convolution layer, batch normal (BN) layer, RELU layer, and average pooling layer. The residual block is constructed in the manner described in Figure 4, and the details are shown in [24].

![Diagram of the multi-input ResNet structure](image)

**Figure 5.** Structure of the multi-input ResNet.

### 3. Experimental Validation

In this section, the standard analog filter circuits in ISCAS’97 are selected for validation which are widely used as verification circuits in the field of analog circuit fault diagnosis. Among them, the Sallen–Key band-pass filter circuit, the CSTV filter circuit, and the leap-frog low-pass filter circuit are the most typical ones, which are often selected as verification circuits in most literature, such as [8,10,13,25–28], etc. Therefore, these three representative circuits are also chosen for experimental verification in this section.

All the fault diagnosis experiments in this article are performed on a computer with RAM of 16 GB and a processor frequency of 2.4 GHz. Multisim 14.0 is used to simulate the excitation signal, thermal noise and CUT and perform Monte Carlo analysis. The analog circuit fault diagnosis method proposed in this paper is implemented in a PyTorch environment through Python.
3.1. Circuit under Test

In practice, the probability of a single fault occurring in analog circuits is greater than that of multiple faults [25], and multiple failures may be caused by concurrent failures triggered by the failure of a single component. Therefore, only a single fault is considered in this paper. Differences in manufacturing processes introduce tolerances to components in analog circuits; therefore, the tolerance of resistance and capacitance is set at 5% in this paper. When the sensitive components in the analog circuit operate within the tolerance range, the circuit is in a normal state; when the sensitive components in the circuit deviate from the nominal value by 5–10% the components start to fail, but the circuit can still operate normally; and when the sensitive components in the circuit deviate from the nominal value by 30–50%, the circuit malfunctions [19,25–27,28]. To diagnose the faults in this circuit, the 30% deviation of the sensitive component and 35% deviation of the less sensitive component or confusion-prone component from the nominal value are taken as the standard of components’ fault setting in this article. Thermal noise with a resistance value of 10 K, temperature of 27 °C, bandwidth of 1 MHz, and signal-to-noise ratio of 10 is added to the input of the circuit to simulate the noise environment in the actual use of the analog circuit.

3.1.1. Sallen–Key Bandpass Filter

Figure 6 shows the Sallen–Key bandpass filter circuit with a centre frequency of 40 kHz. The square wave signal with a frequency of 10 kHz, duty cycle of 50% and amplitude of 5 V is used as the stimulus of the Sallen–Key bandpass filter circuit. The nominal values of the components in the Sallen–Key bandpass filter circuit are shown in Figure 6. The sensitive components of the Sallen–Key bandpass filter circuit are $R_2$, $R_3$, $C_1$, and $C_2$ [25,27]. Sensitive components, together with $R_1$ and $R_4$, are used for fault setting and they mainly have 13 fault states, which are represented as $R_1 \downarrow$, $R_1 \uparrow$, $R_2 \downarrow$, $R_2 \uparrow$, $R_3 \downarrow$, $R_3 \uparrow$, $R_4 \downarrow$, $R_4 \uparrow$, $C_1 \downarrow$, $C_1 \uparrow$, $C_2 \downarrow$, and $C_2 \uparrow$, and referred to as F0–F12, with F0 indicating that the circuit is not in fault. Table 1 is the specific fault setting in this instance.

![Sallen–Key bandpass filter circuit](image)

**Figure 6.** Sallen–Key bandpass filter circuit.

| Fault Code | Fault Type | Nominal | Lower Limit | Upper Limit | Fault Value |
|------------|------------|---------|-------------|-------------|-------------|
| F0         | NF         | -       | -           | -           | -           |
| F1         | $R_1 \downarrow$ | 1 KΩ     | 0.95 KΩ     | 1.05 KΩ     | 0.65 KΩ     |
| F2         | $R_1 \uparrow$ | 1 KΩ     | 0.95 KΩ     | 1.05 KΩ     | 1.35 KΩ     |
| F3         | $R_2 \downarrow$ | 3 KΩ     | 2.85 KΩ     | 3.15 KΩ     | 2.1 KΩ      |
| F4         | $R_2 \uparrow$ | 3 KΩ     | 2.85 KΩ     | 3.15 KΩ     | 3.9 KΩ      |
| F5         | $R_3 \downarrow$ | 2 KΩ     | 1.9 KΩ      | 2.1 KΩ      | 1.4 KΩ      |
| F6         | $R_3 \uparrow$ | 2 KΩ     | 1.9 KΩ      | 2.1 KΩ      | 2.6 KΩ      |
| F7         | $R_4 \downarrow$ | 4 KΩ     | 3.8 KΩ      | 4.2 KΩ      | 2.6 KΩ      |
3.1.2. CSTV Filter Circuit

Figure 7 shows the CSTV filter circuit with a cut-off frequency of 1 kHz. A square wave signal with a frequency of 200 Hz, duty cycle of 50% and amplitude of 5 V is used as the stimulus of the CSTV filter circuit. The nominal values of the components in the CSTV filter circuit are shown in Figure 7. The sensitive components of the CSTV filter circuit are $R_1$, $R_2$, $R_5$, $C_1$, and $C_2$ [29]. Sensitive components, together with $R_3$ and $R_4$, are used for fault setting and they mainly have 15 fault states, which are represented as $R_1\downarrow$, $R_1\uparrow$, $R_2\downarrow$, $R_2\uparrow$, $R_3\downarrow$, $R_3\uparrow$, $R_4\downarrow$, $R_4\uparrow$, $R_5\downarrow$, $R_5\uparrow$, $C_1\downarrow$, $C_1\uparrow$, $C_2\downarrow$, and $C_2\uparrow$, and referred to as F0–F14, with F0 indicating that the circuit is not in fault. Table 2 is the specific fault settings in this instance.

![CSTV filter circuit](image)

Table 2. CSTV filter circuit fault settings.

| Fault Code | Fault Type | Nominal | Lower Limit | Upper Limit | Fault Value |
|------------|------------|---------|-------------|-------------|-------------|
| F0         | NF         | -       | -           | -           | -           |
| F1         | $R_1\downarrow$ | 10 KΩ | 9.5 KΩ | 10.5 KΩ | 7 KΩ |
| F2         | $R_1\uparrow$ | 10 KΩ | 9.5 KΩ | 10.5 KΩ | 13 KΩ |
| F3         | $R_2\downarrow$ | 10 KΩ | 9.5 KΩ | 10.5 KΩ | 7 KΩ |
| F4         | $R_2\uparrow$ | 10 KΩ | 9.5 KΩ | 10.5 KΩ | 13 KΩ |
| F5         | $R_3\downarrow$ | 10 KΩ | 9.5 KΩ | 10.5 KΩ | 6.5 KΩ |
| F6         | $R_3\uparrow$ | 10 KΩ | 9.5 KΩ | 10.5 KΩ | 13.5 KΩ |
| F7         | $R_4\downarrow$ | 10 KΩ | 9.5 KΩ | 10.5 KΩ | 6.5 KΩ |
| F8         | $R_4\uparrow$ | 10 KΩ | 9.5 KΩ | 10.5 KΩ | 13.5 KΩ |
| F9         | $R_5\downarrow$ | 10 KΩ | 9.5 KΩ | 10.5 KΩ | 7 KΩ |
| F10        | $R_5\uparrow$ | 10 KΩ | 9.5 KΩ | 10.5 KΩ | 13 KΩ |
| F11        | $C_1\downarrow$ | 20 nF | 19 nF | 21 nF | 14 nF |
| F12        | $C_1\uparrow$ | 20 nF | 19 nF | 21 nF | 26 nF |
| F13        | $C_2\downarrow$ | 20 nF | 19 nF | 21 nF | 14 nF |
| F14        | $C_2\uparrow$ | 20 nF | 19 nF | 21 nF | 26 nF |

3.1.3. Leap-Frog Low-Pass Filter Circuit

Figure 8 shows the leap-frog low-pass filter circuit with a cut-off frequency of 1.5 KHz. A square wave signal with a frequency of 500 Hz, duty cycle of 50%, and amplitude
of 5 V is used as the stimulus of the leap-frog low-pass filter circuit. The nominal values of components in the leap-frog low-pass filter circuit are shown in Figure 8. The sensitive components of the leap-frog low-pass filter circuit are $R_1$, $R_2$, $R_7$, $R_{12}$, $R_{13}$, $C_1$, and $C_2$. Sensitive components together with $R_9$ and $R_{11}$ are used for fault setting and they mainly have 19 fault states, which are represented as $R_1 \downarrow$, $R_1 \uparrow$, $R_2 \downarrow$, $R_2 \uparrow$, $R_7 \downarrow$, $R_7 \uparrow$, $R_9 \downarrow$, $R_9 \uparrow$, $R_{11} \downarrow$, $R_{11} \uparrow$, $R_{12} \downarrow$, $R_{12} \uparrow$, $R_{13} \downarrow$, $R_{13} \uparrow$, $C_1 \downarrow$, $C_1 \uparrow$, $C_2 \downarrow$, and $C_2 \uparrow$, and referred to as F0–F18, with F0 indicating that the circuit is not in fault. Table 3 is the specific fault settings in this instance.

![Figure 8. Leap-frog low-pass filter circuit.](image)

| Fault Code | Fault Type | Nominal | Lower Limit | Upper Limit | Fault Value |
|------------|------------|---------|-------------|-------------|-------------|
| F0         | NF         | -       | -           | -           | -           |
| F1         | $R_1 \downarrow$ | 10 KΩ   | 9.5 KΩ      | 10.5 KΩ     | 7 KΩ        |
| F2         | $R_1 \uparrow$  | 10 KΩ   | 9.5 KΩ      | 10.5 KΩ     | 13 KΩ       |
| F3         | $R_2 \downarrow$ | 10 KΩ   | 9.5 KΩ      | 10.5 KΩ     | 7 KΩ        |
| F4         | $R_2 \uparrow$  | 10 KΩ   | 9.5 KΩ      | 10.5 KΩ     | 13 KΩ       |
| F5         | $R_7 \downarrow$ | 10 KΩ   | 9.5 KΩ      | 10.5 KΩ     | 7 KΩ        |
| F6         | $R_7 \uparrow$  | 10 KΩ   | 9.5 KΩ      | 10.5 KΩ     | 13 KΩ       |
| F7         | $R_9 \downarrow$ | 10 KΩ   | 9.5 KΩ      | 10.5 KΩ     | 6.5 KΩ      |
| F8         | $R_9 \uparrow$  | 10 KΩ   | 9.5 KΩ      | 10.5 KΩ     | 13.5 KΩ     |
| F9         | $R_{11} \downarrow$ | 10 KΩ   | 9.5 KΩ      | 10.5 KΩ     | 6.5 KΩ      |
| F10        | $R_{11} \uparrow$ | 10 KΩ   | 9.5 KΩ      | 10.5 KΩ     | 13.5 KΩ     |
| F11        | $R_{12} \downarrow$ | 10 KΩ   | 9.5 KΩ      | 10.5 KΩ     | 7 KΩ        |
| F12        | $R_{12} \uparrow$ | 10 KΩ   | 9.5 KΩ      | 10.5 KΩ     | 13 KΩ       |
| F13        | $R_{13} \downarrow$ | 10 KΩ   | 9.5 KΩ      | 10.5 KΩ     | 7 KΩ        |
| F14        | $R_{13} \uparrow$ | 10 KΩ   | 9.5 KΩ      | 10.5 KΩ     | 13 KΩ       |
| F15        | $C_1 \downarrow$ | 10 nF   | 9.5 nF      | 10.5 nF     | 7 nF        |
| F16        | $C_1 \uparrow$  | 10 nF   | 9.5 nF      | 10.5 nF     | 13 nF       |
| F17        | $C_2 \downarrow$ | 20 nF   | 19 nF       | 21 nF       | 14 nF       |
| F18        | $C_2 \uparrow$  | 20 nF   | 19 nF       | 21 nF       | 26 nF       |

3.2. Data Preprocessing

A total of 250 samples are generated by Monte Carlo analysis for the above three CUTs in different faults, and the length of each sample is 1000. According to the ratio of 3:1:1, 250 samples are divided into 150 training samples, 50 verification samples, and 50 test samples to form a training set, a verification set, and a test set for the fault diagnosis.
of the above three CUTs, respectively. When the CUT is fault-free, the responses are shown in Figure 9.

![Figure 9](image)

**Figure 9.** Responses of the CUT. (a) Response of the Sallen–Key bandpass filter circuit; (b) response of the CSTV filter circuit; and (c) response of the leap-frog low-pass filter circuit.

In this paper, the square wave is used as the stimulus of the CUT. Once the frequency of the square wave is determined, the frequency of its odd harmonic components will be determined accordingly. Responses vary from different fault types of the CUT, and therefore have different effects on the odd harmonic components of the response. Based on repeated experiments, when the number of the CUT response components is set to 5, the fault diagnosis efficiency is the highest. Therefore, this article sets the number of components decomposed by EWT to 5. Figure 10 shows the spectrum segmentation result of the fault-free Sallen–Key bandpass filter circuit response obtained by the improved EWT and the traditional EWT.
Figure 10. Spectrum segmentation result of the fault-free Sallen–Key bandpass filter circuit response: (a) spectrum segmentation based on the improved EWT; and (b) spectrum segmentation based on the traditional EWT.

The stimulus of the Sallen–Key bandpass filter circuit is a square wave of 10 kHz. The main purpose of the EWT method is to accurately extract the components with 10 kHz and odd times of 10 kHz as the centre frequency to better analyse the influence of different types of faults on circuit performance. As shown in Figure 10b, the traditional EWT can extract the first four components with the highest amplitude in the spectrum of response signals of the Sallen–Key low-pass filter circuit. The centre frequency of the second component is 10 kHz, and the centre frequency of the fourth component is 30 kHz. However, the fifth component confused the 50 kHz and 70 kHz components with the residual component, and the first and third components were invalid components. As shown in Figure 10a, for the five components obtained by the improved EWT proposed in this paper, the centre frequencies of the first four component signals are 10 kHz, 30 kHz, 50 kHz, and 70 kHz, and the fifth component is the residual component. It can be concluded that the improved EWT can accurately extract the component signal centred at an odd multiple of
the stimulus frequency, which is helpful for improving the efficiency of fault diagnosis. Figure 11 shows the AM-FM components of the responses obtained by the improved EWT when there is no fault in the CUT.

![Image](48x582 to 204x700)

![Image](228x582 to 377x700)

![Image](395x582 to 553x700)

**Figure 11.** AM-FM components of the responses of fault-free CUT based on improved EWT: (a) components of the Sallen–Key bandpass filter circuit response; (b) components of the CSTV filter circuit response; and (c) components of the leap-frog low-pass filter circuit response.

### 3.3. Multi-Input ResNet Model Training

The structure of the multi-input ResNet model proposed in this paper is introduced in Section 3. In the experiment, the detailed information of the convolution block, residual block, and average pooling layer of a single ResNet unit, as well as the subsequent concat layer and FC layer, are shown in Table 4, where \( N_{\text{class}} \) denotes the fault number of a CUT. The number of ResNet units is consistent with the number of components of the CUT response signal; therefore, the multi-input ResNet in the experiment possesses 5 ResNet units with the same structure. The training epoch of the multi-input ResNet is set to 100, and the training loss and validation loss are calculated by the cross-entropy loss function. The Adam optimiser is selected for ResNet parameter optimisation, and the learning rate of the optimiser is set to \( 1 \times 10^{-2} \).

| Unit & Layer       | Convolution Size | Stride | BN | Activation Function |
|-------------------|------------------|--------|----|---------------------|
| convolution block | 5 * 16           | 2      | Y  | Relu                |
| average pooling layer | 3              | 2      | N  | N                   |
| ResNet block1     | 3 * 16           | 2      | Y  | Relu                |
|                   | 3 * 16           | 1      | Y  | Relu                |
| ResNet block2     | 3 * 32           | 2      | Y  | Relu                |
|                   | 3 * 32           | 1      | Y  | Relu                |
| ResNet block3     | 3 * 64           | 2      | Y  | Relu                |
|                   | 3 * 64           | 1      | Y  | Relu                |
| ResNet block4     | 3 * 128          | 2      | Y  | Relu                |
|                   | 3 * 128          | 1      | Y  | Relu                |
| average pooling layer | 7              | 1      | N  | N                   |
| Concat layer      | 128 * 5          | -      | -  | -                   |
| FC layer          | \( N_{\text{class}} \) | -      | -  | Softmax             |

The training set is used to train the multi-input ResNet. For each epoch of training, the generalisation ability and classification ability of the multi-input ResNet model trained in this epoch are tested by the validation set. The training and verification losses are calculated by the cross-entropy loss function. If the multi-input ResNet model trained by the training set in epoch \( p \) is \( ResNet_{-Model, p = 1 \cdots 100} \) and the classification ability and
generalisation ability of $\text{ResNet} - \text{Model}_p$ are verified by the verification set, then the verification accuracy of the model in epoch $p$ is $\text{Vacc}_p$. After 100 epochs of training and verification, we obtain $\text{Model} = [\text{ResNet} - \text{Model}_1, \cdots, \text{ResNet} - \text{Model}_p, \cdots, \text{ResNet} - \text{Model}_{100}]$ and $\text{Vacc} = [\text{Vacc}_1, \cdots, \text{Vacc}_p, \cdots, \text{Vacc}_{100}]$. The curves of training loss, training accuracy, verification accuracy, and verification loss in the training process of the multi-input ResNet model based on CSTV filter circuit response signal $s$ are shown in Figure 12. When the training loss converges, the multi-input ResNet model corresponding to the highest verification accuracy has the best generalisation ability and classification ability, and the diagnosis effect is the best. If the index of the maximum value of $\text{Vacc}$ is $\text{ind}$, the best multi-input ResNet model through 100 epochs of training is $\text{Model}_{\text{optima}} = \text{Model}[\text{ind}]$.

![Figure 12. Multi-input ResNet model training process.](image)

Based on the t-SNE, the original response signal of the CSTV filter circuit and the features extracted by $\text{Model}_{\text{optima}}$ are dimensionalised separately. The visualisation results after dimensionality reduction are shown in Figure 13. From Figure 13a, it is obvious that the discrimination of the original response signals of the CSTV filter circuit is poor and multiple fault types merge with each other and thus are difficult to distinguish. It can be seen from Figure 13b that after feature extraction with $\text{Model}_{\text{optima}}$, features of the same fault type present good aggregation, while those of different fault types present large spacing and good differentiation. These findings verify the effectiveness of the multi-input ResNet's fault feature extraction.
3.4. Experimental Results and Analysis

The test sets of the three CUTs are input to the optimal multi-input ResNet model corresponding to the CUTs to obtain the confusion matrix shown in Figure 14. Figure 14a shows that the various faults of the Sallen–Key bandpass filter circuit can be distinguished well and there is no confusion or difficulty in distinguishing between different fault types. Figure 14c shows that the confusion between F4 and F10 is serious. Two samples with fault code F9 were diagnosed as the fault with fault code F3, a sample with fault code F13 was diagnosed as the fault with fault code F17, two samples with fault code F14 were diagnosed as the fault with fault code F12 and F18, respectively, and a sample with fault code F18 was diagnosed as the fault with fault code F6. In conclusion, the fault diagnosis result of the leap-frog filter circuit is worse than the other two circuits, which also reflects that the more complex the circuit is, the more confusion is generated between different faults and the fault diagnosis is more difficult.

Figure 13. Dimensionality reduction visualisation of CSTV filter circuit responses based on t-SNE. (a) Visualisation of the original response signal; and (b) visualisation of features extracted by Model\textsubscript{optima}.
Figure 14. Confusion matrix. (a) Confusion matrix of the Sallen–Key bandpass filter circuit response; (b) confusion matrix of the CSTV filter circuit response; and (c) confusion matrix of the leap-frog low-pass filter circuit response.

To test the effectiveness of the analog circuit fault diagnosis method proposed in this paper, the proposed method is compared with the results in [8,10,13]. The fault diagnosis results of these three CUTs are shown in Tables 5–7. A sinusoidal sweep signal was used as the stimulus of the CUT in [8], and a single pulse signal was used as the excitation signal of the CUT in [10,13]. Without preprocessing the CUT response signal, the extracted features are directly input into the neural network to complete the fault diagnosis in [8]. EEMD was used to preprocess signals, and then the extracted features were input into ELM to complete fault diagnosis in [10]. WT is used to preprocess signals, and the preprocessed data are input into the convolutional neural network model to complete feature extraction and fault diagnosis in [13].
Table 5. Fault diagnosis result of the Sallen–Key bandpass filter circuit.

| Fault Code | Proposed Method | Traditional EWT + Multi-Input ResNet | [8]  | [10]  | [13]  |
|------------|----------------|-------------------------------------|------|-------|-------|
| F0         | 100.00         | 100.00                              | 34.00| 68.00 | 98.00 |
| F1         | 100.00         | 98.00                               | 100.00| 98.00 | 100.00|
| F2         | 100.00         | 98.00                               | 94.00| 100.00| 100.00|
| F3         | 100.00         | 96.00                               | 70.00| 98.00 | 100.00|
| F4         | 100.00         | 98.00                               | 64.00| 96.00 | 98.00 |
| F5         | 100.00         | 100.00                              | 100.00| 92.00 | 98.00 |
| F6         | 100.00         | 100.00                              | 100.00| 100.00| 100.00|
| F7         | 100.00         | 100.00                              | 100.00| 100.00| 100.00|
| F8         | 100.00         | 100.00                              | 74.00| 100.00| 100.00|
| F9         | 100.00         | 94.00                               | 86.00| 94.00 | 100.00|
| F10        | 100.00         | 100.00                              | 100.00| 100.00| 100.00|
| F11        | 100.00         | 92.00                               | 34.00| 100.00| 100.00|
| F12        | 100.00         | 98.00                               | 100.00| 94.00 | 100.00|
| Average accuracy | 100.00 | 98.00                              | 81.23| 95.38 | 99.54 |

In Table 5, the average diagnostic accuracy of [8] is the lowest. In Table 6, the average diagnostic accuracy of [8] is slightly higher than that of [10]. In Table 7, the average diagnostic accuracy of [8] is only higher than that of [10]. It is clear that preprocessing the CUT response signal helps to improve the accuracy of fault diagnosis. In Tables 5 and 7, the average diagnostic accuracy of [8,10] is lower than that of [13], the proposed method and the traditional EWT combined with multi-input ResNet. Only in Table 6 is the average diagnostic accuracy of [8,10] slightly higher than that of [13]. Therefore, automatic feature extraction based on an ANN can more adequately extract the feature information of the fault than the traditional feature extraction method.

In Tables 4–6, the average diagnosis accuracy of the analog circuit fault diagnosis method proposed in this paper is higher than that of the method in [8,10,13] and the method based on the combination of traditional EWT and multi-input ResNet. In summary, the proposed method in this paper can effectively obtain the response signal characteristics of analog circuits under different faults, and it performs well in analog circuit fault diagnosis.

Table 6. Fault diagnosis result of the CSTV filter circuit.

| Fault Code | Proposed Method | Traditional EWT + Multi-Input ResNet | [8]  | [10]  | [13]  |
|------------|----------------|-------------------------------------|------|-------|-------|
| F0         | 100.00         | 100.00                              | 100.00| 100.00| 100.00|
| F1         | 100.00         | 100.00                              | 100.00| 100.00| 100.00|
| F2         | 100.00         | 100.00                              | 100.00| 100.00| 100.00|
| F3         | 100.00         | 100.00                              | 100.00| 100.00| 100.00|
| F4         | 100.00         | 92.00                               | 100.00| 96.00 | 100.00|
| F5         | 98.00          | 100.00                              | 82.00| 96.00 | 98.00 |
| F6         | 100.00         | 96.00                               | 98.00| 98.00 | 82.00 |
| F7         | 100.00         | 100.00                              | 100.00| 100.00| 98.00 |
| F8         | 100.00         | 100.00                              | 100.00| 100.00| 98.00 |
| F9         | 100.00         | 100.00                              | 100.00| 100.00| 100.00|
| F10        | 100.00         | 100.00                              | 100.00| 100.00| 100.00|
Table 7. Fault diagnosis result of the leap-frog low-pass filter circuit.

| Fault Code | Proposed Method | Traditional EWT + Multi-Input ResNet | [8] | [10] | [13] |
|------------|-----------------|--------------------------------------|-----|-----|-----|
| F0         | 100.00          | 96.00                                | 96.00 | 96.00 | 100.00 |
| F1         | 100.00          | 100.00                               | 92.00 | 100.00 | 100.00 |
| F2         | 100.00          | 100.00                               | 94.00 | 80.00 | 100.00 |
| F3         | 100.00          | 92.00                                | 70.00 | 64.00 | 100.00 |
| F4         | 90.00           | 76.00                                | 68.00 | 72.00 | 100.00 |
| F5         | 100.00          | 98.00                                | 98.00 | 94.00 | 100.00 |
| F6         | 100.00          | 96.00                                | 86.00 | 94.00 | 100.00 |
| F7         | 100.00          | 100.00                               | 96.00 | 98.00 | 100.00 |
| F8         | 100.00          | 100.00                               | 100.00 | 94.00 | 100.00 |
| F9         | 96.00           | 92.00                                | 70.00 | 58.00 | 100.00 |
| F10        | 86.00           | 74.00                                | 64.00 | 70.00 | 78.00 |
| F11        | 100.00          | 100.00                               | 100.00 | 96.00 | 100.00 |
| F12        | 100.00          | 98.00                                | 100.00 | 84.00 | 100.00 |
| F13        | 98.00           | 98.00                                | 100.00 | 96.00 | 100.00 |
| F14        | 96.00           | 96.00                                | 96.00 | 92.00 | 100.00 |
| F15        | 100.00          | 100.00                               | 100.00 | 100.00 | 100.00 |
| F16        | 100.00          | 98.00                                | 94.00 | 90.00 | 96.00 |
| F17        | 100.00          | 100.00                               | 100.00 | 74.00 | 100.00 |
| F18        | 98.00           | 100.00                               | 100.00 | 100.00 | 100.00 |
| Average accuracy | 98.11 | 95.47 | 90.74 | 86.95 | 97.58 |

4. Conclusions

In this paper, a square wave is used as the excitation signal of the circuit under test, which provides another choice for the excitation signal in analog circuit fault diagnosis. EWT is improved based on the stimulus frequency to effectively extract each component of the response signal, thus providing a possibility for more accurate analysis of the influence of different CUT faults on the components. In addition, multi-input ResNet is applied to fault diagnosis of analog circuits, which is used for fault feature extraction and fault classification. This approach applies an ANN for fault diagnosis of analog circuits, and three analog circuits are used to verify the effectiveness of the proposed method.

Author Contributions: Conceptualization, Z.L. and X.L.; methodology, X.L.; software, X.L. and S.X.; validation, X.L., S.X. and J.W.; formal analysis, X.L., J.W. and X.Z.; investigation, X.L., J.W. and X.Z.; data curation, X.L.; writing—original draft preparation, X.L.; writing—review and editing, Z.L. and X.L.; supervision, Z.L.; project administration, Z.L.; and funding acquisition, Z.L. All authors have read and agreed to the published version of the manuscript.

Funding: This work was supported by the National Natural Science Foundation of China under Grants No. U1830133 (NSFC) and Project of Sichuan Youth Science and Technology Innovation Team, China (Grant No. 2020JDSTD0008).

Institutional Review Board Statement: Not applicable.
Informed Consent Statement: Not applicable.
Data Availability Statement: Not applicable.
Conflicts of Interest: The authors declare no conflict of interest.

References

1. Binu, D.; Karivyappa, B.S. A survey on fault diagnosis of analog circuits: Taxonomy and state of the art. AEU-Int. J. Electron. Commun. 2017, 73, 68–83.
2. Bandler, J.W.; Salama, A.E. Fault diagnosis of analog circuits. Proc. IEEE 1985, 73, 1279–1325.
3. Aminian, M.; Aminian, F. Neural-network based analog-circuit fault diagnosis using wavelet transform as preprocessor. IEEE Trans. Circuits Syst. II-Analog Digit. Signal Process. 2000, 47, 151–156.
4. Aminian, F.; Aminian, M. Fault diagnosis of analog circuits using Bayesian neural networks with wavelet transform as preprocessor. J. Electron. Test. -Theory Appl. 2001, 17, 29–36.
5. Aminian, M.; Aminian, F. A Modular Fault-Diagnostic System for Analog Electronic Circuits Using Neural Networks With Wavelet Transform as a Preprocessor. IEEE Trans. Instrum. Meas. 2007, 56, 1546–1554.
6. Tan, Y.H.; He, Y.G. A novel method for fault diagnosis of analog circuits based on WP and GPNN. Int. J. Electron. 2008, 95, 431–439.
7. Cui, J.A.; Wang, Y.R. A Novel Approach of Analog Fault Classification Using a Support Vector Machines Classifier. Metrol. Meas. Syst. 2010, 17, 561–581.
8. Yuan, L.F.; He, Y.G.; Huang, J.Y.; Sun, Y.C. A New Neural-Network-Based Fault Diagnosis Approach for Analog Circuits by Using Kurtosis and Entropy as a Preprocessor. IEEE Trans. Instrum. Meas. 2010, 59, 586–595.
9. Long, B.; Li, M.; Wang, H.; Tian, S. Diagnostics of Analog Circuits Based on LS-SVM Using Time-Domain Features. Circuits Syst. Signal Process. 2013, 32, 2683–2706.
10. Xiong, J.; Tian, S.; Yang, C. Fault Diagnosis for Analog Circuits by Using EEMD, Relative Entropy, and ELM. Comput. Intell. Neurosci. 2016, 2016, 7657054.
11. He, W.; He, Y.G.; Li, B. Generative Adversarial Networks With Comprehensive Wavelet Feature for Fault Diagnosis of Analog Circuits. IEEE Trans. Instrum. Meas. 2020, 69, 6640–6650.
12. Ji, L.P.; Fu, C.Q.; Sun, W.Q. Soft Fault Diagnosis of Analog Circuits Based on a ResNet With Circuit Spectrum Map. IEEE Trans. Circuits Syst. I Regul. Pap. 2021, 68, 2841–2849.
13. Yang, Y.Y.; Wang, L.D.; Nie, X.B.; Wang, Y. Incipient fault diagnosis of analog circuits based on wavelet transform and improved deep convolutional neural network. IIEE Electron. Express 2021, 18, 20210174.
14. Wu, Z.; Huang, N.E. Ensemble Empirical Mode Decomposition: A Noise-Assisted Data Analysis Method. Adv. Data Sci. Adapt. Anal. 2011, 1, 1–41.
15. Gilles, J. Empirical Wavelet Transform. IEEE Trans. Signal Process. 2013, 61, 3999–4010.
16. Deng, W.; Zhang, S.; Zhao, H.; Yang, X. A Novel Fault Diagnosis Method Based on Integrating Empirical Wavelet Transform and Fuzzy Entropy for Motor Bearing. IEEE Access 2018, 6, 35042–35056.
17. Li, H.; Hu, Y.; Li, F.; Meng, G. Succinct and fast empirical mode decomposition. Mech. Syst. Signal Proc. 2017, 85, 879–895.
18. Moltsen, A.K.A.; El-Yazeed, M.F.A. Selection of Input Stimulus for Fault Diagnosis of Analog Circuits Using ARMA Model. AEU-Int. J. Electron. Commun. 2004, 58, 212–217.
19. Liu, Z.; Jia, Z.; Vong, C.-M.; Bu, S.; Han, J.; Tang, X. Capturing High-Discriminative Fault Features for Electronics-Rich Analog System via Deep Learning. IEEE Trans. Ind. Inform. 2017, 13, 1213–1226.
20. Jia, Z.; Liu, Z.B.; Gan, Y.F.; Vong, C.M.; Pecht, M. A Deep Forest-Based Fault Diagnosis Scheme for Electronics-Rich Analog Circuit Systems. IEEE Trans. Ind. Electron. 2021, 68, 10087–10096.
21. Sai Sarathi Vasan, A.; Long, B.; Pecht, M. Diagnostics and Prognostics Method for Analog Electronic Circuits. IEEE Trans. Ind. Electron. 2013, 60, 5277–5291.
22. Slamani, M.; Kaminska, B. Multifrequency Analysis of Faults in Analog Circuits. IEEE Des. Test Comput. 1995, 12, 70–80.
23. Rao, S.; Sundari, K.S. Optimized multi frequency approach to analog fault diagnosis using Monte Carlo analysis. Electr. Electron. Eng. 2014, 4, 25–30.
24. He, K.; Zhang, X.; Ren, S.; Sun, J. Deep residual learning for image recognition. In Proceedings of the IEEE Conference on Computer Vision and Pattern Recognition, Las Vegas, NV, USA, 27–30 June 2016; pp. 770–778.
25. Zhang, C.; He, Y.; Yuan, L.; Xiang, S. Analog Circuit Incipient Fault Diagnosis Method Using DBN Based Features Extraction. IEEE Access 2018, 6, 23053–23064.
26. Gao, T.Y.; Yang, J.L.; Jiang, S.D. A Novel Incipient Fault Diagnosis Method for Analog Circuits Based on GMKL-SVM and Wavelet Fusion Features. IEEE Trans. Instrum. Meas. 2021, 70, 1–15.
27. Chen, P.; Yuan, L.F.; He, Y.G.; Luo, S. An improved SVM classifier based on double chains quantum genetic algorithm and its application in analogue circuit diagnosis. Neurocomputing 2016, 211, 202–211.
28. Gao, T.Y.; Yang, J.L.; Jiang, S.D. A novel fault diagnosis method for analog circuits with noise immunity and generalization ability. *Neural Comput. Appl.* 2021, 33, 10537–10550.

29. Shi, Y.; Deng, Y.; Zhang, W. Diagnosis of Incipient Faults in Weak Nonlinear Analog Circuits. *Circuits Syst. Signal Process.* 2013, 32, 2151–2170.