A GPU accelerated aggregation algebraic multigrid method

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ABSTRACT

We present an efficient, robust and fully GPU-accelerated aggregation-based algebraic multigrid preconditioning technique for the solution of large sparse linear systems. These linear systems arise from the discretization of elliptic PDEs. The method involves two stages, setup and solve. In the setup stage, hierarchical coarse grids are constructed through aggregation of the fine grid nodes. These aggregations are obtained using a set of maximal independent nodes from the fine grid nodes. We use a “fine-grain” parallel algorithm for finding a maximal independent set from a graph of strong negative connections. The aggregations are combined with a piece-wise constant (unsmooth) interpolation from the coarse grid solution to the fine grid solution, ensuring low setup and interpolation cost. The grid independent convergence is achieved by using recursive Krylov iterations (K-cycles) in the solve stage. An efficient combination of K-cycles and standard multigrid V-cycles is used as a preconditioner for Krylov iterative solvers such as generalized minimal residual and conjugate gradient. We compare the solver performance with other solvers based on smooth aggregation and classical algebraic multigrid methods.

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1. Introduction

In the numerical simulation of various physical phenomena, discretizations of partial differential equations result in very large sparse linear system of equations. Computing solutions of these linear systems is a crucial component of the overall simulation. Consequently there has been increasing interest in developing fast linear solvers over the last two decades. Traditionally, these linear solvers are accelerated by using parallel programming techniques on clusters of CPUs. However, hardware architectures such as the one found in the graphics processing unit (GPU) provide efficient, inexpensive alternatives for these computations. The recent developments in general purpose GPUs (GPGPUs) for scientific computations make it feasible to accelerate the numerical simulations by an order of magnitude. This can be achieved by significantly modifying the traditional algorithms to suit the massively parallel hardware architecture of GPUs.

Multigrid methods are among the most efficient and popular solution techniques for solving the linear systems arising from the discretization of elliptic PDEs. Multigrid methods typically fall into one of two classes: geometric multigrid and algebraic multigrid (AMG). Geometric multigrid methods require prior knowledge of the underlying discretization and grid hierarchy, whereas algebraic multigrid methods only require the entries of matrix. Furthermore, the algebraic multigrid coarsening process automatically ensures a reduction of the error components that are not reduced by smoothing [1]. Algebraic multigrid requires little or no tuning for various applications and hence make it very easy to use in the form of an efficient “black-box” solver.

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Algebraic multigrid methods involve the construction of a hierarchy of matrices (or grids) using the entries of the matrix. At each level in the hierarchy, smoothing is performed to remove the high frequency errors. The low frequency errors are removed by recursively restricting the residual to coarse grids and performing smoothing on the coarse grids. The coarse grid solutions are then interpolated from the coarse grid to a fine grid using an interpolation operator.

Classical algebraic multigrid methods obtain the hierarchical grids by partitioning the nodes into coarse and fine grid nodes. The coarse grid nodes form a coarse level, and an interpolation operator is defined via a weighted sum of the coarse grid nodes. The restriction operator in general is the transpose of the interpolation operator [2].

In contrast, aggregation algebraic multigrid methods obtain the hierarchical grids by aggregating few fine grid nodes to form a coarse grid node (see Fig. 1). An interpolation is defined via piecewise constant interpolation from coarse grid node to a fine grid node. This results in very sparse interpolation and restriction operations compared to classical AMG. The interpolation matrix has exactly one nonzero entry per row, which reduces memory requirements and improves efficiency of the interpolation operation. However, these aggregation schemes are not popular since they do not provide grid independent convergence. The convergence is often improved by smooth interpolation (“smooth aggregation”) [3], but smooth aggregation typically produces a hierarchy with very dense coarse level matrices (see Fig. 2), leading to expensive matrix–vector product computations and increasing the memory requirements. Furthermore, unlike classical algebraic multigrid, smooth aggregation multigrid is not robust for various applications.

The opposite viewpoint to smooth aggregation, presented in [4] uses unsmooth aggregation along with K-cycles as preconditioning for iterative Krylov methods, provide an efficient alternative to smooth aggregation schemes. In this work, we accelerate these unsmooth aggregation methods using GPUs and discuss the algorithmic changes considered for the acceleration, both mathematical and computational. We also discuss the advantages in using these schemes where we can efficiently reuse the sparsity structure of the hierarchical matrices.

Some of the initial works for aggregation AMG focused on accelerating only the solve phase [5]. As a consequence, the setup phase becomes a bottleneck due to the serial nature of algorithms [4] for forming the aggregates. There has been development in accelerating the setup phase for classical AMG [6] and for smooth aggregation [7]. We build on these ideas...
and develop fully accelerated unsmooth aggregation AMG as a part of GAMPack, a GPU accelerated algebraic multigrid package.

For forming the aggregates on GPUs, we follow a fine-grain parallel maximal independent set (MIS) algorithm proposed in [7] with some modifications described in Algorithm 2. With this algorithm on a GPU, we see that the setup is almost three times faster than classical AMG setup.

In Section 2, we describe the algorithms for the setup and solution phase of unsmooth aggregation AMG. In Section 3, we briefly explain GPU acceleration of the algorithms described. In Section 4, we provide numerical results for various classes of problems and compare the performance with GAMPack classical AMG and CUSP smooth aggregation.

2. Aggregation AMG

Aggregation AMG is used as a preconditioner for iterative Krylov solvers such as generalized minimal residual (GMRES) and conjugate gradient (CG). Consider a linear system of equations,

\[ Ax = b, \]

where \( A \in \mathbb{R}^{N \times N} \) is a coefficient matrix, \( b \in \mathbb{R}^{N \times 1} \) is a right hand side vector, \( x \in \mathbb{R}^{N \times 1} \) is the solution vector and \( N \) is the number of unknowns. The coarse grid matrices are defined recursively, consider that \( A_0 = A \) and \( n_{k+1} < n_k \), \( n_0 = N \). The coarse grid operators \( (A_{k+1}) \) are defined by,

\[ A_{k+1} = R_k A_k P_k, \quad k = 0, 1, \ldots, L \]

where \( P_k \in \mathbb{R}^{n_k \times n_{k+1}} \) is an interpolation operator, \( R_k (=P_k^T) \) is a restriction operator, \( A_k \in \mathbb{R}^{n_k \times n_k} \) is a fine grid coefficient matrix, while \( A_{k+1} \in \mathbb{R}^{n_{k+1} \times n_{k+1}} \) is a coarse grid coefficient matrix. The coarse grid operators are obtained until the size of the coarse grid operator \( (n_k) \) is sufficiently small, to obtain the exact solution using a direct solver in a reasonable time.

Aggregation algebraic multigrid uses the prior knowledge of null space or near null space of the linear operator that corresponds to low–energy error. In our work, we consider only one near null space vector of the linear operator and propagate the null space to hierarchical operators during the setup stage. For level \( k \), the near null space vector of matrix \( A_k \) is denoted by \( B_k \).

2.1. Setup phase

Due to simple structure of interpolation matrix, unsmooth aggregation schemes have the advantage of low setup and interpolation costs compared to that of smooth aggregation schemes or classical algebraic multigrid schemes. The algorithm for the setup is outlined in Algorithm 1.

Algorithm 1 Aggregation AMG setup

1. **Input**: \( A_0, B_0 \)
2. **Output**: \( A_k, B_k, P_k, R_k \) for \( k = 1, 2, \ldots, L \)
3. **for** \( k = 0, 1, 2, \ldots \) until \( n_k \) is small **do**
4. \( C_k \leftarrow \text{strength}(A_k) \)
5. \( \text{Agg}_k \leftarrow \text{aggregate}(C_k) \)
6. \( P_k, B_{k+1} \leftarrow \text{prolongate}(\text{Agg}_k, B_k) \)
7. \( R_k \leftarrow P_k^T \)
8. \( A_{k+1} \leftarrow R_k A_k P_k \)
9. **end for**

2.1.1. Strong connections

The aggregation scheme groups a set of fine nodes that are strongly negatively coupled with each other into a coarse node, called an aggregate. In general, the strength of the connection between two nodes is defined based on the matrix coefficient corresponding to the connection. Two of the popular strength of measures that are shown to be robust are symmetric strength [3] and classic strength [1]. We observe that classic strength ensures a consistent ratio of the number of aggregates to the fine grid nodes for a large class of problems. A graph of strong connections \( (C) \) is constructed based on classic strength of connection given by,

\[ C_{ij} = \begin{cases} 1 & \text{if } i = j \text{ or } -s_i A_{ij} > \alpha \max_{-s_j A_{ij} > 0} -s_j A_{ij}, \quad s_i = \text{sign}(A_{ii}) \\ 0 & \text{otherwise}. \end{cases} \]

(3)

Here \( \alpha \) is a threshold parameter for strong and weak connections. The coarsening ratio can be tuned by adjusting \( \alpha \). We choose \( \alpha = 0.5 \) as default for systems arising from 3D problems, and \( \alpha = 0.25 \) for systems arising from 2D problems. This selection of \( \alpha \) ensures a good ratio of fine grid nodes to coarse grid nodes for a large class of problems that we considered in this paper.
Note that only strong negative connections are considered for the aggregation. This is because the algebraically smooth error changes slowly along the direction of strong negative connections, while oscillating along the strong positive connections. A detailed analysis of the convergence can be found in Stüben [8].

2.1.2. Construction of aggregates

The pairwise aggregation scheme proposed in [9] is simple and efficient on CPUs, but is not readily parallelizable on GPUs. In addition, this scheme requires two passes of pairwise aggregation to obtain a sufficient reduction in the number of unknowns for the coarse grid, increasing the setup cost. The aggregation algorithm based on maximal independent set, proposed in [7] is highly parallelizable on GPUs and constructs the aggregates faster compared to pair-wise algorithms. In the aggregation scheme, a set of nodes that are maximally independent are selected as root nodes, and aggregates are formed by grouping each of these nodes with their neighbors.

An independent set is a set of nodes, in which no two of them are adjacent and it is maximal independent set (MIS) if it is not a subset of any other independent set. The generalization of MIS is MIS(k), in which the distance between any two independent nodes is greater than k and for every other node there is at least one independent node that is within distance less than or equal to k. For the construction of aggregates, we use MIS(2) nodes as root nodes. For more aggressive coarsening, MIS(k), k > 2 can be used, but these may result in poor interpolation leading to slower convergence.

Algorithm 2 Parallel Maximal Independent Set (2)

1: Input: C, N = N sparse matrix;
2: Output: s, set of MIS(2) nodes
3: s ← {0, 0, 0, . . . , 0} {initialize the state as undecided}
4: r ← random {generate random numbers ∈ (0, 1)}
5: for all i ∈ l do
6:   vi = # {j ∈ Cij = 1} + ri {number of strong influencing connections}
7: while {i ∈ l : si = 0} ̸= ∅ do
8:   for all i ∈ l do
9:     Ti ← (si, vi, i) {initialize tuples}
10:  for d = 1, 2 do
11:     for all i ∈ l do {for each node in parallel}
12:       t ← Ti
13:       for j ∈ Ni do {compare with the strong neighbors}
14:         t ← max(t, Tj)
15:       Ĥi ← t
16:     T = Ĥ
17:     for i ∈ l do {for each node in parallel}
18:       (smax, vmax, tmax) ← T
19:       if si = 0 then {if undecided}
20:         if tmax = 1 then {if maximal}
21:           s1 ← 1 {mark as MIS}
22:         else {else.}
23:           if smax = 1 then {mark as non MIS}
24:             s1 ← −1

In order to aggregate only strongly connected nodes the algorithm utilizes the graph of matrix C instead of A. The parallel MIS Algorithm 2 is similar to the proposed algorithm in [7], additionally it incorporates the number of strongly influencing connections for robustness [4]. Node j is a strongly influencing node of i if Aji is a strong connection, in other words Cji = 1. After the MIS(2) nodes are obtained, every other node i, is aggregated with its nearest MIS node.

2.1.3. Interpolation

The interpolation operator for projecting errors from a coarse grid to fine grid, is defined from the aggregations and the near null space vectors (Bk) of the linear operator (Ak). In this paper we consider only one near null space vector for constructing the interpolation operator. This results in exactly one non zero entry per row in the interpolation matrix and is given by,

\[ B_k = P_k B_{k+1}, \quad P_k^T P_k = I. \]  \hspace{1cm} (4)

Eqs. (4) ensure the near null space vector (Bk) to be in the range space of (Pk) and the interpolation matrix is orthonormal. This is done by copying the entries of Bk to the sparsity pattern of the interpolation matrix and by normalizing the columns. The restriction operator carries the residual from the fine grid to the next coarse grid in the hierarchy. A parallel algorithm for computing the interpolation operator is presented in Algorithm 3.
2.2. Solve phase

We use a multigrid cycle as a preconditioner in a Krylov iteration which is based on either CG or GMRES. Both of these methods can be used for symmetric matrices while only GMRES can be used for non-symmetric matrices.

For unsmooth aggregation, standard V-cycle multigrid given in Algorithm 4 does not provide grid independent convergence. In contrast, the K-cycle Algorithm 5, presented in [4], addresses this issue by recursively applying Krylov iterations on the coarse levels.

Algorithm 4 V-cycle AMG

1. $x_k \leftarrow Vcycle(k, b_k, x_k)$
2. Input : level $k$, rhs $b_k$ and initial guess $x_k$
3. Output : updated solution $x_k$
4. $x_k \leftarrow S_k(b_k, A_k, x_k)$
5. $r_k \leftarrow b_k - A_kx_k$
6. $r_{k+1} \leftarrow r_k r_k$
7. if $k + 1 = l$ then
8. $x_{k+1} \leftarrow A_{k+1}^{-1} r_{k+1}$
9. else
10. $x_{k+1} \leftarrow Vcycle(k + 1, r_{k+1}, 0)$
11. end if
12. $x_k \leftarrow x_k + P_k x_{k+1}$
13. $x_k \leftarrow S_k(b_k, A_k, x_k)$

With K-cycles, the number of coarse grid corrections can grow exponentially, leading to a large number of iterations on the coarser grids. Computing these coarse grid corrections on GPUs is inefficient because of the smaller number of nodes. Interestingly, the experimental results suggest that it is sufficient to use K-cycles for only two levels at the top and V-cycles for the remaining levels to achieve grid independence convergence. This improves the overall runtime performance of the aggregation AMG on GPUs even though the number of iterations increases slightly.

2.2.1. Smoothing

In the above algorithms describing V- and K-cycles, $S_k$ represents a smoothing operator that removes high frequency errors corresponding to the matrix $A_k$. We use one pre- and one post-smoothing step for each level. We consider Jacobi, damped Jacobi and symmetric Gauss–Seidel smoothing, all of which are symmetric. Jacobi and damped Jacobi are easily parallelizable on CPUs and GPUs. However, the Gauss–Seidel algorithm is completely serial in nature and cannot be ported to GPUs efficiently. A multi-color variant of symmetric Gauss–Seidel algorithm has shown to scale well on GPUs for large matrices with fewer numbers of colors. This modified algorithm adds a setup cost of the graph coloring and a reordering of the matrix, and also results in slow down of convergence. Numerical results indicate that damped Jacobi is both scalable and efficient, and requires low setup cost compared to multi-color Gauss–Seidel. In addition, damped Jacobi produces solutions that are independent of number of GPUs/processors unlike multi-color Gauss–Seidel.

Damped Jacobi iteration is given by,

$$x \leftarrow x + \omega D^{-1}(b - Ax), \quad \omega = \frac{4}{3 \rho(D^{-1}A)},$$

where $\rho(D^{-1}A)$ is the spectral radius of the matrix $D^{-1}A$ and it is estimated from the eigenvalues of the Hessenberg matrix obtained using $m$ (≤5) Arnoldi iterations.
Algorithm 5 K-cycle AMG

1: $x_k \leftarrow \text{Kcycle}(k, b_k, x_k)$
2: **Input**: level $k$, rhs $b_k$ and initial guess $x_k$
3: **Output**: updated solution $x_k$
4: $x_k \leftarrow S_k(b_k, A_k, x_k)$  \hspace{1cm} \{ pre-smoothing \}
5: $r_k \leftarrow b_k - A_k x_k$  \hspace{1cm} \{ compute residual \}
6: $r_{k+1} \leftarrow R_k r_k$  \hspace{1cm} \{ restrict the residual to coarse-grid \}
7:
8: **if** $k + 1 = L$ **then**
9: \hspace{1cm} $x_{k+1} \leftarrow A_{k+1}^{-1} r_{k+1}$ \hspace{1cm} \{ exact solution of coarse-grid \}
10: **else**
11: \hspace{1cm} $c_{k+1} \leftarrow \text{Kcycle}(k + 1, r_{k+1}, x_{k+1})$
12: \hspace{1cm} $v_{k+1} \leftarrow A_{k+1} c_{k+1}$
13: \hspace{1cm} $\rho_1 \leftarrow c_{k+1}^T v_{k+1}$, \hspace{0.5cm} $\alpha_1 \leftarrow c_{k+1}^T r_{k+1}$ \hspace{0.5cm} if CG
14: \hspace{1cm} $\rho_1 \leftarrow \|v_{k+1}\|^2$, \hspace{0.5cm} $\alpha_1 \leftarrow v_{k+1}^T r_{k+1}$ \hspace{0.5cm} if GMRES
15:
16: \hspace{1cm} $\tilde{r}_{k+1} \leftarrow r_{k+1} - \frac{\rho_1}{\alpha_1} v_{k+1}$
17: **if** $\|\tilde{r}_{k+1}\| \leq \epsilon \|r_{k+1}\|$ **then**
18: \hspace{1cm} $x_{k+1} \leftarrow \frac{\rho_1}{\alpha_1} c_{k+1}$ \hspace{1cm} \{ inner first Krylov iteration \}
19: **else**
20: \hspace{1cm} $d_{k+1} \leftarrow \text{Kcycle}(k + 1, \tilde{r}_{k+1}, x_{k+1})$
21: \hspace{1cm} $w_{k+1} \leftarrow A_{k+1} d_{k+1}$
22: \hspace{1cm} $\gamma \leftarrow d_{k+1}^T v_{k+1}$, \hspace{0.5cm} $\beta \leftarrow d_{k+1}^T w_{k+1}$, \hspace{0.5cm} $\alpha_2 \leftarrow d_{k+1}^T \tilde{r}_{k+1}$ \hspace{0.5cm} if CG
23: \hspace{1cm} $\gamma \leftarrow w_{k+1}^T v_{k+1}$, \hspace{0.5cm} $\beta \leftarrow \|w_{k+1}\|^2$, \hspace{0.5cm} $\alpha_2 \leftarrow w_{k+1}^T \tilde{r}_{k+1}$ \hspace{0.5cm} if GMRES
24:
25: \hspace{1cm} $\rho_2 \leftarrow \beta - \frac{\gamma^2}{\alpha_1}$
26: \hspace{1cm} $x_{k+1} \leftarrow \left(\frac{\alpha_1}{\rho_1} - \frac{\alpha_2}{\rho_2}\right) c_{k+1} + \frac{\alpha_2}{\rho_2} d_{k+1}$ \hspace{1cm} \{ inner second Krylov iteration \}
27:
28: **end if**
29: **end if**
30: \hspace{1cm} $x_k \leftarrow x_k + P_k x_{k+1}$ \hspace{1cm} \{ prolongation \}
31: \hspace{1cm} $r_k \leftarrow \tilde{r}_k - A_k x_k$ \hspace{1cm} \{ compute new residual \}
32: \hspace{1cm} $x_k \leftarrow S_k(r_k, A_k, x_k)$ \hspace{1cm} \{ post-smoothing \}

3. Implementation

We use the CUDA programming model for our GPU implementations and the OpenMP threading model for CPU counterpart parts. For the setup phase, the matrices are stored in compressed sparse row (CSR) format for both CPU and GPU implementations. For sparse matrix–matrix products (spmm), we also use the coordinate (COO) format for storing one of the multipliers.

In the solve phase, matrix vector multiplication axpy, is the dominant component. For the CPU implementations we store the matrices in CSR format and for GPU implementations we use a hybrid matrix format [10] that is a combination of the ELL and COO formats. For multi-GPU axpy, the matrix entries corresponding to the columns that are processed by other processors are stored in COO format. This way, the multiplication with the matrix stored in ELL, and the communication of the vector data can be overlapped. We use CUDA peer-to-peer transfers for inter GPU communication of vector entries.

For parallel primitive operations such as reduction, sort by key, scan, and gather required for the aggregation algorithm, we use the libraries thrust [11] and/or GAMPACK.

For the coarsest level, to solve the linear system exactly, we use the CPU based direct solver SuperLU [12], for both GPU and CPU implementations. Only one CPU thread is used for solving the coarse linear system. We ensure that dimension of the coarse linear system is about 600 for GPU implementations to minimize the overall runtime.

3.1. Galerkin product

After the interpolation and restriction matrices are computed at a level $k$, the coarse grid operator ($A_{k+1} = R_k A_k P_k$) is constructed using two matrix–matrix multiplications, $A_k \times P_k$ and $R_k \times (A_k \times P_k)$. All of these matrices are stored in CSR format. A GPU implementation of sparse matrix–matrix multiplication is described in [6]. We describe another approach that will be useful in the solution of nonlinear systems of equations, where the aggregations are not constructed at every Newton step when the change in matrix entries is not significant. In these cases, sparsity pattern of the matrices in the hierarchy do not change since the pattern depends only on the aggregations and the sparsity pattern of $A_0$. This is true only if the sparsity pattern of $A_0$ does not change at every Newton step.
The Galerkin product can be represented as,

\[
(A_{k+1})_{ij} = \sum_{i \in G_I, j \in G_J} (R_k)_i (A_k)_{ij} (P_k)_j = \sum_{i \in G_I, j \in G_J} (P_k)_j (A_k)_{ij} (R_k)_i
\]

where, \( G_I \) is a set of nodes that form the \( l \)th aggregate. Each of the fine nodes is grouped into exactly one aggregate. Therefore, for each nonzero of the fine grid matrix \( A_{ij} \neq 0 \), the index of corresponding coarse grid matrix is \( IJ \), where \( I \) and \( J \) are aggregation indices corresponding to \( i \) and \( j \) respectively. These coarse grid nonzero row and column indices are tabulated and sorted based on lexicographic ordering of ordered pair \((I, J)\). The number of nonzeros in coarse grid matrix is obtained by counting the unique indices \((I, J)\). All the coefficients corresponding to a same index, will sum to a unique nonzero of the coarse grid. This summation is done using segmented reduction. The pattern of this computation can be repeated by storing the sorting indices and the segmented reduction indices. This way, the entire Galerkin product can be performed in less than \(1/100\)th of the regular \texttt{spmm} compute time. Note that the interpolation operator is not reconstructed, since its entries depend on the near null space vector and not directly on the entries of the level matrix. For the cases in which we do not reuse the hierarchy information; we use GPU sparse matrix–matrix multiplications.

Consider a matrix \( A_k \), with sparsity graph shown in Fig. 1. The aggregates are shown in different colors and enumerated in Roman numerals. The sequence of operations to compute the coarse grid matrix is illustrated in Eq. (7). Initially the nonzero coefficients \( A_k \)'s are sorted based on the corresponding aggregation indices \((I, J)\) to obtain unique \((I, J)\) pairs. The Galerkin product is then finished by adding all the duplicate pairs \((I, J)\) to corresponding nonzero of the coarse grid matrix \( A_{k+1} \). Note that each nonzero entry \((A_k)_{ij}\) is multiplied with \((P_k)_j\) and \((R_k)_i\) before sorting the coefficients.

**4. Numerical experiments**

In order to determine the efficiency of the aggregation AMG solver, we compare its performance with that of \texttt{GAMPACK} implementation of classical AMG solver. This classical AMG solver was compared with the well known classical AMG solver \texttt{HYPRE} [6]. We compare the best performances of both solvers for each case. We used a flexible GMRES as the outer Krylov solver in all test cases.

In Table 1, we compare the storage complexity of the hierarchy of smooth aggregation from CUSP with unsmooth aggregation for a matrix related to a 2D anisotropic Poisson problem. For smooth aggregation, the density of the coarse grid matrices increase rapidly while it is almost fixed for unsmooth aggregation, ensuring low memory usage by unsmooth aggregation. The dense coarse grids of smooth aggregation increase the communication cost during the smoothing, interpolation,
In Fig. 3, we compare the setup and solve times with those of CUSP smooth aggregation and GAMPACK classical AMG for an anisotropic 2D Poisson problem. The setup times for CUSP are significantly larger while the solve times are significantly smaller compared to that of GAMPACK. Very small solve time of smooth aggregation is due to fewer number of iterations required for the convergence. This is because of very good interpolation between coarse and fine grids. Since the setup is the dominant cost for smooth aggregation, the overall runtime for CUSP smooth aggregation is also very large compared to GAMPACK unsmooth aggregation. The large setup time for smooth aggregation is due to the density of the coarse levels.

In Fig. 4, we compare the setup and solve times with classical AMG. Unlike classical AMG, the setup cost of aggregation is not dominant, and more than half of the time is spent on the solve. The results indicate that the solve stage of V-cycle classical AMG is about two times faster than aggregation AMG while the setup time of classical AMG is three times slower than that of aggregation AMG. Since a major component of computation is the setup for classical AMG, in overall, aggregation AMG is faster than classical AMG for the class of problems we considered. For these test cases, CUSP smooth aggregation could not construct the AMG hierarchy in a reasonable time, hence we do not compare the results with CUSP.

In order to study the scalability of the solver, we consider a series of down-scaled problems corresponding to the SPE10 pressure matrix problem [13]. Since the larger matrices do not fit on one or two GPUs, we include the timings with multiple GPUs for both classical and aggregation AMG in Table 2. As reported in the table, the overall efficiency increases as the problem size increases, for a fixed number of GPUs. However, the efficiency per GPU decreases with the increase in the number of GPUs. This is because of the high ratio of the communication cost to computation cost on GPUs for coarse level matrices. We consider to improve the multi-GPU performance scaling in our future work.

In Fig. 5, we compare the performance of both solvers as the problem size increases. For the smallest problem (∼2M) we considered, classical AMG out-performs aggregation AMG, while aggregation AMG is about 50% faster to classical AMG.
Table 1
Comparison of the hierarchy grid sparsity of CUSP smooth aggregation (left) and GAMPACK unsmooth aggregation (right) for 2D anisotropic Poisson problem on 1000 × 1000 grid.

| Level | Unknowns | nnz | nnz/row | Level | Unknowns | nnz | nnz/row |
|-------|----------|-----|---------|-------|----------|-----|---------|
| 0     | 1,000,000| 4,996,000 | 5.00 | 0     | 1,000,000| 4,996,000 | 5.00 |
| 1     | 275,264  | 4,570,716 | 16.60 | 1     | 274,979  | 1,770,339 | 6.44 |
| 2     | 76,002   | 4,398,710 | 57.88 | 2     | 77,433   | 525,877  | 6.79 |
| 3     | 8,601    | 737,445   | 85.74 | 3     | 21,823   | 147,803  | 6.77 |
| 4     | 923      | 82,045    | 88.89 | 4     | 6,150    | 39,298   | 6.39 |
| 5     | 246      | 28,140    | 114.39| 5     | 938      | 5,128    | 5.47 |

Table 2
Scaling: Timings in seconds for GAMPACK setup and solve for aggregation AMG, and classical AMG on single and multiple GPUs. The first column represents the number of cells in the original SPE10 pressure matrix problem. For each matrix, the first row gives the timings for aggregation AMG while the second row gives the timings for classical AMG. All timings are in seconds and include setup and solve to a relative tolerance of 10^{-6}. The number of GMRES iterations are given in parentheses.

| N     | 1 × M2090 | 2 × M2090 | 3 × M2090 | 4 × M2090 |
|-------|-----------|-----------|-----------|-----------|
| 1.1 M | 0.469 (15)| 0.387 (15)| 0.430 (15)| 0.409 (15)|
|       | 0.583 (10)| 0.365 (10)| 0.326 (10)| 0.312 (11)|
| 2.2 M | 0.821 (15)| 0.580 (15)| 0.561 (15)| 0.500 (15)|
|       | 1.121 (10)| 0.684 (11)| 0.526 (11)| 0.454 (10)|
| 4.4 M | 1.488 (15)| 1.079 (15)| 0.875 (15)| 0.874 (15)|
|       | 2.237 (11)| 1.302 (11)| 0.953 (11)| 0.774 (11)|
| 8.9 M | –         | 1.636 (14)| 1.345 (15)| 1.177 (15)|
|       | –         | 2.556 (12)| 1.856 (12)| 1.564 (15)|
| 13.5 M| –         | –         | 1.813 (15)| 1.471 (14)|
|       | –         | –         | 2.769 (12)| 2.159 (12)|

Fig. 5. Scaling of computation rate (in millions of unknowns per second) with system size. The rates include both setup and solve. The computations ran on 4 NVIDIA Tesla M2090 GPUs.

for the largest problem (~14M) we considered. The performance of aggregation AMG improves at a faster rate compared to that of classical AMG.

From Fig. 6, we observe significant decline in the performance of aggregation AMG as the number of GPUs used for the computation increases. This is because K-cycles require more number of coarse grid corrections and GPUs are inefficient for systems of small size. For aggregation AMG to be efficient on multiple GPUs, the system size has to be sufficiently large.

5. Conclusions and future work

We have presented a robust, efficient and scalable aggregation algebraic multigrid solver and have verified the robustness with matrices from various applications. We compared its performance with a GPU accelerated classical AMG solver and a
GPU accelerated smooth aggregation AMG solver, and observed that the setup cost of aggregation AMG is significantly low and the cost of the solution phase is high. In conclusion, aggregation AMG is extremely efficient for systems of sufficiently large size. Furthermore, for practical applications, that do not require high accuracy of the linear solver, aggregation AMG is extremely efficient due to very low setup costs. Our future work includes using MPI with multi-node computations for very large systems and incorporating polynomial smoothers to improve the convergence of aggregation AMG.

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