Development of an Integrated Power Distribution System Laboratory Platform Using Modular Miniature Physical Elements: A Case Study of Fault Location

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Abstract: The main shortcomings of the software-based power engineering education are a lack of physical understanding of phenomena and hands-on experience. Existing scaled-down analogous educational power system platforms cannot be widely used for experiments in universities due to the high cost, complicated operation, and huge size. An integrated power distribution system laboratory platform (PDSLP) using modular miniature physical elements is proposed in this paper. The printed circuit board (PCB) and microelectronic technology are proposed to construct each physical element. Furthermore, the constructed physical elements are used to set up an integrated PDSLP based on modular assembly technology. The size of the proposed cost-efficient PDSLP is significantly reduced, and the reliability of the proposed PDSLP can be improved greatly because the signal transmission path is shortened and a number of welding points are reduced. A PDSLP for fault location in neutral non-effectively grounded distribution systems (NGDSs) is selected as a typical experimental scenario and one scaled-down distribution network with three feeders is subsequently implemented and discussed. The measured zero-sequence currents by our proposed PDSLP when a single-phase earth fault occurred can reveal the true features of the fault-generated signals, including steady-state and transient characteristics of zero-sequence currents. They can be readily observed and used for students to design corresponding fault location algorithms. Modular renewable energy sources and other elements can be designed, implemented and integrated into the proposed platform for the laboratory education of the active distribution networks in the future.

Keywords: laboratory education; integrated power distribution system laboratory; modular miniature physical elements; fault location; distribution line module

1. Introduction

Laboratory education is an essential part of today’s university-level electric power engineering education. Various laboratory experimental platforms have been developed and widely used, with the aim to provide the students with theoretical knowledge and practical experience. Several decades ago,
electrical machines and high voltage laboratories were constructed and used for teaching practical
skills [1]. However, as setting up a realistic power system in the laboratory environment is very
complex and costly, digital simulation software is now commonly used in the laboratory education [2–4].
The main shortcomings of the software-based power engineering education are that there is a lack
of physical understanding of the phenomena and little hands-on experience can be provided [5]. To
overcome these problems, digital-physical hybrid simulated electrical power system platforms
were proposed and realized to provide hands-on experience on some specific devices [6–11], such as
distribution generators [7] and protective relays [8–11]. One laboratory education tool of operation
optimization in the distribution network was proposed in Reference [7], where physical distributed
energy generators are integrated into the tool, while two independent distribution systems are
simulated in the digital real-time simulator. To improve the educational process of the protective
relay systems, power-system-relaying laboratories were developed for testing the relay operating
characteristics in References [8–10]. Although industrial relays are used in these laboratories, pre- and
post-fault signals are generated by a digital power-system simulator to test the relays. To observe the
interactive behavior between relaying automation and the electrical distribution system operation,
overvoltage and overcurrent protective relays were tested under a real-time environment with a
constructed hardware-in-the-loop educational platform [11]. In these proposed educational platforms,
the electrical power systems are still reluctantly realized by digital simulation.

To provide students with the experience of observing electrical power system phenomena
intuitively, many scaled-down power systems have been proposed and implemented to set up physical
contact with the electrical world in the laboratory environment [12–18]. References [12] and [13]
show several scaled-down systems for demonstrating traditional power system operation features.
In Reference [14], a particular small power system composed of four nodes is constructed to illustrate
the static VAR compensator operating characteristics. In Reference [15], a particular scaled-down physical
power system is designed and simulated for protective relay function demonstration and algorithm
evaluation based on the structure of a 345 kV transmission system. Unlike the abovementioned
scaled-down systems, power distribution system laboratory platforms (PDSLPs) are analyzed in
References [16–21]. References [16–19] discuss the laboratory development of smart distribution
grids consisting of various distributed generating units and/or energy storage devices. The major
influence of distributed generations and storage devices exert in the operation of distribution systems
is analyzed through real-time controllers and other components. Meanwhile, some other scaled-down
PDSLPs used for particular experiments are introduced in References [20–22]. The details of the
multiphase radial power flow experiment and its corresponding hardware design, as well as the
embedded monitoring and control system, are presented for the power distribution system education
in Reference [20]. In References [21,22], distribution automation laboratories are implemented
and described in detail, and their functions mainly include fault location identification and feeder
reconfiguration. In these laboratories, the focus of the education is the performance of fault location
techniques under short-circuit fault with large fault current, while the single-phase earth fault location
experiment in neutral non-effectively grounded distribution systems (NGDSs), which is still an
attractive and challenging technology, cannot be demonstrated. In fact, further investigation shows
that the single-phase earth faults are the most prevalent, accounting for about 80% of all faults [23–25].
Moreover, NGDSs have been widely used in Central and Eastern Europe for many decades, as well as
in China [26]. Therefore, a PDSLP for fault location in NGDSs is selected as a typical experimental
scenario and implemented in this paper.

A PDSLP for the fault location in NGDSs can be directly constructed by some commercial products,
such as the ones from the companies Terco [27], De Lorenzo [28], and Lucas Nülle [29]. However,
the nominal voltage of the commercial experimental distribution system models is usually within
120–1000 V and the capacity of the scaled-down system is from tens to hundreds of kVA normally.
Such systems are expensive and bulky for some schools. In the experiments, students have to use a
number of wires to establish the electrical connections between various devices by hand. This work is
time-consuming and tedious, and it can pose a safety hazard. Another disadvantage of the laboratory education using these commercial platforms is the lack of awareness about the principles and the characteristics of the equivalent electric power components, as these components are packaged and the internal structure is invisible to students.

To overcome the aforementioned problems, an integrated PDSLP using modular miniature physical elements is proposed in this paper. The printed circuit board (PCB) and microelectronic technology are proposed to construct each physical element. Furthermore, the constructed physical elements are used to set up an integrated PDSLP based on modular assembly technology. The main electrical elements, such as the three-phase transmission line module, can be realized in miniaturization by electronic components and PCBs. In addition, the elements can be efficiently and reliably connected by standard pin-outs. The reliability of the proposed integrated PDSLP can be greatly improved because the signal transmission path is shortened and a number of welding points are reduced. Then a PDSLP for fault location in NGDSs is implemented and discussed to prove the high performance of the proposed laboratory scheme.

The rest of this paper is organized as follows: In Section 2, the architecture of the proposed integrated PDSLP is introduced. In Section 3, the main modular miniature physical models used in the proposed platform are introduced, discussed, and validated, including the three-phase distribution line module, voltage/current monitoring circuit module, and short-circuit fault generator module. In Section 4, the general miniature physical elements and specialized zero-sequence current signal monitoring circuit module are designed, and then, the effectiveness of the integrated PDSLP is validated experimentally. Students’ feedbacks are presented in Section 5 to verify the positive effects of our proposed platform on power engineering education. The main findings are concluded in Section 6.

2. Architecture of the Integrated Power Distribution System Laboratory Platform

In this section, the development requirements of the proposed integrated PDSLP need to be analyzed and illustrated. In our proposed scaled-down power distribution system, the main modules to be implemented include the three-phase distribution line module, voltage/current monitoring circuit module and short-circuit fault generator module, etc. Since the distribution feeders usually include overhead lines, underground cables, and combined underground cables and overhead lines, the three-phase distribution line module can be implemented by several standardized modules. Each standardized distribution line module represents one type of distribution line with a certain length.

The architecture of the proposed PDSLP is shown in Figure 1. External power source, such as traditional protective relay tester with three-phase AC power output ports, is connected to the three-phase input port module, and then the following modules are connected in cascade one after another: Three-phase breaker module, voltage/current monitoring module, the first three-phase distribution line module, short-circuit fault generator module, the second three-phase distribution line module, and three-phase-load output port module. The three-phase distribution line module, short-circuit fault generator module, and voltage/current monitoring module will be introduced and discussed in Section 3.

The physical models in the proposed platform are constructed by electronic components, so the PDSLP can be designed and implemented according to the PCB production process. The prototype of the proposed scaled-down PDSLP is shown in Figure 2. It is a scaled-down distribution network with three feeders, and each feeder is constructed by two three-phase distribution line modules. To eliminate the safety problem and permit the open use of the miniature PDSLP, the maximum line-to-ground voltage is limited to 35 V. The total cost of this platform is about US $1000, and the relevant equipment and its detailed cost in our proposed platform are listed in Table 1.
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Since there are usually dozens of distribution lines connected to the same substation bus, we constructed twenty three-phase distribution line modules and embedded them in ten PCB motherboards (the PCB motherboard is integrated with the voltage and current monitoring circuit module, short circuit fault generator and breaker module, the module of input, and out ports). Every two distribution line modules would be embedded into one PCB motherboard. Hence the total cost of our proposed platform reached about US $1000.

The cost comparison was carried out between our proposed platform and the commercial components in the common market [27–29]. The detailed cost comparison is given in Table 2. Compared with the cost of the commercial devices, it can be derived the cost of the distribution line module proposed in our paper was reduced by 98.9%, and the total cost of the proposed platform was reduced by 98.6%.

**Figure 1.** Architecture of the proposed integrated power distribution system laboratory platform.

**Figure 2.** Prototype of the scaled-down educational distribution network platform.

**Table 1.** Equipment list and its cost in our proposed platform.

| Equipment Description | Unit | Cost/US $ | Number | Total Cost/US $ |
|-----------------------|------|-----------|--------|-----------------|
| Three-phase distribution line module | | | 16 | 20 | 320 |
| PCB motherboard for distribution feeder | | | 36 | 10 | 360 |
| DC power supply | | | 150 | 1 | 150 |
| Other equipment | | | / | / | 150 |
| **Total cost/US $** | | | / | / | 980 |

**Table 2.** Cost comparison between our platform and the commercial platform.

| Equipment Description | Commercial Platform Cost/US $ | Number | Total Cost/US $ | Our Platform Cost/US $ | Number | Total Cost/US $ | Cost Reduced By % |
|-----------------------|-------------------------------|--------|-----------------|------------------------|--------|-----------------|-----------------|
| Three-phase distribution line module | 1500 | 20 | 30,000 | 16 | 20 | 320 | 98.9% |
| PCB motherboard for distribution feeder | / | / | / | 36 | 10 | 360 | / |
| DC power supply | / | / | / | 150 | 1 | 150 | / |
| Other equipment | / | / | 41,500 | / | / | / | 150 / |
| **Total cost/US $** | / | / | 71,500 | / | / | 980 | 98.6% |

The size of the proposed platform was about 30 cm × 30 cm × 3 cm. The major devices included six PCBs of the distribution line module, three PCBs of the short-circuit fault generator module, nine PCBs of the voltage and current monitoring circuit module, one PCB of the three-phase input port module, three PCBs of the three-phase breaker module, and three PCBs of the three-phase-load output port module. For the traditional \( \pi \) -shape three-phase transmission line model implemented...
Table 1. Equipment list and its cost in our proposed platform.

| Equipment                                      | Description                                                                 | Unit Cost/US $ | Number | Total Cost/US $ |
|------------------------------------------------|-----------------------------------------------------------------------------|----------------|--------|-----------------|
| Three-phase distribution line module           | Equivalent π-type distribution line model with different parameters represent different lengths of the overhead line or cable | 16             | 20     | 320             |
| PCB motherboard for distribution feeder         | PCB motherboard integrated with the voltage and current monitoring circuit module, short circuit fault generator and breaker module, module of input, and out ports | 36             | 10     | 360             |
| DC power supply                                | To power the voltage and current monitoring circuit module                  | 150            | 1      | 150             |
| Other equipment                                | Antistatic experimental table, soldering iron, etc.                         | /              | /      | 150             |
| Total cost/US $                                |                                                                            | /              | /      | 980             |

Since there are usually dozens of distribution lines connected to the same substation bus, we constructed twenty three-phase distribution line modules and embedded them in ten PCB motherboards (the PCB motherboard is integrated with the voltage and current monitoring circuit module, short circuit fault generator and breaker module, the module of input, and out ports). Every two distribution line modules would be embedded into one PCB motherboard. Hence the total cost of our proposed platform reached about US $1000.

The cost comparison was carried out between our proposed platform and the commercial components in the common market [27–29]. The detailed cost comparison is given in Table 2. Compared with the cost of the commercial devices, it can be derived the cost of the distribution line module proposed in our paper was reduced by 98.9%, and the total cost of the proposed platform was reduced by 98.6%.

Table 2. Cost comparison between our platform and the commercial platform.

| Equipment                                      | Commercial Platform | Our Platform | Cost Reduced By |
|------------------------------------------------|---------------------|--------------|-----------------|
|                                                 | Unit Cost/US $      | Number       | Total Cost/US $ | Unit Cost/US $ | Number | Total Cost/US $ | %    |
| Three-phase distribution line module           | 1500                | 20           | 30,000          | 16             | 20     | 320             | 98.9% |
| PCB motherboard for distribution feeder         | /                   | /            | /               | 36             | 10     | 360             | /     |
| DC power supply                                | /                   | /            | /               | /              | /      | /               | /     |
| Other equipment                                | /                   | /            | 41,500          | /              | /      | 150             | /     |
| Total cost/US $                                | /                   | /            | 71,500          | /              | /      | 980             | 98.6% |

The size of the proposed platform was about 30 cm × 30 cm × 3 cm. The major devices included six PCBs of the distribution line module, three PCBs of the short-circuit fault generator module, nine PCBs of the voltage and current monitoring circuit module, one PCB of the three-phase input port module, three PCBs of the three-phase breaker module, and three PCBs of the three-phase-load output port module. For the traditional π-shape three-phase transmission line model implemented in References [30,31], the size of the one line model module was about 80 cm × 40 cm × 70 cm, which is shown in Figure 3. If six traditional transmission line model modules were used to set up one distribution network with three feeders, the total size of the platform would be larger than 480 cm × 40 cm × 70 cm. In other words, the size of the proposed miniature PDSL in this paper was about 0.2 percent of the size of the traditional platform.
3. Description and Discussion of the Modular Miniature Physical Modules

3.1. Three-Phase Distribution Line Module

The schematic diagram of the analogue three-phase distribution line module is shown in Figure 4. The module was constructed by two eight-pin male header connectors, four resistors \((R_1, R_2, R_3, \text{ and } R_4)\), four inductors \((L_1, L_2, L_3, \text{ and } L_4)\), and eight capacitors \((C_1, C_2, C_3, C_4, C_5, C_6, C_7, \text{ and } C_8)\).

![Figure 3. Traditional π-shape physical model of the three-phase transmission line.](image)

![Figure 4. The schematic diagram of the three-phase distribution line module.](image)

If the length of the real distribution line is \(l\), the parametric values of the corresponding electrical elements in the distribution line module can be calculated by [32],

\[
R_1 = R_2 = R_3 = r_+ l, \quad (1)
\]

\[
L_1 = L_2 = L_3 = L_+ l, \quad (2)
\]

\[
C_1 = C_2 = C_3 = C_5 = C_6 = C_7 = C_+ l/2, \quad (3)
\]

\[
R_4 = (r_0 - r_+)l/3, \quad (4)
\]

\[
L_4 = (L_0 - L_+)l/3, \quad (5)
\]

\[
C_4 = C_8 = 1.5C_+ C_0 l/(C_+ - C_0), \quad (6)
\]

where \(L_+\), \(r_+\), and \(C_+\) represent the positive-sequence self-inductance, resistance, and capacitance, respectively; and \(L_0\), \(r_0\), and \(C_0\) are the zero-sequence self-inductance, resistance, and capacitance, respectively.

Typical positive-sequence and zero-sequence parameters of overhead lines and underground cables in China are given in Table 3, and the negative-sequence parameters were approximately the same as the positive-sequence parameters. The parametric values of the electrical elements in the three-phase distribution line module could be calculated as those given in Table 4. To reduce the cost of
this module, typical non-custom resistors were selected and they were connected in series to provide the calculated resistances. Similarly, typical non-custom capacitors were used and they were connected in parallel to the required capacitances.

Table 3. Typical sequence parameters of distribution lines.

|                      | Positive |           |         |                 |                 | Zero          |
|----------------------|----------|-----------|---------|-----------------|-----------------|---------------|
|                      | $r_+$ (Ω/km) | $L_+$ (mH/km) | $C_+$ (µF/km) | $R_0$ (Ω/km) | $L_0$ (mH/km) | $C_0$ (µF/km) |
| overhead line         | 0.096    | 1.22      | 0.011   | 0.23            | 3.66            | 0.007         |
| cable                | 0.11     | 0.52      | 0.29    | 0.34            | 1.54            | 0.19          |

Table 4. Typical values of the elements in module of the distribution line module.

|                      | Length (km) | $R_1$ (Ω) | $R_4$ (Ω) | $L_1$ (mH) | $L_4$ (mH) | $C_1$ (µF) | $C_4$ (µF) |
|----------------------|-------------|-----------|-----------|-------------|-------------|------------|------------|
| overhead line         | 10          | 0.96      | 0.45      | 12.2        | 8.13        | 0.055      | 0.288      |
| cable                | 1.5         | 0.165     | 0.115     | 0.78        | 0.51        | 0.218      | 1.24       |
| cable                | 5           | 0.55      | 0.383     | 2.6         | 1.7         | 0.725      | 4.13       |
| cable                | 10          | 1.65      | 1.15      | 7.8         | 5.1         | 2.18       | 12.4       |

Table 5 shows that several common commercially available resistors (the number of the cascaded components was less than 3) were connected in series to form the required resistor given in Table 4 with an acceptable error.

Table 5. Detailed cascaded resistor components to form the required resistor.

| Serial Number | Target Values (µF) | Detailed Cascaded Resistor Components | Error |
|---------------|--------------------|--------------------------------------|-------|
|               |                    | Cascaded Components’ Number | Detailed Nominal Values/µF |       |
| 1             | 0.115              | 1                                   | 0.12  | 4.3%       |
| 2             | 0.165              | 1                                   | 0.15  | 9.1%       |
| 3             | 0.383              | 2                                   | 0.15/0.22 | 3.4%    |
| 4             | 0.45               | 2                                   | 0.22/0.22 | 2.2%    |
| 5             | 0.55               | 1                                   | 0.56  | 1.8%       |
| 6             | 0.96               | 1                                   | 0.91  | 5.2%       |
| 7             | 1.15               | 1                                   | 1.2   | 4.3%       |
| 8             | 1.65               | 1                                   | 1.6   | 3.0%       |

Similarly, Table 6 shows that several common commercially available capacitors (the number of the parallel components was less than 4) were connected in parallel to form the required capacitor with acceptable error given in Table 4. All the resistors and capacitors were integrally installed in the designed PCB by digital surface-mount technology (SMT).

Table 6. Detailed parallel capacitor components to form the required capacitor.

| Serial Number | Target Values (µF) | Detailed Parallel Capacitor Components | Error |
|---------------|--------------------|----------------------------------------|-------|
|               |                    | Parallel Components’ Number | Detailed Nominal Values/µF |       |
| 1             | 0.055              | 1                                   | 0.056 | 1.8%       |
| 2             | 0.218              | 1                                   | 0.22  | 0.9%       |
| 3             | 0.288              | 3                                   | 0.027/0.047/0.22 | 2.1%    |
| 4             | 0.725              | 3                                   | 0.047/0.22/0.47 | 1.7%    |
| 5             | 1.24               | 3                                   | 0.047/0.22/1 | 2.2%    |
| 6             | 2.18               | 3                                   | 2.2   | 0.9%       |
| 7             | 4.13               | 3                                   | 1/1/2.2 | 1.7%    |
| 8             | 12.4               | 3                                   | 0.22/2.2/10 | 0.2%    |
Due to the high inherent resistance of regular inductors, the inductor elements of the three-phase distribution line module need to be customized, which was formed by a ferrite bead and twining varnished wires.

The constructed PCB module of the three-phase distribution line module is shown in Figure 5. The resistors and capacitors were placed on the reverse side and the customized inductors were placed on the obverse side.

![Figure 5](image_url)

**Figure 5.** Layout of the three-phase distribution line printed circuit board (PCB) module: (a) obverse side; and (b) reverse side.

### 3.2. Voltage and Current Monitoring Circuit Module

Due to the input limit of A/D converters and/or microprocessors, the output signals of the voltage and current transducers need to be processed so that the monitoring requirements for the signal amplification and bias circuits can be met.

#### 3.2.1. Signal Conditioning Circuit for Voltage Transducer

The schematic diagram of the signal conditioning circuit for the voltage transducer is shown in Figure 6. In our miniature PDSL, voltage transducer SPT204A has been selected to transmit the voltage, with 2-mA nominal input and the output currents. In Figure 6, \( U_i \) represents the input voltage, and \( R_1 \) represents the current-limiting resistance. For any input voltage between 1 V to 100 V, the resistance \( R_1 \) is set to 50 kΩ, so that the maximum input current can be limited to 2 mA. The values of other elements in the circuit shown in Figure 6 were set by following: \( R_2 = 3 \, \text{k}\Omega, \, R_3 = 200 \, \text{k}\Omega, \, C_2 = 0.01 \, \text{μF}, \, R_4 = R_5 = R = 20 \, \text{k}\Omega, \, R_7 = 4.7 \, \text{k}\Omega, \, V_{\text{ref}} = -6.9 \, \text{V}, \) and \( R_6 = 3.3 \, \text{k}\Omega. \) The output voltage signal \( U_{\text{uo}} \) could be calculated by,

\[
U_{\text{uo}} = \frac{R_7}{R_1} (R_2 U_i + V_{\text{ref}}) = (-0.0141 U_i + 1.6215) \text{V}. \tag{7}
\]

![Figure 6](image_url)

**Figure 6.** The schematic diagram of the signal conditioning circuit for a voltage transducer.
3.2.2. Signal Conditioning Circuit for Current Transducer

The signal conditioning circuit for the current transducer was similar to that for the voltage transducer, which is shown in Figure 7. In our miniature PDSL, current transducer SCT254AK was selected. The rated output current was 2.5 mA. In Figure 7, \( I \) represents the input current. The parametric values of other components in the circuit shown in Figure 7 were set by following: \( R_8 = 2.4 \text{ k}\Omega, R_9 = 200 \text{ k}\Omega, R_{10} = R_{11} = 20 \text{ k}\Omega, R_{12} = 3.3 \text{ k}\Omega, R_{13} = 4.7 \text{ k}\Omega, C_3 = 27 \mu\text{F}, \) and \( C_4 = 0.01 \mu\text{F} \). The output voltage signal \( U_{io} \) could be calculated by,

\[
U_{io} = -\frac{R_{13}}{R_{10}} \left( \frac{R_8}{2000} nI + V_{\text{ref}} \right) = (-0.282nI + 1.6215)V.
\] (8)

Figure 7. The schematic diagram of the signal conditioning circuit for a current transducer.

3.2.3. Experimental Test for Signal Monitoring Circuits

In order to test the performance of the designed signal conditioning circuit for the voltage transducer, the output signals of the circuit under different input signals were measured and compared with the theoretical values calculated by Equation (7). When the input voltage was zero, the calculated and the measured outputs were 1.6215 V and 1.628 V, respectively, and the error was 0.4%. When the input voltage was set as \( 50 \times \sin (314t) \) V, the measured output signal is shown in Figure 8a, and the error was 0.7%.

![Figure 8](image-url)  
(a)  
(b)

Figure 8. Experimental test results of the signal conditioning circuit: (a) voltage transducer; and (b) current transducer.

Similar experimental tests were carried out to prove the effectiveness of the designed signal conditioning circuit for the current transducer. Experimental tests indicate that the error was 0.2%
when the input signal was zero. The measured output signal is shown in Figure 8b when the input signal was set to $0.15 \times \sin(314t)$ A, and the error was 1.1%.

3.3. Short-Circuit Fault Generator and Breaker Module

Since the voltage level and rated power of the proposed miniature PDSLPS was low, the three-phase breaker module could be constructed by an isolated circuit, including a relay and corresponding drive circuit. The circuit of the breaker module installed at phase A of one feeder is shown in Figure 9.

The schematic diagram of the short-circuit fault generator module is shown in Figure 10. Either toggle switches or relay modules could be used to replace switches S1, S2, S3, and S4. All fault types can be simulated through different state combinations by the four switches.

4. Experiment for the Single-Phase Earth Fault Location

4.1. Requirements of the Experimental Platform for Fault Location

Since the equivalent zero-sequence impedance is extremely high when a single-phase earth fault occurs in NGDSs, the three-phase line-to-line voltage remains symmetrical. In this condition, the system is allowed to continue to operate within a time frame of 30 min to 2 h [33]. Hence, the reliability of the power supply can be significantly enhanced in the NGDSs. Moreover, the fault needs to be located and cleared to avoid further propagation, which can eventually lead to severe blackouts due to the persistent single-phase earth faults. As the single-phase earth fault current is small, identifying the faulted line from a large number of lines connected to the same distribution bus has become one of the biggest challenges in the above distribution systems [34–36].

Existing single-phase earth fault location methods are mainly developed according to the magnitude and the phase difference of the zero-sequence currents between the faulted line and sound lines. Traditional faulted line detection methods include zero-sequence-overcurrent protection, zero-sequence-current direction-based protection, etc. [37,38], and these methods have been widely used in the industry. However, the faulted line may be identified incorrectly due to the weak fault current and noise jamming. To overcome these problems, the similarity of the transient zero-sequence current waveforms between the faulted line and sound lines has been analyzed and used for the selection of the faulted line currently [39–41]. Regardless of which method the fault location is used,
the zero-sequence currents of all lines connected to the same bus should be measured first under
different single-phase earth fault conditions, such as the fault position.

Once a single-phase earth fault occurs in an NGDS, the zero-sequence currents of all feeders
connected to the same substation bus are used to identify the faulted feeder. Therefore a specialized
zero-sequence current signal monitoring circuit module should be implemented besides the general
modules discussed in the next section.

4.2. Experimental Platform for Fault Location

4.2.1. Zero-Sequence Current Signal Monitoring Circuit

A summing amplifier is used to generate the zero-sequence current. The summing amplifier must
work in the inverting mode so that low input impedance can be provided. The schematic diagram
of the relevant circuit for this amplifier is shown in Figure 11, where \( U_{IA}, U_{IB}, \) and \( U_{IC} \) represent the
output signal of the phases A, B, and C, respectively. The parametric values of the electrical components
in the circuit shown in Figure 11 were set by following: \( R_{18} = R_{19} = R_{20} = 10 \, \text{k}\Omega, \) \( R_{14} = R_{15} = R_{16} = 30 \, \text{k}\Omega, \) and \( R_{17} = 5.1 \, \text{k}\Omega, R_{21} = 5.1 \, \text{k}\Omega. \) It can be readily seen that the output voltage \( U_{lb} \) was \((U_{IA} + U_{IB} + U_{IC})/3.\)

\[
\begin{align*}
U_{IA} & \quad R_{14} \\
U_{IB} & \quad R_{15} \\
U_{IC} & \quad R_{16} \\
\text{OP07A} & \quad +12V \\
\text{LM358} & \quad +12V \\
\text{R20} & \quad U_{lc} \\
\text{R21} & \quad -12V \\
\end{align*}
\]

\textbf{Figure 11.} The schematic diagram of the zero-sequence current signal monitoring circuit.

To evaluate the accuracy of the zero-sequence current signal monitoring circuit, three input signals
representing phases A, B, and C were used to generate the zero-sequence signal. As shown in Figure 11,
when \( U_{IA}, U_{IB}, \) and \( U_{IC} \) were set to 1 V, 3 V, and 5 V, respectively, the measured output signal was
2.99 V, with an error of 0.3%. When \( U_{IA}, U_{IB}, \) and \( U_{IC} \) were set as \( 9 \times \sin (314 \, t) \) V, \(-3 \times \sin (314 \, t) \) V,
and 3 V, respectively. The measured output signal is shown Figure 12 and the error was 1.3%.

\textbf{Figure 12.} Experimental test results of the zero-sequence signal monitoring circuit.
It can be seen that the designed signal monitoring circuits were capable of meeting the educational demand of monitoring the characteristics of zero-sequence currents of all feeders under a single-phase earth fault in one NGDS.

4.2.2. An Implemented Distribution Network Platform for the Fault Location Experiment

For demonstration purposes, the experimental platform in this paper was developed based on a typical distribution network configuration with three feeders. The first feeder consisted of 20 km of overhead lines and 3 km of underground cables, the second feeder was a 10-km underground cable line, and the third consisted of 30 km of underground cables. As the reusability of the module is important for the platform, the first feeder was realized by two 10-km overhead line modules and two 1.5-km cable modules. Similarly, the second and the third feeders were realized by two 5-km and 15-km cable modules, respectively. The parameters of the distribution lines were the same as those shown in Table 1.

The experimental platform set-up is shown in Figure 13. In the experiments, the root mean square (RMS) value of phase voltage was set to 5 V. The DC power source was used to power the signal conditioning circuit and the zero-sequence current signal monitoring circuit. Two dual-channel digital oscilloscopes were used to measure and record the experimental results.

4.3. Experimental Results for Single-Phase Earth Fault Location

In the experiments, phase voltage of the three-phase power source was set to 5 V. The zero-sequence currents of three feeders under a single-phase earth fault with different fault locations were measured and recorded, which have been shown in Figures 14–19.

![Experimental platform set-up for fault location in distribution systems.](image-url)

**Figure 13.** Experimental platform set-up for fault location in distribution systems.

(a) (b) (c)

![Experimental results of zero-sequence currents when a single-phase earth fault occurs on the middle of the first feeder.](image-url)

**Figure 14.** Experimental results of zero-sequence currents when a single-phase earth fault occurs on the middle of the first feeder: (a) first feeder; (b) second feeder; and (c) third feeder.
Figure 15. Experimental results of zero-sequence currents when a single-phase earth fault occurs on the end of the first feeder: (a) first feeder; (b) second feeder; and (c) third feeder.

Figure 16. Experimental results of zero-sequence currents when a single-phase earth fault occurs on the middle of the second feeder: (a) first feeder; (b) second feeder; and (c) third feeder.

Figure 17. Experimental results of zero-sequence currents when a single-phase earth fault occurs on the end of the second feeder: (a) first feeder; (b) second feeder; and (c) third feeder.

Figure 18. Experimental results of zero-sequence currents when a single-phase earth fault occurs on the middle of the third feeder: (a) First feeder; (b) second feeder; and (c) third feeder.
When the single-phase earth fault occurred, the zero-sequence current of the faulted feeder (the first feeder) suddenly changed and dropped from zero to the negative peak value, and then it got into the steady-state fault condition, which is shown in Figure 14a. The zero-sequence current of the sound feeder (the second feeder or the third feeder) changed at the same time but rose from zero to the positive peak value, which is shown in Figure 14b,c. The phase of the zero-sequence current of the faulted feeder (the first feeder) was opposite to that of sound feeders (the second feeder and third feeder). The RMS values of the zero-sequence currents of all feeders were closely studied and it indicated that the RMS value of the zero-sequence current of the faulted feeder equaled the sum of those of the sound feeders. The faulted line can be identified based on the characteristics of the phase and the magnitude of the recorded zero-sequence currents. Compared with the zero-sequence currents shown in Figure 14 and those shown in Figure 15, it could be derived that the magnitude of zero-sequence current almost kept the same since the single-phase earth fault occurred on the same feeder, namely the first feeder. The reason was that the zero-sequence current was mainly determined by the shunt grounded capacitance of the distribution line.

When single-phase earth faults occurred on the second feeder with different fault locations, the zero-sequence currents of all feeders were recorded and shown in Figures 16 and 17. In Figure 16, the phase of the zero-sequence current of the second feeder was opposite to that of the first feeder and the third feeder, and the magnitude of the zero-sequence current of the second feeder equaled the sum of those of the sound feeders. The same conclusion could be derived from Figure 17. It is worth noting that the polarity of the zero-sequence current of each feeder at the fault time in Figure 16 was opposite to that of the corresponding feeder in Figure 17 because of the different fault inception angle.

When the single-phase earth fault occurred on the third feeder, similar conclusions could be reached by comparing the recorded zero-sequence currents shown in Figure 18 with those given in Figure 19.

Further analysis revealed that the transient behavior of zero-sequence currents could be also briefly observed in Figures 14–19. The polarity of the zero-sequence current of the faulted line was always opposite to that of the sound line, this characteristic could be used to identify the fault line. However, the duration of the most transient responses was less than 3 ms, which would lead to unsuccessful faulted feeder identification. High sampling rates are needed to be applied in the fault indicators if transient zero-sequence currents are used to locate the fault position.

4.4. Validation of the Lab Facility Outputs in Comparison to Real Phenomena

To validate the lab facility outputs, the experimental results should be compared with the fault records data under single-phase earth fault conditions in NGDSs provided by the field operators. One typical recorded zero-sequence current waveform of the fault records data is shown in Figure 20. In Figure 20, the single-phase earth fault occurred at 16.9 ms, and then the zero-sequence current reached its instantaneous maximum value after the transient process with a 1.3 ms time frame. The instantaneous maximum value of zero-sequence current of Feeder 124 (faulted line) reached
13.88 A (the ratio of the current transformer was 50:5), while those of other feeders was negative and the absolute values were much smaller than that of Feeder 124. Namely, in the fault transient process, the polarity of the zero-sequence current of the faulted line was always opposite to that of the sound line, and its magnitude was the biggest. At the end of the short-period fault transient process, the steady-state process followed on. In the steady-state process, it could be derived that the phase of the zero-sequence current of Feeder 124 (faulted line) was opposite to that of other feeders, and the RMS value of the current of Feeder 124 was the biggest.

Figure 20. One typical recorded zero-sequence current waveform under a single-phase earth fault in the field.

The transient and steady-state waveforms of the measured zero-sequence currents shown in Figures 14–19 were compared with the fault records data in the field shown in Figure 20. We could conclude that the measured zero-sequence currents from our proposed PDSLP when a single-phase earth fault occurred could reveal the true features of the fault-generated signals, including steady-state and transient characteristics of zero-sequence currents. Undergraduate and graduate students could readily observe relevant physical phenomena and design corresponding fault location algorithms based on this platform.

5. Students’ Evaluation

The proposed platform was used for the undergraduate course “Power System Protective Relaying” at Wuhan University of Technology, China, during the second semester of 2017 and the first semester of 2018. In this course, the platform was used to explain the principle of overcurrent protection in the distribution network. The experiment designed based on this platform was fault location for the single-phase earth fault in the neutral indirectly earth systems. Three exercises were assigned to the students:
Faulted line identification based on the magnitude difference of the steady-state zero-sequence currents.

Faulted line identification based on the phase difference of the steady-state zero-sequence currents.

Faulted line identification based on the polarity difference of the transient zero-sequence currents.

The objectives of this experiment were to make the student become familiar with zero-sequence current characteristics measured at the beginning of the distribution feeders once a single-phase earth fault occurs. In particular, students were exposed to the inapplicability of traditional overcurrent protection for single-phase earth faults in NGDSs and the influence of weak fault current and noise jamming on zero-sequence-current feature-based protection. The three exercises were also performed by computer-based simulation tools (e.g., Matlab/Simulink) during the course. At the end of the course, anonymous questionnaires were prepared and then filled by the students in order to get feedback regarding the exercises on our proposed platform. The responses on the questionnaires were analyzed and the final evaluation score is shown in Table 7 (5 = Excellent, 1 = Very Poor).

| Survey Question                           | Student Population | Median | Standard Deviation |
|------------------------------------------|--------------------|--------|--------------------|
| Overall, I would rate this platform as:  | Proposed platform  | 4.4    | 0.65               |
|                                           | Matlab/Simulink    | 3.8    | 1.03               |
| The platform provides an appropriate     | Proposed platform  | 4.5    | 0.45               |
| hands-on approach:                       | Matlab/Simulink    | 2.8    | 1.26               |
| The platform provides an easy way to     | Proposed platform  | 4.5    | 0.55               |
| study fault location in NGDSs            | Matlab/Simulink    | 4.1    | 0.78               |

A comparison of students’ survey responses between semesters shows that the proposed platform was positively reviewed by students. Response data collected from students indicated that students felt that the proposed platform could provide hardware-based experiments and immersive physical phenomenon visualization of fault location in NGDSs. Students also indicated that the power waveforms could be easily observed and recorded when a “real-world” single-phase earth fault occurred using the short-circuit fault generator module.

6. Conclusions

In this paper, an integrated power distribution system laboratory platform (PDSL-P) was proposed for education purposes using modular miniature physical elements. The platform was implemented in a laboratory environment to provide students with the visualized experience on the distribution network phenomenon. Modular miniature physical elements, such as a three-phase distribution line module, were constructed by printed circuit boards (PCBs) and microelectronic technology. Furthermore, the constructed physical elements were used to set up the integrated scaled-down analogous electrical power systems based on modular assembly technology.

More specifically, a laboratory educational platform for fault location in neutral non-effectively grounded distribution systems (NGDSs) was selected as a typical experimental scenario in this paper. A scaled-down distribution network with three feeders was implemented and its nominal voltage was below 35 V. The total cost of this platform was about US $1000, and the size of the proposed platform was about 30 cm × 30 cm × 3 cm. The zero-sequence currents of three feeders under a single-phase earth fault at different fault locations were measured and analyzed. It shows that the phase of the zero-sequence current of the faulted feeder was opposite to that of sound feeders, and the RMS value of the zero-sequence current of the faulted feeder equaled the sum of those of the sound feeders. Although the polarity of the zero-sequence current of the faulted line was always opposite to that of the sound line, the duration of most transient responses was less than 3 ms. Therefore, a high sampling rate was needed for the transient-information-based fault location algorithm.
In future, modular renewable energy sources and other elements will be designed, implemented, and integrated into the proposed platform. Then the proposed PDSLP can be extended to provide a multi-purpose and cost-effective platform and means for the laboratory education of the active distribution networks.

7. Patents

There were patents resulting from the work reported in this manuscript: Invention Patents in China (#ZL 2017 1 0278706.8); Utility Model Patents in China (#ZL 2018 2 0752870.8 and #ZL 2018 2 0657681.2).

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