Fabrication of a porous alumina mask on the large surface area of a semi-insulating semiconductor substrate

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We describe a method for fabricating a large-scale homogeneous porous alumina membrane as a mask for dry etching of the large surface area (of the order of one square centimeter) of a semi-insulating semiconductor substrate. It employs direct anodization of the aluminum layer deposited on the substrate, which is simple, therefore, suitable for high-throughput productions. However, the process with a non-conductive semiconductor substrate suffers from the issues of quality control of the pores caused by nonuniform current flow in the aluminum layer in the anodization process. Hence, we have implemented a new fitting to the anode that provides uniform current flow into the aluminum layer, which allows us to fabricate highly homogeneous nanopores on the large-scale alumina membrane.

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1. Introduction

Fabrication of regularly ordered nanopores on the surface of a semiconductor is one of the fundamental processes for semiconductor device fabrication and is widely used in the MEMS, solar cells and photonics applications. One of the common schemes for this process is a dry etching with a mask prepared by photo-lithography. The photo-lithography is useful for small area of the order of square micrometers; however, it is not suitable or even unrealistic to apply for the large area of the order of square millimeters from the time and cost-performance point of view. Instead, one can use the electrochemical process called “anodization”, where a porous alumina membrane is fabricated by anodizing an aluminum film in an acid solution. It provides a simple and low-cost alternative for the preparation of a large-scale porous mask.

In the anodization process, there are two choices for setting a porous alumina membrane on the substrate: i.e., indirect and direct processes. In the indirect process, the porous alumina membrane is prepared separately and set on the substrate afterwards. This process has been widely used because of the good quality of the porous membranes; however, it had required the toxic mercury (II) chloride solution to peel off the porous alumina from the aluminum film, which had caused low processibility. Recently, the method of creating free-standing alumina membranes has been developed for the indirect process, which provides a promising direction to circumvent the issue of toxic solutions for peeling.

The direct process, on the other hand, is characterized by the anodization of the aluminum film directly deposited on the semiconductor substrate. It is beneficial in that no process for setting a membrane on the substrate is required, which results in high processibility, thus, high throughput. The problem is that it is difficult to control the quality of the membrane on a non-conductive substrate; the anodization process tends to proceed nonuniformly, which hinders the formation of uniform nanopores. We have found that the quality of the pores largely depends on the electric current flow in the aluminum layer; therefore, we have implemented a new fitting to the anode that provides uniform current flow in the aluminum layer, resulting in the improvement of the quality of the membrane. In this article, we report on the details of the fabrication procedure along with the new fitting.

2. Fabrication method

The procedure of the direct process is shown in Fig. 1. In our experiment, we used a single-crystal wafer of semi-insulating InP doped with Fe (350 μm thick, Acrotec) as a semiconducting substrate. On this substrate, aluminum metal was deposited in a vacuum to form aluminum thin film a few micrometers thick, which was followed by the anodization of the whole sample in 0.3 M oxalic acid solution for 2 min at 20°C with the applied voltage of 40 V. In this process, we used a new fitting to the anode to secure the uniform current flow inside the anode, which will be described in the next section. Consequently, we obtained a completely oxidized alumina layer with numerous nanopores in it. Then, we soaked the sample in the mixture of 6 wt% phosphoric acid and 1.8 wt% chromic acid at 60°C for 1 min. This process has the effects of pore widening and the removal of the barrier layers remaining at the bottoms of the pores, which is very effective to obtain well-ordered nanopores.

Figure 2(a) shows the scanning electron microscope (SEM) image of the sample at the end of this process. One finds that the nanopores completely penetrate the alumina layer and reach the InP substrate. Subsequently, the sample was subjected to the inductive coupled plasma reactive ion etching (ICP-RIE) process for 5 min under the following conditions: gas flow rate: Cl2/Ar = 1/1

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(1) Aluminum deposition

(2) Anodization in acids

(3) Pore widening

(4) ICP-RIE (dry etching)

(5)

(6) Mask removal

Fig. 1. Nanopore fabrication on the surface of a semiconducting substrate using a porous alumina mask (direct process).

Fig. 2. (a) SEM image of the top part of the sample before the dry etching process. A part of the surface of the InP substrate is exposed for demonstration purpose (lower part of the image). (b) Cross-sectional SEM image after the dry etching process. The upper and the lower layers correspond to the porous alumina and the InP substrate, respectively.

The main challenge in this method is to fabricate homogeneous nanopore arrays over the large area of the aluminum layer deposited on the semiconducting substrate. It is often the case in the conventional setup as shown in Fig. 3 that the anodization proceeds nonuniformly, which results in a partial anodization of the sample. In most cases, the anodization occurs only at the top part of the sample, but in other cases, there appear some islands (spots) of unanodized regions at the middle or the bottom of the sample. In addition, we find that the quality of the pores at the anodized part is low; Fig. 4 shows some SEM images of the upper part of the sample prepared with the conventional setup, which exhibit uneven surfaces.

beam direction. As a compromise between these conditions, we have empirically found that the pores can be best fabricated for the thickness of the alumina mask up to about 0.5–1.0 μm. Let us define the tilt angle θ as that of the pore axis from the surface normal, then the maximum θ allowed for the etching is given by

$$\theta_{\text{max}} = \arctan(1/A_r)$$

where $A_r$ is the aspect ratio. In the present case, $A_r = 10–20$, which yields $\theta_{\text{max}} = 3–6^\circ$. This means that the tilt angles of the pores are within about 6°.

3. Current uniformization in the anodization process

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These results suggest that the nonuniformity originates from that of the electric current flowing inside the aluminum layer. We find it essential that both the porous alumina and the substrate are insulators; once the top of the aluminum layer is oxidized, the current path to the rest of the aluminum layer vanishes and the anodization process stops, resulting in a nonuniform anodic alumina film. In fact, we have found in the case of a conductive
(Si-based) substrate that the entire surface of the aluminum layer on the substrate can be homogeneously anodized even with the conventional setup.

To circumvent this problem, we have implemented a fitting to the anode, whose details are shown in Fig. 5. In this setup, the sample is enclosed with an aluminum plate, which serves as an "electrode." In addition, we have secured the contact between the "electrode" and the aluminum layer on the sample by pressing them with a silicone rubber seal. Since the current is supplied to the sample evenly from the periphery, homogeneous anodization can be realized. Figure 6(a) shows a top view of the sample described in the previous section, which suggests that a large area of the order of one square centimeter is uniformly anodized. The SEM images of the same sample (Fig. 2) demonstrate good quality of the pores. On the other hand, the process with the "electrode" insufficiently contacted to the aluminum layer on the sample has proved unsuccessful: Fig. 6(b) shows a top view of the sample, where we intentionally left the bottom part of the sample uncovered with the "electrode," which resulted in a large unanodized area at the bottom. This result demonstrates the importance of the firm contact between the "electrode" and the aluminum layer.

SEM observations suggest that in most cases the aluminum layer remains firmly contacted to the substrate even after such nonuniform anodization; therefore, a possible exfoliation of the aluminum layer from the substrate during the anodization process, even if it occurs, may not be the primary origin of the nonuniform anodization. Nevertheless, since the uniform current supply to the aluminum layer is the key for the uniform anodization, we expect that the present method may be effective even in the case of the exfoliated aluminum layer.

Finally, we should note that it is crucial to anodize the "electrode" and the aluminum layer on the sample concurrently to prevent the concentration of the current. This is why we use aluminum as an "electrode" material; otherwise, the current is concentrated in either the aluminum layer on the sample or the "electrode" depending on the conductivity of the "electrode" material, which causes the nonuniform anodization.

4. Conclusion

We have developed a method for uniformly anodizing an aluminum layer deposited directly on a non-conductive semiconductor substrate. The implementation of the fitting to the anode causes the equalization of the current in the aluminum layer, resulting in homogeneous nanopores over the entire surface of the sample.

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