Minimax design of digital FIR filters using linear programming in bandwidth interleaving digital-to-analog converter

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Abstract: Bandwidth interleaving digital-to-analog converter (BI-DAC) is a new method for breaking through the bandwidth restriction of the DAC to generate a wideband signal. However, there are some errors in the BI-DAC system such as the aliasing errors caused by the non-ideal performance of the analog filters. To achieve the aliasing errors cancellation, this paper studied the minimax design of digital finite impulse response (FIR) filters. The design goal was to meet a given desired spurious free dynamic range (SFDR) of the BI-DAC system. The problem of designing the digital FIR filters was formulated as a linear programming (LP) problem which could be used to find the global optimal solution of the coefficients of the digital FIR filters. Additionally, this proposed design method performance analysis consist of the computational complexity was derived. Finally, all the proposed designs are verified by both theoretical analysis and numerical simulations, and satisfactory simulation results were achieved.

Keywords: bandwidth interleaving, linear programming, minimax design, aliasing errors cancellation, digital FIR filters

Classification: Circuits and modules for electronic instrumentation

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1 Introduction

Bandwidth interleaving digital-to-analog (BI-DAC) [1, 2, 3, 4, 5] is an emerging method for breaking through the bandwidth limitation of digital-to-analog converter (DAC) and increasing the available signal bandwidth. However, in the BI-DAC system, the wideband output can be distorted due to the aliasing errors caused by the non-ideal analog filters and mixers performance. Non-ideal mixers performance was compensated by the digital pre-distortion technique, but this paper did not identify the method for compensating the non-ideal analog filters performance [2]. Non-ideal filters performance could be compensated by pre-distorting the input digital signal with the calibration matrix [3], or by designing the digital filters [4], and yet these methods either have heavy computation requirements, or they need additional digital filters, increasing the implementation complexity of the BI-DAC system.

It well known that, the aliasing errors caused by the non-ideal analog filters performance, can be canceled by designing the digital finite impulse response (FIR) filters which are used to split the digital wideband signal into multiple sub-bands in
the BI-DAC system. Recently, the digital FIR filters optimal design methods have been the subject of active research [6, 7, 8, 9, 10]. In [6], the high-accuracy low-complexity FIR variable fractional delay (VFD) digital filters using linear programming (LP) in the minimax error sense, was proposed to minimize the maximum absolute error of the variable frequency response (VFR) of an FIR VFD filter. This paper did not mention that how to realize the digital FIR filters optimal design in the BI-DAC system, but it had its advantages and provided the reference for the digital FIR filters optimal design in the BI-DAC system. In the bandwidth interleaving analog-to-digital converter (BI-ADC) system which is the inverse system of BI-DAC, the digital FIR filters design using the biconjugate gradient stablished algorithm was proposed for canceling the aliasing errors caused by the non-ideal analog filters performance [10]. It well known that, if one of the digital and analog filter banks is designed firstly and fixed, the other filter bank can be formulated as an optimization problem. Therefore, the digital FIR filters design method proposed in [10], is also applicable for the BI-DAC system. But comparing with the LP technique, this method can achieve a larger aliasing errors and computational complexity.

This paper proposed a new digital FIR filters design using LP for the BI-DAC system. In this design method, Butterworth filters were chosen as the analog analysis filters. Given the analog analysis filters, the digital FIR filters were chosen for splitting the digital wideband signal into multiple sub-bands. The aliasing errors can be canceled by designing these digital FIR filters. The problem of designing these digital FIR filters was formulated as an optimization problem and solved using LP. As an optimization problem, if the global optimal solution exists, LP can be used to find it. Further, this design method can be proposed under the minimax criteria. This is very important because the aliasing errors can be markedly lower and improve the accuracy of the BI-DAC system by controlling the spurious free dynamic range (SFDR). The SFDR, which is equal to the ratio of the root mean square (RMS) amplitude of the maximum signal component to the RMS value of the next-highest distortion component, is a significant measure of the DAC accuracy [11, 12].

The main contributions of this paper are summarized as follows

1) In the specific context of BI-DAC, we proposed the digital FIR filters design using LP under the minimax criteria for canceling the aliasing errors caused by the non-ideal analog filters performance.

2) We compared the effectiveness and the computational complexity of the optimization design method in [10] and the minimax design using LP proposed in this paper. It was verified that our proposed design method had a better aliasing errors cancellation and much lower computational complexity than the optimization design method in [10].

The rest of this paper is organized as follows. The principle of the BI-DAC system is illustrated in Section 2. Section 3 gives the analog analysis filters design, as well as the minimax design of the digital FIR filters using LP. In Section 4, the effectiveness and the computational complexity of this proposed design method are evaluated and analyzed by different simulations. Ultimately, conclusions are summarized in Section 5.
2 Principle of BI-DAC

An $M$-channel BI-DAC is shown in Fig. 1, where the sampling rate and the bandwidth of the BI-DAC system are, respectively, $\frac{1}{T}$ and $\frac{\pi}{C_25}$. In the $m$th channel, as shown in Fig. 1(a)–(c), the digital FIR filter $F_m(e^{j\omega})$ is used to split the input digital wideband signal $X(e^{j\omega})$ into a digital sub-band signal which is then down-converted to the baseband. The unwanted sideband of the digital baseband signal is filtered out using the digital low pass filter $H_{dlm}(e^{j\omega})$, and then is down-sampled to generate the DAC input $X_m(e^{j\omega})$. So the DAC output is given by

$$X_m(j\Omega) = \frac{1}{MT} \sum_{k=-\infty}^{+\infty} X_m(e^{j(\Omega MT - 2\pi k)}) H(j\Omega) = \frac{1}{MT} \sum_{k=-\infty}^{+\infty} X(e^{j(\Omega MT - \frac{2\pi}{MT} + \frac{\pi}{2MT} k)}) H(j\Omega) \times F_m(e^{j(\Omega MT - \frac{2\pi}{MT} + \frac{\pi}{2MT} k)}) H_{dlm}(e^{j(\Omega MT - \frac{2\pi}{MT} + \frac{\pi}{2MT} k)}),$$

where $\omega = \Omega MT$, and $H(j\Omega)$ is the frequency response of the preserving function of the DAC. As shown in Fig. 1(d), the images in $X_m(j\Omega)$ are located at the center frequency $k\Omega_M$, where $k$ is contributed from (1) and $\Omega_M = \frac{2\pi}{MT}$. Then, as shown in Fig. 1(e), these images are filtered out by the analog low pass filter $H_{alm}(j\Omega)$. The baseband analog signal is up-converted to the high-frequency band, which is the sum of the unwanted and wanted sidebands. After canceling the unwanted sideband using the analog bandpass filter $H_{abm}(j\Omega)$, the $m$th channel output $Y_m(j\Omega)$ is given by

$$Y_m(j\Omega) = \frac{1}{MT} \sum_{k=-\infty}^{+\infty} X(e^{j(\Omega MT - \frac{2\pi}{MT} + \frac{\pi}{2MT} k)}) F_m(e^{j(\Omega MT - \frac{2\pi}{MT} + \frac{\pi}{2MT} k)}) \times H_{dlm}(e^{j(\Omega MT - \frac{2\pi}{MT} + \frac{\pi}{2MT} k)}) H\left(j\left(\Omega - \frac{(m - 1)}{MT} \pi\right)\right) \times H_{alm}\left(j\left(\Omega - \frac{(m - 1)}{MT} \pi\right)\right) H_{abm}(j\Omega).$$

Finally, all of the outputs $Y_m(j\Omega)$, $m = 1, \ldots, M$ are summed together to yield the analog wideband signal $Y(j\Omega)$ with $\Omega \in [0, \frac{\pi}{C_25}]$, which is given by

Fig. 1. Structure diagram of an $M$-channel BI-DAC.
\[ Y(j\Omega) = \sum_{m=1}^{M} Y_m(j\Omega) \]
\[ = \frac{1}{MT} \sum_{m=1}^{M} \sum_{k=-\infty}^{+\infty} \tilde{X}(e^{j(\Omega T - \frac{2\pi k}{M})}) F_m(e^{j(\Omega T - \frac{2\pi k}{M})}) \]
\[ \times \hat{H}_{dlm}(e^{j(\Omega T - \frac{2\pi k}{M}) - \frac{(m-1)}{MT} \pi}) H(j(\Omega - \frac{(m-1)}{MT} \pi)) \]
\[ \times H_{alm}(j(\Omega - \frac{(m-1)}{MT} \pi)) H_{abm}(j\Omega). \]  

As shown in Fig. 1(f), the bandwidth of \( Y_j(j\Omega) \) is \( M \) times as that of \( Y_m(j\Omega) \), \( m = 1, \ldots, M \). Consequently, the bandwidth limitation of each sub-DAC can be broken through in the BI-DAC system, which is the same as the description in Section 1.

3 Filters design in the BI-DAC system

In this Section, we mainly describe the designs of the analog analysis filters and the digital FIR filters. Further, we also analyze the computational complexity of our proposed design method.

3.1 Analog analysis filters design

In the BI-DAC system, high-performance wideband signals require that analog analysis filters not have constant group delay and passband ripples. Considering that Butterworth filters have stable passband magnitude response without constant group delay and passband ripples, second-order Butterworth filters were chosen as the analog analysis filters due to the generality of the BI-DAC system design, and they were used to eliminate the images in the DAC output and the unwanted bands generated by the up-conversion, respectively.

3.2 Digital FIR filters design

In (3), we only remain the terms with \( k = 0, \ldots, M - 1 \) so that (3) can be rewritten as

\[ Y(j\Omega) = \sum_{k=0}^{M-1} \hat{X}(e^{j(\Omega T - \frac{2\pi k}{M})}) T_k(j\Omega), \]  

where

\[ T_k(j\Omega) = \frac{1}{MT} \sum_{m=1}^{M} \tilde{F}_m(e^{j(\Omega T - \frac{2\pi k}{M})}) \]
\[ \times \hat{H}_{dlm}(e^{j(\Omega T - \frac{2\pi k}{M}) - \frac{(m-1)}{MT} \pi}) H(j(\Omega - \frac{(m-1)}{MT} \pi)) \]
\[ \times H_{alm}(j(\Omega - \frac{(m-1)}{MT} \pi)) H_{abm}(j\Omega), \]

and \( T_k(j\Omega), k = 0, 1, \ldots, M - 1 \) denote the real transfer function of the BI-DAC system. It can be seen from (4) that the wideband signal is a mixture of the input signals frequency-shifted versions. The distortion function \( T_0(j\Omega) \) denotes the
group delay and the magnitude gain of the BI-DAC system. The aliasing function $T_k(j\Omega)$ for $k = 1, \ldots, M - 1$ denote the unwanted and shifted versions of the input signal that should be canceled. In the BI-DAC system, $F_m(z)$ denotes an N-tap digital FIR filter with $z$-transform as follows

$$F_m(z) = \sum_{n=0}^{N-1} f_m(n)z^{-n}.$$  

Hence, the frequency response $\hat{F}_m(e^{j(\Omega T - \frac{2\pi k}{M})})$ can be expressed as

$$\hat{F}_m(e^{j(\Omega T - \frac{2\pi k}{M})}) = \sum_{n=0}^{N-1} f_m(n)e^{-j(\Omega T - \frac{2\pi k}{M})n} = f_m^T(\Omega) - js_k(\Omega),$$  

where $f_m(n)$ is the $m$th digital FIR filter coefficient to be determined, and

$$f_m = [f_m(0), f_m(1), \ldots, f_m(N - 1)]^T$$

$$c_k(\Omega) = \left[1, \cos{(\Omega T - \frac{2\pi k}{M})}, \ldots, \cos{(N - 1)(\Omega T - \frac{2\pi k}{M})}\right]^T$$

$$s_k(\Omega) = \left[0, \sin{(\Omega T - \frac{2\pi k}{M})}, \ldots, \sin{(N - 1)(\Omega T - \frac{2\pi k}{M})}\right]^T.$$  

Thus, the real transfer function $T_k(j\Omega)$ becomes

$$T_k(j\Omega) = \sum_{m=1}^{M} f_m^T r_{k,m}(\Omega) - j \sum_{m=1}^{M} f_m^T i_{k,m}(\Omega) = r_k^T(\Omega)f - j i_k^T(\Omega)f$$

$$= \text{Re}\{T_k(j\Omega)\} - j \text{Im}\{T_k(j\Omega)\},$$  

where

$$f = [f_1^T, f_2^T, \ldots, f_M^T]^T$$

$$\text{Re}\{T_k(j\Omega)\} = r_k^T(\Omega)f, \text{Im}\{T_k(j\Omega)\} = i_k^T(\Omega)f$$

$$r_k(\Omega) = [r_{k,1}^T(\Omega), r_{k,2}^T(\Omega), \ldots, r_{k,M}^T(\Omega)]^T, \quad i_k(\Omega) = [i_{k,1}^T(\Omega), i_{k,2}^T(\Omega), \ldots, i_{k,M}^T(\Omega)]^T$$

and

$$r_{k,m}(\Omega) = \frac{1}{MT} c_k(\Omega)H_{\text{dim}}(e^{j(\Omega T - \frac{2\pi k}{M}) - \frac{(m-1)\pi}{MT}})H_{\text{dim}}(j\Omega)$$

$$\times H\left(j\left(\Omega - \frac{(m-1)\pi}{MT}\right)\right)H_{\text{dim}}\left(j\left(\Omega - \frac{(m-1)\pi}{MT}\right)\right)$$

$$i_{k,m}(\Omega) = \frac{1}{MT} s_k(\Omega)H_{\text{dim}}(e^{j(\Omega T - \frac{2\pi k}{M}) - \frac{(m-1)\pi}{MT}})H_{\text{dim}}(j\Omega)$$

$$\times H\left(j\left(\Omega - \frac{(m-1)\pi}{MT}\right)\right)H_{\text{dim}}\left(j\left(\Omega - \frac{(m-1)\pi}{MT}\right)\right).$$  

Further, $\text{Re}\{\bullet\}$ and $\text{Im}\{\bullet\}$ denote the real and imaginary parts of a vector or a complex number, respectively. If the analog filters are ideal in the BI-DAC system, an ideal wideband signal $Y(j\Omega)$ will be generated and this ideal signal will be a delayed and scaled version of the input signal, which is called the perfect-reconstruction condition. The ideal transfer function of this BI-DAC system is given by

$$D_k(j\Omega) = \begin{cases} c e^{-j\Omega M T d}, & k = 0 \\ 0, & k = 1, \ldots, M - 1 \end{cases}, \quad \Omega \in \left[0, \frac{\pi}{T}\right]$$  

where \( d \) and \( c \) are the BI-DAC system delay and gain, respectively. The approximation error, can be computed by using (7) and (8) as

\[
e_k(\Omega) = T_k(j\Omega) - D_k(j\Omega) = \text{Re}\{e_k(\Omega)\} - j\text{Im}\{e_k(\Omega)\}
\]

(9)

with

\[
\text{Re}\{e_k(\Omega)\} = r^T_k(\Omega)f - R_k(\Omega)
\]

\[
\text{Im}\{e_k(\Omega)\} = i^T_k(\Omega)f - I_k(\Omega).
\]

(10)

where

\[
R_k(\Omega) = \text{Re}\{D_k(\Omega)\}, \quad I_k(\Omega) = \text{Im}\{D_k(\Omega)\}.
\]

To approximate the perfect-reconstruction in the minimax sense, the minimax design can be formulated as follows:

minimize \( \delta \)

subject to \( W_k(\Omega)|e_k(\Omega)| \leq \delta, \quad \Omega \in \Omega_i, \quad k = 0, \ldots, M - 1, \)

where \( W_k(\Omega) \) is a positive weighting parameter specifying the relative importance between the distortion and aliasing errors, and \( \Omega_i \) is the first Nyquist zone of the BI-DAC system. It can be known from (10) that the real part approximation error \( \text{Re}\{e_k(\Omega)\} \) and the imaginary part approximation error \( \text{Im}\{e_k(\Omega)\} \) are all the function of the digital FIR filter coefficients vector \( f \). Consequently, we can reformulate the minimax design (11) as

minimize \( \delta_R + \delta_I \)

subject to \( W_k(\Omega)|\text{Re}\{e_k(\Omega)\}| \leq \delta_R \)

\( W_k(\Omega)|\text{Im}\{e_k(\Omega)\}| \leq \delta_I \)

(12)

Clearly, the minimax problem in (12) can be solved by using LP. The constraints in (12) can be rewritten as

\(-\delta_R \leq W_k(\Omega)\text{Re}\{e_k(\Omega)\} \leq \delta_R \)

\(-\delta_I \leq W_k(\Omega)\text{Im}\{e_k(\Omega)\} \leq \delta_I \)

i.e.,

\(-\delta_R \leq W_k(\Omega)(r^T_k(\Omega)f - R_k(\Omega)) \leq \delta_R \)

\(-\delta_I \leq W_k(\Omega)(i^T_k(\Omega)f - I_k(\Omega)) \leq \delta_I \)

or equivalently

\[
\begin{cases}
-\delta_R - W_k(\Omega)r^T_k(\Omega)f \leq -W_k(\Omega)R_k(\Omega) \\
-\delta_R + W_k(\Omega)r^T_k(\Omega)f \leq W_k(\Omega)R_k(\Omega)
\end{cases}
\]

and

\[
\begin{cases}
-\delta_I - W_k(\Omega)i^T_k(\Omega)f \leq -W_k(\Omega)I_k(\Omega) \\
-\delta_I + W_k(\Omega)i^T_k(\Omega)f \leq W_k(\Omega)I_k(\Omega)
\end{cases}
\]

In the frequency band \( \Omega_i \), the frequency variable \( \Omega \) is discretized over a dense set of frequency points \( \{\Omega_i, 1 \leq i \leq P_L\} \). \( P_L \) is the total number of these frequency points. \( \Omega_i \) is evenly distributed in \( \Omega_i \). Using the one-to-one mapping \( \Omega_i \rightarrow i \), we get the one-to-one mappings
As a result, (12) is cast to the following LP problem

\[ W_k(\Omega_i) r_k^T(\Omega_i) \rightarrow W_k^R(i, :) \]
\[ W_k(\Omega_i) R_k(\Omega_i) \rightarrow d_k^R(i) \]

and

\[ W_k(\Omega_i) \tilde{R}_k^T(\Omega_i) \rightarrow W_k^I(i, :) \]
\[ W_k(\Omega_i) I_k(\Omega_i) \rightarrow d_k^I(i). \]

As a result, (12) is cast to the following LP problem

\[
\begin{align*}
\text{minimize} & \quad c^T x_1 + c^T x_2 \\
\text{subject to} & \quad A_k^R x_1 \leq \tilde{d}_k^R \\
& \quad A_k^I x_2 \leq \tilde{d}_k^I,
\end{align*}
\]

where

\[ c = [1, O_{1 \times NM}]^T, \quad x_1 = [\delta_R, f^T]^T, \quad x_2 = [\delta_I, f^T]^T \]

\[ A_k^R = \begin{bmatrix} -I_{P_L \times 1} & -W_k^R \\ -I_{P_L \times 1} & W_k^R \end{bmatrix}, \quad A_k^I = \begin{bmatrix} -I_{P_L \times 1} & -W_k^I \\ -I_{P_L \times 1} & W_k^I \end{bmatrix} \]

\[ \tilde{d}_k^R = \begin{bmatrix} -d_k^R(1) \\ d_k^R(2) \\ \vdots \\ d_k^R(P_L) \end{bmatrix}, \quad \tilde{d}_k^I = \begin{bmatrix} -d_k^I(1) \\ d_k^I(2) \\ \vdots \\ d_k^I(P_L) \end{bmatrix} \]

Additionally, \( O_{1 \times NM} \) and \( I_{P_L \times 1} \) denote, respectively, a 1-by-\( NM \) zero vector and a \( P_L \)-by-1 one vector. Here, SeDuMi can be used to solve the LP problem in (13), which applies a variant of the primal-dual-interior-point method called centering-predictor-corrector method [13].

The digital FIR filter length \( N \) is a significant parameter that determines the BI-DAC system performance. As we know, when \( N \) is longer, the distortion error is smaller, but the aliasing error attenuation is larger and the system complexity also increases. As mentioned in Section 1, the performance of the DAC is evaluated by SFDR. The relationship between the desired SFDR and the BI-DAC system resolution is described as \( SFDR \text{ (dBc)} = 6.02 \times SFDR_{\text{bits}} \), where \( SFDR_{\text{bits}} \) denotes the effective number of bits [14]. For a given resolution \( SFDR_{\text{bits}} \) in the BI-DAC system, the digital FIR filter length \( N \), used in this proposed design method, is chosen in a way that decreases the distortion error to nearly the ideal of 0 dB, and the aliasing errors are reduced to a level lower than the desired SFDR.

### 3.3 Computational complexity

The main aim of this paper is to reduce the computational complexity of the digital FIR filters optimal design in the BI-DAC system. For instance, if we use the optimization design method in [10], the computational complexity is \( O(N_{D1}(NM(MP_L)^3 + (1 + MP_L)(NM)^2)) \), where \( N_{D1} \) is the number of iterations of this design method.

In contrast to the optimization design method in [10], the computational complexity of our proposed design is derived as follows:

1) Obtaining the matrix \( A_k^R x_1 \) in (13) requires \( O(2P_L(NM + 1)) \).
2) Obtaining the matrix \( A_k^I x_2 \) in (13) requires \( O(2P_L(NM + 1)) \).
Therefore, the computational complexity of the minimax design using LP is 

\[ O(4P_L \times N_{D2}(NM + 1)) \]

where \( N_{D2} \) is the number of iterations of our proposed design method. It can be seen that the computational complexity of the minimax design using LP is lower than that of the optimization design method in [10].

4 Design examples and simulation results

In this section, numerical design examples are conducted to evaluate and analyze the effectiveness and the computational complexity of our proposed design method.

4.1 Effectiveness analysis

To evaluate the effectiveness of our proposed design method, we designed a four-channel 10-bit BI-DAC system with a system sampling rate \( f_s = \frac{1}{T} \). Unless otherwise specified, in this design example, the BI-DAC system delay \( d \), the system gain \( c \), the number of the frequency sample points, and the order of the digital FIR filters are set to 25, 1, 80, and 50, respectively. The LP problem was solved using the SeDuMi Matlab toolbox [13]. The effective number of bits of the BI-DAC system can be taken any values, it is only used to calculate the desired SFDR and there is no any influence on the simulation result. As mentioned in Section 3.2, the desired SFDR of this BI-DAC system is \( 10^{\frac{60.2}{10}} \) dB. Consequently, the aliasing errors should be lower than \(-60.2\) dB in this design example. Second-order Butterworth filters were chosen as the analog analysis filters. The digital FIR filters were designed using LP under the minimax criteria. The positive weighting parameter \( W_k(\Omega) \) for \( k = 0, 1, 2, 3 \) all were set to 1 in the BI-DAC system output frequency band \([0, \frac{T}{138}]\). As described in Section 1, the optimization design method in [10] is also suitable for the BI-DAC system so that it is carried out for comparison.

The frequency responses of the optimization design method in [10] and the minimax design using LP are shown in Fig. 2. It can be seen from Fig. 2(a) that, the maximum distortion error of the optimization design method in [10] was around 0.05 dB, and the minimax design using LP had a lower distortion error which was around 0.02 dB. From Fig. 2(b), we can see that, the minimum and maximum aliasing errors of the minimax design using LP were \(-61\) dB and \(-100\) dB, respectively. So it satisfied the desired SFDR. In contrast, the minimum and maximum aliasing errors of the optimization design method in [10] were, respectively, \(-50\) dB and \(-85\) dB so that the desired SFDR could not be satisfied. Consequently, the minimax design using LP can more effectively cancel the aliasing errors than the optimization design method in [10].

4.2 Computational complexity analysis

As described in Section 3.3, we compared the computational complexity between the optimization design method in [10] and the minimax design using LP using the following parameter: \( P_L = 80 \), \( N_{D1} = 8 \) and \( N_{D2} = 5 \).

Fig. 3 shows, respectively, the computational complexity comparisons of these two design methods versus the digital FIR filters length when the number of the sub-channels \( M = 4 \), and the computational complexity comparisons of these two
Fig. 2. Optimization design method in [10] and Minimax design using LP: (a) Frequency responses of the distortion function, (b) Frequency responses of the aliasing functions.
design methods versus the number of the sub-channels when the digital FIR filters length $N = 50$. From Fig. 3(a) and (b), we found that the computational complexity of these two design methods raised as the digital FIR filters length or the number of the sub-channels increased. Further, the minimax design using LP had much lower computational complexity than the optimization design method in [10], which is the same as the description in Section 3.3. Therefore, comparing with the optimization design method in [10], our proposed design method can achieving the goal for reducing the computational complexity of the digital FIR filters optimal design in the BI-DAC system.

**Fig. 3.** Comparisons of the computational complexity between the optimization design method in [10] and Minimax design using LP: (a) Complexity versus the digital FIR filters length when the number of the sub-channels $M = 4$, (b) Complexity versus the number of the sub-channels when the digital FIR filters length $N = 50$. 
5 Conclusion

In this paper, we investigated the principle of the BI-DAC system and deduced the real transfer function of this system. To cancel the aliasing errors caused by the non-ideal analog filters performance in the BI-DAC system, the minimax design of the digital FIR filters design using LP was proposed. As an optimization problem, LP can be used to find the global optimal solution. Theoretical analysis and simulation results demonstrated that the minimax design using LP had more effective aliasing errors cancellation and much lower computational complexity than the optimization design method in [10]. Therefore, the minimax design using LP is more applicable to the case with lower aliasing errors and computational complexity.

In the future work, we will explore how to formulate the problem of designing the digital FIR filters as another optimization problems such as second-order cone programming (SOCP) which is one of the convex optimization problems.

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