A novel modified space vector pulse width modulation (MSVPWM) strategy for Z-Source inverter is presented. By rearranging the position of shoot-through states, the frequency of inductor current ripple is kept constant. Compared with existing MSVPWM strategies, the proposed approach can reduce the maximum inductor current ripple. So the volume of Z-source network inductor can be designed smaller, which brings the beneficial effect on the miniaturization of the electric vehicle controller. Theoretical findings in the novel MSVPWM for Z-Source inverter have been verified by experiment results.

1. Introduction

Electric vehicles (EV) have attracted widespread research interest worldwide due to their attractive contributions to reducing gasoline consumption and carbon dioxide emissions [1]. DC bus voltage can be adjusted and the shoot-through state is allowed in Z-Source inverter (ZSI) [2], which improves the reliability of the inverter. By shifting between the shoot-through and non-shoot-through states, capacitors and inductors are charged and discharged, which boosts the DC-link voltage. Since it was proposed, Z-Source inverter is highly regarded by the researchers. Many research works have been done for electrical vehicle applications [3–6]. Connecting with bidirectional Z-Source network between DC power source and inverter (Figure 1), bidirectional Z-Source network can solve the mismatch problem of battery voltage and DC bus voltage, which ensures the DC bus voltage constant and enlarges working area of the electric machines.

However, the Z-Source inverter itself also has some inherent defects, such as the volume of inductor and capacitor taking up much space. Especially in electric vehicle applications, it affected the miniaturization of motor drives. In order to reduce the volume of Z-Source inductor, there are generally two kinds of measures. One is adopting coupled inductor [7, 8] and the other is improving modulation method to decrease the inductance current ripple.

Many PWM control methods have been proposed for ZSI by far, including simple SPWM [2], maximum SPWM [9], maximum constant boost control [10], and modified SVPWM (MSVPWM) [11, 12]. Compared with SPWM, MSVPWM has many advantages such as high voltage utilization, low current harmonics, and low switching losses [13], which is widely used in vector control and flux weakening control of electric vehicle motor drives.

There are three different MSVPWM to distribute the shoot-through states: MSVPWM1 [14, 15], MSVPWM2 [15, 16], and MSVPWM3 [17] (Figure 2). For MSVPWM1, the shoot-through state is evenly divided into six parts. This pattern succeeds in replacing the zero state with shoot-through state while keeping the switch frequency unchanged, but the maximum boost control cannot be achieved. The divided six-part shoot-through time intervals are also fulfilled in each control cycle in MSVPWM2, achieving maximum shoot-through time. MSVPWM3 divides the shoot-through state into four parts evenly and inserts them between adjacent two zero states and active state. Compared with MSVPWM1 and MSVPWM2, shoot-through state between two active states disappears.
In [18], the author obtained the conclusion that the MSVPWM1 and MSVPWM2 resulted in the lower current ripples when compared with the MSVPWM3. However, from the perspective of inductor design, we are more concerned about the maximum inductor current ripple, which directly affects the volume and weight of the inductor. In this paper, we draw a conclusion that the MSVPWM3 resulted in the lower maximum current ripples when compared with the MSVPWM1 and MSVPWM2. Next section describes the process of detailed analysis. In [19], the shoot-through time intervals of three phase legs are calculated and rearranged according to the active state and zero state time intervals to achieve the minimum current ripple across the Z-source inductor. In fact, with this modulation strategy, the maximum current ripple across Z-source inductor is equal to the MSVPWM3.

In this paper, we proposed a novel MSVPWM strategy, called MSVPWM4, which divides shoot-through state into six parts but fixes two states' position. It can achieve lower maximum inductor current ripple than the MSVPWM mentioned above.

2. Analysis of Maximum Current Ripple for Existing MSVPWM

Traditional SVPWM divides the space into six sectors with eight space vectors as shown in Figure 3, among which $U_1 \sim U_6$ are the six active vectors and $U_0$, $U_7$ are the two zero vectors. $\vec{V}_{ref}$ is the reference vector. $T_1$ and $T_2$ are acting time of $U_6$ and $U_4$ in sector III with the expression as (1). $T_0$, $t_0$, and $T_1$ are the acting time of zero vector, shoot-through zero
vector, and sampling period, respectively. $V_{dc}$ is DC voltage and $m$ is the modulation index.

Modified SVPWM applied in ZSI has additional shoot-through zero vector partly or fully replacing conventional zero vector without changing the active vector time. Consider

$$\begin{align*}
T_1 &= \sqrt{3} \frac{|V_{ref}|}{V_{dc}} T_s \sin \left( \frac{\pi}{3} - \theta \right) \\
T_2 &= \sqrt{3} \frac{|V_{ref}|}{V_{dc}} T_s \sin \theta \quad 0 \leq \theta \leq \frac{\pi}{3} \\
T_0 &= T_s - T_1 - T_2.
\end{align*}$$

Assuming that the reference vector is in sector III, the distribution of vectors and the inductor current ripple for MSVPWM2 are shown in Figure 4. The maximum shoot-through time is increased to the zero vector time $T_0$ accordingly and maximum boost control can be achieved.

For MSVPWM2, the inductor is charged during shoot-through period and inductor current rises, while the inductor discharges during non-shoot-through period and inductor current declines. The instantaneous values of inductor current are the same at the beginning and the end of a sampling period $T_s/2$. So the current value at $T_s/2$ is $I_L$. The instantaneous inductor current ripple can be achieved as

$$\begin{align*}
y_1 &= \frac{V_{in} - V_{c}}{L} t_1, \\
y_2 &= y_1 + \frac{V}{L} (t_2 - t_1), \\
y_3 &= y_2 + \frac{V_{in} - V_{c}}{L} (t_3 - t_2), \\
y_4 &= y_3 + \frac{V_{c}}{L} (t_4 - t_3), \\
y_5 &= y_4 + \frac{V_{in} - V_{c}}{L} (t_5 - t_4), \\
y_6 &= y_5 + \frac{V}{L} (t_6 - t_5).
\end{align*}$$

(2)

The maximum inductor current ripple occurs in the adjacent sector position where $\theta = 0^\circ$ or $\theta = 60^\circ$. On this working point, the current ripple is shown in Figure 5.

The maximum inductor current ripple for MSVPWM2 becomes

$$\begin{align*}
\Delta I_{L-2} &= 2 \max \{|y_1|, |y_2|, |y_3|, |y_4|, |y_5|, |y_6|\}.
\end{align*}$$

(3)

Inductor current ripple:

$$\Delta I_{L-2} = \frac{Dm}{2Lf_s (1 - 2D)} V_{dc}.$$  

(4)

With the similar analysis method above, the maximum inductor current ripple for MSVPWM1 and MSVPWM3 is

$$\begin{align*}
\Delta I_{L-1} &= \frac{D(1 + (3/4)m)}{2Lf_s (1 - 2D)} V_{dc}, \\
\Delta I_{L-3} &= \frac{\sqrt{3} Dm}{4Lf_s (1 - 2D)} V_{dc}.
\end{align*}$$

(5)

For MSVPWM1 and MSVPWM2, the time of two different active states varies, thus causing the shoot-through state between them to move correspondingly. The space vector has six sector critical positions where shoot-through time becomes four parts instead of six and inductor current ripple becomes the biggest. For MSVPWM3, shoot-through state between two active states disappears. Though the time of two active states varies, the sum of them merely changes a little. The position of shoot-through states beside these two active states is almost unchanged, keeping the frequency of inductor current ripple constant, and the maximum current ripple is reduced than that of MSVPWM1 and MSVPWM2.

3. Novel MSVPWM to Reduce the Volume of Inductor

To compromise the maximum current ripple and average current ripple, MSVPWM4 is presented. Based on MSVPWM3,
MSVPWM4 divides shoot-through state into six parts again but fixes two states’ position (Figure 6). From left to right, the second and fifth shoot-through state are fixed to \((T_s/4 - t_0/12 \sim T_s/4 + t_0/12)\) and \((3T_s/4 - t_0/12 \sim 3T_s/4 + t_0/12)\) separately. The frequency of inductor current ripple is six times the sampling frequency. It can achieve lower inductor current ripple. The disadvantage is increasing the switching frequency.

For MSVPWM4, maximum inductor current ripple occurs when \((T_1 + T_2)\) reaches maximum when \(\theta = 30^\circ\). Inductor current is centrosymmetric within half a sampling period. The current values at \(T_s/4\), \(T_s/2\), and \(T_s\) are all equal to the average inductor current. Maximum inductor current ripple for MSVPWM4 can be calculated as

\[
\Delta i_{L-4} = \frac{D \left(\frac{(3 \sqrt{3})}{2} m + D - 1\right)}{6L_f s (1 - 2D)} V_{dc}. \tag{6}
\]

Compared with (4)–(6), we can conclude that \(\Delta i_{L-4} < \Delta i_{L-3} < \Delta i_{L-2} < \Delta i_{L-1}\). Figure 7 shows the required inductance with different MSVPWM, using the same parameter of \(\Delta i_L\), \(V_{dc}\), \(f_s\), \(m\), and \(D\). Obviously, the ZSI inductance designed with MSVPWM4 is much smaller than that with the other three MSVPWM, which is about 40% smaller than MSVPWM2.

4. Experimental Verification

For illustration of the validity of the proposed approach, the maximum inductor current ripple is both theoretically calculated and experimentally measured for different MSVPWM under the conditions as Table 1. Experimental results are summarized in Table 2 and Figure 8.

From Table 2 and Figure 8, it is noted that the proposed MSVPWM4 has the most superior performance in lowering maximum inductor current ripple when compared with other MSVPWM. The experiment results are very close to the theoretical analysis. Experiment results with \(D = 0.15\) and \(D = 0.2\) are shown in detail in Figures 9 and 10.

The experiment results show that DC link voltage is boost to 150V with \(V_{dc} = 90\)V when \(D = 0.2\). The maximum inductor current ripple with proposed MSVPWM, called MSVPWM4, is 67.7% smaller than that of MSVPWM1, 57.8% smaller than MSVPPWM2, and 51.3% smaller than MSVPWM3.

5. Conclusions

In this paper, a novel MSVPWM with lower inductor current ripple for Z-Source inverter is proposed based on rearranging the distribution of shoot-through states. Experimental results are provided to verify the effectiveness of the proposed
Figure 6: Vector distribution and inductor current ripple for MSVPWM4.

Figure 7: Inductance for different MSVPWM versus allowable inductor current ripple.

Table 1: Parameters of experiment.

| DC Source (Vdc) | Modulation Index (m) | Carrier Frequency (fs) | Z-Source inductor (L) | Z-Source capacitor (C) |
|-----------------|----------------------|------------------------|-----------------------|-----------------------|
| 90 v            | 0.6                  | 2 kHz                  | 1 mH                  | 2700 uF               |
Figure 8: Comparison between theoretical predictions and experimental results of $\Delta i_L$.

Figure 9: Maximum inductor current ripple for different MSVPWM with $D = 0.15$. 
modulation method. The MSVPWM4 has smaller inductor current ripple than that of existing methods, thus reducing the volume of ZSI inductance, which brings the beneficial effect on the miniaturization of the drives on electric vehicles.

**Conflict of Interests**

The authors declare that there is no conflict of interests regarding the publication of this paper.

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