Gaussian Process Models with Parallelization and GPU acceleration

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Abstract

In this work, we present an extension of Gaussian process (GP) models with sophisticated parallelization and GPU acceleration. The parallelization scheme arises naturally from the modular computational structure w.r.t. datapoints in the sparse Gaussian process formulation. Additionally, the computational bottleneck is implemented with GPU acceleration for further speed up. Combining both techniques allows applying Gaussian process models to millions of datapoints. The efficiency of our algorithm is demonstrated with a synthetic dataset. Its source code has been integrated into our popular software library GPy.

1 Introduction

Gaussian processes (GPs), as non-parametric, data driven approaches, are very popular for regression and dimension reduction problems. Their formulation is responsible for their power but also their memory and computational limitations. Considering a regression problem with observed input and output data, collected by rows in matrices $Y \in \mathbb{R}^{N \times D}$ and $X \in \mathbb{R}^{N \times Q}$ respectively, the input and output are related according to a set of latent functions $f_d$ plus Gaussian noise:

$$y_{n,d} = f_d(x_n) + \epsilon_{n,d}, \epsilon_{n,d} \sim \mathcal{N}(0, \beta^{-1}),$$

where $y_{n,d}$ denotes the $d$th dimension of the $n$th output point and $x_n$ denotes the $n$th input point. A GP prior with covariance function $k_f$ is placed on the noise-free observation $f_d(x_n)$, which can later be integrated out. This will couple the data points in a $N \times N$ covariance matrix $K_{ff} = k_f(X, X)$, the inversion of which scales with $O(N^3)$. The main line of work in the literature attempting to speed up GPs is related to low rank approximations [1, 2, 3, 4, 5, 6] which decouple the function instantiations $F = f(X)$ given a set of $M$ auxiliary (or inducing) function input-outputs pairs, denoted by $z_m$ and $u_m$ respectively, that result in a low rank approximation of $K_{ff}$. However, even if the resulting computational cost is $O(NM^2)$, in big data domain (millions of data points), the computational bottlenecks encountered in practice are actually associated with large $N$, since the number of inducing points $M$ can be kept small.

In this paper we are interested in scaling up inference of GP models using parallel computations with respect to the data points. To achieve this, we exploit the independence assumptions induced by using auxiliary variables as well as the full independence assumption in $p(y_{n,d} | E_d(x_n))$ of the noise model of equation (1). In practice, this means that all operations involving data points can be written as sums over $N$. This observation has recently been exploited by Hensman et al. [7] for stochastic variational inference and by Gal et al. [8] for distributed variational inference. Here we adopt a similar formulation, but focus on presenting a distributed implementation which combines large-scale parallelization (with MPI) and GPU acceleration. It is transparently embedded for sparse GP based models in GPy, such as Bayesian GP-LVM [9, 10], MRD [11] and deep GPs [12]. With

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experiments on synthetic data, we show that our inference algorithm can efficiently make use of hundreds of computer nodes, which allows for the consideration of genuine big data.

2 Distributed inference in GP models

The sparse inference method employed in GPy follows the variational formulation of [6], which constructs a variational lower bound to the true log marginal likelihood of the GP using inducing point representations. Specifically, we have, \( \log p(Y|X) \geq \sum_{d=1}^{D} F_d \), with:

\[
F_d = \log \frac{\beta^{N/2} |K_{uu}|^{1/2}}{(2\pi)^{N/2} |K_{uu} + \beta\Phi|^{1/2}} - \frac{\beta}{2} y_d^T y_d \\
+ \frac{\beta^2}{2} y_d^T K_{fu} (\beta\Phi + K_{uu})^{-1} K_{fu}^T y_d - \frac{\beta}{2} \sum_{n=1}^{N} \Tr(K_{uu}^{-1})
\]  

(2)

where \( \phi = \text{tr}(K_{ff}) \) and \( \Phi = K_{fu}^T K_{fu} \). Here, \( K_{fu} \) contains the cross-covariances between the training and inducing inputs (\( X \) and \( Z \)) and \( K_{uu} \) is the matrix of (co)variances between points in \( Z \).

As can be seen, the variational bound is factorised with respect to data dimensions. However, for many practical applications we would like parallelization with respect to the number of data, \( N \). Towards this direction, we can use the fact that the covariance matrices can be calculated in a decomposable way, as mentioned in [9]. Specifically, we can write \( \phi = \sum_{n=1}^{N} k_f(x_n, x_n) \) and \( \Phi = \sum_{n=1}^{N} (K_{fu})^T_n (K_{fu})_n \), where \( (K_{fu})_n \) is an \( M \)-dimensional vector with its \( m \)th element given by \( k_f(x_n, z_m) \). By additionally introducing \( \Psi = \sum_{n=1}^{N} (K_{fu})^T_n y_n \), we can re-write the full variational bound (for all dimensions) as \( \log p(Y|X) \geq F \), with:

\[
F = D \log \frac{\beta^{N/2} |K_{uu}|^{1/2}}{(2\pi)^{N/2} |K_{uu} + \beta\Phi|^{1/2}} - \frac{\beta}{2} \sum_{n=1}^{N} y_n^T y_n \\
+ \frac{\beta^2}{2} \Psi^T (\beta\Phi + K_{uu})^{-1} \Psi - \frac{\beta D \phi}{2} + \frac{\beta D}{2} \Tr(K_{uu}^{-1})
\]  

(3)

Notice that the above bound is not fully factorised, due to the term \((\beta\Phi + K_{uu})^{-1}\). However, that term is an \( M \times M \) matrix which is cheap to invert for the usual choices of \( M \), whereas all the other expensive computations associated with data points can be parallelised. In the unsupervised version of this model, namely the Bayesian GP-LVM, \( X \) is treated as a latent variable and is also integrated out, after introducing a prior distribution \( p(X) \) and a variational posterior distribution \( q(X) \). Both of these distributions are Gaussian and factorised with respect to \( N \). Then, similarly to the sparse GP regression case, we can define a variational lower bound on \( \log p(Y) \) where:

\[
\log p(Y) \geq \langle F \rangle_{q(X)} - \sum_{n=1}^{N} \int q(x_n) \log \frac{p(x_n)}{q(x_n)} \, dx_n .
\]  

(4)

Due to the factorisation of \( q(X) \) w.r.t. datapoints, the summations over \( N \) are maintained in the unsupervised version. Specifically, the only difference between \( F \) and \( \langle F \rangle_{q(X)} \) is that \( \phi, \Psi \) and \( \Phi \) are turned into expectations over \( q(X) \), e.g. \( \phi = \sum_{n=1}^{N} \int_{x_n} k_f(x_n, x_n) q(x_n) \) and similarly for \( \Phi \) and \( \Psi \). Compared to the approach taken in [7], the formulation presented here is not fully factorised but results in a tighter bound and, additionally, extends to the unsupervised case straightforwardly. This formulation is similar to the distributed variational inference proposed by Gal et al. [8]. Using the above variational bound as an objective function means that we have the following parameters to optimise: the kernel parameters \( \theta \), the noise parameter \( \beta \) and the inducing inputs \( Z \). For the rest of our analysis we assume the more general unsupervised scenario, which also involves the parameters of \( \{q(x_n)\}_{n=1}^{N} \), namely \( \{\mu_n, \Sigma_n\}_{n=1}^{N} \).

As mentioned above, all the computations w.r.t. datapoints are combined according to a couple of summations. Therefore, data parallelism is naturally applicable, so that all datapoints are distributed across computer nodes and every node only performs the computation w.r.t. its local portion of data, i.e. the computation of \( \phi, \Phi, \Psi \) (and also the second term of equation (4) in the Bayesian GP-LVM case). Although all the computations w.r.t. datapoints are parallelizable, to recover the exact lower bound, a few steps of indistinguishable computations have to be done after collecting the intermediate
Due to the specific architecture of GPU, there are a couple of constraints for programming, e.g., different computing blocks are not synchronized within a GPU function. It is extremely efficient at solving a large number of similar small tasks, which is ideal for our inference algorithm. Therefore, we take advantage of the specialized architecture by shifting the computation of Φ and Ψ onto GPU. The key for making full use of its computational power is to properly divide computational workloads for parallelization within GPU. Due to the specific architecture of GPU, there are a couple of constraints for programming, e.g., different computing blocks are not synchronized within a GPU function and synchronizion of writing GPU global memory is very expensive. The optimal division of workloads depends on the specific type of data, e.g. the size difference among N, M, D and Q. Our implementation tries to balance between different choices and gives a generic design, which is suitable for most cases.

In particular, we assign each computing block of GPU a subset of inducing inputs for computing Ψ and a subset of inducing input pairs for computing Φ. We assign each thread within a computing block of GPU a subset of inducing input pairs for computing Ψ. In particular, we assign each computing block of GPU a subset of inducing inputs for computing Ψ.

More sophisticated synchronization schemes are supported with new cards, but, to support a wide range of GPU cards, we currently stick to compute capability 2.0.

### Table 1: The GPU function for computing Ψ or Φ

| Line | Description |
|------|-------------|
| 1 | for each inducing input m (each pair \((m_1, m_2)\) for \(\Phi\)) do ▷ distributed across GPU blocks |
| 2 | for each datapoint n do ▷ distributed across GPU threads |
| 3 | Compute \(\Psi_{nm}\) or \(\Psi_{m_1m_2}\) |
| 4 | Write \(\Psi_{nm}\) into global GPU memory, when computing \(\Psi\). |
| 5 | Write \(\Phi_{m_1m_2}\) into shared local memory, when computing \(\Phi\). |
| 6 | end for |
| 7 | Sum \(\Phi_{m_1m_2}\) from all threads and write into global memory, when computing \(\Phi\). |
| 8 | end for |

### Table 2: Computing the gradients that depend on \(\Psi\) or \(\Phi\)

| Line | Description |
|------|-------------|
| 1 | for each input dimension \(q\) do |
| 2 | Set \(\frac{\partial L}{\partial \Phi}\) or \(\frac{\partial L}{\partial \Psi}\) = 0. |
| 3 | for each datapoint n do ▷ distributed across GPU threads |
| 4 | for each inducing input m (each pair \((m_1, m_2)\) for \(\Phi\)) do ▷ distributed across GPU blocks |
| 5 | Compute \(\frac{\partial \Phi_{nm}}{\partial \Phi}, \frac{\partial \Phi_{nm}}{\partial \Psi}, \frac{\partial \Phi_{nm}}{\partial S}, \frac{\partial \Phi_{nm}}{\partial \theta}, \frac{\partial \Phi_{m_1m_2}}{\partial \Phi}, \frac{\partial \Phi_{m_1m_2}}{\partial \Psi}, \frac{\partial \Phi_{m_1m_2}}{\partial S}, \frac{\partial \Phi_{m_1m_2}}{\partial \theta}\), or \(\frac{\partial \Phi_{m_1m_2}}{\partial \beta}\). |
| 6 | Compute the derivatives of \(L\) w.r.t. parameters by combining \(\frac{\partial L}{\partial \Phi}\) or \(\frac{\partial L}{\partial \Psi}\). |
| 7 | Add \((\frac{\partial L}{\partial \Phi})_{nm}\) into \((\frac{\partial L}{\partial \Psi})_{mq}\). |
| 8 | end for |
| 9 | Sum the intermediate results for \((\frac{\partial L}{\partial \Phi})_{mq}\) and \((\frac{\partial L}{\partial \Psi})_{mq}\). |
| 10 | end for |
| 11 | end for |
| 12 | Sum the intermediate results for \(\frac{\partial L}{\partial \beta}\). |

results from individual nodes, e.g., \((\beta \Phi + \mathbf{K}_{uu})^{-1}\) in particular. After getting the lower bound, the derivatives w.r.t. model parameters can be estimated locally. In principle, with a distributed optimizer, all the local parameters can be determined locally, while the global parameters such as kernel parameters can be determined by synchronizing their gradients among all the computer nodes. However, to make use of the existing optimizers like L-BFGS-B in Scipy, we currently collect the gradients of both local and global parameters into one node, estimate the new parameters according to the chosen optimizer, and spread the new parameters across the rest computer nodes.

### 3 GPU acceleration

The scheme mentioned above dramatically scales up computations by exploiting data parallelism. However, we notice that for some models like the Bayesian GP-LVM, the quantities \(\Phi\) and \(\Psi\) constitute the computational bottleneck. Computing these quantities requires to go through all datapoints multiple times, something which can take more than 99% of inference time for large datasets. To further speed up the inference, we make use of GPU acceleration. GPU is a type of specialized computation hardware, which consists of a large number of small processing units (e.g., GTX TITAN black has about 2,880 cores). It is extremely efficient at solving a large number of similar small tasks, which is ideal for our inference algorithm. Therefore, we take advantage of the specialized architecture by shifting the computation of \(\Phi\) and \(\Psi\) onto GPU. The key for making full use of its computational power is to properly divide computational workloads for parallelization within GPU.
block a subset of datapoints. With this division, for computing $\Psi$, each thread can write its result directly into the GPU memory, and for $\Phi$ the intermediate results w.r.t. datapoints are stored in shared local memory; these results are later summed together across different threads and written into the GPU memory (see Table 1). For computing the gradients w.r.t. parameters that depend on $\Psi$ and $\Phi$, the same division of workloads is applied. Due to the different sizes of individual parameters, their gradients are produced at different levels of loops (see Table 2). On top of GPU acceleration, the parallelism scheme mentioned in the previous section can be easily integrated. The computation of the lower bound can be distributed across multiple GPU cards by assigning each GPU card a subset of datapoints. Then, the computation of $\Phi$ and $\Psi$ for the local portion is shifted onto individual GPU cards, and the local results are combined as mentioned before.

4 Experiments

The performance of our parallelization and GPU acceleration algorithms are evaluated through a synthetic dataset. The dataset was generated by randomly sampling 64k 1D datapoints, and mapping into 3D space by sampling according to a Radius Basis Function (RBF) kernel function. The task is to recover the 1D latent representations from these 3D datapoints by applying the Bayesian GP-LVM as a dimension reduction algorithm. Therefore, the dimensionality of latent space is set to be one, and the number of inducing inputs is set to be 100. A list of datasets with different sizes, ranging from 1k to 64k, are used with a list of different parallelization configurations. All the experiments ran on a single machine with 4 AMD Opteron processors (32 cores in total) and 4 NVIDIA GTX 480 GPU cards, but can directly run on clusters without any changes on the code. The efficiency of our parallel inference algorithm is measured by the average computational time per iteration (shown in Fig. 1a). The computational time scales linearly with the number of datapoints, which is expected from the computational complexity, and the speed of inference scales roughly linearly with the number of CPUs/GPUs, which shows the efficiency of our parallel implementation, in which the communication overhead is negligible. Note that the speed with a single GPU card is significantly higher than a 32-core computer node. Additionally, the percentage of time used for indistributable computation is shown in Fig. 1b. It shows that most of time is spent on distributable computation, which means that with more computer resources the speed can further increase.

5 Conclusion

We have presented an efficient parallelised implementation of Gaussian process models and, additionally, the first algorithm with GPU acceleration in this domain. Our results constitute a counter argument against the prejudice that GPs are not applicable to big data, as long as efficient implementations are considered. As future work, we plan to scale up other GP-based models already implemented in GPy and compare our results to methods that currently dominate the domain of big data, like deep learning.
Acknowledgments

We acknowledge funding by the RADIANT and WYSIWYD (EU FP7-ICT) projects. JH was funded by an MRC fellowship.

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