Supplementary Materials

Short-Term Memory Dynamics of TiN/Ti/TiO$_2$/SiO$_x$/Si Resistive Random Access Memory

Hyojong Cho and Sungjun Kim *

Division of Electronics and Electrical Engineering, Dongguk University, Seoul 04620, South Korea; chj9102@dgu.ac.kr
* Correspondence: sungjun@dongguk.edu

Figure S1. Statistical distribution (cycle-to-cycle) of TiN/Ti/TiO$_2$/SiO$_x$/Si device in the HRS and LRS.
Figure S2. Potentiation and depression curves of TiN/Ti/TiO₂/SiOₓ/Si device depending on the pulse width. Potentiation: (a) 100 μs, (b) 1 ms, (c) 10 ms, (d) 100 ms. Depression: (e) 100 μs, (f) 1 ms, (g) 10 ms, (h) 100 ms.