A COMPENSATED IMPEDANCE BASED GROUND FAULT LOCATOR FOR AN UNEARTHED POWER SYSTEM

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Abstract

Continuous operation of a power system during a ground fault can be achieved by employing either a High Resistance Grounded (HRG) power source or a floating power source. However, if a second ground fault occurs in the system it can cause significant problems and therefore the presence of the First Ground Fault (FGF) must be detected, located, and repaired quickly. In this study, an active impedance based fault location method is proposed to locate the FGF in a HRG power system. The study presented shows that the magnitude of the fault resistance has only a small influence on the proposed technique. However, for unbalanced faults, the impedance of the healthy phases can distort the impedance measured and impair the accuracy of the fault location method. Therefore, a new compensated method is proposed to compensate for this distortion. The results show that a high accuracy can be achieved with the compensation, with a location error lower than 5%.

1. Introduction

The use of High-Resistance Ground (HRG) or ungrounded supplies in Marine power systems has shown many benefits compared to the use of low-resistance or solidly grounded systems. The most important benefit is the ability for continuous power flow even in the presence of a single (first) fault to ground [1]. This is from the perspective of power quality and management. Moreover, this grounding technique also helps in minimising possible arc events (that may lead to damage of electrical apparatus) [1,2,3] reducing outage time, production losses and equipment damage [4].

The major issue with this type of grounding is that if the first ground fault has not been cleared, a second ground fault leads to a phase-to-phase fault condition with a high fault current or an arcing hazard. Consequently, the time to detect and locate the first ground fault (FGF) is highly important. However, locating the FGF is challenging for traditional fault location techniques that use the fault current and voltage to estimate the impedance to the fault location [4].

Recent research on fault location in HRG or ungrounded power systems has been published in [4 – 13]. The authors in [5] used the voltage measurement at source end to locate the fault. The Wavelet Transform (WT) with multi-resolution analysis (MRA) is utilised to extract specific patterns for each fault location from system fundamental and the background noise. However, the technique requires a zonal system configuration as the fault location strategy is based on a zonal pattern. Experimental validation of the technique is presented in [6], where the inherent high-frequency signals associated with the switching events of converters is used to locate the fault. Another drawback of this method is the requirement of measurements in a relatively high frequency range (over 100 kHz). The influence of the converter’s coupling capacitance on the accuracy of the background noise pattern is assessed in [7,8]. The study showed that the coupling capacitance has a potential influence on the scheme that requires the used pattern to be changed for different coupling capacitance values. The study also showed that the WT requires a high-frequency decomposition band. A different technique is suggested in [9]. In this research, the authors used the midpoint voltage measurement on the DC side of an AC-DC-AC converter to identify and locate a line-to-ground fault. The algorithm is based on comparing the measured DC Voltage (Vng) to three possible voltages in the system (line side, dc side and drive side). If the measured voltage is similar to one of these voltages, the fault is at that location in the system [9]. However, this technique is not tested on a distribution system with different configurations. The same author presented a self-diagnosis algorithm for multiple converters in a HRG system. The disadvantage of proposed technique is the complex hardware and high-level hardware/software required [10]. Single-ended and two ended approaches are proposed to locate faults on multi-phase systems which include laterals in [11]. The faulted section is firstly detected, and then the fault position is located through detailed circuit and topology analysis of the faulty section. However, the technique requires measurements from several different terminals.

Active fault location methods have been proposed by other researchers in order to improve the signal to noise ratio of measurements made when trying to identify and locate a FGF. Active impedance estimation is used to locate the fault in the HRG integrated power system presented in [12]. Although the technique showed reliable outcomes, the study did not consider a proper resolution for high load powers. The authors suggested switching off the high load during the fault location process. Moreover, this study suggested a method based on
impedance measurements but not influenced by the system load [13]. However, the method requires multiple measurements throughout the system to increase the accuracy, and a communication channel is essential between the measurement units and the central processing unit. The authors used an injected (5A to 10 A) current pulse operating at the tenth harmonic (600 Hz) to locate the FGF in a HRG system in [4]. The fault is located by monitoring and sensing the injected pulse. However, the accuracy of the scheme requires a high number of sensing devices throughout the system which increases the installation cost. Another study suggested the utilisation of a two-ended impedance based method [14]. Though this method offers a good accuracy, it required two sets of synchronised voltage and current measurements for each protected line.

This paper presents a direct and precise method for first ground fault location in HRG integrated power systems which uses measurement of the ground voltage and current i.e. it is a single ended impedance based method. The work also suggests a new technique to overcome the influences of high load levels and the non-faulted phases on the accuracy of the estimated fault location. The single-ended estimation method operates at non-fundamental frequencies and are shown to locate the first ground fault with errors of less than 6%.

2. The Proposed FGF Location Technique

The non-compensated and compensated active first ground fault location techniques applied to a HRG power system will be introduced in this section.

2.1. The non-compensated approach

A simplified integrated power system such as the type found in a naval power system with a HRG power source and floating phase, be used to detect the presence of the FGF. When the fault is shown in Fig. 2 [12]. The non-compensated approach to overcome the influences of high load levels and the non-faulted phases on the accuracy of the estimated fault location. The single-ended estimation method operates at non-fundamental frequencies and are shown to locate the first ground fault with errors of less than 6%.

The switched transient current flows through both the faulted (Z_{s-c} and Z_{x-c}) and healthy cable (Z_{non-f}) to the fault point. Z_{non-f} is calculated as following:

\[ Z_{non-f} = Z_T + Z_{load-c} + Z_{l-xc} \]

where \( Z_T \) is the equivalent impedance of the healthy phases and described as

\[ Z_T = (Z_{s-a} + Z_{x-a} + Z_{l-xa})/(Z_{s-b} + Z_{x-b} + Z_{l-xb}) \]

The majority of the switched transient current will flow through the faulted phase if \( Z_{non-f} \) is significantly larger than \( Z_{s-c} \) and \( Z_{x-c} \). Hence, the part that flows through \( Z_{non-f} \) is usually negligible. The distance between the fault location and the source end is calculated as:

\[ X_{x-c} = \frac{\text{imag} \left( \text{FFT}(V_{\text{switch}}) \right)}{\text{FFT}(V_{\text{switch}}) - Z_{x-c}} \]

\( Z_{s-c} \) can be estimated using measurements made immediately before the fault occurs at the source terminals. Consequently, removing the FGF as quickly as possible will prevent the possibility of a high fault current that could be created due to a second ground fault.

2.2. The compensated approach

A significant part of the transient current will pass through \( Z_{non-f} \) whenever \( Z_{non-f} \) is comparable to the impedance of the faulted branch – this is particularly true when the FGF
impedance is high. As a result of this condition, (3) will lead to a high estimation error. To solve this issue, a new compensation technique is proposed to compensate for the error present due to the current passing through $Z_{\text{non-f}}$. A derivation of the method is presented below using the equivalent circuit of Fig. 2.

\[
V_{\text{switch}} = Z_{\text{g}} * I_s + Z_{\text{x-c}} * I_s + R_f * I_f \tag{4}
\]

But, $I_s = I_{\text{switch}} - I_{\text{non-f}}$, then

\[
V_{\text{switch}} = Z_{\text{g}} * (I_{\text{switch}} - I_{\text{non-f}}) + Z_{\text{x-c}} * (I_{\text{switch}} - I_{\text{non-f}}) + R_f * I_{\text{switch}} \tag{5}
\]

Dividing (5) by $I_{\text{switch}}$ yields,

\[
\frac{V_{\text{switch}}}{I_{\text{switch}}} = Z_{\text{g}} * \left(1 - \frac{I_{\text{non-f}}}{I_{\text{switch}}}\right) + Z_{\text{x-c}} * \left(1 - \frac{I_{\text{non-f}}}{I_{\text{switch}}}\right) + R_f \tag{6}
\]

Rearrange (6) for $X_{\text{x-c}}$, gives (7),

\[
X_{\text{x-c}} = \text{Im} \left[ \frac{\text{FFT}(V_{\text{switch}})}{\text{FFT}(I_{\text{switch}})} \frac{Z_{\text{g}} * (1 - \frac{I_{\text{non-f}}}{I_{\text{switch}}})}{Z_{\text{x-c}} * (1 - \frac{I_{\text{non-f}}}{I_{\text{switch}}})} \right] \tag{7}
\]

where, \( \frac{I_{\text{non-f}}}{I_{\text{switch}}} = \frac{Z_{\text{g}} + Z_{\text{x-c}}}{Z_{\text{x-c}} + Z_{\text{g}} + Z_{\text{non-f}}} \tag{8}\)

Finally, an iterative approach is required to apply (7) and (8) as follows:

1) In the beginning, pre-fault measurement is used to estimate the source impedance and the load.
2) $Z_{\text{x-c}}$ is assumed to equal Zero, and an initial value for $\frac{I_{\text{non-f}}}{I_{\text{switch}}}$ using (8) is calculated.
3) Then, (7) is used to estimate a new value for $X_{\text{x-c}}$ and a new fault location.
4) $X_{\text{x-c}}$ calculated in step (3) is used to update $\frac{I_{\text{non-f}}}{I_{\text{switch}}}$ using (8).
5) Again, $X_{\text{x-c}}$ is updated using (7).
6) This iteration is continued until the distance between two successive iterations converges to a pre-set tolerance such as 0.5 m.

3. Verification Through Simulation

A three-phase integrated power system has been simulated in Matlab/Simulink as shown in Figure 3. The system consists of the primary source grounded with high resistance ($Z_{\text{ground}}$), a three-phase transmission line, and a floating three-phase load. The transmission line is divided into five equal sections of 10 metres. Further details are presented in Table 1. The measured line to ground voltage is used to detect the presence of the FGF, and after 10ms, $R_{\text{switch}}$ is switched into the ground circuit for 30 µs. An example of the measured voltage and current are shown in Fig. 4 with two different $R_{\text{switch}}$ values. The lower impedance creates a higher magnitude transient injection. Fig. 5 shows another measurement but with two different fault resistances. This Figure shows that the fault resistance has a very small influence on the location technique for these low values of $R_{\text{switch}}$.
Table 1 The parameter values for the simulated system

| Parameter  | Value             |
|------------|-------------------|
| Voltage    | 440V, 50Hz        |
| Z_s        | 0.0011 + j0.0096 Ω|
| R_{line/m} | 30 μΩ/m           |
| L_{line/m} | 0.24 μH/m         |
| Load       | 10-100 kW         |
| Z_{earth}  | 100 kΩ            |

![Fig. 5 V_{switch} and I_{switch} with R_{switch} = 200Ω, for different fault resistances](image)

Fig. 5 V_{switch} and I_{switch} with R_{switch} = 200Ω, for different fault resistances (a) Switching voltage, (b) Switching current

Fig. 6 presents the estimated reactance for three locations, 0.0m (S-end), 30m and 50m compared to the actual reactance of the line to these fault locations. A third line is included for each location which shows a curve fit of the estimated reactance using a first order least square curve fitting technique. It is clear that the technique offers a high accuracy for which the highest error was 4.6%, occurring when the fault is located at the end of the line. The accuracy could be increased if a lower switching resistance is used which will create higher magnitude voltage and current transients and therefore a better SNR. For example, the error reduced to 1.45% when R_{switch} is reduced to 20Ω. Note that the estimated distance is the average of all the possible distances over the frequency range used (between 250Hz and 3kHz) [15].

The second scenario was to study the algorithm robustness against load change. Hence with the same system conditions, the load is increased to 100kW. The reactance estimation presented in Fig. 7(a) shows that the accuracy reduces significantly with a maximum error of 22% for the same fault conditions initiated in the previously described scenarios. To further examine the reason behind this divergence in accuracy, the switched current pulse was examined and it had been found that the majority of the transient pulse flowed through the healthy phases. This is because, when the load power increases, the load resistance decreases and hence, the equivalent impedance of the non-faulted phases reduced and becomes comparable to the faulted phase total impedance. A simulation was carried out to verify this theory. The healthy phase powers were reduced to 10kW and then 1kW.

![Fig. 6 Estimated reactance with R_{switch} = 200Ω, R_f = 0.1Ω](image)

![Fig. 7 The estimated reactance using (3), (a) balanced three phase load of 100 kW (b) The healthy phases load is changed to 10 kW and then 1kW.](image)
influence of the load resistance for a fault at 50m. This significant outcome shows that the error was due to the equivalent impedance of the non-faulty phases and loads and the neglected part of the transient flowing through them when (3) is used to calculate fault location.

A third set of simulations was performed to demonstrate the compensation algorithm developed in Section 2.2. The same fault and system conditions were utilised to verify the proposed technique. The results obtained from applying the iterative method of equations (7) and (8) are shown in Fig. 8. These three estimated reactances indicate that the proposed algorithm can reliably compensate the influence of the equivalent impedance of the non-faulty phases. The error is potentially reduced for a fault at the end of line from 22% to almost 5%. A similar enhancement is achieved for the other two faults. The algorithm mostly needs three iterations to converge to the pre-set tolerance which in this case is 0.5m. Note the calculated error is presented on the figure legend for the curve fitted using a first-order least square method while the fluctuating line is the actual estimated reactance.

5. References

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