Optimal and resilient coordination of virtual batteries in distribution feeders

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Abstract—This paper presents a novel hierarchical framework for real-time, network-admissible coordination of responsive grid resources aggregated into virtual batteries (VBs). In this context, a VB represents a local aggregation of directly controlled loads, such as smart inverters, electric water heaters, and air-conditioners. The coordination is achieved by solving an optimization problem to disaggregate a feeder’s desired reference trajectory into constraint-aware set-points for the VBs. Specifically, a novel, provably-tight, convex relaxation of the AC optimal power flow (OPF) problem is presented to optimally dispatch the VBs to track the feeder’s desired power trajectory. In addition to the optimal VB dispatch scheme, a real-time, corrective control scheme is designed, which is based on optimal proportional-integral (PI) control with anti-windup, to reject intra-feeder and inter-feeder disturbances that arise during operation of the power system. Simulation results conducted on a modified IEEE test system demonstrate the effectiveness of the proposed multi-layer VB coordination framework.

I. INTRODUCTION

A. Background and Motivation

Coordinated control of demand-side, distributed energy resources (DERs), such as grid-tied PV inverters, distributed battery storage, and thermostatically controlled loads (TCLs; e.g., water heaters and air conditioners) is part of the solution that supports a renewable energy future [1–4]. Much of the recent literature on the coordination of DERs has focused on distributed control methodologies to turn large-scale aggregations of DERs into dispatchable grid assets (similar to [5–7]). Since the aggregation of DERs is dispatched as a single entity by a centralized coordinator and is subject to power and energy limits, the DER fleet is often referred to as a “virtual battery” (VB) [8–10]. To control and dispatch the VBs, much of the literature has focused on optimizing the VB operation over ISO/TSO market signals, but this does not directly consider the underlying AC network and the distribution system operator (DSO) constraints (e.g., voltage or power limits), see for example [11], [12].

To avoid violating operational limits of the grid and to ensure system reliability with DERs at scale, coordination between a DSO and DER owners and aggregators will become critical. This has spurred a multitude of concepts and models for how DSOs can interact with DERs, aggregators, and whole-sale (transmission) markets [13–15]. In this manuscript, we focus on the so-called “Market DSO” model, e.g., see [13], where the DSO performs coordination and aggregation of DERs to deliver grid services. While such a setup could preclude independent DER aggregators (i.e., increases regulatory complexity), the model simplifies the interaction between wholesale market signals and the DSO and the ideas herein can be adapted further to enable independent

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network-admissible coordination of VBs, this paper presents a hierarchical (multi-layer) framework for coordinating demand-side flexibility in the form of VBs (please see Fig. 1). The hierarchical coordination consists of a novel, convex OPF relaxation, which is provably tight at optimality under realistic conditions and generates grid-aware, feasible set-points for the VBs. The optimization is represented by the box “Optimal VB set-point dispatcher” in Fig. 1.

To ensure real-time tracking of VB set-points despite forecast and modeling errors and to ensure resilience, the hierarchy is augmented with a real-time controller to correct the feeder economic set-points. The real-time controller borrows concepts from wide-area control (WAC) [22], including (local) droop [23] and (regional) automatic generation control, and adapts them to dynamically managing VB power in distribution feeders. This results in a VB coordination scheme that enables a feeder head-node to optimally track a power reference while correcting, in real-time, for unexpected small (local, intra-feeder) and large (regional, inter-feeder) disturbances.

This scheme thus effectively deals with stochasticity, both in the input disturbances to these VBs (e.g., solar variations) - through the PI control mechanism, which rejects these disturbances, as well as that in the VB parameters themselves - by re-solving the OPF problem about every minute, and re-tuning the corrective controller gains about every 5 minutes with updated parameters. Unlike most of the existing literature, the presented framework explicitly and effectively accounts for AC (radial) networks and energy-constrained VB resources and is robust and resilient to grid disturbances. Similar to [9], [24], we consider VBs at the scale of 100-200 DERs per VB that are managed locally (e.g., from the same neighborhood) via load control (with full state information) and subject to lags from device dispatch and communications.

Prior work on hierarchical control of DERs in microgrids (e.g., [25]) has mainly considered using frequency and voltage droop characteristics to generate active and reactive power set-points for DERs using local measurements of frequency and voltage and compensating for the deviations, but in this paper, we compensate for the deviation in the head node power of the feeder from the economic set-point, thus taking into account the economic trajectory. Moreover, in this paper, the design of the proportional and PI gains is done based on the grid topology and device constraints, unlike local droop-based control strategies that are generally not cognizant of the network. While [26] develops a local (proportional) controller that incorporates network conditions into gains to minimize voltage deviations with active power injections, it does not consider system-wide power tracking objectives such as an economic trajectory that satisfies voltage limits across the feeder. Reference [27] suggests a Distributed Averaging PI (DAPI) control strategy to ensure proportional power-sharing and economic optimality, but since it requires extensive communication between the DERs, it may not be feasible on a large scale. Moreover, while the droop coefficients in [27] are chosen proportionally according to DER power limits, state-of-charge limits are not considered, and the coefficients are not optimized to minimize head node power deviations from the economic trajectory. In this paper, we overcome the above limitations through a novel hierarchical control of DERs.

To be more specific, the “optimal VB set-point dispatcher” in Fig. 1 considers a convex relaxation of the (balanced) AC network model. Furthermore, the convex OPF formulation is proven tight at optimality, which guarantees that the prediction of future physical operating states of the grid and the VBs are accurate. This is achieved by decomposing the feeder head-node (i.e., substation) economic reference into the aggregate VB dispatch, net-demand, and approximated total feeder line losses. The key feature here is the use of a first-order prediction model for the line losses to simplify the problem. It is shown through analysis and with simulations that this VB-plus-losses reference can achieve a tight optimal solution under practical conditions. To provide the formulation with updated estimates of feeder losses and network topology, it is assumed that a distribution system state estimator (DSSE) is available [28] (not considered in this work).

The VB set-points from the above optimization problem are further augmented with a real-time controller, which enables tracking under modeling and net-load variations due to, for example, solar PV injections. The controller is divided into intra-feeder and inter-feeder operations. The intra-feeder, droop-like proportional controller is implemented to correct the optimal VB set-points in real-time to track the feeder’s head-node power reference signal. The inter-feeder proportional-integral (PI) controller with anti-windup allows for real-time disturbance rejection in the event of larger disturbance across multiple feeders, such as malfunctions in the VB’s cyber network or the network structure, thus, ensuring resilience. The only measurement necessary for this purpose is the head-node power from the feeders.

B. Original Contributions

In summary, the main contributions of this paper are as follows:

- A hierarchical framework adapted from wide-area control for coordinated control and optimization of VBs in radial, balanced distribution feeders, that considers multiple spatial and temporal scales.
- A novel second-order conic AC OPF formulation of a multi-period optimization of VBs designed for tracking the desired power reference at the head-node of the feeder. In this formulation, the feeder structure is taken into account and the second-order cone relaxation is proven to be exact even under significant reverse power flow and with a non-monotonic cost function, which is an improvement over the present state-of-science in radial network OPF presented in [29].
- A practically relevant, real-time, corrective VB control scheme for intra-feeder and inter-feeder fluctuations in net-demand and localized communication outages, that requires only the measurement of the head node power, and is dynamically self-adjusting based on estimates of VB power and energy.
- Systematic, simulation-based analysis on IEEE distribution feeders is performed, illustrating the validity of the approach.

C. Practical implementation and Data management

In this subsection, we explain how the presented framework (in Fig. 1) can be feasibly implemented in a practical scheme. In the proposed DSO-centric methodology, we assume that the Market DSO acts as the coordinator and aggregator of DERs and manages the entire scheme. The DSO may also use technology services to manage the DERs via a software interface (e.g., the VB-DER interface in Fig. 1), but this is to help the DSO reduce costs and manage constraints. The DSO runs an OPF every minute or so for each feeder based on the economic head node power reference. In this regard, it is reasonable to assume that the DSO has access to SCADA data.
and is aware of the grid topology and receives VB state of charge (SoC) estimates from the VB-DER interface to run the AC OPF. For the VB interface, the only information required is the corrected VB power set-point. Apart from this, the VB interface does not require any other information and it is not involved in solving an OPF.

The re-tuning of controller gains takes place by executing a nonlinear optimization problem by the DSO about every 5 minutes and only if the model parameters/grid structure change. For that purpose, the requirements are the updated grid topology, VB models, the operating point from the optimal dispatcher, and the VB power and SoC every minute, but only the head node power measurement is required in real-time (on the order of 100 ms). The VB power and SoC estimates are provided by the VB-DER interface. While every other network communication can be accomplished through SCADA, broadcasting head-node power measurements in real-time is not possible through SCADA, which operates on the order of 2-5 seconds. However, real-time automation controllers (e.g., SEL RTAC [39]) are sufficient and can process distribution substation power measurements and broadcast the tracking error to the feeder’s VBS via the DSO’s Gigabit Ethernet or fiber-optic networks on a timescale of about 100 ms [31].

II. CONVEX OPF FORMULATION FOR REFERENCE TRACKING

A. Mathematical Notation

Consider a radial, balanced distribution network as a graph \( G = (\mathcal{N}, \mathcal{E}) \), where \( \mathcal{N} \) is the set of nodes and \( \mathcal{E} \) is the set of branches, such that \( (i,j) \in \mathcal{E} \). If nodes \( i,j \in \mathcal{N} \) are connected, and \( |\mathcal{E}| = n, |\mathcal{N}| = n + 1 \). Node 0 is assumed to be the head-node (i.e., substation) node with a fixed voltage \( V_0 \) and define \( N^+ := \mathcal{N} \setminus \{0\} \).

Let \( V_i \) be the voltage phasor at node \( i \) and \( I_{ij} \) the current phasor in branch \( (i,j) \). Then, we define \( v_i := |V_i|^2 \) and \( \|i\|_2 \) are described by the VB's energy state, (e.g., see [8], [9], [38]):

\[
\dot{B}_i(t) = -\alpha_{b,i} B_i(t) - p_{b,i}(t) \\
\tau_i p_{b,i}(t) = -p_{b,i}(t) + p_{b,i}(t - T_{2,i}) \\
B_i(t) = B_i(t) \leq B_i^\star \\
p_{b,i} \leq p_{b,i}(t) \leq p_{b,i}^\star,
\]

where at node \( i \in N^+ \), \( B_i(t) \) is the VB’s state of charge (SoC), \( p_{b,i}(t) \) its active power output, and \( p_{b,i}(t) \) its desired total active power. The upper (lower) bound of the SoC is given by \( B_i^\star \) (\( B_i \)) while the VB’s upper (lower) power limits are denoted \( p_{b,i}^\star \) (\( p_{b,i} \)). Note that \( B_i^\star \) captures the SoC dynamics with \( \alpha_{b,i} \) as the energy dissipation rate. The coupling between the VB’s ability to change output power and the VB’s control and communication of DERs and their response is generalized with \( \tau_i \) as a lag-and-delay model. In that model, \( \tau_i \) is the time delay associated with communicating with the \( i \)th VB and \( \tau_i \) is the time constant of the first-order model (similar to [39] for example). Specifically, \( \tau_i \) was formed by taking note of the following facts: i) The DERs that compose a VB turn on/off (possibly) sequentially, and there are power electronic components present inside each VB, both of which contribute to a net lag \( \tau_i \); ii) There are communication delays (generally of the order of 200 ms) between the head node of the feeder and each VB [40], [41], and delays associated with disaggregating the control signal into device-level signals [37]. The delays we consider in the VB model are in fact both these types of delays lumped together. The effect of time delays on stability is investigated in Section III-D. For this work, the battery charge and discharge efficiencies are assumed to be unity. Inclusion of non-unity battery efficiency requires binary variables to avoid simultaneous charging and discharging. A detailed description of the battery model with non-unity efficiency and analysis on avoiding simultaneous charging and discharging is provided in [42], [43]. This full VB model described in (5)-(8) is employed in simulations involving the...
real-time, corrective control scheme detailed in Section III. However, for the optimal dispatch to be presented next, since we optimize the VB set-points on a minutely timescale and $\alpha_{b,L}$ is small in realistic settings, it is reasonable to consider a simplified, discretized VB model for set-point optimization. This reduced, predictive model is below:

$$B_i[k+1] = B_i[k] - \Delta t p_{b,i}[k]$$ \hspace{1cm} (9)

$$B_i \leq B_i[k] \leq B_i^r, \quad p_{b,i}[k] \leq p_{b,i}^r$$ \hspace{1cm} (10)

valid for any $k$ and where $\Delta t$ is the discretization timestep. Next, we augment the discrete-time VB model with the AC network model to formulate the feeder head-node reference power tracking OPF problem.

C. Conventional convex formulation

Convex relaxation techniques have gained popularity recently due to the existence of global optimality guarantees for AC OPF problems [29], [44]. In this subsection, we present a traditional second-order cone programming (SOCP) relaxation of the AC power flow equations to formulate a convex, multi-period reference tracking OPF problem, (P1). Decoupling the cost function in the form:

$$\sum_{i \in N} f_i(p_i[k]) = (p_0[k] - p_0^{\text{con}}[k])^2 + (q_0^{\text{con}}[k] - \sum_{i \in N^+} p_{r,i}[k])^2,$$

the optimization problem can be expressed as:

$$\begin{align*}
\text{(P1)} & \quad \min_{p_{b,i}[k] \in \mathbb{N}} \sum_{i \in N} f_i(p_i[k]) \\
\text{s.t.:} & \quad (1) - (3), (9) - (10) \\
& \quad p_i[k] = p_{b,i}[k] - P_{r,i}[k] + P_{L,i}[k], \quad \forall i \in N^+ \\
& \quad q_i[k] = -Q_{L,i}[k], \quad \forall i \in N^+ \\
& \quad l_{ij}[k] \geq \frac{|S_{ij}[k]|^2}{v_{ij}[k]}, \quad \forall (i,j) \in E \\
& \quad s_i[k] \in S_i, \quad i \in N^+ \\
& \quad v_i \leq v_i[k] \leq v_i^r, \quad i \in N^+ 
\end{align*}$$

for discrete time-step $k \in \mathcal{T}$ over a prediction horizon $\mathcal{T} := \{0, \ldots, T - 1\}$ and where the power injection $s_i$ at a node $i \in N^+$ is constrained to be in a pre-specified, compact, convex set $S_i \subset \mathbb{R}$. The prediction horizon length $T$ is chosen subject to availability of forecasts and depending upon the dynamics of VBs $\mathbb{S}$. In our formulation, we choose $T = 10$. The set $S_i \subset \mathbb{R}$ depends upon the VB constraints, e.g., in case of inverters this set is given by: $\{(p_{b,i},q_i)| p_i^2 + q_i^2 \leq S_i^2\}$, where $S_i$ is the apparent power limit of the inverter. In (11), the cost function (11a) minimizes the deviation of the head-node power $p_0[k] \in \mathbb{R}$ from the economic head-node reference trajectory $p_0^{\text{con}}[k] \in \mathbb{R}$, which is composed of: i) desired economic aggregate VB dispatch, $p_0^{\text{con}}[k] \in \mathbb{R}$; ii) total predicted losses, $L[k] := \sum_{i \in N^+} (P_{r,i}[k] - P_{L,i}[k])$, where $P_{r,i}[k] \in \mathbb{R}$ is the active power demand at node $i \in N^+$, and $P_{L,i}[k] \in \mathbb{R}$ is the solar PV generation at node $i \in N^+$. We are optimizing over the VB dispatch, $p_{b,i}[k] \in \mathbb{R}$, $\forall i \in N^+$, which appears in power balance constraint (11c) while reactive power demand, $Q_{L,i}[k] \in \mathbb{R}$, is used in (10). The second-order cone relaxation of the nonlinear equation (3) is given in (11e), (11f) and (11g) provide constraints on power injection and voltage magnitudes.

Several works in literature such as [29] provide conditions under which the second-order cone relaxation is exact for distribution networks. If an optimal solution of (P1) $w^* = (s^*, S^*, v^*, l^*, s_i^*)$ is feasible for OPF, i.e., $w^*$ satisfies (4), then $w^*$ is global optimum of OPF and (P1) is said to be exact. Theorem 1 in [29] provides conditions for the SOCP problem in (P1) to be exact, however, it requires the part of cost function $f_0(p_0) = (p_0 - p_0^{\text{con}})^2$ to be strictly increasing, which is not the case when tracking a reference power signal. Thus, even under the conditions provided in [29], (P1) may not be exact. To overcome these shortcomings of (P1), we propose a novel method for convexifying the AC OPF while ensuring an exact solution at optimality. Specifically, we utilize a linearized approximation of line losses and through this obtain a cost function that is strictly increasing in $p_0$ in order to satisfy the conditions in [29].

D. Reformulated convex formulation

To reformulate (P1), we consider each piece of the two-part composition of feeder’s predicted head-node power, $p_0[k] \approx -\sum_{i \in N^+} p_{r,i}[k] + L_i[k]$. Specifically, we employ a first-order approximation of total predicted line losses, $L_i[k]$, in the cost function (via $p_i$-to-loss sensitivity factors, which we denote by $\zeta_i$), and prove that with approximated losses, the solution is tight at optimality. Thus, the predicted grid response to an optimized VB dispatch is AC feasible. To achieve the above, we use $p_0^{\text{con}}$ and $p_0^{\text{con}}$ mentioned earlier. This leads to a multi-objective reference tracking problem, similar to the form of a linear quadratic regulator (LQR) from optimal control [45].

$$\begin{align*}
\text{(P2)} & \quad \min_{p_{b,i}[k], k \in N^+} \sum_{i \in N^+} f_i[p_i[k]]^2 + \alpha f_{\text{VB}}[k]^2 + \epsilon p_0[k] \\
\text{s.t.:} & \quad f_{\text{HB}}[k] = \sum_{i \in N^+} (p_{r,i}[k] + P_{b,i}[k] - L_i[k] - p_0^{\text{con}}[k]) \\
& \quad f_{\text{VB}}[k] = p_0^{\text{con}}[k] - \sum_{i \in N^+} p_{b,i}[k] \\
& \quad L_i[k] = L_{0,i} + \sum_{i \in N^+} \zeta_i \delta p_i[k] \\
\end{align*}$$

for discrete time-step $k \in \mathcal{T}$ over a prediction horizon $\mathcal{T} := \{0, \ldots, T - 1\}$. The parameters $\alpha, \epsilon \in \mathbb{R}_+$ are chosen appropriately with $\epsilon \ll 1$; $L_i[k] \in \mathbb{R}$ is the first-order estimate of the total feeder line losses at time-step $k$ and $L_{0,i} \in \mathbb{R}$ is the loss estimated for the operating point at time $k$. The term $\epsilon p_0$ results in a tight relaxation as will be shown next in Theorem II.1, whereas the term $\zeta_i \delta p_i[k] = \zeta_i (p_i[k] - p_i[0])$ represents the change in network loss due to change in active power injection at node $i \in N^+$, with $p_i[0]$ being the nominal injection. The factors $\zeta_i \in \mathbb{R}$ provide the first-order change in feeder losses due to changes in VB power injections. Similar power transfer distribution factors are often used in transmission system analysis but have recently been adapted for distribution networks [46].

Remark. The formulation in (P2) can easily be extended to account for solar curtailment as a control variable resulting in a more general formulation. If $P_{C,i}^{\text{con}} \in \mathbb{R}_+$ represents the curtailment reference trajectory and $P_{C,i} \in \mathbb{R}_+$ represents the solar curtailment at node $i \in N^+$, then $\sum_{i \in N^+} (P_{S,i} - P_{C,i})$ is the net solar output
and \( \sum_{i \in N^+} P_{C,i} - P_{\text{con}}^C \) is the error in tracking the curtailment trajectory.

In the next section, Theorem II.1 proves that under practical conditions, the (P2) has a zero duality gap.

**E. Exactness of formulation (P2)**

In order to explain the notation in Theorem II.1, consider \( \mathcal{L} := \{ l \in N \mid \forall k \in N \text{ such that } k \rightarrow l \} \), which denotes the collection of leaf nodes in the network. For a leaf node \( l \in \mathcal{L} \), let \( n_l + 1 \) denote the number of nodes on path \( P_l \), and suppose

\[
P_l = \{ n_{l-1} \rightarrow l \} \cup \{ l \rightarrow l_0 \},
\]

with \( n_l = l \) and \( l_0 = 0 \).

Also, define \( A^\dagger_0, A^\dagger_l \) as the 2 \times 2 identity matrix, and let \( I_2 \) denote the 2 \times 2 identity matrix, and matrices

\[
\hat{I}_l := I_2 - \frac{2}{v_0^l} \left[ \frac{r_{ij}}{x_{ij}} \left( \hat{P}_{ij}^p + \hat{Q}_{ij}^q \right) \right]
\]

for \((i,j) \in \mathcal{E}\) where \( \hat{P}_{ij}^p \) and \( \hat{Q}_{ij}^q \) are upper bounds on \( P_{ij}^p \) and \( Q_{ij}^q \) and are chosen so that \( A^\dagger_l \) only depends on the SOCP parameters \((r_{ij}, x_{ij}, v_0, \mathcal{P}, s)\).

Furthermore, let \( (\hat{S}, \hat{v}) \) denote the solution of the Linear DistFlow model, then

\[
\hat{S}_{ij}(s) = \sum_{h \in P_c} s_h, \quad \forall (i,j) \in \mathcal{E}
\]

\[
\hat{v}_i(s) := v_0 + 2 \sum_{(j,k) \in P_i} \text{Re}(\pi_{jk} \hat{S}_{jk}(s)), \quad \forall i \in N^+
\]

where \( P_i \) denotes the unique path from node \( i \) to node 0. Since the network is radial, the path \( P_i \) exists and is unique. Physically, \( \hat{S}_{ij}(s) \) denotes the sum of power injections \( s_h \) towards node 0 that go through line \((i,j)\). Note that \( (\hat{S}(s), \hat{v}(s)) \) is affine in \( s \), and equals \((S,v)\) if and only if if line loss \( \Delta z_{ij} = 0 \) for \((i,j) \in \mathcal{E}\). Then based on the DistFlow model define:

\[
S_{\text{col}} := \{ s \in \mathbb{C}^n | \hat{v}_i(s) \leq v^*_i, \quad \forall i \in N^+ \}
\]

which denotes the power injection region where \( \hat{v}(s) \) is upper bounded by \( \mathcal{P} \). Since \( v(s) \leq v^*_i(s) \) (Lemma 1 in [29]), the set \( S_{\text{col}} \) is a power injection region where voltage upper bounds do not bind. Then based on this notation, Theorem II.1 below proves the exactness of (P2).

**Theorem II.1.** The SOCP problem (P2) is exact if the C1 and C2 conditions given in Theorem 1 of [29] are satisfied:

**C1:** \( A^\dagger_1, A^\dagger_{i+1}, \ldots, A^\dagger_{n_s}, u_t > 0 \) for any \( l \in \mathcal{L} \) and any \( s, t \) such that \( 1 \leq s \leq t \leq n_l \).

**C2:** Every optimal solution \( (s^*, S^*, v^*, \ell^*, s^*_0) \) satisfies \( s^* \in S_{\text{col}} \).

**Proof.** The cost function of the optimization problem (P2) can be expressed as:

\[
f_0(p_0) = \epsilon p_0
\]

\[
f_i(p_i) = f^R_{\text{RMS}} + \alpha f^2_{\text{VB}} \quad \forall i \in N^+
\]

As \( f_0 \) in the cost function in [12a] is strictly increasing, the SOCP formulation satisfies all the conditions provided in Theorem 1 of [29] and hence the proof is a direct application of Theorem 1 in [29] under conditions C1 and C2. This concludes the proof.

The term \( \epsilon p_0 \) is added to satisfy the additional condition in Theorem 1 of [29], where the cost function must be increasing with respect to \( p_0 \). Note that the inclusion of this term in the cost function affects the optimal solution. However, \( \epsilon > 0 \) can now be made arbitrarily small (per the proof of Theorem II.1), which ensures that the impact on the optimal solution is negligible.

C1 can be checked apriori and efficiently since \( A \) and \( u \) are simple function of \((r_{ij}, x_{ij}, v_0, \mathcal{P}, s)\) that can be computed in \( O(n) \) time and there are no more than \( n(n+1)/2 \) inequalities in C1. For practical parameters ranges of \((r_{ij}, x_{ij}, v_0, \mathcal{P}, s)\), line resistance and reactance \( r_{ij}, x_{ij} << 1 \) per unit for \((i,j) \in \mathcal{E}\). Line flow \( P_{ij}^p, Q_{ij}^q \) are on the order of 1 per unit for \((i,j) \in \mathcal{E}\) and voltage lower bound \( v^*_i \approx 1 \) per unit for \( i \in N^+ \). Hence, \( A^\dagger_i \) is close to \( I \) for \( i \in N^+ \), and therefore C1 is likely to hold. As has been shown in [29], C1 holds for several test networks, including those with high penetration of renewables.

To show the practical restriction of condition C2, Fig. 2 shows the increase in \( \hat{v} \) with increase in reverse power flow due to increased VB injection. From the figure, it can be seen that the condition is valid for VB injections up to more than 400% of demand, compared to the base case of 20%. It can also be seen from Fig. 2 that \( \hat{v} \) matches the actual voltage \( v \) very closely due to the low impedance of the IEEE-37 node system resulting in small loss term \( \Delta z_{ij} \). However, with solar PV penetration and an increase in impedance values, the maximum VB injection limit will reduce.

The next section presents simulation results that illustrate the effectiveness of (P2).

**F. Optimal VB dispatch and head node tracking simulation**

Simulation tests for the optimal reference tracking with VBs were conducted on the balanced version of the IEEE-37 node test feeder [47] to compare the conventional (P1) and the proposed (P2) formulations against the actual AC load flow from Matpower [48].

Simulation results in Fig. 3a show the reference tracking results in Matpower achieved through the flexibility of VBs using (P1) and (P2). The figure shows that (P2) can track the reference trajectory whereas (P1) cannot. For (P2), the error in tracking at each step change in the reference trajectory is due to the first-order loss approximation used in (P2). Since the loss approximation is updated every time-step, the effect on tracking error is small and corrected quickly as shown in Fig. 3a which means that (P2) represents a reference-tracking OPF formulation that can effectively dispatch VBs while guaranteeing network admissibility. On the other hand,
for the convex (P1), the non-zero duality gap creates a mismatch between the predicted power flow values and the actual AC power flow, which results in the sub-par tracking illustrated in Fig. 3a. Specifically, (P1) predicts perfect tracking, but the realized AC head node power does not match the grid reference, which results in suboptimal use of VB resources. The comparison of the aggregate State of Charge (SoC) obtained through (P1) and (P2) is shown in Fig. 3b. Clearly, (P1) predicts a different SoC trajectory than (P2) due to the non-physical solution of (P1).

Remark (High voltage conditions). The voltage condition in Theorem 2 (C2) is not restrictive for practical distribution networks as can be seen from Fig. 2. Importantly, condition (C2) can still be satisfied under large reverse power flows, which occurs in feeders with significant penetrations of batteries or solar PV generation. To illustrate the effect of reverse power flows, we present simulation results in Fig. 4. For example, in Figs. 4a and 4b, it is shown that the predicted active head-node power matches the actual power while satisfying the voltage constraints. Of course, reverse power flows and high voltage conditions are related, which means that we are assuming that appropriate DER hosting capacity studies have been conducted to inform operations and avoid high voltage conditions. Nonetheless, theoretically, there are reverse power flows for which condition (C2) is violated at optimality, which means that the convex relaxation in (P2) may not be tight. Figs. 4c and 4d illustrate the effects of a non-tight solution and show that the mismatch between the predicted and actual head-node power can lead to voltage violations due to predicted (relaxed, fictitious) losses that ensure a feasible solution in (P2), but are not realized in the physical feeder and, thus, reduce the head-node power further. To guarantee an exact solution that is always physically meaningful, we could include additional constraints to (P2) that capture condition (C2) implicitly (e.g., augment (P2) with the LinDist formulation’s voltage variables, $\hat{v}_i$, and $\hat{v}_i$’s upper voltage bound), which ensures that $v < \hat{v}_i$, if (P2) is feasible [29]. Alternatively, we could just project (P2)’s optimal solution onto the AC feasible set and accept the loss of optimality.

Note that the solve time for the above optimization problem is typically less than a second for a feeder with 37 nodes. Our prior work on large scale three-phase systems has shown that the OPF scales well and can be solved in under a minute [43]. The next section develops the real-time control of VBs to achieve tracking of grid reference by countering variations in net-demand and also to provide support under contingencies. In Section IV, the OPF is integrated with the real-time corrective control action to show the effectiveness of the combined approach through simulation results.

III. RESILIENT & CORRECTIVE CONTROL OF VIRTUAL BATTERIES

The optimal VB set-point dispatcher depicted in Fig. 1 solves (P2) about every minute and provides optimal active power set-points to all VBs in the feeder, such that the economic power trajectory for each feeder is tracked optimally. The aggregate of DERs that make up these VBs is then expected to provide and maintain those power set-points until the next set-point update. However, due to the nature of distribution feeders with solar PV, there will inevitably be short-term fluctuations in net-demand, which act as disturbances within a feeder. The flexibility available from the VBs can be used to mitigate these intra-feeder disturbances, by correcting the set-points provided by the optimal VB set-point dispatcher. Furthermore, for a utility with multiple feeders connected to a substation, one feeder may suffer from larger disturbances, e.g., forecast errors not accounted for in (P2), cyberattacks on the ‘VBs’ or DERs’ communication channels, and changes to network topology from local outages. In these cases, it becomes important for the system to be resilient [49] and maintain the economic set-point provided by the market despite such inter-feeder disturbances. In this section, we hence provide a resilient and corrective control mechanism for mitigating these intra-feeder and inter-feeder disturbances by leveraging the flexibility of VBs. This ensures that feeders with high penetration of solar PV can be effectively dispatched to provide energy market services. This “real-time” corrective action will be executed in the order of a few hundred milliseconds.

The ideas proposed here are similar to existing ideas in wide-area control, including primary (droop) control and automatic generation control (AGC) of frequency [22], but they are adapted for distribution system operations and regulating power. For example, the intra-feeder control mechanism is needed to respond to small disturbances within a feeder without invoking VBs in other feeders and hence needs to operate on a fast time scale (similar to primary frequency control). The inter-feeder control, on the other hand,
needs to operate only when there is a large contingency that necessitates a response from VBs present in all the feeders, and thus its response is expected to be slower than intra-feeder control, which is similar to automatic generation control (AGC). Unlike primary frequency control and AGC, we also need to take into account saturation in the energy and power of the VBs while designing for and implementing the real-time controllers.

A. Overview of Intra-Feeder Control of Virtual Batteries

The purpose of the intra-feeder control scheme is to reject short-term disturbances (like solar PV/demand fluctuations) that enter the nodes inside a feeder and maintain the economic market set-point at the feeder’s head-node (i.e., substation). Our proposed intra-feeder control scheme essentially consists of a bank of proportional controllers, one to control each VB in the feeder. For this purpose, the only measurement required is the active power at the head-node of the feeder, which is readily available at the substation. The block diagram in Fig. 5 represents one feeder with \( n \) VBs and disturbances in the form of uncontrollable, unscheduled nodal injections. The VBs are modeled as mentioned in Section II-B where the power and energy bounds of the VBs are represented by the saturation blocks.

\( P_{n,f} \) denotes the corrected economic reference head-node power for this feeder, as calculated by the inter-feeder controller described in the next section, and needs to be tracked by this intra-feeder control scheme. \( P_h \) denotes the head node power of the feeder. The corrected VB set-point for the \( r \)th VB, \( p_{m,r} \), is then obtained as follows:

\[
p_{m,r} = K_r K_{adj,r} (P_{uf} - P_h) + P_{set,r}
\]

where \( P_{set,r} \) refers to the set-point provided by the optimal set-point dispatcher about every minute. \( K_r \) is a proportional controller that is further adjusted by a factor \( K_{adj,r} \) (as defined in [18], see below). The net VB power, \( p_{h,r} \), (computed from (6)) is injected into the “Feeder” block (which represents the feeder with nodal active power injections as input and its head node active power as output) at the respective location. The “Feeder” block is linearized when designing the gains, as will be described later. The plant that is controlled here thus consists of the VBs, together with the grid. Estimates of the SoC signal, \( B_r \), computed by the DSO based on DER models, and the net VB power signals, \( p_{h,r} \), obtained from the VB interfaces, are used to dynamically modify the adjustment factor for the proportional gain (described below), \( K_{adj,r} \) about every minute (this adaptive and dependent behavior of \( K_{adj,r} \) is indicated by slanted arrows in Fig. 5). This adjustment factor ensures that an unreasonable value of power that might deplete or fully charge the VB is not demanded from the VB. The computation of the control input is performed at the nodal service transformers that are monitored by the DSO. To obtain the response from the set of DERs in practice, however, we are assuming that a device level algorithm (like the priority-based scheme in [30]) is in place to ensure that the DERs can provide the response that is desired from them. This device-level dispatch can be performed every few hundreds of milliseconds.

To ensure a good disturbance rejection capability, we select the proportional gains, \( K_r \), optimally, using an approach similar to standard LQR. Specifically, we assume that the system (i.e., the feeder) is affected by nodal injections that are stochastic. This is reasonable since solar PV and demand fluctuations are typically random. The objective is then to reduce the effect of these injections on deviating the head-node power from the economic reference. Alternatively, we can treat the reference signal itself as stochastic and reduce the standard deviation of the tracking error. To do that, first, the system is linearized by 1) ignoring the saturation blocks, and 2) linearizing the AC power flow equations about an operating point set by \( P_2 \). The latter is done by finding the sensitivity of the head node active power of the feeder to the active power injection at the particular nodes where the VBs are situated. The time-delay blocks are also ignored, but to account for time-delays, the controller design is done in such a way that the gain-crossover frequency of the open-loop system, or the bandwidth of the closed-loop system (without delays), is less than \( 1/5T_{d,max} \), where \( T_{d,max} \) is the maximum time delay in the system. Effectively, this ensures that the delay-free system and the delayed system behave similarly. Then, assuming that the system is excited by zero-mean wide-sense stationary Gaussian inputs, we minimize the sum of the variance of the error, \( P_e \), denoted by \( \sigma^2_{P_e} \), and a weighted sum of the variances of the control inputs to each VB, \( P_{u1}, ..., P_{un} \), denoted by \( \sigma^2_{P_{u1}}, ..., \sigma^2_{P_{un}} \) respectively:

\[
\min_K \sigma^2_{P_e}(K) + \sum_{r=1}^{n} \rho_r \sigma^2_{P_{u,r}}(K),
\]

where \( K = [K_1 \ K_2 \ ... \ K_n]^{\top} \). Here, \( \sigma^2_{P_e}(K) \) and \( \sigma^2_{P_{u,r}}(K) \) are related to the standard deviation of the reference via the \( H_2 \) norm of the transfer function, and to the gains \( K_r \), through the Lyapunov equation, \( A \Sigma + \Sigma A^{\top} + BB^{\top} = 0 \), where \( A, B \) are system state matrices and \( \Sigma \) is the state covariance matrix. See [50] for details, \( \rho_r > 0 \) is a constant penalty parameter that we design to be inversely proportional to the power capacity of the \( r \)th VB. This penalizes power extraction from VBs that have a lower capacity to provide power output, thus resulting in a constraint-aware controller. The above nonlinear optimization problem can be solved efficiently (typically within six seconds for 30 VBs or less) using a path following algorithm [51].

Note that there is a trade-off to be considered while choosing these constants. If \( \rho \ll 1 \), the control effort is less penalized, and hence, the resulting gains would be large (allowing larger control effort), and thus, VBs would saturate before they can assist in recovering the total head node power during a major disturbance. On the other hand, if \( \rho \gg 1 \), the control effort is penalized, and hence the resulting small gains may not be sufficient for better disturbance rejection capability. Thus, these constants should be chosen by taking into account this trade-off.

As a VB’s energy state approaches its full capacity, the charging rate should be proportionately reduced, and when the energy state becomes empty, the discharging rate should be proportionately
reduced. This helps to avoid a sudden step-change in power to zero when the VB saturates (either empty or full capacity). To achieve this, the proportional VB controller gains, $K_r$, are then passed through an adjustment factor $K_{adj,r}$ (similar to standard gain scheduling) that is updated about every minute as follows:

$$K_{adj,r} = \begin{cases} 
\left( \frac{B_r - B_{r,i}}{W_r} \right)^w, & \text{if } B_{r,i} \leq B_r < B_{r,i}, \\
1, & \text{else if } B_{r,i} \leq B_r \leq B_r, \\
\left( \frac{B_r - B_{r,i}}{W_r} \right)^w, & \text{if } B_{r,i} < B_r \leq B_r, \\
1, & \text{else if } B_{r,i} \leq B_r < W_r, \\
1, & \text{else if } B_{r,i} = 0
\end{cases}$$

where $B_r$ and $B_{r,i}$ represent the lower and higher bounds on the state of charge of the $r$th VB respectively, $B_r$ represents the state of charge of the $r$th VB in the absence of any power inputs from infrasonic feeder control systems, and $B_{r,i}$ is the unsaturated output of the $r$th VB, $w$ is a constant greater than 1, and $B_{r,i}$ and $B_r$ are some chosen constants such that $B_{r,i} < B_r < \frac{(B_r + B_{r,i})}{2} < B_{r,i} < B_r$.

B. Overview of Inter-Feeder Control

The intra-feeder control action described above is useful for mitigating small nodal disturbances that arise inside one feeder. However, to mitigate larger disturbances, such as when many of the VBs in one feeder saturate, the VBs inside one feeder may not be sufficient, and the flexibility of VBs in all the feeders will be required to reject the larger disturbance until (P2) updates the set-points. The block diagram of the real-time control system for mitigating these inter-feeder disturbances is shown in Fig. 6. It is essentially a PI control scheme that corrects the economic set-point references to the $m$ intra-feeder control systems, the structure of each of which is shown in Fig. 5. To ensure that the PI control is effected only when there is an appreciable change in the total head node power, a dead zone is also implemented before the PI controller. Moreover, a standard anti-windup mechanism is implemented inside this control scheme. This ensures that if all VBs inside all the feeders saturate in power, the PI control action is cut-off to avoid any integral windup. This control scheme requires measurements from the head node powers from all the feeders. Since there is communication overhead involved in receiving the head node powers from all the feeders, the inter-feeder control action is proposed to be effected only about every 5 s.

The working of the inter-feeder control system is described as follows. The sum of measured head node active powers from all feeders, denoted by $P_{h,net}$, is compared with the total economic market set-point for all feeders, $P_{econ,net}$. Then, for each time step $k$ (executed about every 5 s), the control input to the $i$th intra-feeder control system, $P_{u,f,i}[k]$, is computed as follows:

$$P_{u,f,i}[k] = K_{fi} P_{u,net}[k] + P_{econ,i}[k],$$

where $P_{u,net}$ is the output from the saturation block:

$$P_{u,net}[k] = \text{sat}\left(\tilde{P}_{u,net}[k]\right) = \begin{cases} 
\tilde{P}_{u,net}[k], & \text{if } \tilde{P}_{u,net}[k] > P_{u,net} \\
P_{u,net}[k], & \text{if } P_{u,net}[k] < \tilde{P}_{u,net}[k] \\
\tilde{P}_{u,net}[k], & \text{if } \tilde{P}_{u,net}[k] \leq P_{u,net}
\end{cases}$$

and $\tilde{P}_{u,net}$ is given by the integrator as

$$\tilde{P}_{u,net}[k+1] = \tilde{P}_{u,net}[k] + K_i \left( P_{e,net}[k] - \tilde{P}_{u,net}[k] \right) + K_w \left( \tilde{P}_{u,net}[k] - P_{u,net}[k] \right),$$

where

$$P_{e,net}[k] = P_{h,net}[k] - P_{econ,net}[k],$$

$$\tilde{P}_{e,net}[k] = \begin{cases} 
P_{e,net}[k], & \text{if } |P_{e,net}[k]| > P_{ed} \\
0, & \text{if } |P_{e,net}[k]| \leq P_{ed}
\end{cases}$$

and $P_{h,net}$ is the tracking error, $P_{ed}$ is the deadzone limit, $P_{e,net}$ is the output of the deadzone, $K_w$ is the anti-windup gain, $K_p$ is the proportional controller gain and $K_i$ is the integral gain in the PI controller, $\tilde{P}_{u,net}$ is the unsaturated output of the PI controller, $P_{h,net}$ is the upper limit of the sum of control inputs to the intra-feeder control systems, $P_{econ,net}$ is the lower limit, $K_{fi}$ is a scaling factor, and $P_{econ,i}$ is the economic reference for the $i$th feeder. $P_{u,net}$ and $P_{e,net}$ are computed by assuming that the VBs are at their power limits.

To penalize the extraction of power from feeders with lower capacity to provide power, the inter-feeder gains $K_{fi}$ are chosen in proportion to the total capacity of the feeder with which they are associated. If the feeder $i$ has a higher capacity, $K_{fi}$ is assigned a higher value, and if it has a lower capacity, $K_{fi}$ is assigned a lower value. Specifically, $K_{fi} = \frac{P_i}{P_f}$, where $P_i$ is the power capacity of the ith feeder, and $P_f$ is the total power capacity of all feeders.

The PI gains are chosen to satisfy the desired requirement of phase margin and settling time. A high phase margin ensures that the system remains robust to VB failure, while a low settling time of less than a minute ensures that any large disturbance is quickly rejected before the optimal dispatcher provides new set-points to VBs every minute. To do this, first, the system is linearized in the same manner mentioned in Section III-A. Then, the phase margin of the open-loop transfer function from $P_{e,net}$ to $\Delta P_{econ,net}$, as well as the settling time of the entire closed-loop system due to the response to a step input applied at $\Delta P_{econ,net}$, are computed for different values of $K_p$ and $K_i$, and the PI gains for which the phase margin is high and settling time is low is selected.

Fig. 7 shows an example of this process of selection of PI gains on a two-feeder system with 2 VBs in each feeder. The values of $K_p$ and $K_i$ were varied, keeping their ratio fixed, to study their effects on the phase margin and settling time. It can be seen that when $K_p = K_i$ (blue curve), there are some values of $K_p$ for which the system has a low settling time ($< 30$ s, as indicated by the green dotted vertical line) but the phase margin varies significantly from $20^\circ$ to $80^\circ$. In that region, there is a value of $K_p$ and $K_i$ for which the settling time is the lowest, and phase margin is above $60^\circ$, indicating the optimal value for the PI gains. Other values of $K_p$ and $K_i$, as those obtained from the $K_i = 0.1K_p$ and $K_i = 10K_p$ curves, lead to sub-optimal performance.
D. Effect of Intra-Feeder Controller Gains and Delays on Stability

In this subsection, an analysis of the effect of time-delays and controller gains on the stability of the control system is presented. Consider the intra-feeder control diagram shown in Fig. 5 but with two controllable VBs (i.e., \( n = 2 \)). For analysis, let this system be linearized as mentioned in Section III. The time-constants of the VBs are chosen to be \( \tau_1 = \tau_2 = 1 \) s. The region of stability was plotted (Fig. 7b) using the system transfer function from \( P_{\text{con}} \) to \( P_h \), replacing the delay with a third-order Padé approximation (to extract poles). Fig. 7b shows the effect of a delay in the response of one of the VBs on the region of stability. The delay in the second VB, \( T_{d2} \), was varied from 0 to 200 ms, 500 ms, and 1 s (these values are chosen for the sake of illustration - in reality, delays may be smaller/larger), keeping no delay (\( T_{d1} = 0 \)) in the first VB. The regions of stability are shown (in Fig. 7b) by different shades of green, with the lightest corresponding to no delay and the darkest corresponding to a delay of 1 s. Note that the regions depicted by the lighter shades of green include those depicted by the darker.

It can be seen that when there are no time-delays in either VB, the region of stability is a half-space. Indeed, it can be shown by analyzing the poles of the transfer function that the region of stability is given by \( A_{p1}K_1 + A_{p2}K_2 > -1 \) (this provides an analytic bound of permissible controller gains). It also indicates that there is a competition between the VBs: if one of the controller gains increases (and is hence more responsive to changes in head node power), the other must decrease to ensure stability. With time-delays, however, the region of stability is no longer a half-space but reduces to a triangular region (in the case of 2 VBs). Furthermore, this reduction is larger for VBs with greater communication delays.

Thus, the above analysis indicates that for stability, allowable controller gains are limited by 1) the presence of other VBs, which introduce competitive behavior, 2) communication delays, and that only a more restrictive set of gains is permissible for VBs with more communication delays. For an arbitrary grid, once the structure is known, one can do a similar analysis and determine the range of allowable controller gains that maintain system stability, which would be important for the DSO.

IV. Simulation Results

To test the real-time VB corrective control mechanism along with the optimal tracking, 10 IEEE-37 node feeders (single-phase equivalents) were simulated (Fig. 8), with VBs at certain randomly picked locations inside each feeder, and active and reactive power noise sources at certain randomly picked locations, as shown in Table I. Both the OPF presented in Section II and the real-time control mentioned in Section III are simulated together in this section, with the real-time control using the full VB model (5)-(8) at every time-step and the OPF using the reduced VB model (9)-(10) every minute. The parameters \( \tau \) and \( T_{d} \) of the VBs are also listed in Table I. Each feeder was assumed to have the same nominal active and reactive power demand that is present in the first phase of the standard IEEE-37 node feeder (which is about 727 kW in active power demand and 357 kVAR in reactive power demand). A unity power factor is assumed for the VBs. The sample time for the simulations was assumed to be 500 ms. The anti-windup gain was assumed to be 1, and the dead zone limit in the tracking error to be 72.7 kW, which is 10% of the total base demand in each feeder. The active power limits of the VB was set to ±24.2 kW when there were 6 VBs in the feeder, and at ±29 kW when there were 5 VBs in the feeder, such that the total capacity of the VBs in each feeder represents 20% of the nominal demand. The energy bound of each VB was assumed to be 97 kWh (where there were 6 VBs in the feeder) and 116 kWh (where there were 5 VBs), thus, representing a maximum 4-hour continuous operation of each VB at its power limit. This is a reasonable scenario for many DERs, including water heaters and residential batteries. The controllers were designed according to the process mentioned in Section III.
TABLE II
SYSTEM STRUCTURE

| Feeder | VB Locations | Active Power Noise Locations | Reactive Power Noise Locations | VB Time Constants, \( \tau \) (s) | VB Delays, \( T_d \) (s) |
|--------|--------------|-----------------------------|-------------------------------|----------------------|---------------------|
| 1      | 712,722,706,703,727,708 | 705,712,727,728,730 | 712,718,707,727,729 | 1.0 1.4 1.6 | 1.5 1.9 1.8 |
| 2      | 702,722,724,729,708,732 | 713,704,732,727,729 | 701,724,706,703,744,741 | 1.8 1.7 1.9 | 1.8 1.6 |
| 3      | 712,713,718,732,738,713,704,707,732,727,735 | 712,725,744,731 | 714,727,727,740,736 | 1.3 1.1 1.2 | 2.0 1.8 |
| 4      | 724,709,731,737,736 | 724,709,737,740,735 | 702,713,706,703,744,731 | 1.0 1.4 1.2 | 1.1 1.9 |
| 5      | 712,742,713,703,710 | 702,742,707,711 | 718,720,706,727,737,710 | 1.4 1.7 1.0 | 2.0 1.4 |
| 6      | 722,744,728,738,711,736 | 701,705,714,707,732,711 | 705,744,731,733,711,710 | 1.1 1.6 1.7 | 1.0 1.5 2.0 |
| 7      | 724,737,738,710,735 | 713,704,714,744,736 | 714,720,730,709,711 | 1.7 1.2 0.1 1.3 | 1.9 |
| 8      | 701,718,737,741,735 | 724,703,744,731,708,710 | 742,724,709,737,710,735 | 1.7 1.5 2.0 | 1.1 1.1 |
| 9      | 712,713,707,711,736 | 720,727,728,709,733,741 | 731,737,711,710,736 | 1.3 1.4 1.3 2.0 | 1.6 |
| 10     | 712,722,706,703,727,708 | 705,712,727,728,730 | 712,718,707,727,729 | 1.0 1.4 1.6 | 1.5 1.9 1.8 |

Fig. 8. Power System Structure

Fig. 9. Intra-feeder disturbance rejection

A. Intra-feeder disturbance rejection scenario

We first illustrate the rejection of intra-feeder disturbances such as fluctuations in solar PV, which represent clouds obstructing solar PV panels. Since (P2) operates every minute, such disturbances would have to be mitigated within a minute. Also, since such a disturbance is local, other feeders should not be actuated to reject such a disturbance. The results for a 1-minute simulation are shown in Fig. 9 after small modeling errors were corrected for by two consecutive runs of (P2) at \( t = 60 \) s and \( t = 120 \) s, a step disturbance was introduced at around 125 s into all the nodes of Feeder 1 at the active and reactive power noise locations shown in Table II. This step disturbance can represent, for example, a big cloud cover obstructing the sun. From 145 s to 180 s, random noise was injected into the same nodes instead of a step disturbance. This can represent small clouds intermittently covering the sun. The blue curve denotes the total actual head node power from all feeders. The orange dashed line depicts the economic market set-point desired to be met by all the feeders. The total head node power predicted by employing VB set-points resulting from solving (P2), every minute, is shown by the red dotted line. The optimization horizon for (P2) is 10 minutes. The purple dash-dotted line shows the head node power if the VBs were optimally dispatched, but no real-time control mechanism, as described in Section III, was in place. The results show that in the presence of a step disturbance, the proportional gains inside the feeder try to bring the power back towards the economic set-point. However, due to the nature of proportional control, there is a steady-state error. Moreover, if the noise is random, the intra-feeder control mechanism reduces the standard deviation of the random noise, as in Fig. 9, where the standard deviation reduces from 10.8 kW to 8.5 kW, a reduction by 26%. Note that inter-feeder PI control is not activated in the above simulation since the disturbances are all within the dead zone of 72.7 kW.

B. Inter-feeder disturbance rejection

In this subsection, we illustrate the effect of rejecting an inter-feeder disturbance and thus, the resilience of the VB coordination framework. It is assumed that due to an adversarial cyberattack on a feeder, all the VBs in that feeder are set to their power limits. Fig. 10a shows the results of a 5-minute simulation with optimal PI controllers. At \( t = 150 \) s, one of the ten feeders is attacked in an above-mentioned manner. As a result of this attack, the power delivered by VBs suddenly change from 36.1 kW to 145.4 kW (an increase by 302.7%). It is seen that PI control brings the head node power back to the economic set-point. Because other feeders also participate in this major disturbance, the head node power from them also changes after the disturbance, as illustrated in Fig. 10b.

Remark (Voltage values). In both the intra-feeder and inter-feeder disturbance rejection, the control mechanism successfully keeps the voltages within acceptable limits. In the examples shown above, voltages in the nodes of the feeders are all above 0.97 pu, with the maximum voltage being 1.0 pu (which is the fixed head node voltage that assumes reactive power support from tap changing transformers is separately available).

C. Integration with Multiple runs of OPF

In this subsection, a 2-min simulation is performed (Fig. 11) to show the full hierarchy of the OPF and the real-time corrective control action in the case of VBs undergoing cyberattack.
Specifically, two of the VBs in both the 9th and 10th feeder are assumed to be saturated (via attack) after 15 s, resulting in the PI control mechanism being activated, and obtained power (blue line) being restored to the desired value (yellow line). Note that without real-time control (purple dash-dotted line), restoration would not have been achieved. At the next run of the OPF at 1 min, however, even with no real-time control, the optimal set-point dispatcher adequately re-dispatches remaining VBs to result in power close to the desired power. Note that the spike in the blue line at 1 min exists because the VB set-points change, and so does the net real-time control action (which is the sum of set-point provided by the optimal set-point dispatcher and the corrective control).

V. CONCLUSIONS AND FUTURE WORK

In this paper, analysis and simulation results have been presented in support of a novel framework for large-scale coordination of DERs to support deep penetration of renewable energy. The explicit consideration of (temporal) energy and (spatial) grid constraints and the economic and reference-tracking ( techno-economic) objectives have been achieved via a spatio-temporal decomposition approach that leverages information on demand-side flexibility to disaggregate grid economic trajectory into reference control signals for virtual batteries in distribution feeders. A convex optimal power flow (OPF) formulation has been presented that ensures a provably tight optimal dispatch of virtual batteries (VBs) to track an economic power trajectory. Resilient and real-time control techniques to adapt and recover from intra-feeder and inter-feeder disturbances have been analyzed and designed. To show the effectiveness of the decomposition approach and the real-time control mechanism, simulation results have been conducted on a modified IEEE-37 test system.

Future work will incorporate reactive power control of VBs and extend the regulation of voltage with inverters. Further, the market economic problem will be considered explicitly [52] to study the coupling between the market layer economic problem and the feeder constraint aware dispatch. Future work will also try to extend this approach to the three-phase unbalanced system operation [43]. Finally, we are interested in extending the multi-period (P2) to a robust formulation to trade off conservativeness of dispatch and the probability of voltage and VB violations [53].

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