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All-optical logic gates using a plasmonic MIM waveguide and elliptical ring resonator

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Abstract All-optical logic gates OR, XOR, AND and NOT based on two dimensional (2D) plasmonic metal-insulator-metal (MIM) coupled with Elliptical Ring Resonator (ERR) are presented, simulated and investigated by using the numerical method of the FEM (finite elements method). The results are compared and validated with the finite difference time domain (FDTD) method. The proposed logic gates are achieved with the same structure using the constructive and destructive optical interferences between a control signal and input signal(s). Their characterization was mainly done for two spectral regions, visible and near-infrared. A high intensity contrast ratios (CR) between the logic states (“1” and “0”) can be achieved (28 dB) at these spectral regions. We introduce a new parameter, “gap-threshold ratio (GTR)”, to characterize the gap between the maximum and minimum of the transmitted signal intensity for all logic gates. The suggested value of transmission threshold between logic “0” and logic “1” states is \( T_{th} = 0.2 \). A comparison of the two parameters, (CR) and (GTR), with previous works shows that the proposed structure gives very good results for all logic gates configurations. The proposed all-optical logic gates configuration can be a key components in optical processing and telecommunication devices.

Keywords Surface plasmons SPPs · Optical computing · Metal-Insulator-Metal (MIM) waveguides · Contrast ratio · all-optical logic gates · FEM · FDTD

1 Introduction

The use of all-optical devices has been subject to extensive research in recent years due to its potential applications to replace low-speed electronic components. The basic technology in an optical fiber system is subjected to ”optical-electronic-optical” conversion at terminals of the emission and reception [1]. In this regard, photonics and its devices have emerged as a key technology to facilitate optic/photonic integrated circuits on-chip [2]. The challenge of the conversion ”optical-electronic-optical” can be tackled by using all-optical devices such as multiplexer/demultiplexer [3], optical filters [4], modulators and switches [5] and optical logic gates [6]. The potential of this later has encouraged researchers to explore it intensively. It uses photons as information carriers that can offer high speed, high capacity and low loss components [7].

Recently, various all-optical logic gates have been proposed in the literature. Shaik et al. proposed plasmonic logic gates based on all-optical T-shaped resonator [8], a Y-shaped resonators have been proposed in the literature by Rani et al. to realize an AND gate in photonic crystal waveguide [9], Pan et al. studied the XOR, OR and the all-optical Y-shaped NOT gate [10]. Younis et al. also reported the combination of the ring cavities and the Y-shaped line defect placed between two waveguides of the all-optical AND gate [6], Fu. et al. have also realized all-optical gate XNOR, XOR, NOT and OR by slot [11].

The surface plasmons polaritons (SPPs), electromag-
netic waves that travel along a metal-dielectric or metal-air interface, are considered as a promising alternative in high-integration optical circuits due to their ability to manipulate light at the sub-wavelength scale thus overcoming the diffraction limit [12]. They offer many opportunities for optical communication without compromising on size, power consumption and manufacturing cost [13, 14]. Moreover, all-optical devices based on SPPs motivate many new researches to overcome the major constraints performance of the electronic devices based-on semiconductors which is the entrenched delay, high heat generation and limitations on electronic interconnections [15]. Thus, in this paper, we have used the geometry Metal–Insulator–Metal (MIM) waveguide for its inherent characteristics of subwavelength confinement [16]. Metal-Insulator-Metal (MIM) plasmonic waveguides are suitable components in signal processing, optimization, information processing and optical communication. Many configurations of MIM have been presented in the literature for designing plasmonic logic gates [17–23].

In this work, we will design all-optical logic gates based on a MIM waveguide coupled to an elliptical ring resonator (ERR). There are reasonable motivations for choosing ERR resonator. First, different fabrication techniques for elliptical geometry (for example, electron beam lithography [24] or focused ion beam [25]) fabrication process may be easier and more precise than the shape with corners, such as rectangular and triangular shapes [26, 27]. In addition, the use of sharp-angled resonators can be limited due to the high energy loss in the corners, as a result, by rounding them and choosing an optimal radius of curvature, thus, this loss of energy can be suppressed [28, 29]. Moreover, in the elliptical shape, the various optical resonance tuning scenarios are presumably compared to the other shapes, so, the performances of the device can be adjusted by tuning the main radius and the small radius of the elliptical shape. Compared to most previous resonator systems using MIM waveguide coupled to elliptical resonators have high coupling coefficient and quality factor [30]. This structure offers a simple and efficient solution to realize all-optical logic gate, thus it is suitable for on-chip optical computation and can also be used in WDM (Wavelength Division Multiplexing) systems.

In order to design all-optical logic gates, the main idea is based on the control of the constructive/destructive interference of two or more signals from light waves in two or more plasmonic waveguides. The constructive/destructive interference is considered a switching operation and devices that perform this operation called all-optical switching devices. Generally, the logic operations of plasmonic logic gates such as OR, AND, NOR, NAND, XOR, XNOR and NOT can constitute the basic units of the future optical processor. The transmission of the waveguides may be weakened or improved due to such constructive or destructive interference with the resonant system, therefore, the output logic states “1” and “0” may be represented by the “HIGH” OR “LOW” levels of the optical power transmission at the output of the resonant system, respectively.

In this article, we propose the largest number of plasmonic logic gates with the same resonant frequency in the structure and the same plasmonic resonator, i.e., without the need to modify or reconfigure the ERR resonator used to analyze and implement the proposed plasmonic logic gates, then, we confine ourselves to study OR, XOR, AND and NOT optical logic gates. In our design, Two-dimensional (2D) metal-insulator-metal (MIM) waveguides are chosen to build the entire logic device, due to their simple configuration as well as their ability to provide light confinement at the nanoscale, low crosstalk, reasonable propagation distance making them ideal candidates for various ultra-compact devices and other characteristics [31]. By using current advanced nanolithography techniques, with which it is possible to design and implement these devices with great precision. In the next investigations, we will show that the MIM-based logic device can efficiently perform various logic functions by switching the signals in the input and in the control port without changing the phase of these signals for the same logic gate. In addition, important values of the contrast ratios between logic states “1” and “0” can be obtained. The presented device is simple, compact, efficient and can be easily integrated into other nanophotonic devices which can be operated in a wide range of frequencies, making it a promising element for optical computation and optical interconnection networks on chip and to create developments in nano-optical processors for optical computing technologies. At the end of these numerical investigations, the obtained results are compared with the values of the contrast ratio (CR) previously reported in the literature. Our structure allows a considerable improvement of this ratio. Thus, the proposed device opens the way for the design of optical gates on a chip in the field of high-speed optical communication networks.

2 Study of an elliptical ring resonator

As preliminary study of optical logic gates, we propose a simple plasmonic structure made up of an input MIM waveguide, an elliptical ring resonator (ERR) and an output MIM waveguide, shown in fig.1a. In this part, our objective is to study optical properties through the
characterization of the resonance peaks and modes behavior.

The properties of ERR are numerically investigated by using the FEM (Finite-element-method) method with COMSOL Multiphysics software and the results will be compared initially with the FDTD (Finite-difference time-domain) method by using Lumerical FDTD software. Note that all of these investigations were performed by using a two-dimensional (2D) model as the characteristics of 3D models can be approximately similar by using 2D models, provided that the thickness of the proposed structure in the third dimension is much greater than the size of the entity in the calculation plane (2D) [32,33]. Thus, the effect of the thickness of the substrate on the obtained results can be mathematically neglected by assuming that the structure is infinite in the third dimension [34]. In addition, the 2D-model gives much faster simulations for high resonance devices without compromising the accuracy of the calculation.

In the following simulations, the transmittance of the SPPs is calculated using the elements of the system diffusion matrix $T = |S_{21}|^2$. First, we use the finite element method (FEM), where a polarized plane wave $\mathbf{TM}$ (the magnetic field is perpendicular to the plane in Figure 1 (a)), $\left( \mathbf{H}/\mathbf{Z} \right)_i$ is injected into the MIM structure [35]. The incident light wave will be coupled into the bus waveguide while the SPPs waves form up on the two metal interfaces. For the 2D-FDTD method, the simulation parameters are as follows: The grid size is set to $2 \text{ nm}$ in order to keep convergence, the input source type is Gaussian modulated continuous wave field, the simulation time is $1400 \text{ fs}$ with perfectly matched layer (PML) which is an absorbing boundary condition used to absorb the outgoing waves and to minimize simulation errors and reflections from sidewalls.

In addition, SPPs can be excited in the ERR resonator when a polarized light wave $\mathbf{TM}$ is launched at the resonant wavelength of that resonator. For the proposed configuration, the $\text{SiO}_2$ and silver are the dielectric and the metal considered, respectively. The silver (Ag) whose complex dielectric constant can be calculated with the Drude model:

$$\epsilon_{Ag} = \epsilon_{\infty} - \frac{\omega_p^2}{\omega^2 + j\omega\gamma}$$

(1)

Here, $\epsilon_{\infty}$ is the dielectric constant at the infinite frequency and $\omega_p$ and $\gamma$ stands for the bulk plasma frequencies and the electron collision, respectively. $\omega$ is the angular frequency of incident light. The parameters for silver can be set as $\epsilon_{\infty} = 3.7$, $\omega_p = 9.1 \text{ eV}$ and $\gamma = 0.018 \text{ eV}$ [36]. For the $\text{SiO}_2$, the refractive index is $n = 1.474$ around the visible region wavelength ($\lambda_4 = 684 \text{ nm}$) and $n = 1.47$ around the near-infrared region ($\lambda_2 = 1186 \text{ nm}$) [37].

In Fig. 1(a), the geometrical parameters of the structure are: the width (w) of the MIM waveguide, the distance (d) between the MIM waveguides and the resonator, the mean of minor radius of the elliptical ring (r) and the mean of major radius (R) of the elliptical ring. In this work, we use to choose the w=50nm to ensure that only the fundamental transverse magnetic mode $TM_0$ is supported by the simulation, while the other modes are cut off.

The transmission spectrum of the ERR resonator is illustrated in Fig. 1(b) where we observe the resonance modes with transmittances of large values which are obtained at wavelengths $\lambda_2 = 1186 \text{ nm}$ ($T_2 = 0.67$), $\lambda_3 = 803 \text{ nm}$ ($T_3 = 0.795$), $\lambda_4 = 624 \text{ nm}$ ($T_4 = 0.8$) and $\lambda_5 = 514 \text{ nm}$ ($T_5 = 0.79$). According to standing wave theory [38], the resonant wavelength of an ERR resonator is obtained as:

$$\lambda \approx \frac{2Re(\eta_{eff})L_{eff}}{m}$$

(2)
Where $Re(n_{eff})$ is the real part of the effective refractive index for a wavelength in the MIM waveguide, $L_{eff}$ represents the effective length of the resonator and $m$ is the order of the resonance mode ($m$ is a positive integer).

In order to consolidate the simulated results, a comparison between transmission spectrum of the MIM waveguide structure obtained with both FEM and FDTD methods is presented in Fig. 1(b). The overall shape of the both spectra is similar with a slight difference in the intensity and small shift of resonance peaks. These tiny differences are imputed to the different approaches of the two methods, such as the mesh, the boundary conditions; see reference [27, 39]. All of the following results will be obtained by using the FEM method. In Fig. 2, we present the magnetic field distributions $|Hz|$ corresponding to the resonance peaks ($\lambda_2, \lambda_3, \lambda_4, \lambda_5$). We observe that $\lambda_2$ corresponds to $TM_2$ (two nodes), the $\lambda_3$ to $TM_3$ (three nodes), the $\lambda_4$ to $TM_4$ (four nodes) and the resonance $\lambda_5$ to $TM_5$ (five nodes). This observation is in good agreement with the resonance condition mentioned in analytical equation (2). For the part of the study left, a characterization of a fundamental logic functions will be presented in two regions by studying the two resonance peaks $\lambda_2 = 1186$ nm (near-infrared) and $\lambda_4 = 624$ nm (visible). We demonstrate the realization of the principal logic gates, i.e., OR, XOR, AND, NOT without any need of changing the structure configuration. The proposed design consists of straight waveguides and elliptical ring resonator. The width ($w$) of the MIM waveguide is 50 nm, the distance ($d$) between the MIM waveguides and the resonator is 10 nm, the mean of minor radius of the elliptical ring ($r$) and the mean of major radius ($R$) of the elliptical ring are 125 nm and 225 nm, respectively. The geometrical parameters of our structure are chosen to achieve the highest power transmission ratio. A detailed study, not shown here for the sake of the purpose consistency and paper subject, was carried out.

The transmission of optical power and the position of the resonance peaks can be modified according to the geometrical parameters of the proposed structure, the selected materials, their refractive indexes, the location of the ports, the polarization of the incident field and its phase of the input light wave at the input ports. The interaction between the waveguides and the Elliptical Ring Resonator (ERR) causes new localized surface plasmon resonances, which are the result of the coupling between the resonator and the MIM waveguides. Besides, since SPPs waves only couple strongly in the near field at very short distances, the coupling distance $d$ was chosen to allow this mechanism to be maintained and to obtain the best field improvement. From the results obtained by our simulations, the optimum coupling distance between the ERR and the waveguides for the initially proposed structure was chosen to be $d=10$ nm.

3 The structure layout and characterization concept

The proposed structure is shown schematically in fig.3(a). It consists of three straight waveguides (two input ports and one output port) and an elliptical ring resonator. The SPPs are excited by injecting a $TM_0$ polarized plane wave to the input ports (and the control port). The expected function for all the proposed plasmonic logic gates is achieved by the principle of constructive and destructive interferences between the input signals or between the input signals and the control signal which depends on the phase of the incident light wave. Most of the times, the performances of the four plasmonic logic gates, presented in this paper, are characterized by the following two criteria [40]: i) the transmission of the optical power ratio of input ports to the output port. This can be done by choosing a transmission threshold value in order to logically distinguish between the logical “1” value (the “HIGH” state of the transmission) and the logical “0” value (“LOW” state of the transmission) at the output. ii) the contrast ratio (CR) between the transmission of the “HIGH” and “LOW” states of the output port. These two criteria are described by the following equations:

$$T = \frac{P_{in}}{P_{out}}$$

(3)

Where, $T$ is the transmission, $P_{out}$ is the optical power obtained at the output port and $P_{in}$ is the optical power injected at the input ports (and the control port).
$CR = 10. \log \left( \frac{P_{H}^{Avr}}{P_{L}^{Avr}} \right)$  \hspace{1cm} (4)

Where $P_{L}^{Avr}$ and $P_{H}^{Avr}$ are the average output powers to “0” and “1” logical states, respectively.

The Achilles heel of this later can be seen when the average output powers for the “0” logic state is zero or close to zero. In this case, the value of CR is undefined or not significant and thus, it can’t be used to characterize some all-optical logic gates. Moreover, it doesn’t give any indication about the gap between the maximum and minimum of the transmitted signal intensity which is essential in logical states discretization.

For all these reasons, we introduce for the first time in all-optical logic gates characterization a new parameter to evaluate the optical intensity gap. We call this indicator the gap-threshold ratio (GTR). It is defined by the difference between maximum and minimum of the transmitted intensity normalized to the transmission threshold value. It can be expressed as a ratio or in dB:

$$GTR = 10. \log \left( \frac{T_{\text{max}} - T_{\text{min}}}{T_{th}} \right)$$  \hspace{1cm} (5)

For all-optical logic gates operations presented in this work, the transmission threshold value was chosen as $T_{th} = 0.2$.

4 All-optical logic gates based on ERR

4.1 Realization of the optical OR gate

The OR gate is designed by using the structure shown in fig. 3(a). To achieve the correct operation of OR gate, the transmission threshold value chosen is $T_{th} = 0.2$ (“1” state and “0” state correspond to $T > T_{th}$ and $T < T_{th}$, respectively). The input signals are assumed to be injected into port “A” and port “B” (fig. 3(a)). The input power $P_{in}$ is taken 1 W (that corresponds to “1” logical state). For the near-infrared region ($\lambda_2 = 1191$ nm), If a single signal is injected into input A or B, the transmission intensity at the output port is approximately $T_2 = 0.32$ which corresponds to “1” state according to $T_{th}$ value. (“0 OR 1=1” or “1 OR 0=1” see Table. 1). For the case of the visible region ($\lambda_4 = 618$ nm), when the single signal is injected into input A or input B, the transmission at the output port is approximately $T_4 = 0.35$ which corresponds to “1” state according to the $T_{th}$ value. When the signal is injected into both inputs simultaneously with the same phase, i.e., $\phi_A = \phi_B = 0^\circ$; (“1 OR 1=1”) the output port is close to $T_2 = 1.26$ and $T_4 = 1.43$ for the near-infrared

and visible region, respectively (see Table. 1). When the phase difference between the two input signals is null, a constructive interference between them occurs at the ERR and consequently leads to the high transmission intensity at output port $T > 1$. In addition, the magnetic field distributions, at the inset of Fig. 3(b), of the OR gate for different input states of the two modes (2$^{nd}$ and 4$^{th}$ modes) confirm accurately this interpretation. The truth table and the transmission coefficient of the proposed OR gate are shown in Table. 1. The gap-threshold ratio for the OR gate is $GTR_2 = 2.43$ dB and $GTR_4 = 2.04$ dB for the near-infrared region and the visible region, respectively.
Fig. 4 The transmission of the elliptical ring resonator (ERR) for different input states (Input port A and input port B) for the logic gate XOR. The distribution of the magnetic fields for different combinations of the logic gate are shown in the inset of the figure (The units of the intensity profile are distributed arbitrarily between the minimum value and the maximum value). The geometrical parameters of the structure are as follows: w=50 nm, d=10 nm, r=125 nm and R=225 nm.

### Table 1: The truth table and transmission spectrum for the proposed optical OR gate

| Input "A" state | Input "B" state | Phase (°) | Output state | Transmission (λ = 1191 nm) | Transmission (λ = 618 nm) |
|-----------------|-----------------|-----------|--------------|-----------------------------|-----------------------------|
| 0               | 0               | 0°        | 0            | 0                           | 0                           |
| 0               | 1               | 0°        | 1            | 0.32 (GTR_2 = 2.43 dB)       | 0.35 (GTR_4 = 2.04 dB)       |
| 1               | 0               | 0°        | 1            | 0.32 (GTR_2 = 2.43 dB)       | 0.35 (GTR_4 = 2.04 dB)       |
| 1               | 1               | 0°        | 1            | 1.26                        | 1.43                        |

4.2 Realization of the optical XOR gate (EXCLUSIVE OR)

In order to design the XOR logic gate, we have kept the same plasmonic structure at Fig. 3(a). Here, the destructive interference between the both input signals is required. Therefore, the phase difference between the two inputs A and B is set to \( \Delta \phi = 180° \) (\( \phi_A = 0° \) and \( \phi_B = 180° \)). The truth table of working principle of the gate is presented in Table 2. When the two inputs (A and B) are at logic “1” state, the both signals are coupled to the ERR resonator and interfere destructively (due to the phase difference \( \Delta \phi \)), see the magnetic distribution field for the two modes at the insert in Fig. 4. Then, it gives rise of a weak transmitted signal at the output port (approximately zero intensity, \( T = 7 \times 10^{-4} \)). For the logic operations “0 XOR 1=1” or “1 XOR 0=1”, the input signal is coupled to the ERR and transmitted to the output for the two resonant modes. The transmissions spectra to the output port for different input states are shown in figure 4. By using the equation (4), the contrast ratio of the XOR gate is \( CR_2 = 28.75 \text{ dB} \) for the 2nd mode and \( CR_4 = 28.35 \text{ dB} \) for the 4th mode, while the gap-threshold ratio for the XOR gate is \( (GTR_2 \approx 2.43 \text{ dB}) \) and \( (GTR_4 \approx 2.04 \text{ dB}) \) for the near-infrared region and the visible region, respectively.

From our knowledge, these results are considered as one of the highest CR reported in the literature. The truth table and the transmission coefficient of the proposed XOR gate are shown in Table 2.

4.3 Realization of the optical AND gate

To implement the optical AND logic gate, the same configuration used previously can be adopted with an additional port (probe port) as show in Fig. 5(a). The logic function of the AND gate is performed only when both inputs are set to “1” logic state, the logic state “1” can be realized at the output port. Here, we need both constructive and destructive interference between the input signal (s) and the control signal. This later is always enabled with a phase of \( (\phi_P = 180°) \). The AND logic operations are depending of the intensity threshold. One way to control the output signal intensity is to tune the distance \( g \) between the control port and the ERR. We use to choose \( g=20 \text{ nm} \) to properly define the intensity threshold to perform the AND output logic state (a study not shown here). When the input ports correspond to these logic operations “1 and 0” and “0 and 1”, destructive interferences occur between input signal and control signal due to their opposite phases (\( \phi_A = \phi_B = 0° \) and control signal phase \( \phi_P = 180° \)), which leads to a low transmitted signal \( T_2 = 0.08 \) (near-infrared) and \( T_4 = 0.07 \) (visible). For the logic operation “1 and 1” a constructive interference occurs between input signals which leads to \( \Lambda “1” \) logic state with a high output intensity where \( T_2 = 0.64 \) (near-infrared) and \( T_4 = 0.67 \) (visible). The contrast ratio of the AND gate is \( CR_2 = 9.5 \text{ dB} \) for the 2nd mode and \( CR_4 = 9.81 \text{ dB} \) for the 4th mode, while the gap-threshold ratio for the AND gate is \( (GTR_2 = 4.77 \text{ dB}) \) and \( (GTR_4 = 4.47 \text{ dB}) \) for the near-infrared region and
Table 2 The truth table and transmission spectrum for the proposed optical XOR gate

| Input “A” state | Input “B” state | Phase (°) | Output state | Transmission (λ = 1191 nm) | Transmission (λ = 618 nm) |
|-----------------|-----------------|-----------|--------------|-----------------------------|-----------------------------|
| 0               | 0               | 0         | 0            | 0.32 (CR₂ = 28.75 dB)       | 0.35 (CR₄ = 28.35 dB)       |
| 0               | 1               | 180°      | 1            | 0.32 (GTR₂ ≈ 2.43 dB)       | 0.35 (GTR₄ ≈ 2.04 dB)       |
| 1               | 0               | 180°      | 0            | 7 × 10⁻⁴                    | 7 × 10⁻⁴                    |

The proposed NOT gate is similar to XOR gate where one of the two input ports becomes a control port set to “1” logic state, so, in order to design this logic gate, we have kept the same plasmonic structure at Fig.3(a). The function of a NOT gate can be achieved by a destructive and constructive interference between the input signal and the control signal (the inset of Fig. 6(b)). The phase difference between the input and control ports is set to (Δφ = 180° (φᵣ = 0° et φₚ = 180°)). For both modes (m₂ and m₄), If a single signal is injected into input port, the signal couple with the control port signal via ERR, which leads to a near-zero output. This corresponds to the logic operation “NOT 1=0”. On the other side, if no signal is launched into the input port, the signal of the control port couple to the ERR which leads to a high intensity signal which corresponds to the logic operation “NOT 0=1”. The truth table of working principle of this logic gate is presented in Table. 4. By using the equation (4), the contrast ratio of the XOR gate is CR₂ = 28 dB for the 2ⁿᵈ mode and CR₄ = 28.81 dB for the 4ᵗʰ
mode, while the gap-threshold ratio for the NOT gate is $GTR_2 = 2.9$ dB and $GTR_4 = 2.04$ dB for the near-infrared region and the visible region, respectively.

### 4.5 Comparison of contrast ratios for different literatures

Table 5 and Table 6 summarize a comparison of the two parameters (CR) and (GTR) respectively with some previous works. The obtained (CR) is significantly high compared to other studies for XOR, AND and NOT logic gates (28.83 dB). As we introduce for the first time the GTR parameter, we calculated it in Table 6 for other papers. The suggested value of transmission threshold between logic 0 and logic 1 states is $T_{th} = 0.2$ for all papers. The comparison with our GTR shows that our structure exhibits a large gap intensity between the logic states. The GTR parameter allows to compare the logic gate of the same structure between them. For our case the highest GTR is obtained for the AND logic gate (4.77 dB). Finally and in contrast with CR, the GTR can be calculated for OR logic gate in our case (2.04 dB).

### 5 Conclusion

In this paper, a two-dimensional (2D) plasmonic metal-insulator-metal (MIM) coupled with Elliptical Ring Resonator (ERR) is studied using the numerical method of the FEM (finite elements method). The ERR configuration is proposed and characterized as an All-optical logic gates OR, XOR, AND, and NOT. The characterization was mainly done for two spectral regions, visible and near-infrared. A high intensity contrast ratios (CR) between the logic states (“1” and “0”) was achieved (28 dB). The obtained (CR) is significantly high compared to other studies for XOR, AND and NOT logic gates (28.83 dB). The new parameter introduced, gap-threshold ratio (GTR), shows a good results for all logic gates compared to other studies. The GTR parameter allows to compare the logic gates of the same structure between them. For our case the highest GTR is obtained for the AND logic gate (4.77 dB). The proposed structure can be a good candidate as an all-optical logic gates in optical processing.
Table 4 The truth table and transmission spectrum for the proposed optical NOT gate

| Input state | Control port state | Phase (°) | Output state | Transmission (λ = 1191 nm) | Transmission (λ = 618 nm) |
|-------------|-------------------|-----------|--------------|----------------------------|--------------------------|
| 0           | 1                 | 180°      | 1            | 0.31 (CR = 28 dB)           | 0.39 (CR = 28.81 dB)     |
| 1           | 1                 | 180°      | 0            | 5 × 10⁻⁴ (GTR = 2.9 dB)     | 5 × 10⁻⁴ (GTR = 2.04 dB) |

Table 5 Comparison of the obtained value of the contrast ratio with different optical gates reported in the literature

| Reference | This paper | [17] 2020 | [18] 2014 | [19] 2018 | [20] 2019 | [21] 2012 | [22] 2018 | [23] 2017 |
|-----------|------------|-----------|-----------|-----------|-----------|-----------|-----------|-----------|
| Numerical method used | 2D-FEM | 2D-FDFT | 2D-FEM | 2D-FDFT | 2D-FEM | 2D-FDFT | 2D-FEM | 2D-FDFT |
| Resonator geometry | Elliptical ring resonator | 2D Pho- tonic Crystal | Double circular ring resonator | Circular ring resonator | octagon shaped cavity resonator | Circular cavity resonator | Double circular ring resonator | Slot cavity resonator |
| XOR | 4⁴th mode | 2²nd mode | 28.35 | 28.75 | - | - | 15 | 12.8 | 23.52 | 26 | - | 25.86 |
| AND | 9.81 | 9.5 | 10.96 | - | - | 6 | - | - | - | - |
| NOT | 28.83 | 28 | - | 3.16 | 15 | 18.8 | - | 26 | 11.32 | 4 |

(*) 4 air channels cut into a silver background at the input port/a single channel at the output port.

Table 6 Comparison of the obtained value of the gap-threshold ratio (dB) with different optical gates reported in the literature

| Reference | This paper | [19] 2018 | [21] (**) 2012 | [23] (**) 2018 |
|-----------|------------|-----------|---------------|---------------|
| Resonator geometry | Elliptical ring resonator | Circular ring resonator | Circular cavity resonator | Double circular ring resonator |
| OR | 4⁴th mode | 2²nd mode | - | - | - |
| XOR | 2.04 | 2.43 | 1.96 | 3.21 | - |
| AND | 4.47 | 4.77 | - | - | - |
| NOT | 2.04 | 2.9 | 3.98 | 3.21 | 4.86 |

(**) The threshold is not defined in this paper, to calculate the GTR we use the proposed threshold in our paper (Tₘₜₜ = 0.2).

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Conflict of interest The authors declare that they have no conflict of interest.

Availability of data and material The authors confirm that the data supporting the findings of this study are available within the article.

Code availability Not applicable.

Ethics approval Not applicable.

Consent to participate All authors gave their consent to participate.

Consent for publication All authors gave their consent for publication.

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