Hybrid Control for Power System Based on STATCOM and UPFC with Two 3-level 48-pulse under Different Conditions

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Abstract: This research presents the proposed model, to control the power flow in the transmission power system by applying Unified Power Flow Controller (UPFC), STATCOM and two 3-level 48-pulse converter. This hybrid has been used to improve performance and reduce the maximum over shoot which is obtained from proposed model when the fault is occurring or suddenly system changes. The behavior of the system is analyzed under different three cases. The first case, the model is applied to plus load at bus 3. The second case, the model is operating at normal work and the third case, the three phase fault is occurred at bus4. In this research, the performance of system is studied under applied all cases of the current, voltage and power for the system. The numerical results of the proposed model are introduced to show the maximum over shoot and RMS values after applied proposed control at three different cases to prove the suggested model gave a good performance especially, during three phases fault and after fault clearance.

Keywords: Power System, STATCOM, UPFC, SSSC, Fault Conditions

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1. Introduction

The active power filters which provide flexible current harmonic compensation and help in reactive power control are possible these days due to the development of power electronics. Hence by implementation of the power electronic based devices, such as Static Var Compensator and static synchronous compensator (STATCOM) [1], it is perfect for improving the quality of power given to the consumers [2]. Its principal function is to inject reactive power into the system which helps to support the system voltage profile, however, it has some other advantages; reduce the flicker that is found in fluctuating loads, moderate the power oscillations, reduce the hypo-synchronous oscillations, increase the power transfer and as consequences the power system performance has improved [3], [4].

The UPFC is the multi-functions FACTS device, which provides control of voltage magnitude (simultaneous and independent) and power flow (active and reactive) [5]. UPFC is consists of two FACTS a device: STATCOM is defined as Static Synchronous Compensator, also SSSC is defined as Static Series Synchronous Compensator and is linked across DC link [6], [7]. SSSC is applied to control the voltage which is connected in series with the line, while shunt converter (STATCOM) is applied to inject reactive power flow (VAR) into the line, voltage regulation at the connection point, and balancing the actual power flow passed between the series inverter and the transmission line [8], [9].

The series inverter is applied to control the magnitude and phase voltage also, control the active and reactive line power flow for the transmission line [10]. That way, the UPFC is ability to fulfill the functions of reactive shunt compensation, active and reactive series compensation and phase shifting. Also, the UPFC had another important function which is to supply stability control to repress power system oscillations for reducing the transient period of power system [11]. UPFC is flexible and fast controller for power flow controllers, which is a corresponding want for reliable and actual models of them to investigate the effect of these on the characteristics of the power system [12].

In this paper, the proposed model is presented, to control the power flow in the transmission power system by applying Unified Power Flow Controller (UPFC), STATCOM and two 3-level 48-pulse converter. The performance of the system is studied under different the three cases. The first case, the system is applied to pulse load at bus 3. The second case, the system is operating at normal work and the third case, the system is subjected to three phase fault at bus4. In this paper, the performance of system is studied under applied all cases -of the current, voltage and power for the system.
2. Static Synchronous Compensator (STATCOM)

A STATCOM is a shunt tool that’s applied to compensate the reactive power in electricity gadget and that way stabilizes the voltage. The STATCOM makes use of a voltage supply converter (VSC) linked in shunt to a transmission line. In maximum instances, the dc voltage help for the VSC is supplied by using the dc capacitor of exceptionally small power storage capability. In constant state operation, the power exchanged with the line is maintained to zero [13], [14].

STATCOM is defined as the static synchronous generator working as a static compensator related in parallel whose output modern inductive or capacitive can be managed independently of the voltage. This cutting-edge feeds an ac/dc energy converter, which produces a hard and fast of outputs with controllable 3-segment voltages [15]. Additionally, the frequency of those voltages is the ac gadget frequency. The ac/dc strength converter is Controlled by using PWM techniques, so the output voltages completed are nearly sinusoidal. Those controllers are possible by using the excessive switching frequency of the IGBT, GTo, IGCT or IEGT transistors of the power converter [16]. The basic structure of the STATCOM is consists of DC voltage result from inverter which is connected to shunt capacitor with PWM converter. This combination is connected to grid through transformer as shown in Fig. 1 which is represented a transformer, grid a inductor and resistor [17], [18].

3. Unified Power Flow Controller (UPFC)

The UPFC is consists of two voltage source inverter (VSI) with common DC charge capacitor and linked to power system through transformers. First one of VSI is linked to shunt transmission line system through shunt transformer; second VSI is linked with series transformer as shown in Fig.2. [19]

The shunt converter of UPFC can be injected a current controller to transmission line system. The two components of current controller are real and reactive component. The real component is in phase or opposite phase with the transmission line voltage and it is controlled the real power of series inverter. The reactive component of the current controller is desired to inject or absorb reactive power from transmission line [20].

3.1. UPFC Static Model

In this section, it is represented modify estimation of state – state for UPFC by analyzing power equation (active and reactive) which are introduced the static analysis by determine the voltage nodes as illustrate in Fig. 3. [21], [22], [23].

An active and reactive power equation for static model can be analyses as follows:

\[ P_{km} = V_k^2 G_{kk} + V_k V_m (G_{km} \cos(\theta_k - \theta_m) + B_{km} \sin((\theta_k - \theta_m) + \theta_k v_r G_{km} \cos(\theta_k - \theta_{kr}) + B_{km} \sin((\theta_k - \theta_{kr})) + V_k v_r G_{km} \cos(\theta_k - \theta_{kr}) + B_{km} \sin((\theta_k - \theta_{kr})) \]

(1)

\[ Q_{km} = V_k^2 B_{kk} + V_k V_m (G_{km} \sin(\theta_k - \theta_m) + B_{km} \cos((\theta_k - \theta_m) + \theta_k v_r G_{km} \sin(\theta_k - \theta_{cr}) - B_{km} \cos((\theta_k - \theta_{cr})) + V_k v_r G_{km} \sin(\theta_k - \theta_{cr}) - B_{km} \cos((\theta_k - \theta_{cr})) \]

(2)

\[ P_{mk} = V_m^2 G_{mm} + V_m V_k (G_{mm} \cos(\theta_m - \theta_k) + B_{km} \sin((\theta_m - \theta_k) + V_m v_r G_{mm} \sin(\theta_m - \theta_{cr}) + B_{mm} \sin((\theta_m - \theta_{cr})) \]

(3)

\[ Q_{mk} = V_m^2 B_{mm} + V_m V_k (G_{mm} \sin(\theta_m - \theta_k) + B_{km} \cos((\theta_m - \theta_k) + V_m v_r G_{mm} \sin(\theta_m - \theta_{cr}) - B_{mm} \cos((\theta_m - \theta_{cr})) \]

(4)

At static mode condition of UPFC, it is not supply or absorb active power. So, it can be calculated the active power of UPFC converter as follow:

\[ P_c = V_k^2 G_{mm} + V_k V_r G_{mm} \cos(\theta_{cr} - \theta_{kr}) + B_{km} \sin((\theta_{cr} - \theta_{kr})) + V_k v_r G_{mm} \cos(\theta_{cr} - \theta_m) + B_{km} \sin((\theta_{cr} - \theta_m)) \]

(5)

\[ P_r = -V_k^2 v_{kr} + V_k v_r G_{mm} \cos(\theta_{cr} - \theta_{kr}) + B_{km} \sin((\theta_{cr} - \theta_{kr})) \]

(6)

\[ P_c + P_r = 0 \]

(7)
3.2. UPFC Control Model

The basic parameters are used to control of UPFC such as magnitude and phase shift of voltage, power (active & reactive). UPFC is consists of series and shunt converter with the transmission line through a series transformer and shunt transformer respectively as shown in Fig. 4.

Series converter is applied to control of power flow and voltage injection to the transmission line in this model, and it is can be controlled of power (active & reactive) errors and reduced its by using PI controller to compute references \( V_d, V_q \) [21].

Shunt converter (STATCOM) is used to control the line ac voltage and DC voltage by applied voltage regulation technique while SSSC is applied to control of reactive power and active power [22], [24].

3.3.2. Three level 48-pulse voltage

The three level 48-pulse converter working can be obtained by connecting two 24-pulse converter with phase shifted being 7.5 from each other. By applying symmetrical shift, To obtain the 7.5 phase shift can be applied by coupling two transformer with 3.75 phase shift to one of 24-pulse converter and +3.75 on the other two coupling transformer of the second 24-pulse converter [21], [25]. The phase-shift model on each four 12-pulse converter cascade operates as follows:

The output voltage obtained from the first 12-pulse converter is:

\[
V_{ab12} (t) = 2[V_{a1} \sin(\omega t + 30^\circ) + V_{a21} \sin(11\omega t + 195^\circ) + V_{a31} \sin(13\omega t + 225^\circ) + V_{a23} \sin(23\omega t + 60^\circ) + V_{a25} \sin(25\omega t + 120^\circ)]
\]  (8)

The output voltage obtained from the second 12-pulse converter is:

\[
V_{ab12} (t) = 2[V_{a1} \sin(\omega t + 30^\circ) + V_{a11} \sin(11\omega t + 15^\circ) + V_{a31} \sin(13\omega t + 75^\circ) + V_{a23} \sin(23\omega t + 60^\circ) + V_{a25} \sin(25\omega t + 120^\circ)]
\]  (9)

The output voltage Obtained from the third 12-pulse converter is:

\[
V_{ab12} (t) = 2[V_{a1} \sin(\omega t + 30^\circ) + V_{a11} \sin(11\omega t + 285^\circ) + V_{a31} \sin(13\omega t + 345^\circ) + V_{a23} \sin(23\omega t + 240^\circ) + V_{a25} \sin(25\omega t + 300^\circ)]
\]  (10)

The output voltage Obtained from the fourth 12-pulse converter is:

\[
V_{ab12} (t) = 2[V_{a1} \sin(\omega t + 30^\circ) + V_{a11} \sin(11\omega t + 105^\circ) + V_{a31} \sin(13\omega t + 165^\circ) + V_{a23} \sin(23\omega t + 240^\circ) + V_{a25} \sin(25\omega t + 300^\circ)]
\]  (11)

The four identical 12-pulse converters provide shifted ac output voltages, introduced by (34)–(37), which are added in series on the secondary windings of the transformers. The total output voltage 48-pulse is obtained by:

\[
V_{ab48} (t) = V_{ab12} (t) + V_{ab12} (t) + V_{ab12} (t) + V_{ab12} (t) \ldots
\]  (12)

\[
V_{ab48} (t) = 8[V_{a1} \sin(\omega t + 30^\circ) + V_{a21} \sin(47\omega t + 150^\circ) + V_{a31} \sin(49\omega t + 210^\circ) + V_{a23} \sin(95\omega t + 330^\circ) + V_{a25} \sin(97\omega t + 30^\circ)]
\]  (13)

The output voltage 48-pulse ac from the STATCOM model is given by:

\[
V_{ab48} (t) = 8/\sqrt{3} \sum_{n=(48K+1)} V_{abn} \sin(n\omega t + 187.5^\circ n - 187.5^\circ i)
\]  (14)

Voltages \( v_{ab48} (t) \) and \( v_{ac48} (t) \) have a same sinusoidal wave with a phase shifting of 120 and 240, respectively, from phase a \( v_{ab48} (t) \).

4. Proposed Control Model

4.1. Mathematical Control Model of STATCOM

The vector control (d-q) method is applied to control the STATCOM. This method can control active and reactive power by controlling the voltage and current id d-q reference frame, so it is introduced STATCOM control model in d-q reference[21]. The voltage equations across R-L are:
\[
\begin{bmatrix}
V_d \\
V_q
\end{bmatrix} = R_f \begin{bmatrix}
i_d \\
i_q
\end{bmatrix} + L_f \frac{d}{dt} \begin{bmatrix}
i_d \\
i_q
\end{bmatrix} + \omega_s L_f \begin{bmatrix}
-i_d \\
i_q
\end{bmatrix} + \begin{bmatrix}
V_{d1} \\
V_{q1}
\end{bmatrix}
\] (15)

Where:
- \(\omega_s\): angular frequency for grid voltage
- \(V_d, V_q\): the grid voltage components of d-q
- \(V_{d1}, V_{q1}\): the converter output voltage components of d-q
- \(i_d, i_q\): is the current flows between system and STATCOM components of d-q

From equation (1) it can be obtained the next equations of the line voltages for grid and output converter at steady state space at d-q reference frame:

\[
\begin{align*}
V_{dq} &= R_f i_{dq} + L_f \frac{d}{dt} i_{dq} + j\omega_s L_f i_{dq} + V_{dq1} \\
V_{dq} &= R_f i_{dq} + j\omega_s L_f i_{dq} + V_{dq1}
\end{align*}
\] (16) (17)

And the instantaneous equations of power (active and reactive) at d-q for the ac system are:

\[
P_{ac}(t) = V_d i_d + V_q i_q \approx V_d i_d
\] (18)

\[
q_{ac}(t) = V_q i_d - V_d i_q \approx -V_d i_q
\] (19)

In equations (2),(3) after neglecting \(R_f\), the current equations are calculated as follow:

\[
I_{dq} = \frac{V_{dq1} - V_{dq}}{jX_f} = \frac{V_{dq1} - V_d}{jX_f} + \frac{V_{q1}}{X_f}
\] (20)

Where:
- \(X_f\): is the combination reactance for the transformer and grid.

By substitution into the equations (4),(5) in terms of (6), it is obtained the following equation:

\[
P_{ac} = \frac{V_d V_{q1}}{X_L}, \quad Q_{ac} = \frac{V_d}{X_L} (V_d - V_{d1})
\] (21)

Fig. 5 is represented simulation STATCOM model according to analysis the previous equations.

4.2. Dynamic Model of UPFC

In Fig. 6, it can be applied Kirchhoff law on the equivalent circuit to analyze the dynamic model of the UPFC [23], [24]. So, the following three equations are obtained:
The equivalent circuit for dynamic of the UPFC

\[ L \frac{di_{s1}}{dt} + r_{i} s_{1} = V_{s1} - V_{c1} - V_{ch1} \]  
\[ L \frac{di_{s2}}{dt} + r_{i} s_{2} = V_{s2} - V_{c2} - V_{ch2} \]  
\[ L \frac{di_{s3}}{dt} + r_{i} s_{3} = V_{s3} - V_{c3} - V_{ch3} \]  

The equations of the shunt circuit are:

\[ L \frac{di_{p1}}{dt} + r_{i} p_{1} = V_{p1} - V_{c1} - V_{ch1} \]  
\[ L \frac{di_{p2}}{dt} + r_{i} p_{2} = V_{p2} - V_{c2} - V_{ch2} \]  
\[ L \frac{di_{p3}}{dt} + r_{i} p_{3} = V_{p3} - V_{c3} - V_{ch3} \]  

The DC circuit equations of are:

\[ \frac{1}{2} c \frac{dV_{dc}^{2}}{dt} = P_{e} - P_{ep} \]  
\[ P_{e} = V_{c1} i_{ch1} + V_{c2} i_{ch2} + V_{c3} i_{ch3} \]  
\[ P_{ep} = V_{p1} i_{p1} + V_{p2} i_{p2} + V_{p3} i_{p3} \]  

Where, \( V_{dc} \) is the DC voltage.

For the dynamic equations of the shunt compensator, assuming the active power in the capacitor is zero. Also, no active power losses in converter so the equations of the series circuit are:

\[ \frac{d}{dt} \begin{bmatrix} i_{s1} \\ i_{s2} \\ i_{s3} \end{bmatrix} = \begin{bmatrix} \frac{-r}{L} & 0 & 0 \\ 0 & \frac{-r}{L} & 0 \\ 0 & 0 & \frac{-r}{L} \end{bmatrix} \begin{bmatrix} i_{s1} \\ i_{s2} \\ i_{s3} \end{bmatrix} + \begin{bmatrix} \frac{1}{L} V_{s1} - V_{c1} - V_{ch1} \\ \frac{1}{L} V_{s2} - V_{c2} - V_{ch2} \\ \frac{1}{L} V_{s3} - V_{c3} - V_{ch3} \end{bmatrix} \]

The equations of the shunt circuit are:

\[ \frac{d}{dt} \begin{bmatrix} i_{p1} \\ i_{p2} \\ i_{p3} \end{bmatrix} = R_{f} \begin{bmatrix} \frac{-r}{L} & \frac{w}{L} & \frac{-r}{L} \\ \frac{-w}{L} & \frac{-r}{L} & \frac{-w}{L} \end{bmatrix} \begin{bmatrix} i_{s1} \\ i_{s2} \\ i_{s3} \end{bmatrix} + \frac{1}{L} \begin{bmatrix} V_{s1} - V_{c1} - V_{ch1} \\ V_{s2} - V_{c2} - V_{ch2} \\ V_{s3} - V_{c3} - V_{ch3} \end{bmatrix} \]

The dynamic equations of the circuit are:

\[ \frac{dV_{dc}}{dt} = \frac{3}{2c} (V_{cd} i_{chd} + V_{cq} i_{chq}) \]  
\[ i_{chd} = i_{sd} + i_{pd} \]  
\[ i_{chq} = i_{sq} + i_{pq} \]  

The active and reactive powers deliver are:

\[ P_{s} = \frac{3}{2} (V_{sd} i_{sd} + V_{sq} i_{sq}) \]  
\[ Q_{s} = \frac{3}{2} (V_{dq} i_{sd} - V_{sd} i_{dq}) \]  

The active and reactive powers absorb are:

\[ P_{ch} = \frac{3}{2} (V_{chd} i_{chd} + V_{chq} i_{chq}) \]  
\[ Q_{ch} = \frac{3}{2} (V_{chq} i_{chd} - V_{chd} i_{chq}) \]  

Fig. 7 is represented simulation UPFC model according to previous analysis.
According to analysis for STATCOM and UPFC for the proposed model, it can be built Matlab/simulation as shown in Fig. 8.
4.3. The Twice Three Level 48-pulse Converter

4.3.1. Converter principle

The 48-pulse voltage source converter (VSC) is designed from four 3-level inverter. It is obtained from converter that the output voltage is not a pure sinusoidal voltage but has a harmonics, which is not suitable for high power and high voltage applications [21].

To obtain the sinusoidal voltage with low harmonic content twice three level 48-pulse VSC model is designed in the proposed model as shown in Fig. 9.

The twice 3-level and 48-pulse converter are applied in high voltage and high power applications which have a very low harmonic without using any filter circuits as shown in Fig. 9.

5. Numerical Analysis

The proposed unified power flow controller (UPFC) is used to control the power flow in a 500 kV transmission system which is operated under different conditions. This system is subjected to three cases which are the normal operating condition, with different load conditions, and operates with three phase fault conditions. A Matlab/Simulink block of the proposed system is built as shown in fig. 10. The proposed system comprises a UPFC located between the bus 1 and bus 2 at 500 kV, which is applied to control of power flow through bus 2 and control voltage at bus 1. UPFC consists of two 3-level 48-pulse converter (500KV, 100MW); the first circuit is shunt converter which is connected to bus 1 and the second circuit is the series converter connected between bus 1 and bus 2. The shunt converter is connected to the series converter through DC buses. 48-pulse converter is connected to UPFC controller which is contained shunt STATCOM controller, series UPFC controller and SSSC to control injected voltage as shown in Fig. 11. The variation load conditions is connected directly before bus 4 which is consists of inductive load at 0≤t≤0.1. Three phase fault is occurred between bus 4 and grid at 0.3≤t≤0.4. While the proposed system worked at normal conditions at 0.1≤t≤0.2, 0.4≤t≤0.7.

The system parameters For UPFC controller are taken as follows:

**Shunt (STATCOM):** rated voltage= 1 pu for base 500KV, parameters For voltage regulator are \( K_p=36 \), \( K_i=9000 \),

Parameters For current regulator (Iq) are \( K_p=5 \), \( K_i=40 \), \( Q_{ref}=-0.5*10^8 \) VAR, \( P_{ref}=8*10^8 \) Watt.

**Series (UPFC):** rated DC voltage=20KV, parameters For current regulator are \( K_p=0.025 \), \( K_i=6 \), \( Q_{ref}=-0.5*10^8 \) VAR, \( P_{ref}=8*10^8 \) Watt.

**Series (SSSC):** parameters for DC voltage regulator are
Kp=0.000095, Ki=0.004, parameters for ac voltage regulator are Kp=3, Ki=100.

**UPFC rating:** Shunt transformer rating = 500kV/20kV, and rated power 100MVA, Series transformer rating = 20kV/500kV, and rated power 100MVA, Vdc = 20 kV, Capacitor rating = 2700 μF.

![Diagram of the proposed system](image1)

**Figure 10.** A Matlab/Simulink block of the proposed system.

![Diagram of Matlab simulation](image2)

**Figure 11.** Matlab/simulation for controlling injected voltage by using SSSC.
Fig. 12 shows the time responses of the grid (voltages, currents and power) which are represented the performance of the proposed system at the three cases study, when applied load before bus 3 at $0 \leq t \leq 0.1$, also under normal condition at $0.1 \leq t \leq 0.3$, $0.4 \leq t \leq 0.7$ and under applied three phase fault at $0.3 \leq t \leq 0.4$ which are the results tabulated at table. It is clear in Fig. 12 (a) at $0 \leq t \leq 0.3$, there are no disturbance or harmonic in the voltages waveform with constant maximum over shoot value but at $0.3 \leq t \leq 0.4$, the voltages are equal zero at fault is occurred while the voltages waveform is returned to normal condition values after fault is clearance at $0.4 \leq t \leq 0.7$ with the maximum over shoot being 1.35 times by normal voltage value.

In Fig. 12 (b) at $0 \leq t \leq 0.1$, the current is reduced compared to rated grid current when the pulse load is connected to the system with the maximum over shoot being 1.34 times by rated current value, at $0.1 \leq t \leq 0.3$, the current waveform is a pure wave with constant maximum over shoot, while at $0.3 \leq t \leq 0.4$, the maximum over shoot is equal 1.732 times by rated rid current fault occurred. In Fig. 12 (c) at normal conditions from $0.1 \leq t \leq 0.3$ and from $0.4 \leq t \leq 0.7$, the active power is equal 100 MW.

Fig. 13 shows the performances of the input shunt controller (voltages and currents). While Fig. 14 shows the performances of the output series controller (voltages and currents) which are the results tabulated at table 1.
Fig. 15 represents the DC voltage of the output shunt controller which at normal conditions from $0.1 \leq t \leq 0.3$ and from $0.4 \leq t \leq 0.7$ is constant while it has a small variation at $0.3 \leq t \leq 0.4$ when fault is occurred.

![Figure 15. The DC voltage of the output shunt controller](image)

Fig.16 shows the voltage and currents for bus 1. In Fig.16 (a) at $0 \leq t \leq 0.3$ there are no disturbance or harmonic in the voltages waveform with almost constant maximum overshoot value but at $0.3 \leq t \leq 0.4$, the voltages are reduced only by 22% when fault is occurred while the voltages waveform is returned to normal condition values after fault is clearance at $0.4 \leq t \leq 0.7$ with the maximum over shoot being 1.1 times by normal voltage value.

In Fig.16 (b) at $0 \leq t \leq 0.1$, the current waveform at bus1 is a pure wave with the maximum over shoot being only 1.25 times by rated current value, at $0.1 \leq t \leq 0.3$, the current waveform is a pure wave with constant maximum over shoot, while at $0.3 \leq t \leq 0.4$, the maximum over shoot is equal 2 times by rated grid current fault occurred. The results are tabulated at table 1.

![Figure 16. Time responses of the bus1 (a) the three phase voltages, (b) the three phase currents](image)

Fig.17 illustrates the performances for bus 2. In Fig.17 (a) at $0 \leq t \leq 0.3$, there are no disturbance or harmonic in the voltages waveform with almost constant maximum over shoot value, but at $0.3 \leq t \leq 0.4$, the voltages are reduced only by 1.12% when fault is occurred while the voltages waveform is returned to normal condition values after fault is clearance at $0.4 \leq t \leq 0.7$ with the maximum over shoot being 1.17 times by normal voltage value which are the results tabulated at table 1.

In Fig.17 (b) at $0 \leq t \leq 0.1$, the current waveform is a pure wave with the maximum over shoot being only 1.4 times by rated current value, at $0.1 \leq t \leq 0.3$, the current waveform is a pure wave with constant maximum over shoot, while at $0.3 \leq t \leq 0.4$, the maximum over shoot is equal 1.49 times by rated grid current fault occurred. While Fig.18 shows an injection voltage for SSS.
6. Conclusion

The goal about this paper is improvement of the performance of system by using unified power flow controller (UPFC), STATCOM and two 3-level 48-puls converter for the power flow in a transmission power system. The proposed control model is analyzed under different the three cases. The first case, the model is applied to plus load at bus 3. The second case, the model is operating at normal work and the third case, the three phase fault is occurred at bus4. From observing the simulation results, it is clear that the suggested console gives a good transient and stable condition especially when the three phase fault is clearance. Also, it is found that the grid current is not significantly affected. Only the maximum value of the current increases when the fault is occurred by 1.35 times from rating value and for a period of time not exceeding 0.02 sec. at normal conditions from 0.1≤t≤0.3 and from 0.4≤t≤0.7 the active power is equal a rated value.

During three phases fault is occurred, the voltage at bus1 is reduced only by 22% from nominal voltage while the maximum over shoot of the current is equal 2 times by rated grid current. After the fault is clearance and at normal conditions the voltage and current performance there are no harmonic.

At bus2 the voltage is reduced only by 1.12% from nominal voltage and during the maximum over shoot of the current is equal 1.49 times by rated grid current during the fault is occurred. After the fault is clearance, and at normal conditions the voltage and current performance there are no harmonic.

Table 1. Results of the maximum over shoot and RMS obtained from proposed model.

| Time interval (sec.) | \(0 \leq t < 0.1\) | \(0.1 \leq t < 0.3\) | \(0.3 \leq t < 0.4\) | \(0.4 \leq t < 0.7\) |
|---------------------|------------------|------------------|------------------|------------------|
| V (KV)              | I (A)            | V (KV)           | I (A)            | V (KV)           |
| Grid                | Max. Over Shoot  | 491              | 2100             | 480              | 2590             | 0                | 14260            | 658              | 2971             |
| R.M.S               |                  | 480              | 1561             | 480              | 2590             | 0                | 8232             | 487              | 2511             |
| Shunt control       | Max. Over Shoot  | 500              | 693              | 500              | 295              | 375              | 520              | 520              | 485              |
| R.M.S               |                  | 500              | 243              | 500              | 277              | 350              | 349              | 500              | 347              |
| Series control      | Max. Over Shoot  | 510              | 317              | 486              | 228              | 385              | 312              | 500              | 299              |
| R.M.S               |                  | 478              | 222              | 468              | 225              | 349              | 231              | 490              | 220              |
| Bus 1               | Max. Over Shoot  | 511              | 3717             | 508              | 2685             | 417              | 7477             | 582              | 2888             |
| R.M.S               |                  | 506              | 2944             | 507              | 2652             | 362              | 3602             | 500              | 2600             |
| Bus 2               | Max. Over Shoot  | 536              | 1666             | 496              | 1134             | 395              | 1611             | 580              | 1653             |
| R.M.S               |                  | 487              | 1185             | 498              | 1130             | 333              | 1079             | 495              | 1130             |
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