Multi-Port and -Functional Power Conditioner and Its Control Strategy with Renewable Energy Access for a Railway Traction System

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Abstract: To relieve the contradiction between supply and demand, a multi-port power conditioner (MP-PC) and control strategy with renewable energy access for a railway traction system is presented, which is mainly composed of full-bridge-based MMC and isolated DC/DC converters. As for the full-bridge-based MMC, the equivalent model is established and its novel voltage control method is proposed, which can provide a medium/low-voltage DC-link. A renewable energy system is connected to the system through the DC-link, so the MP-PC can achieve on-site consumption and balance between the load power and output power of RESs. Meanwhile, with the proposed control strategy, MP-PC can achieve three-phase power balance control and improve the operation performance of the railway traction system. Finally, the traction power platform and simulation model are established in the lab, and the topology and control strategy of MP-PC are verified effectively.

Keywords: multi-port power conditioner; railway traction system; renewable energy system; power quality; feedforward control

1. Introduction

Recently, distributed renewable energy systems (RESs), due to their pollution-free and flexible features, have been extensively applied in the various practical fields, such as dc microgrids and electric vehicles [1–4]. Naturally, wide dissemination of these new power supply modes integrated with RESs has also resulted in re-thinking and reformation in other industries, typically including railway traction power supply systems (TPSSs) [5–7]. Moreover, the contradiction between dramatic development and distressed power supply capacity has exacerbated the dilemma of the current high-speed railway TPSS, which urgently needs new methods to achieve balance between the supply and demand. Hence, in order to ease the tension in TPSSs, the conventional TPSS should be transformed to provide access for RESs, eventually realizing the coexistence of them. As for power quality improvement, some railway power conditioners were proposed [8]. A perspective on power quality issues through railway electrification development and investigation of the system requirements for appropriate power quality is presented, and a railway power conditioner integrated with RES access was proposed to achieve the comprehensive management of power quality and RES access [9]. In [10], a hybrid traction power supply system integrated with photovoltaic sources was proposed. Some scholars have attempted to apply energy storage to railway traction systems, which is used to relieve the power pressure of a railway system [11,12].

A typical configuration of a TPSS that facilitates access to RESs, called a VSC-based medium-voltage dc (MVDC) TPSS, was proposed in [6], comprising numerous ac-dc and dc-dc converters. The VSC-based MVDC TPSS, where the existing single-phase 25-kV ac
catenary gives way to a 24-kV dc catenary, combines the utility grid and RESs via the dc line to feed the mobile loads. Furthermore, the topology of the converters for traction also makes the corresponding adjustments according to the change of the catenary voltage. Compared to the traditional 25-kV ac system, the advantages of the VSC-based MVDC TPSS can be summarized as follows [7]:

(a) Improvement of the power quality;
(b) Catenary network without neutral sections;
(c) Coexistence of distributed RESs and TPSS;
(d) Connectivity among railway systems with heterogeneous RESs.

Although the DC traction power supply system has many advantages, achieving the replacement of the railway power supply system in a short time is not realistic. More precisely, the 25-kV AC traction system has become the standard for high-speed railway TPSSs, and the resulting equipment related to the AC 25-kV is almost rigid. Therefore, the 24-kV dc voltage replacing the original electrification principle will lead to a thorough change for TPSS, for instance, the devices of the traction substation, the topology of converters for traction, and settings of the protection equipment. Consequently, it is significant and urgent to propose a new access configuration, embedded into the current TPSS and facilitating the connectivity of heterogeneous RESs without dumping the ac catenary. Therefore, it can alleviate the contradiction of the supply and demand of the energy and manage the inherent power quality problems in high-speed railway TPSSs, including negative-sequence currents and reactive power [13–15].

As for the interface topology of RESs and the layout for multi-energy systems, a great deal of investigation has been devoted to achieving the interconnection of multiple voltage-class distribution networks and coexistence of different subsystems [16–21]. Later, a new energy delivery and management system based on power electronic transformers (PETs), also called solid state transformers (SSTs), has gradually formed [22–26]. Note that PETs can obtain excellent performance, such as galvanic isolation, reduced space requirement, and full-range control via medium- to high-frequency transformers and power converters. Furthermore, with the development of PETs, a number of topologies have appeared, like three-stage PETs composed of a front end, isolation end, and back end [22,26]. Considering the limitations of traditional PETs, the topology should be connected in series or parallel to promote high-voltage and large-capacity applications of PETs [26]. In [27,28], a cascaded multilevel converter, enlarging the range of the input voltage, was adopted in the front-end stage with several dc/dc converters as the isolation stage. Similarly, a cascaded diode-clamped multilevel converter has been applied for the front-end stage and isolation dc/dc stage [29]. Later, with the proposal of MMC, this topology has been used in PETs to realize modular expansion and high efficiency [30,31]. Besides theoretical research, various meritorious projects based on a multi-level topology have proceeded, such as FREEDM, UNIFLEX, and HEART, which are dedicated to coordinating and optimizing the delivery and management of RESs [17,32–34].

Based on the analysis above, a multi-port and -functional power conditioner, applying the PET with a full-bridge MMC topology, for the interconnection of RESs and current AC TPSS is proposed. This conditioner, where the ac ports (similar to the high-voltage ac ports of PET) are connected to the AC traction power arms, and the dc ports (similar to the low-voltage dc links of PET) are connected to RESs, can directly accelerate the energy exchange between RESs and TPSS without abandoning the original power supply mode. In addition, the full-bridge MMC can arbitrarily adjust the voltage on the DC side to facilitate the access of new energy sources. On the one hand, the proposed design can realize the balance between the supply and demand in TPSS by means of an interface of RESs while, on the other hand, the conditioner can regulate the power quality of the railway power system according to the corresponding methods [35,36].

This paper is organized as follows. In Section 2, the basic topology as well as the energy control strategy of the conditioner is briefly introduced. In Section 3, the control method of the isolated dc-dc converter is analyzed in detail. Then, the control method of
the full-bridge based MMC for the high-voltage stage is described in Section 4. In Section 5, the experimental process is designed, then the experiment is carried out and finally the experimental conclusion is drawn, which verifies the effectiveness of the topology and its control strategy proposed in this article. The work done in this article and its significance is summarized in Section 6.

2. Topology and Power Management Strategy of MPPC

In [9], a multi-port railway power conditioner (MP-RPC) with renewable energy access was proposed to realize on-site power consumption and negative-sequence compensation. In this paper, the topology of the multi-port power conditioner (MP-PC) is proposed for better performance, such as a fault isolation capability due to the full bridge MMC. The topology is shown in Figure 1a. The topology consists mainly of a full-bridge-based MMC and an intermediate-stage dc/dc converter. The sub-module of MMC adopts the full-bridge module, and the output port of MMC can generate a lower/medium-voltage DC, which can reduce the input-series number of DC-DC converters. As is known to us, the half-bridge-based MMC cannot output a negative level; therefore, under the same AC-side voltage level, sub-module voltage level, and the same modulation degree, the half-bridge-based MMC cannot output the medium/lower-voltage DC like the full-bridge-based MMC. In addition, the full-bridge-based MMC can also realize the self-blocking function in the DC short-circuit fault. It only needs to block all the switches of the full-bridge sub-module, and then gain re-access after the fault is cleared [37,38]. So, the full-bridge-based MMC can effectively ride through the fault and improve the reliability of MP-PC. The isolated-stage DC-DC converter mainly converts the output medium-voltage DC of MMC into low-voltage DC, and at the same time realizes electrical isolation. The input sides of the DC-DC converters are in series; meanwhile, the other sides of the DAB converters are in parallel, which can form low-voltage DC-Bus for the access of RESs and energy storage systems (ESSs) in the latter stage.

In theory, the DC-link voltage of the full-bridge-based MMC can be stably operated at any set value [0, +∞], so the DC-side voltage of MMC can be controlled at a suitable value to form a common DC-link, which can be directly connected to the PV and ESS. It will not only reduce the intermediate conversion, greatly reducing the cost and volume, but also enhance the operating efficiency of the system, as shown in Figure 1b. However, the disadvantage of Figure 1b is that there is no intermediate isolation. Additionally, if the DC-side voltage is set too low, the DC current will be large at the same power level, which will increase the current stress of the power switches of MMC. In summary, compared with the traditional compensation systems, these proposed MP-PCs have many advantages, such as:

MP-PC can achieve on-site access for RESs along the railway line effectively, and the on-site consumption of RESs’ power, which can mitigate the conflict between the supply of and demand for energy. Meanwhile, through the proper power-conditioning strategy, MP-PC can achieve the active-power transferring and reactive-power compensation of two traction power arms for the three-phase power balance.

Assuming that \( P_{\text{in}} \) is the total input power of RESs, \( P_{\text{La}} \) and \( P_{\text{Lb}} \) represent the load power of two traction power arms, as shown in Figure 1a. In order to realize the power balance of the three-phase traction grid, the power management strategy of MP-PC is as follows:

When \( P_{\text{in}} < P_{\text{La}} + P_{\text{Lb}} \), it means that the total power generated by RESs is less than the total power of the locomotive loads. At this time, it is necessary to absorb part of the power from the three-phase traction grid to power the loads and realize the balance of the active power. Similarly, when \( P_{\text{in}} > P_{\text{La}} + P_{\text{Lb}} \), it means that the total power generated by RESs is greater than the total power of the locomotive loads. At this time, the system can not only supply the locomotive loads, but also deliver part of the active power into the three-phase traction grid. So, generally, the max input power \( P_{\text{in}} \) should be lower than the capacity of the traction transformer.
Figure 1. System structure. (a) Structure of traction system. (b) Topology of MP-PC. (c) Topology of MP-PC without the intermediate stage.
For the purpose of realizing the power balance and power quality control [39] on the three-phase side of the railway traction grid, the expected output power of the two traction arms can be derived as:

\[ P_{sa} = P_{sb} = \frac{P_{La} + P_{Lb} - P_{in}}{2} \]  

(1)

where \( P_{sa} \) and \( P_{sb} \) are positive values, indicating that the traction grid will release active power for the loads; meanwhile, \( P_{sa} \) and \( P_{sb} \) are negative values, indicating that the traction grid will absorb active power. Then, the reference values of the phase-a and -b output power (\( P_{ca}, P_{cb} \)) of MP-PC can be obtained:

\[
\begin{align*}
P_{ca}^{\text{ref}} &= P_{sa} - P_{La} = \frac{R_{sa} + R_{Lb} - R_{in}}{2} - P_{La} \\
P_{cb}^{\text{ref}} &= P_{sb} - P_{Lb} = \frac{R_{sa} + R_{Lb} - R_{in}}{2} - P_{Lb}
\end{align*}
\]  

(2)

From the equations above, there is:

\[ P_{ca}^{\text{ref}} + P_{cb}^{\text{ref}} + P_{in} = 0 \]  

(3)

3. Topology and Power Management Strategy of MPPC

From the structure above, it can be seen that the full-bridge-based MMC is the core component of MP-PC, responsible for connecting the two-phase traction power arms and adjunct system, managing the power-flow distribution of the system. Here, the equivalent model of the full-bridge-based MMC is established, and a novel voltage balance control method is proposed for MMC, which can provide a stable medium/low-voltage DC-link.

3.1. Equivalent Model of the Full-Bridge-Based MMC Rectifier

Taking the A-phase of MMC as an example, the equivalent circuit of the full-bridge-based MMC is constructed as shown in Figure 2, where \( u_{sa} \) and \( i_{ca} \) are the A-phase traction voltage and current, respectively; \( L \) is the bridge arm inductor; and \( L_s \) is the filter inductor. \( u_{kp} \) and \( u_{kn} \) \((k = t, u)\) represent the upper and lower bridge arm voltages of the \( t \) and \( u \) phases of MMC; \( i_{kp} \) and \( i_{kn} \) represent the upper and lower bridge arm currents of the two phases of MMC; and \( u_{dc} \) is the output DC voltage of MMC. The full-bridge cell-based bridge arm of MMC can be equivalent to one voltage source. Therefore, the neutral point \( N \) of the AC grid can be considered as the reference point, and according to Kirchhoff’s law and Figure 2b, the equations for the upper arm and the lower arm can be respectively obtained as:

\[
\begin{align*}
\frac{u_{tp}}{2} - L_s \frac{di_{tp}}{dt} + L \frac{d^2i_{tp}}{dt^2} + u_{tp} &= \frac{u_{kp}}{2} - u_{ntO} \\
\frac{u_{tn}}{2} - L_s \frac{di_{tn}}{dt} - L \frac{d^2i_{tn}}{dt^2} - u_{tn} &= -\frac{u_{kn}}{2} - u_{ntO} \\
\frac{u_{up}}{2} - L_s \frac{di_{up}}{dt} - L \frac{d^2i_{up}}{dt^2} - u_{up} &= \frac{u_{kp}}{2} - u_{ntO} \\
\frac{u_{un}}{2} - L_s \frac{di_{un}}{dt} + L \frac{d^2i_{un}}{dt^2} + u_{un} &= -\frac{u_{kn}}{2} - u_{ntO}
\end{align*}
\]  

(4)
In order to maintain the balance between the DC voltage, the neutral point N or the midpoint O of MMC’s dc-link is grounded [40], and if the system is in the symmetric operation: $U_{i0} \approx 0$. According to the ac and dc equivalent circuits of the system shown as Figure 2c,d, the voltage and current can be obtained as:

$$
\begin{align*}
    u_{tp} &= u_{tp,z} + u_{tp,f} \\
    u_{tn} &= u_{tn,z} + u_{tn,f} \\
    u_{up} &= u_{up,z} + u_{up,f} \\
    u_{un} &= u_{un,z} + u_{un,f}
\end{align*}
$$

where $u_{X,z}, u_{X,f}$ are the dc and ac components of $u_X$, respectively; $i_{X,f}$ is the ac components of $i_X$; $i_{zt}, i_{zu}$ are the dc circulating currents of the $t, u$ two-phase bridge arms; and $i_{dcl}$ is the current flowing through dc-link. Ignoring the voltage on the bridge arm inductor and the filter inductor, in the steady state, the total voltage and current of the upper and lower bridge arms of the $t, u$ phases can be obtained as:

$$
\begin{align*}
    u_{tp} &= u_{un} = \frac{U_{dc,ref}}{2} + \frac{U_m}{2} \\
    u_{tn} &= u_{ap} = \frac{U_{dc,ref}}{2} + \frac{U_m}{2} \\
    i_{tp,z} &= \frac{i_{ap}}{2} + \frac{i_{tn}}{2} \\
    i_{tp,f} &= \frac{i_{un}}{2} + \frac{i_{tp}}{2} \\
    i_{zt} &= i_{tp} + i_{tn} = \frac{i_{tp} + i_{tn}}{2}
\end{align*}
$$

where $U_{dc,ref}$ is the reference value of $u_{dc}$, and the AC modulation degree $M$ of each cell can be calculated as:

$$
M = \frac{U_m/2}{N \times U_{SM,ref} - U_{dc,ref}/2},
$$

Figure 2. The A-phase equivalent circuit of the full-bridge-based MMC.
Among them, \( U_m \) is the peak value of the grid voltage; \( U_{SM,\text{ref}} \) is the voltage reference of the sub-module capacitor; and \( N \) is the number of bridge arm sub-modules. According to (6), there is:

\[
N = N_{dc} + N_{ac} = \frac{U_{dc,\text{ref}}/2}{U_{SM,\text{ref}}} + \frac{U_m/2}{\mu U_{SM,\text{ref}}},
\]

(8)

Among them, \( N_{dc} \) is used to generate the output DC component of the medium voltage for MMC, and \( N_{ac} \) is used to generate the AC component of the medium voltage, realizing power exchange with the grid voltage. The reference voltage of each bridge arm of MMC contains the DC component \( U_{dc,\text{ref}}/2 \). Therefore, the output voltages of the upper/lower arms are added together to obtain the medium-voltage DC output \( U_{dc,\text{ref}} \).

In the paper, the AC port of MMC is a 27.5 kV AC grid. It is assumed that the capacitor voltage of each sub-module is 750 V, and the MMC is used to output 3 kV medium-voltage DC Bus, i.e., \( U_{dc,\text{ref}} = 3 \text{ kV} \). According to (8), \( N_{dc} = 2 \) is selected. If the AC modulation degree is \( M = 0.9 \), then \( N_{ac} = 29 \) is selected according to (8). So, 31 full-bridge cells are used for each upper and lower bridge arm of the MMC.

Assuming the grid voltage and current of A phase traction arm are:

\[
\begin{align*}
\phi &= \frac{u_{sa}}{U} = \frac{U_m \sin \omega t}{U} = \frac{I_m \sin(\omega t - \varphi)}{I}
\end{align*}
\]

(9)

Assuming that the two-phase bridge arms are symmetrical, and the parameters are the same, the current will be evenly distributed in each bridge arm in the steady state. The currents of the t-phase upper/lower bridge arm can be expressed as:

\[
\begin{align*}
i_{tp} &= I_{dc,H}/2 - I_m \sin(\omega t - \varphi)/2 \\
i_{tn} &= I_{dc,H}/2 + I_m \sin(\omega t - \varphi)/2
\end{align*}
\]

(10)

Then, the output power of the AC grid is:

\[
P_{ac} = u_{sa}i_{ca} = \frac{U_m I_m \cos \varphi}{2} - \frac{U_m I_m \cos(2\omega t - \varphi)}{2},
\]

(11)

The power flowing through the MMC DC port can be expressed as:

\[
P_{dc} = u_{dc}i_{dc} = u_{dc} \times C \times \frac{du_{dc}}{dt},
\]

(12)

where \( C \) is the DC-link equivalent capacitance. According to the power balance, if the loss of the MMC is neglected, the steady-state dc power can be expressed as: \( U_{dc}I_{dc,H} = U_m I_m \cos \varphi/2 \). Meanwhile, secondary fluctuation power is also contained in the DC-link, which will cause a secondary frequency ripple of the DC-link voltage.

Taking the t-phase upper arm as an example, according to (6) and (10), the instantaneous value of the bridge arm power of the t-phase can be obtained as:

\[
P_{tp} = (\frac{U_d}{2} - \frac{U_m \sin \omega t}{2}) \times (\frac{I_{dc,H}}{2} - \frac{I_m \sin(\omega t - \varphi)}{2})
\]

(13)

\[
= \frac{U_{dc}I_{dc,H}}{4} + U_m I_m \cos \varphi - \frac{U_{dc}I_{dc,H} \sin(\omega t - \varphi)}{4} - \frac{I_{dc,H}U_m \sin \omega t}{4} - \frac{U_m I_m \cos(2\omega t - \varphi)}{8},
\]

The DC power components can cancel each other out, so the instantaneous value of the bridge arm power can be expressed as:

\[
P_{tp} = -\frac{U_{dc}I_{dc,H} \cos(\omega t - \varphi)}{4} - \frac{I_{dc,H}U_m \cos \omega t}{4} - \frac{U_m I_m \cos(2\omega t - \varphi)}{8}
\]

(14)

From the formula above, \( P_{tp} \) has no DC component, only the AC component, and the energy fluctuation of the t-phase upper arm in one fundamental cycle is zero, indicating the energy and capacitance voltages of the t-phase upper arm are controllable. It is also
conceivable that the energy and capacitance voltages of the other bridge arms are controllable, so that the entire MMC converter is controllable. At the same time, it can be seen that the DC-side capacitor voltage of each H-bridge cell not only has a fundamental frequency ripple, but also has a secondary frequency ripple.

3.2. Control Method of the Full-Bridge-Based MMC Rectifier

The overall control block diagram of MP-PC is shown in Figure 3. Among them, the current command of the traction power supply arm is generated by the control part shown in Figure 3a. The control part shown in Figure 3b is responsible for sub-module voltage balance. The control part shown in Figure 3c is responsible for generating the overall DC voltage command value, and the control part shown in Figure 3d is responsible for generating the PWM command signal.

![System control block diagram of MMC-PWM rectifiers](image)

**Figure 3.** System control block diagram of MMC-PWM rectifiers.

3.2.1. Reference Current Calculation

According to the power management strategy of MP-RPC, the reference current signals of the a, b-phase of MMC under the v/v traction system [9] can be obtained as follows:

\[
\begin{align*}
    i_{ca}^r &= P_{ca}^{\text{ref}} \times \frac{\sin(\omega t - \pi/6)}{2U_m} + (P_{La} + P_{Lb} - P_m) \times \frac{\sin(\omega t + \pi/3)}{4U_m} \\
    i_{cb}^r &= P_{cb}^{\text{ref}} \times \frac{\sin(\omega t - \pi/2)}{2U_m} + (P_{La} + P_{Lb} - P_m) \times \frac{\sin(\omega t - \pi)}{4U_m}
\end{align*}
\]

(15)

where \(i_{ca}^r\) and \(i_{cb}^r\) are the reference currents of phase a and b respectively. Based on these current references, the output active current adjustment signals of the total voltage outer-loop control are obtained (\(\Delta I_d\) is multiplied by the synchronization signals of two traction power arms respectively to obtain the active current adjustment signals), and finally the two-phase output current references of MMC \(i_{ca}, i_{cb}\) can be gained as:

\[
\begin{align*}
    i_{ca}^r &= i_{ca}^s + \Delta I_d \times \sin(\omega t - \pi/6) \\
    i_{cb}^r &= i_{cb}^s + \Delta I_d \times \sin(\omega t - \pi/2)
\end{align*}
\]

(16)
3.2.2. Bridge Arm Capacitor Voltage Balance Control

From the previous analysis, the bridge arm capacitor voltage balance control mainly includes three levels. One is the total voltage balance control of the bridge arm, which is used to absorb AC-side power and balance the power loss of MMC. The second level is the capacitor voltage balance control between the different bridge arms, that is, the energy balance control of each bridge arm of MMC in the horizontal direction. The third level is the capacitor voltage balance control inside the bridge arm, that is, the voltage balance control of the individual sub-module in each arm in the vertical direction, and the control diagram is shown in Figure 3b. First, detecting the capacitor voltage $u_{C_{ki}}$ of the H-bridge Cell of each bridge arm [k indicates the kth bridge arm ($k = t, u, v, w$), i indicates the ith sub-module ($i = 1 \sim 2N$)], the capacitor voltage average value of each bridge arm and the total voltage average value can be obtained as follows:

$$
\begin{align*}
    u_{kav} &= \frac{1}{2N} \sum_{1 \sim 2N} u_{C_{ki}} \\
    u_{av,T} &= \frac{1}{2} \sum_{k=t,u} u_{kav}
\end{align*}
$$

The total voltage balance control: the module voltage reference $U_{SM\_ref}$ is used to subtract from the total voltage average value $u_{av,T}$, and the active adjustment signal $\Delta I_d$ for adjusting the total capacitor voltage balance can be obtained by PI control. The output is sent to the reference signal calculation link to obtain two-phase active current adjustment signals.

Horizontal voltage balance control: as shown in the Figure 3b, the error between the capacitor voltage average value of the k-phase bridge arm and $u_{av,T}$ is processed by a regulator K1, which can obtain the circulating current reference of the k-phase bridge arm. The inner-loop current control also adopts a regulator K2 to realize the closed-loop control of the DC signal. At the same time, a second resonant controller R2 is adopted to suppress the second harmonic circulating current, and finally to obtain the circulating voltage control component $u_{zk}$ of the k-phase bridge arm, which is used to achieve the power balance between the bridge arms.

Vertical voltage balance control: the error between the capacitor voltage of each cell and the average voltage value of each bridge arm passes through a regulator K3, and then correction of the symbol function $\text{sign}(i_{kp}, i_{kn})$ is used to obtain the voltage balance control component $u_{zki}$ of each sub-module, which is used to control the voltage balance of the individual sub-modules inside the bridge arm.

3.2.3. DC-Link Voltage Closed-Loop Control

Since the active power of the latter-stage RESs is injected into the medium/low-voltage DC-link of the pre-stage MMC by the converters, closed-loop control of the medium/low-voltage dc-link is required to maintain a stable voltage. Here, the voltage of the medium-voltage DC-bus is controlled the MMC as shown in Figure 3b. The error of the DC-link voltage reference $U_{dc\_ref}$ and the detected value $u_{dc}$ is handled with a PI regulator, and then the output is superimposed with half of $U_{dc\_ref}$ (as the DC voltage feedforward signal), and the DC offset adjustment signal $U_{dc\_bias}$ of the upper and lower bridge arms of each-phase cluster can be obtained. This signal is superimposed on the AC modulation signal of the H-bridge cell of each bridge arm. Through the PWM modulation link, each H-bridge cell can output a stable and equal DC offset, achieving the closed-loop control of the DC-link voltage.

3.2.4. Current Closed-Loop Control and Modulation

In order to make the output AC currents $i_{ca}$ and $i_{cb}$ of the a and b phases of MMC accurately track the current references, quasi-PR control is used here as follows:

$$
\begin{align*}
    e_a &= G_{PR} \ast (i_{ca}^r - i_{ca}) \\
    e_b &= G_{PR} \ast (i_{cb}^r - i_{cb})
\end{align*}
$$
where GPR is the transfer function of the PR controller. According to the above analysis, the output \( v_{dr} \), \( U_{dc\_bias} \), kth-phase \( (k = l, u, v, w) \) voltage balance control command \( u_{dc} \) and the \( i_{th} \) submodule voltage balance control command \( u_{dc}\_ref \) are added together, and then divided by NL to achieve normalization. Finally, the modulation signal of each H-bridge cell of each bridge arm can be obtained and sent to the CPS-PWM link, and then the corresponding AC and DC signals can be output. Totally, the sub-module modulation waves of the a-phase of MMC can be obtained as (19). Similarly, the modulation wave of each sub-module of the b-phase can be obtained as (20):

\[
\begin{align*}
\frac{u_{tpi}}{U_{dc\_bias}} &= \frac{1}{N_{SM\_ref}} (U_{dc\_bias} - \frac{e_p}{2} - \frac{e_q}{2} + u_{zt} + u_{zl}) \quad (i = 1 \sim N/2) \\
\frac{u_{uni}}{U_{dc\_bias}} &= \frac{1}{N_{SM\_ref}} (U_{dc\_bias} + \frac{e_p}{2} + \frac{e_q}{2} + u_{zt} + u_{zl}) \quad (i = N/2 + 1 \sim N) \\
\frac{u_{upi}}{U_{dc\_bias}} &= \frac{1}{N_{SM\_ref}} (U_{dc\_bias} + \frac{e_p}{2} + \frac{e_q}{2} + u_{zt} + u_{zl}) \quad (i = 1 \sim N/2) \\
\frac{u_{wpi}}{U_{dc\_bias}} &= \frac{1}{N_{SM\_ref}} (U_{dc\_bias} - \frac{e_p}{2} - \frac{e_q}{2} + u_{zt} + u_{zl}) \quad (i = N/2 + 1 \sim N) \\
\frac{u_{vni}}{U_{dc\_bias}} &= \frac{1}{N_{SM\_ref}} (U_{dc\_bias} + \frac{e_p}{2} + \frac{e_q}{2} + u_{zt} + u_{zl}) \quad (i = N/2 + 1 \sim N) \\
\frac{u_{wni}}{U_{dc\_bias}} &= \frac{1}{N_{SM\_ref}} (U_{dc\_bias} - \frac{e_p}{2} - \frac{e_q}{2} + u_{zt} + u_{zl}) \quad (i = N/2 + 1 \sim N)
\end{align*}
\]

(19)

4. Operation Principle and Control of DC/DC Converters

From the structure above, it can be seen that the intermediate isolated DC/DC converter is another key component of MP-PC, responsible for connecting the pre- and post-stage subsystems. According to [41], the equivalent model of the dual active bridge (DAB) converter is shown in Figure 4.

![Figure 4](image)

**Figure 4.** The equivalent circuit model of a two-port converter.

The power is delivered between two ports:

\[
P_{12} = \frac{\phi (\pi - \phi)}{A} V_1 V_2
\]

(21)

where \( A = 2\pi^2 f_o (L_1 + L_2) \); \( I_1 \) and \( I_2 \) are the equivalent DC current of two ports; \( V_1 \) and \( V_2 \) are the equivalent DC voltage; \( L_1 \) and \( L_2 \) are leakage inductors; \( P_{12} \) is the active power transferred between two ports; and \( \phi \) is the phase-shift angle. According to the equation above, assuming that the delivered power is fixed, there is a unique solution for the unknown variable \( \phi \). Since the phase-shift range of \( \phi \) is between \( (-\pi/2, \pi/2) \), according to the solution principle of the quadratic equation, it can be found using:

\[
\phi = \left\{ \pi - \sqrt{\pi^2 - \frac{4AP_{12}}{V_1V_2}} \right\}/2
\]

(22)

According to (22), \( P_{12} < V_1 V_2 / 8f_o (L_1 + L_2) \) must be met. That is to say, the maximum power transferred between two ports should be less than \( V_1 V_2 / 8f_o (L_1 + L_2) \). In this way, if the rated transmission power has been set, the phase-shift angle \( \phi \) of the output AC voltage of the port can be calculated, thus a rapid power response can be achieved.

In order to establish the small-signal model of the DC/DC converter, it is assumed that the system is stable at the operating point D, where \( \Delta I_1 \) denotes the current deviation and \( \Delta \phi \) denotes the deviation of the phase-shift angle. According to the power transmission
model, the expressions of the steady component and the deviation component can be derived respectively. If the quadratic terms of the deviation are ignored:

\[(I_{1D} + \Delta I_1) \star V_1 = \frac{\phi_D (\pi - \phi_D) V_1 V_2}{A} + \Delta \phi (\pi - 2\phi_D) V_1 V_2 \]  

(23)

The following equation can be obtained:

\[
\begin{align*}
I_{1D} &= \frac{\phi_D (\pi - \phi_D) V_2}{A} \\
\Delta I_1 &= \frac{(\pi - 2\phi_D) V_2}{A} \Delta \phi = H \Delta \phi \\
H &= \frac{(\pi - 2\phi_D) V_2}{A}
\end{align*}
\]

(24)

From the equation above, the small-signal transfer function \(H\) of the DC/DC converter can be obtained. On this basis, the control block diagram of the DC/DC converter can be constructed as shown in Figure 5. The voltage and current dual-loop control is adopted. The voltage outer-loop is used to control and maintain the low-voltage DC-bus, where the intermediate DC-bus of the DC/DC converters is controlled by the MMC-based PWM rectifier. The current inner-loop is adopted to realize closed-loop control of the current and power. In order to improve the dynamic performance of the system and the adaptability of the control parameters, the control system adopts the feed-forward link \(1/H\) on the basis of the small-signal model as shown in Figure 5.

\[\text{Figure 5. Control system block diagram of the DC/DC converter i.}\]

At the same time, in order to achieve the current and power sharing for the N-parallel system, a virtual impedance \(R_{av}\)-based current-sharing control is adopted here. The output current of each unit is multiplied by the virtual impedance to obtain a correction value \(\Delta V_{ref}\) of the voltage reference signal, which can achieve a fine adjustment of the output current and power of each unit. Meanwhile, according to the rated delivered power \(P_{ref}\), the reference value \(\phi_{ref}\) of the phase-shift angle can be calculated as follows:

\[
\left\{ \begin{array}{ll}
\phi_{ref} = \frac{\pi}{2} & \left( 1 - \sqrt{1 - \frac{4A_{ref}}{\pi V_1^2}} \right) \\
P_{ref} = \frac{P_{ref}}{V_1^2 / M}
\end{array} \right.
\]

(25)

where \(M\) represents the number of DC/DC modules. According to \(\phi_{ref}\) and the output signal \(\Delta \phi_i\) of the current closed-loop controller, the phase-shift angle \(\phi_i\) of the DC/DC converter \(i\) can be obtained. Since both \(\phi_{ref}\) and the feed-forward link \(1/H\) are determined by the delivered power in different operating stages, the control parameters will vary with the delivered power, which can enhance the dynamic performance of the system and improve the adaptability of the control parameters.

5. Experiments

To verify the feasibility of the proposed topology and method of MP-PC in the paper, the 1 kV-level \(v/v\) TPSS and MP-RPC are established via hardware-in-the-loop (CHIL) experiments. As shown in Figure 6, the main circuit is simulated by the RT-LAB real-time system, and the control system is composed of a digital signal processor (DSP, TMS320F2812). The H-bridge-based MMC rectifier adopts 2 cells for each upper/lower bridge leg. The diode rectifier source is adopted to simulate the photovoltaic (PV) generation system, and the
power is injected into the lower-voltage dc-bus through a boost converter. In the middle stage, a dc/dc isolated converter is used, where the ratio of the isolation transformer is 1:1. The dc-link voltage of a single DC/DC converter is set as 200 V at each port, and the switching frequency is set as 10 kHz. The detailed circuit and control parameters are shown in Table 1.

![Diagram of the system](image)

**Figure 6.** Physical implementation of the system.

**Table 1.** Circuit parameters for the system.

| Symbol | Parameter Description | Value |
|--------|-----------------------|-------|
| S      | Locomotive capacity   | 40/kW |
| U_rms  | Traction grid voltage | 1000/V |
| f_o    | Grid frequency        | 50/Hz |
| N      | Module number of per upper/lower arm | 2 |
| L     | arm inductance        | 2/mH |
| L_s   | filter inductance     | 2/mF |
| C_cell| Submodule capacitance | 5/mF |
| f_c   | Carrier frequency for MMC | 2/kHz |
| U_SM_ref| Capacitor voltage of each cell | 500/V |
| U_dc_ref| Dc-link voltage of MMC | 400 V |
| C     | Dc-link Capacitance of MMC | 10/mF |

**DC/DC converters**

| Symbol | Transformation Ratio of DAB | Value |
|--------|-------------------------------|-------|
| n1:n2  | 1:1                           |       |

| Symbol | Description                  | Value |
|--------|------------------------------|-------|
| M      | Module number of DAB        | 2     |
| L_r    | Leaking inductance          | 0.03/mH |
| V_1,V_2| Port voltage for DAB converters | 200 V |
| f_s    | Carrier frequency for dc/dc converters | 10/kHz |
| C_p    | Port capacitance for dc/dc converters | 5/mF |

5.1. The Experimental Procedures

(1) To clarify the purpose of the experiment, the proposed MP-PC aims to balance the power of the railway traction system while absorbing new energy on the spot.
(2) Corresponding models are built in RT-LAB based on the MP-PC structure in this article, including dual active bridge modules and full-bridge MMC modules.

(3) The control system is designed based on the model, including the MMC sub-module control part, railway power control part, new energy management part, and design, and the control parameters are debugged.

(4) According to the operation situations of the railway traction system, disturbances in the load and new energy modules are set up.

(5) The operation performance of MP-PC is analyzed according to the experimental waveforms and data, and experimental conclusions are drawn.

In order to verify the topology and control method proposed in this paper, the experiments were performed. First, a 40 kW load is installed on the phase-b traction arm. Before the compensation, the three-phase grid currents are asymmetric as shown in Figure 7a, and there is a large amount of negative sequence current. At the time T1, the conditioning function of MP-PC is enabled. According to the power management strategy of the traction system, some active power and reactive power are transferred and compensated for rapidly by the MMC rectifier. The dc currents $i_{dca}$ and $i_{dcb}$ flowing through dc-link of MMC are almost opposite, and there is approximately 20 kW of active power transferred from the b-phase traction arm to the other arm. After the compensation, the negative sequence current is greatly reduced, and the two-phase traction currents and three-phase grid currents are basically symmetrical. It can be seen that the dc-link voltage can be controlled at 400 V as shown in Figure 7i, and there is almost no ripple, which is suppressed by the 2nd resonant controller R2. Meanwhile, the submodule capacitor voltages of MMC are maintained at 500 V as shown in Figure 7h, but there are some 1st and 2nd harmonic frequency ripples. The upper/lower arm currents and arm circuiting currents of MMC can be controlled well as shown in Figure 7e,f.

![Figure 7. Cont.](image-url)
Figure 7. Cont.
Figure 7. Cont.
Figure 7. Cont.
At the time T2, the input power of the simulated PV system jumps from 0 to 15 kW. At this point, the active power injected by RESs is almost absorbed by the intermediate DC/DC converters, delivered to the DC-link of the full-bridge-based MMC, and then the MMC will absorb the power and transfer it to the respective traction arms. The voltage and current waveforms of the two ports of the DC/DC converters are shown in Figure 7i–k. According to the power cooperative strategy of the system, the active power released by the a-phase and b-phase MMC rectifiers are 26.83 and −12.03 kW, respectively, so basically it can track the power reference of each port. After the power conditioning, the two-phase traction currents and three-phase grid currents are reduced, and the three-phase grid currents are basically symmetrical as shown in Figure 7a,b. Therefore, the injected active power of RESs is effectively absorbed by the MP-PC system, which can reduce the supplied power of the traction network and achieve on-site consumption and balance between the load and RESs. The DC-link voltage of MMC is basically stable at about 400 V.

5.2. Case 1: Power Change of Simulated Loads

In order to verify the dynamic control ability of MP-PC for power quality, a 40 kW load is switched into the a-phase traction arm at T3. The experimental waveforms of the system are shown in Figure 7. After T3, according to the power management strategy of the system, the output powers of the MMC are quickly adjusted. The two-phase output powers released by MMC are 7.28 and 7.21 kW, respectively (the references are +7.5 and +7.5 kW). Nearly, it can track the power references of each port, while there are some power losses caused by converters. As seen in Figure 7a,b, the two-phase traction currents and three-phase grid currents greatly increase, indicating that the system needs to absorb more active power from the traction grid to supply the loads. After power conditioning, the two-phase traction currents and three-phase grid currents are symmetrical. In this case, the voltage and currents of the two ports of the intermediate DC/DC converters almost remain the same, as shown in Figure 7j,k. The DC-link voltage and submodule capacitor voltage of MMC can be maintained to be stable.

5.3. Case 2: The Power Change of PV System

In order to verify the power dynamic adjustment ability of MP-PC, the input power of the simulated PV system is increased from 15 kW to 30 kW at T4. The experimental
waveforms of the system are shown in Figure 7. After T4, the input power of the simulated PV system is increased, and then the input and output currents of the intermediate DC/DC converters jump. The output power of the DC/DC converters is 28.58 kW, so the output power of the PV system is basically absorbed. The output voltage and current waveforms are shown in Figure 7j,k. At this time, according to the power management strategy of the system, the two-phase output power of MMC is quickly adjusted, and the two-phase released power of MMC is 14.22 and 14.44 kW, respectively. After the power conditioning, the three-phase grid currents are reduced; that is, the injected active power of RESs is effectively absorbed by the MP-PC system, which can reduce the supplied power of the traction network and achieve on-site power consumption and balance as shown in Figure 7a,b. The currents and voltages of MP-PC can be controlled well, so the proposed topology and control method are effective, which can achieve stable operation of the system.

5.4. Experimental Result

From the above two experiments, it can be seen that the MP-PC proposed in this paper can not only effectively transfer the unbalanced power, but also control and balance the fluctuations caused by railway loads. In addition, the output power of the new energy system can be absorbed effectively and directly supplied to the railway loads, improving the utilization rate of the new energy. Furthermore, it can also reduce the pressure put on the power grid by the railway loads.

6. Conclusions

In this paper, a multi-port and -functional power conditioner with renewable energy access for a railway power system was proposed to achieve the goal of on-site consumption and balance between the load power and output power of RESs. According to the traction characteristics of the railway system, the power control strategy for MP-PC was proposed. A novel voltage control strategy of a full-bridge-based MMC rectifier was proposed, which can generate a proper medium/low-voltage DC-link for easy access of RESs. Experiments were carried out using RT-Lab platform. Through load fluctuations and photovoltaic output fluctuations, the dynamic control performance of MP-PC was verified through experiments. Finally, from the experimental results, MP-PC can achieve on-site consumption of new energy and three-phase power balance control, obtaining complementarity and benefits between the railway traction system and RESs.

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