A Unidirectional Insulated AC-DC Converter Based on the Hexverter and Multipulse-Rectifier

Gustavo Lambert, Student Member, IEEE, Alessandro Costabeber, Member, IEEE, Pat Wheeler, Senior Member, IEEE, and Yales R. De Novaes, Member, IEEE

Abstract—This paper proposes an insulated two-stage ac-dc converter to be applied in unidirectional power flow systems. The two-stage structure is the combination of a Hexverter and a series-type 12 pulse rectifier. The key features of the proposed structure are inherited from the modular multilevel converter concept such as modularity, scalability, and expandability. Besides the topology, this article presents a complete control strategy based on the output (dc) voltage regulation and Hexverter’s stored energy. In particular, an unusual method to regulate the Hexverter’s branch energy unbalances is proposed. All currents in the Hexverter are controlled which increases the robustness for operation in harsh environments such as subsea systems. Mathematical analysis of the control loops is carried out to develop the necessary control-oriented models for each control loop. The converter and the control strategy are verified through experiments in a 24 cells Hexverter and a current-fed series-type 12-pulse rectifier.

Index Terms—Hexverter, Multipulse Rectifier, ac-dc, Medium Frequency, Current Source.

I. INTRODUCTION

SEVERAL offshore facilities are usually dependent on gas turbines, which operate continuously in a harsh environment to supply power and gas compression for the subsea processing systems. In the harsh environment where these machines are installed issues with the turbine, although not so often, can delay the production and compromise the operation. Then, alternative methods for supplying the system or even restructuring it in a subsea power grid can potentially reduce the cost and complexity of bringing offshore hydrocarbons in a more processed form to the surface. Together or instead of the gas turbines, alternative power supplies as offshore wind farms [1] or energy from onshore, as the Valhall project [2], seem very good alternatives to increase significantly the reliability and availability of the system. In addition, they may reduce the environmental impact of the offshore industry.

Modifications in the power supply of such a subsea power system, however, requires attention not only in the power supply itself but all the elements involved in the power transmission, distribution, and the electronic equipment, as reported in [3]. Due to the harsh environment of a subsea power system (very high pressures and high temperatures), there is a concern to all the various power components such as pressure tolerant power electronics, high-voltage circuit-breakers, power umbilical, wet-mate connector, and other. Moreover, such systems are expected to operate for a long period without failure or even maintenance since the access to the equipment is difficult due to the distance and pressure involved. Consequently, one of the main key features of subsea equipment is reliability [4].

Among the possible arrangements a subsea power system could have, this paper is focused on the unidirectional power-flow arrangement as shown in Fig. 1. Also, for the distances involved, it is assumed that the ac line is economically and technically viable [5]. Then, the choice for ac lines benefits from the developed protection mechanisms and equipment. For the power loads, it is considered that the majority are driven by a Variable Speed Drive (VSD) fed by the dc distribution lines. The later removes the need for a rectification stage commonly found in VSDs and, ideally, avoid the energy quality problems related to the ac lines, such as resonances and harmonics circulation [6]. For such a scenario, this paper address a power electronic converter topology that enables the connection between the ac power line and the dc loads (ac-dc block in Fig. 1). Due to the power levels (MW) and distance, this converter is expected to operate with ac input in the tens of kW range while the dc output may range in the units of kV. For this task few are the power electronics means to process the energy without the use of low-frequency transformers. Recently, the more successful results have been achieved by the modular multilevel converters, which developments in its family are reviewed in [7].

![Fig. 1. Simplified power system block diagram for ac transmission and dc distribution. The Variable Speed Drives (VSD) main loads are high-power pumps, compressors and drilling machines.](image)

Nevertheless, the isolation feature is important for the operation, thus a transformer must be incorporated into the structure. One possibility is to create a mix of modular multilevel converters and low-frequency transformers [8], [9]. Another alternative is to operate the transformer with a frequency higher than the ac power line. This could be achieved by...
modifying the ac-dc structure into an ac(low frequency)-ac(medium frequency)-dc one. Such a link would be a design variable and could be used to reduce the size of the transformer and energy variation in the modular multilevel converter. Among the modular multilevel converters able to create this ac link, the main alternatives are the back-to-back MMC (BTB-MMC), the Modular Multilevel Matrix Converter (MMMC) and the Hexverter. Although comparisons have shown that the BTB-MMC and the MMMC as usually superior to the Hexverter [10], [11], in this application it might not be true. Since there is no frequency lower than the grid where high-power is required, the main advantages for the MMMC are not seen. In the case of the BTB-MMC, there is a chance that for increased blocking capabilities the full-bridge submodules are required instead of half-bridge, which would increase greatly the number of elements. Then, although just slightly better, the Hexverter is chosen among the other due to its blocking features and a smaller number of elements, which includes not only the branches but also auxiliary circuits and sensors. Not only the second ac link, but also the use of a solid-state active converter enables stable operation, within limits, even under severe voltage or frequency variations. Furthermore, other functionalities could be incorporated in this active converter, e.g. the ac line reactive power compensation.

Considering unidirectional operation, the multipulse rectifier class is selected for the ac-dc conversion [12]. Using this rectifier not only isolation is achieved but also a reduced amount of not active energy circulation and the feasibility of lower voltage rectifier bridges connected in series to reach MVDC.

As a result, this paper presents the two-stage arrangement composed of an ac-ac converter and an isolated multipulse rectifier. More specifically, this is realized by the Hexverter and the series-type 12-pulse rectifier arrangement. In addition to the structure, the analysis and development of the control strategies are also shown. The converter’s operation and control strategy are presented along with necessary mathematical analysis to obtain control-oriented models. The proposed Hexverter’s branch energy regulation loops are based on the works of [13], [14]. But in our approach, we use information from the submodule’s voltages, decomposed by the \( \alpha \beta \) transformation, to generate the circulating currents references for the branch energy control. Furthermore, the Hexverter’s input currents are defined by the stored energy and the Hexverter’s output currents are set by the 12-pulse rectifier in an inner loop for the output voltage control. Experimental results are shown to verify the converter operation and to test the proposed control strategy.

II. HEXVERTER ELEMENTS AND DEFINITIONS

The Hexverter, first proposed in [15] and further discussed in [16], is a modular multilevel converter able to perform three-phase to three-phase ac-ac conversion. This converter is composed of six branches connected in series where each branch has a cascade connection of N submodules (SMs). For ac-ac operation, each SM is required to be bi-directional both in voltage and current. Because of it, the most common choice is the full-bridge converter. As other modular multilevel converters, inductors (\( L_b \)) are inserted in series with each branch to limit the current derivative during the submodule switching process. According to [17], the Hexverter is classified as Double-Delta Bridge-Cells (DDBC). Further details on the topology can be found in [11].

The ideal Hexverter has two different three-phase three-wire systems (system 1 and system 2 as shown in Fig. 2. For the converter analysis the voltage and current references are defined according to Fig. 2. Then, assuming no current circulation in the branches, by the Kirchhoff voltage law the each branch voltages (\( v_{b,m}, m = \{1, 2, ..., 6\} \) as function of system 1 \( v_{1,k}, k = \{1, 2, 3\} \) and system 2 voltages \( v_{2,k}, k = \{1, 2, 3\} \) are

\[
\begin{bmatrix}
v_{b,1} \\
v_{b,2} \\
v_{b,3} \\
v_{b,4} \\
v_{b,5} \\
v_{b,6}
\end{bmatrix}
= \begin{bmatrix}
1 & 0 & 0 & -1 & 0 & 0 & 1 \\
0 & -1 & 0 & 1 & 0 & 0 & -1 \\
0 & 1 & 0 & 0 & -1 & 0 & 1 \\
0 & 0 & -1 & 0 & 1 & 0 & -1 \\
0 & 0 & 1 & 0 & 0 & -1 & 1 \\
-1 & 0 & 0 & 0 & 0 & 1 & -1
\end{bmatrix}
\begin{bmatrix}
v_{1,1} \\
v_{1,2} \\
v_{1,3} \\
v_{2,1} \\
v_{2,2} \\
v_{2,3} \\
v_{st}
\end{bmatrix}
\tag{1}
\]

where \( v_{st} \) is the voltage difference between system 1 and system 2 center node, which is named star-voltage. Due to the coupling existent in a three-phase three-wire system, these set of equations can be simplified by the use of \( \alpha \beta \) transformation into a decoupled two variables system.

\[
\begin{bmatrix}
x_\alpha \\
x_\beta
\end{bmatrix}
= \begin{bmatrix}
x_1 \\
x_2 \\
x_3
\end{bmatrix}
= \begin{bmatrix}
2/3 & -1/3 & -1/3 \\
0 & 1/\sqrt{3} & -1/\sqrt{3} \\
0 & -1/\sqrt{3} & 1/\sqrt{3}
\end{bmatrix}
\begin{bmatrix}
x_1 \\
x_2 \\
x_3
\end{bmatrix}
\tag{2}
\]

Using the \( T_{\alpha \beta} \) and (1), the branch voltages can be rewritten as (3). In this representation of the converter voltages, the addition of a common voltage (\( v_{c,m} \)) to all branch voltages is suggested in [15]. This voltage is defined as \( v_{c,m} = \sum_{m=1}^{6} v_{b,m} \).
and used to control the circulating current $i_{circ}$, which is defined as $i_{circ} = \sum_{m=1}^{6} v_{b,m}/6$.

\[
\begin{bmatrix}
v_{b1} \\
v_{b2} \\
v_{b3} \\
v_{b4} \\
v_{b5} \\
v_{b6}
\end{bmatrix} = \begin{bmatrix}
1 & 0 & -1 & 0 & 1 & 1/6 \\
1/2 & \sqrt{3}/2 & 1/2 & -1/2 & 1/6 & 1/6 \\
-1/2 & \sqrt{3}/2 & 1/2 & \sqrt{3}/2 & 1/6 & 1/6 \\
1/2 & \sqrt{3}/2 & -1/2 & \sqrt{3}/2 & -1/6 & -1/6 \\
-1/2 & \sqrt{3}/2 & -1/2 & -\sqrt{3}/2 & 1/6 & 1/6 \\
-1 & 0 & -1/2 & -\sqrt{3}/2 & -1/6 & -1/6
\end{bmatrix} \begin{bmatrix}
v_{1,\alpha} \\
v_{1,\beta} \\
v_{2,\alpha} \\
v_{2,\beta} \\
v_{st} \\
v_{sum}
\end{bmatrix},
\]

(3)

Hexverter’s operation requires each branch to synthesize voltages with the frequency of system 1 and system 2. The operation of the Hexverter focus on the balancing of system 1 and system 2 voltages by the branch voltages. Then, the voltage of each adjacent branch pair must at least balance one system’s line voltage. For instance, the adjacent pair voltage $v_{b1} + v_{b2}$ exchange power with the line voltage $v_{1,2} - v_{1,1}$ while the adjacent pair $v_{b2} + v_{b3}$ exchange power with the line voltage $v_{2,2} - v_{2,1}$. The use of (1) or (3), naturally results in a correct match of branch voltages and the voltages from system 1 or system 2. For example, the resultant voltage of $v_{b1} + v_{b2}$ voltage has a frequency $f_1$ and the resultant voltage of $v_{b4} + v_{b5}$ has a frequency $f_2$.

III. AC-DC CONVERTER OPERATION AND CONTROL

The proposed converter is shown in Fig. 3. The first stage (Hexverter) converts the input (system 1) ac into output (system 2) ac which has a higher frequency. The second stage is the isolated series-type 12-pulse rectifier with capacitive filter. Regardless of the control strategy, the ac-ac converter is meant to be operated as a balanced three-phase sinusoidal current source to feed the 12-pulse rectifier, accordingly to the approach presented in [12]. This operation mode presents advantages as the ac side inherited short-circuit protection, transformer’s voltage step limited by the output rectifier voltage and current limiting during overloads.

In compass with the operation, the control strategy is required to control the output voltage and the Hexverter’s stored energy. This is realized by a central unit, which limits the modularity and expandability but is less complex than a distributed strategy [18]. These requirements are addressed by the control strategy through three external control loops: the output voltage control, the total voltage control and the branch energy regulation loop. In comparison with [14], [16], the work in this paper does not set the input, output or branch power directly. Here the Hexverter’s powers (input, output or branch) are set indirectly by the output voltage, by the SMs total voltage and by the branches total voltages differences. This approach was chosen to give direct control over the variables of interest.

A. Output voltage control

The output voltage control is composed by a cascaded of two loops as shown in the block diagram of Fig. 4. The inner loop controls the 12-pulse rectifier input current, which are the currents of system 2. These sinusoidal currents references are a orthogonal set ($i_{2,\alpha}$ and $i_{2,\beta}$) that is modulated in amplitude by the outer loop control action ($u_{vo}$/α/β).

B. Total voltage control

This loop indirectly controls the Hexverter’s stored energy by the outer loop. For the sake of simplicity, the control variable is set to be the sum of all SM capacitor voltages. In the inner loop of this cascaded strategy, the input currents are in-phase with system 1 sources and their amplitudes are modulated by the total voltage loop control action ($u_{vt}$) in the block $u_{vt}/\alpha/\beta$.

C. Proposed Branch Energy Regulation

As other modular multilevel converters, the Hexverter requires control methods to evenly distribute the stored energy among its SMs. The energy distribution unbalances can be split in two categories. The first occurs among the SMs inside each converter’s branch. This is basically due to the

Fig. 4. Block diagram of the output voltage ($v_{o}$) control and system 2 current ($i_{2,\alpha/\beta}$) control.

Fig. 5. Block diagram of the total voltage ($v_{t}$) control and input current ($i_{1,\alpha/\beta}$) control.
adopted modulation and differences in losses of each SM. Such unbalance is usually compensated by sorting algorithms [19], [20]. The second is a result of the power absorbed or provided by each branch. This result is exploited in [14], where the authors explain the branch power and suggest how to regulate it by the means of different components in the star voltage (v_{st}) and in the circulating current (i_{circ}) which gives several degrees of freedom usable or not according to the Hexverter’s operation frequencies in system 1 and 2.

Following the main principle beyond the branch energy regulation that is to control the branch power in order to charge or discharge it. It is proposed the use of i_{circ} to regulate the energy distribution between the branches while the u_{st} remains constant. The i_{circ} elements are chosen to compensate specific branch energy unbalances. These are monitored by the means of each branch equivalent total voltage (v_{q,m}), which is the sum of all capacitor’s voltages in a given branch (v_{q,m}, m \in \{1,2,\ldots,6\}). Although a fixed value of v_{st} is proposed, it is known from the literature that there is an optimal ratio v_{st}/i_{circ} value for different power situations [21].

The proposed cascaded control loops for the branch energy relation and the circulating current are shown in Fig. 6 and further detailed in Fig. 7. Notice that instead of calculating the required branch power as previously presented by other authors, this strategy uses the information contained in V_{q,m} to generate the circulating current reference (i_{circ}^{*}). Ideally, i_{circ}^{*} has in its composition a dc element from i_{v,1}, two ac elements with frequency ω_{1} (u_{v,2} and u_{v,3} and two ac elements with frequency ω_{2} (i_{v,4} and i_{v,5}).

For a given difference from v_{q,m} reference (\bar{N} \cdot V_{sm} = V_q^*) and the measured voltage, the resultant error can be obtained by e_{v,m} = V_q^* - v_{q,m}. This implies in the following control actions derived as functions of e_{v,m}.

\[ u_{v,1} = -e_{v,1} + e_{v,2} - e_{v,3} + e_{v,4} - e_{v,5} + e_{v,6} \] (4)
\[ u_{v,2} = \cos(\theta_1) \cdot \left( \frac{2}{3} e_{v,1} + e_{v,2} - e_{v,3} - e_{v,4} - e_{v,5} - e_{v,6} \right) \] (5)
\[ u_{v,3} = \sin(\theta_1) \cdot \left( \frac{e_{v,3} + e_{v,4} - e_{v,5} - e_{v,6}}{\sqrt{3}} \right) \] (6)
\[ u_{v,4} = -\cos(\theta_2) \cdot \left( \frac{e_{v,4} + e_{v,5} + e_{v,6} + e_{v,1} - 2 (e_{v,2} + e_{v,3})}{3} \right) \] (7)
\[ u_{v,5} = -\sin(\theta_2) \cdot \left( \frac{-e_{v,4} - e_{v,5} + e_{v,6} + e_{v,1}}{\sqrt{3}} \right) \] (8)
which results in the circulating current reference

\[ i_{circ}^{*} = K_{uv} \cdot \left( u_{v,1} + u_{v,2} + u_{v,3} + u_{v,4} + u_{v,5} \right) \] (9)

An important understanding about the elements u_{v,2}, u_{v,3},..., u_{v,5} is that the resultant circulating current implies a set of branch power which addition will be zero. This characteristic enhances the stability of the method because the energy extracted from a pair of branches is send to the remaining ones. To prove this statement consider the branches 1 and 2, they are connected in parallel with the line voltage v_{1,1} - v_{1,2}. The instantaneous power in this branch combination is

\[ p_{12} = v_{h,1} \cdot i_{h,1} + v_{h,2} \cdot i_{h,2} \] (10)

It is known that the branch current has several components, but the interest here is in the power generated by the energy regulating loop. Then, assuming the circulating current control to operate adequately, the remaining power is

\[ p_{12,ic} = (v_{h,1} + v_{h,2}) \cdot i_{circ} \] (11)

which can be rewritten using the v_{h,m} in αβ coordinates. Also, for the sake of simplicity, the circulating current is supposed to be composed only by i_{dc}+i_{f_1}+i_{f_2}. Due to the superposition principle, what is obtained for the system 1 currents (i_1) is also valid for system 2 currents (i_2). Then, for the system 1 frequency, the power produced by the circulating current in the branch pair 1-2 is

\[ p_{12,ic1} = (v_{h,1} + v_{h,2}) \cdot K_{uv} \cdot (u_{v,2} + u_{v,3}) \] (12)

Substituting the branch voltages values but v_{st} and v_{sum}.

\[ p_{12,ic1} = \left( \frac{3}{2} v_{1,\alpha} + \frac{\sqrt{3}}{2} v_{1,\beta} \right) \cdot K_{uv} \cdot (u_{v,2} + u_{v,3}) \] (13)
All the other adjacent branches power can be obtained in the same fashion.

\[ p_{34,1} = \sqrt{3} v_{1,\beta} \cdot K_{uv} \cdot u_{v,3} \]  \tag{14}
\[ p_{56,1} = (-3/2)v_{1,\alpha} - \sqrt{3}/2 v_{1,\beta}) \cdot K_{uv} (u_{v,2} + u_{v,3}) \]  \tag{15}
\[ p_{23,2} = (3/2)v_{2,\alpha} - \sqrt{3}/2 v_{2,\beta}) \cdot K_{uv} (u_{v,4} + u_{v,5}) \]  \tag{16}
\[ p_{45,2} = \sqrt{3} \cdot v_{2,\beta} \cdot K_{uv} \cdot u_{v,5} \]  \tag{17}
\[ p_{61,2} = (-3/2)v_{2,\alpha} - \sqrt{3}/2 v_{2,\beta}) \cdot K_{uv} (u_{v,4} + u_{v,5}) \]  \tag{18}

where \( p_{12,1} + p_{34,1} + p_{56,1} = 0 \) as well as \( p_{23,2} + p_{45,2} + p_{61,2} = 0 \).

The same reasoning applies to \( u_{v,1} \) and the power generated in all branches due to it. For instance, the expected average power in the branch 1 (\( p_{1,u,1} \)) is

\[ \bar{p}_{1,u,1} = v_{st} \cdot K_{uv} \cdot u_{v,1} \]  \tag{19}

where all six branches will have the same power magnitude. However, branches 1, 3 and 6 differ from branches 2, 4 and 6 in power flow due to \( v_{st} \) polarity.

**IV. CONTROL-ORIENTED MODELS**

For the development of the control-oriented models it is considered that the Hexverter’s system 1 and 2 are balanced three-phase voltage sources. Although some characteristics of the 12-pulse rectifier are considered for the output voltage control, the current control of the Hexverter only accounts for the needed power. In terms of energy, the only element storing energy considered is the SM’s capacitor. And, each Hexverter branch is assumed to have an equivalent submodule per branch, which voltage and capacitance values are the series equivalent of its SMs.

**A. Input, output and circulating current models**

For the proposed control strategy the Hexverter, current models are already available from previous publications as [15], [16], where it is shown the coupling between the Hexverter currents. Since external inductances are inserted to make \( L_b \ll L_1, L_2 \) and \( R_b \ll R_1, R_2 \) true. The decoupled currents model presented in [15] is presented in the form of Laplace transfer functions as

\[ \frac{I_{1,\alpha}(s)}{V_{1,\alpha}(s)} = \frac{1}{L_1 s + R_1} \]  \tag{20}
\[ \frac{I_{2,\alpha}(s)}{V_{2,\alpha}(s)} = \frac{1}{L_2 s + R_2} \]  \tag{21}
\[ \frac{I_{circ}(s)}{V_{sun}(s)} = \frac{1}{6 L_b s + R_b} \]  \tag{22}

where the four \( \alpha \beta \) converter voltages \( (v_{1,\alpha}, \ldots, v_{2,\beta}) \) are related to the control actions \( (u_{1,\alpha}, \ldots, u_{2,\beta}) \) by \( v_{x,y} = u_{x,y} \cdot N \cdot V_{sun} \cdot k_{pwm} \), where \( k_{pwm} \) is the modulator gain, \( x \in \{1, 2\} \) and \( y \in \{\alpha, \beta\} \).

**B. Total voltage model**

This model is developed considering the difference between the input power \( (P_1) \) and the output power \( (P_2) \). Such power difference \( (\Delta P = P_1 - P_2) \) implies in charging or discharging all the Hexverter’s submodules. For a balanced three-phase system, the power variation and the converter’s stored energy variation are

\[ \Delta P = 3 \cdot v_1 \cdot i_1 - P_2 = \frac{d (\Delta W_T)}{dt} \]  \tag{23}

where \( i_1 \) and \( v_1 \) are the source’s current and phase voltage, respectively. Then, the total energy variation \( (\Delta W_T) \) is modeled as the series equivalent of all capacitors in the Hexverter \( (C_{sm}/6 \cdot N) \) by

\[ \Delta W_T = \frac{C_{sm}}{12 \cdot N} \cdot (v_{i,f}^2 - v_{i,i}^2) \]  \tag{24}

where \( v_{i,f} \) is the final voltage and \( v_{i,i} \) is the initial voltage. Substituting \( v_{i,f} = v_{i,i} + \Delta v_i \) into (24), the \( \Delta W_T \) becomes

\[ \Delta W_T = \frac{C_{sm}}{12 \cdot N} \cdot (2 \cdot v_{i,i} \cdot \Delta v_i + (\Delta v_i)^2) \]  \tag{25}

If the value \( (\Delta v_i)^2 \) it is assumed small enough to be neglected when compared to \( 2 \cdot v_{i,i} \cdot \Delta v_i \), the power variation is

\[ \Delta P = \frac{d (\Delta W_T)}{dt} = \frac{C_{sm} \cdot v_{i,i}}{6 \cdot N} \cdot \frac{d (\Delta v_i)}{dt} \]  \tag{26}

Since the control variable related to the \( P_1 \) is the input current amplitude, (26) is rewritten as

\[ 3 \cdot v_1 \cdot i_{1,pk}/\sqrt{2} = \frac{C_{sm} \cdot v_{i,i}}{6 \cdot N} \cdot \frac{d (\Delta v_i)}{dt} + P_2 \]  \tag{27}

where the input current amplitude value assumes a pure sinusoidal wave.

Finally, the transfer function relating the total voltage variation \( (\Delta v_i(s)) \) and the input current \( (I_{1, pk}(s)) \) is

\[ \frac{V_i(s)}{I_{1, pk}(s)} = \frac{18 \cdot v_1 \cdot N}{\sqrt{2} \cdot v_{i,i} \cdot C_{sm} \cdot s} = \frac{18 \cdot v_1 \cdot N}{\sqrt{2} \cdot 6 \cdot N \cdot v_{sm} \cdot C_{sm} \cdot s} \]  \tag{28}

where \( P_2 \) is assumed to be a perturbation in the model.

**C. Output voltage model**

The transfer function for a 12-pulse rectifier with an input balanced sinusoidal current source has already been presented in [22]. The small-signal model is

\[ \frac{V_o(s)}{I_{2, pk}(s)} = \frac{3 \cdot n_{p,y} \cdot 1 + \cos(30^\circ) \cdot \sin(15^\circ) \cdot Z_o}{n_{p,y} \cdot (Z_o \cdot C_o \cdot s + 1)} \]  \tag{29}

where \( I_{2, pk}(s) \) represents the amplitude of a balanced three-phase sinusoidal current source reference. The transformer primary and secondary wye windings turns ratio is \( n_{p,y}/n_{s,y} \) and the output load impedance is \( Z_o \).
D. Branch energy exchange models

For the outer loop of the control strategy presented in Fig. 7, there are two transfer functions to be obtained in order to design the gain $K_{uv}$. One is the relation between the voltages difference $(v_{q,dif} = v_{q,1} + v_{q,3} + v_{q,5} - v_{q,2} - v_{q,4} - v_{q,6})$ and the circulating current reference $(i_{v,1} = K_{uv}u_{v,1})$. The other is the pairs of voltage differences $(2V^q - v_{q,1} - v_{q,2}, \ldots)$ and their circulating current references $(i_{v,2} = K_{uv}u_{v,2}, \ldots)$, which even seeming unrelated have all the same dynamic.

Once the power produced due to the circulating current is known by (19), all six branch power equations related to $i_{v,1}$ are added as

$$6 v_{st} \cdot K_{uv} \cdot u_{v,1} = \bar{p}_1 - \bar{p}_2 + \bar{p}_3 - \bar{p}_4 + \bar{p}_5 - \bar{p}_6$$

(30)

considering an operating point and a small-signal variation $(v_{q,1} = V_{q,1} + \tilde{v}_{q,1}$ and $i_{v,1} = I_{v,1} + \tilde{i}_{v,1}$). The use of the capacitor’s energy ($E_{q1} = C_{sm}v_{q,1}^2/2N$) derivative gives

$$6 v_{st} \cdot (\tilde{i}_{v,1} + I_{v,1}) = C_{sm} \frac{d}{dt} \left(2V_{q,dif} \cdot \tilde{v}_{q,dif} + \tilde{v}_{q,dif}^2 \right)$$

(31)

If the second order $(\tilde{v}_{q,dif}^2)$ and dc $(I_{v,1})$ elements are neglected, the use of Laplace’s transformation provides the transfer function

$$\frac{V_{q,dif}(s)}{I_{v,1}(s)} = \frac{6Nv_{st}}{V_q C_{sm} s} = \frac{6v_{st}}{V_{sm} C_{sm} s}$$

(32)

In the case of the alternating current elements of the circulating currents, the model is developed using (14) but any other would result in the same. As in the previous case, the stored energy information of the branches related to the power $p_{34,c,1}$ is used

$$p_{34,c,1} = \sqrt{3}v_{1,\beta} \cdot K_{uv} \cdot u_{v,3} = \frac{d}{dt} \left( \frac{1}{2} \frac{C_{sm} v_{q,3}^2}{2N} \right)$$

(33)

where $v_{q,3} = v_{q,3} + v_{q,4}$. $K_{uv} \cdot u_{v,3} = i_{v,3}$, and the system 1 peak value of any phase voltage is $V_{1,pk}$. In addition, it is assumed the operating point and small-signal perturbations $v_{q,3} = V_{q,3} + \tilde{v}_{q,3}$ and $i_{v,3} = I_{v,3} + \tilde{i}_{v,3}$. Thus,

$$\sqrt{3}V_{1,pk}(I_{v,3} + \tilde{i}_{v,3}) = \frac{C_{sm} v_{q,3}^2}{4N} \cdot \frac{d}{dt} \left(2V_{q,3} \tilde{v}_{q,3} + \tilde{v}_{q,3}^2 \right)$$

(34)

Once more the second order and dc elements are neglected. Applying the Laplace’s transformation and extending the results for the other loops

$$\frac{V_{qq}(s)}{I_{v,3}(s)} = \frac{V_{qq}(s)}{I_{v,2}(s)} = \frac{2N \sqrt{3} V_{1,pk}}{V_{qq} C_{sm} s} = \sqrt{3} \frac{V_{1,pk}}{V_{sm} C_{sm} s}$$

(35)

$$\frac{V_{qq}(s)}{I_{v,4}(s)} = \frac{V_{qq}(s)}{I_{v,5}(s)} = \frac{2N \sqrt{3} V_{2,pk}}{V_{qq} C_{sm} s} = \sqrt{3} \frac{V_{2,pk}}{V_{sm} C_{sm} s}$$

(36)

where $V_{qq} = V_{q,3} + V_{q,4} = 2N V_{sm}$.

V. DISCUSSION AND EXPERIMENTAL RESULTS

The presented three-phase ac-dc converter is tested according to the available hardware, which assembled set up is shown in Fig. 8. In total there are 24 full-bridge cells. The control platform is composed of a DSP and four FPGA boards. The DSP board has a Texas Instruments 225-MHz TMS320C6713 which is responsible for executing the control laws and for the interface with a PC. While the FPGA (Actel ProASIC3 A3P400) boards are used for the data acquisition and PWM signal generation. Connected to the DSP board, it is equipped a daughter card for the interface and online data logging through a MATLAB host port interface (HPI).

![Fig. 8. Prototype in the rig (left to right 12-pulse rectifier, Hexverter, Variac and PC for the interface).](image)

All the experiments parameters are shown in TABLE I where the given capacitance results in an H constant of approximately 60 mH [23]. Further than these, the DSP control loops and FPGA sampling frequencies are set to 7.2 kHz. The obtained results were captured with an oscilloscope Tektronix MSO 2024 but some internal variables were acquired FPGA.

| Parameter | Value |
|-----------|-------|
| $f_1$     | 50 Hz |
| $L_1$     | 5 mH  |
| $L_b$     | 0.99 mH |
| $N$       | 4     |
| $v_{sm}$  | 150 V |
| $v_{st}$  | 0.1-N-v_{sm} |
| $v_{1,r,m,s}$ | 150 V |
| $v_o$     | 200 V |
| $R_o$     | 30.6 $\Omega$ |
| $C_o$     | 1650 $\mu$F |

The steady-state operation stability is verified through the controlled input currents, dc output voltage and SMs’ voltages as shown in Fig. 9 and Fig. 10. These input current are ideally synchronized with the grid by the use of a phase-locked loop. In particular, it is shown by each branch modulation index in Fig. 11 that no saturation occurs under normal operation.
Fig. 9. Input currents ($i_{1,x} x \in \{1, 2, 3\}$) and output voltage ($v_o$).

Fig. 10. All the 24 submodule’s capacitor voltages. In each graph, the four voltages of each branch are plotted.

The regulating branch energy loops control action $u_{v}$ are shown in Fig. 12. For the adjacent power loop (control action $u_{v,1}$), the gain $K_{uv}$ is adjusted in order to provide a gain margin of 30.3 dB at 51.3 Hz and a phase-margin of 14.2° at 8.81 Hz. Note that the measured $v_{q, dif}$ is filtered by a 2.5 Hz low-pass filter. The other regulating energy loops have a gain margin of 25 dB at 3.6 kHz and a phase-margin of 88.4° at 130 Hz, which are the same because system 1 and system 2 phase voltages are ideally the same.

The proposed control strategy is verified by each loop main waveform. For the Hexverter current controllers, the $\alpha/\beta$ variables are shown in Fig. 13 a) and Fig. 13 b), respectively. While the circulating current controller is shown in Fig. 13 c). Note that different than [14], our compensating power does not reach zero ($v_{st} \cdot i_{circ}$). The dc level in it indicates $Q_1 \neq Q_2$ and the periodical oscillation indicates the non-active power required by the 12-pulse rectifier due to the remaining harmonics. Both causes unbalance in the branch’s energy.

System 1 and system 2 current controllers use a resonant plus proportional structure. While the circulating current controller is a proportional controller only. In all cases, the proportional controller is adjusted for the Bode Diagram zero cross frequency to be around 1.2 kHz, which is six times lower than the control law calculation frequency.

The converter stability is put under test by a fast step-up and an abrupt step-down of the output voltage reference. The intermediate voltage level is chosen in order to provide
a power variation of 50%. Both output voltage controller and the total voltage \((v_t)\) controller are proportional plus integral controllers. The sensed output voltage is filtered by a 20 Hz low-pass filter. While the total voltage is filtered by a 15 Hz low-pass filter. The output voltage controller is tuned to have a gain margin of 35.1 dB at 153 Hz and a phase-margin of 51.4° at 16 Hz \((\approx f_2/10)\). While the total voltage controller is tuned to have a gain margin of 44.6 dB at 125 Hz and a phase-margin of 60.9° at 6 Hz \((\approx f_1/10)\). The results of these controllers tunings are shown in Fig 14, where the overshoot and undershot occurred in accordance with the tuning.

![Fig. 13. References and measured values (from top to bottom) for system 1 αβ currents, system 2 αβ currents and the circulating current.](image1)

![Fig. 14. Transient response of the total voltage \((v_t)\) and the output voltage \((v_o)\) during step on the output voltage reference. Case a) step-up and case b) step-down.](image2)

Note that there is room for optimization in several variables. For instance, \(i_{circ}\) could be reduced by setting higher \(v_s\) or by reducing the gain of the branch energy regulation loops. Also, each regulation loop could have different gains.

VI. CONCLUSION

This paper has proposed a two-stage ac–dc converter which is the combination of a Hexverter and a series-type 12-pulse rectifier. A complete control strategy is presented in order to control the output voltage and the Hexverter stored energy. In particular, a new method for monitoring and action upon Hexverter’s branch energy unbalance is explained and detailed. Experimental results verify the steady-state and transient stability of the converter and its control strategy.

Since the control strategy is based on the Hexverter’s current control, the converter inherits short-circuit protection from the Hexverter point of view. Also, apart from the output filter and power line parasitic elements, even the rectifier current is limited under transient overloads. For instance, this feature eases the commissioning of its loads.

The energy regulation in all stages (even distribution and total value in Hexverter plus the output voltage) have been accomplished by the means of a simple control strategy. However, further analysis must be done when in the presence of a VSD (Variable Speed Drive) as a load. Also, transmission/distribution lines from the input or the output must be taken into consideration.

Finally, although one rectifier output is shown, the structure itself and the control strategy allows the insertion of multiple outputs by adding other windings in the transformer or another multipulse rectifier.

REFERENCES

[1] R. N. Fard and E. Tedeschi, “Integration of distributed energy resources into offshore and subsea grids,” CPSS Transactions on Power Electronics and Applications, vol. 3, no. 1, pp. 36–45, March 2018.
[2] B. Westman, S. Gilje, and M. Hyttinen, “Valhall re-development project, power from shore,” in PCIC Europe 2010, June 2010, pp. 1–5.
[3] K. Rajashekara and H. Krishnamoorthy, “Power electronics for subsea systems: Challenges and opportunities,” in 2017 IEEE 12th International Conference on Power Electronics and Drive Systems (PEDS), Dec 2017, pp. 986–991.
[4] T. Hazel, H. H. Baerend, J. J. Legeay, and J. J. Bremnes, “Taking power distribution under the sea: Design, manufacture, and assembly of a subsea electrical distribution system,” IEEE Industry Applications Magazine, vol. 19, no. 5, pp. 58–67, Sept 2013.
[5] J. Song-Manguelle, M. H. Todorovic, S. Chi, S. K. Gunturi, and R. Datta, “Power transfer capability of hvac cables for subsea transmission and distribution systems,” IEEE Trans. Ind. Appl., vol. 50, no. 4, pp. 2382–2391, July 2014.
[6] B. Monsen, K. S. Rongye, T. Lægreid, and C. Gutschler, “Asgard subsea gas compression—technology qualification testing with high-speed vsd and very long step-out cable,” IEEE Trans. Ind. Appl., vol. 50, no. 2, pp. 1552–1561, March 2014.
[7] H. Akagi, “A review of developments in the family of modular multilevel cascade converters,” IEEE Transactions on Electrical and Electronic Engineering, vol. 13, no. 9, pp. 1222–1235, 2018.
[8] S. Cui, J. Lee, J. Hu, R. W. De Doncker, and S. Sul, “A modular multi-level converter with a zigzag transformer for bipolar mvdc distribution systems,” IEEE Trans. Power Electron., vol. 34, no. 2, pp. 1038–1043, Feb 2019.
[9] H. Nademi, A. Das, R. Burgos, and L. E. Norum, “A new circuit performance of modular multilevel inverter suitable for photovoltaic conversion plants,” IEEE Journal of Emerging and Selected Topics in Power Electronics, vol. 4, no. 2, pp. 393–404, June 2016.
[10] K. Ilves, L. Bessegato, and S. Norriga, “Comparison of cascaded multilevel converter topologies for ac/ac conversion,” in Power Electronics Conference (IPEC-Hiroshima 2014 - ECCE-ASIA), 2014 International Conference, May 2014, pp. 1087–1094.
[11] D. Karwatski, L. Baruschka, and A. Mertens, “Survey on the hexverter topology - a modular multilevel ac/ac converter,” in Power Electronics and ECCE Asia (ICPE-ECCE Asia), 2015 9th International Conference on, June 2015, pp. 1075–1082.
[12] G. Lambert, M. V. Soares, P. Wheeler, M. L. Heldwein, and Y. R. de Novaes, “Current fed multipulse rectifier approach for unidirectional hvdc and mvdc applications,” IEEE Trans. Power Electron., pp. 1–1, 2018.
[13] F. Kammerer, J. Kolb, and M. Braun, “A novel cascaded vector control scheme for the modular multilevel matrix converter,” in IECON 2011 - 37th Annual Conference of the IEEE Industrial Electronics Society, Nov 2011, pp. 1097–1102.

[14] D. Karwatzki, L. Baruschka, M. von Hofen, and A. Mertens, “Branch energy control for the modular multilevel direct converter hexverter,” in Energy Conversion Congress and Exposition (ECCE), 2014 IEEE, Sept 2014, pp. 1613–1622.

[15] L. Baruschka and A. Mertens, “A new 3-phase direct modular multilevel converter,” in Power Electronics and Applications (EPE 2011). Proceedings of the 2011-14th European Conference on, Aug 2011, pp. 1–10.

[16] ——, “A new three-phase ac/ac modular multilevel converter with six branches in hexagonal configuration,” IEEE Trans. Ind. Appl., vol. 49, no. 3, pp. 1400–1410, May 2013.

[17] H. Akagi, “Classification, terminology, and application of the modular multilevel cascade converter (mmcc),” IEEE Trans. Power Electron., vol. 26, no. 11, pp. 3119–3130, Nov 2011.

[18] S. Yang, Y. Tang, and P. Wang, “Distributed control for a modular multilevel converter,” IEEE Trans. Power Electron., vol. 33, no. 7, pp. 5578–5591, July 2018.

[19] A. Dekka, B. Wu, N. R. Zargari, and R. L. Fuentes, “Dynamic voltage balancing algorithm for modular multilevel converter: A unique solution,” IEEE Trans. Power Electron., vol. 31, no. 2, pp. 952–963, Feb 2016.

[20] H. Peng, R. Xie, K. Wang, Y. Deng, X. He, and R. Zhao, “A capacitor voltage balancing method with fundamental sorting frequency for modular multilevel converters under staircase modulation,” IEEE Trans. Power Electron., vol. 31, no. 11, pp. 7809–7822, Nov 2016.

[21] D. Karwatzki, L. Baruschka, M. von Hofen, and A. Mertens, “Optimised operation mode for the hexverter topology based on adjacent compensating power,” in 2014 IEEE Energy Conversion Congress and Exposition (ECCE), Sept 2014, pp. 5399–5406.

[22] G. Lambert, M. L. Heldwein, and Y. R. de Novaes, “Simplified modeling and control of a high-power high-voltage isolated dc–dc converter,” in 2015 IEEE 13th Brazilian Power Electronics Conference and 1st Southern Power Electronics Conference (COBEP/SPEC), Nov 2015, pp. 1–6.

[23] A. Suzuki and H. Akagi, “Hvdc circuit breakers combining mechanical switches and a multilevel pwm converter: Verification by downscaled models,” IEEE Trans. Power Electron., vol. 34, no. 5, pp. 4259–4269, May 2019.

Alessandro Costabeber (S’09–M’13) received the Degree with honours in Electronic Engineering from the University of Padova, Padova, Italy, in 2008 and the Ph.D. in Information Engineering from the same university in 2012, on energy efficient architectures and control techniques for the development of future residential microgrids. In the same year he started a two-year research fellowship with the same university. In 2014 he joined the PEMC group, Department of Electrical and Electronic Engineering, University of Nottingham, Nottingham, UK as lecturer in Power Electronics. His current research interests include HVDC converters topologies, high power density converters for aerospace applications, control solutions and stability analysis of AC and DC microgrids, control and modeling of power converters, power electronics and control for distributed and renewable energy sources. Dr. Costabeber received the IEEE Joseph John Suozzi INTELEC Fellowship Award in Power Electronics in 2011.

Patrick Wheeler (M’00–SM’12) received his BEng [Hons] degree in 1990 from the University of Bristol, UK. He received his PhD degree in Electrical Engineering for his work on Matrix Converters from the University of Bristol, UK in 1994. In 1993 he moved to the University of Nottingham and worked as a research assistant in the Department of Electrical and Electronic Engineering. In 1996 he became a Lecturer in the Power Electronics, Machines and Control Group at the University of Nottingham, UK. Since January 2008 he has been a Full Professor in the same research group. He was Head of the Department of Electrical and Electronic Engineering at the University of Nottingham from 2015 to 2018. He is currently the Head of the Power Electronics, Machines and Control Research Group, Global Director of the University of Nottingham’s Institute of Aerospace Technology and is the Li Dak Sum Chair Professor in Electrical and Aerospace Engineering. He is a member of the IEEE PELS AdCom and was an IEEE PELS Distinguished Lecturer from 2013 to 2017. He has published over 600 academic publications in leading international conferences and journals.

Gustavo Lambert (S’14) received the B.S. and M.S. degrees in electrical engineering from the Santa Catarina State University (UDESC), Joinville, Brazil, in 2012 and 2015, respectively. He received the Dr. degree from the Santa Catarina State University in 2019 for his work on Modular Multilevel Converters. Currently, he is a postdoctoral researcher at EPFL (Electric Power Processing Group), Santa Catarina State University. His research interests include the design and control of modular multilevel converter topologies for HVDC and MVDC applications.

Yales Romulo De Novaes Yales Rômulo De Novaes (IEEE member) was born in Indaial, Santa Catarina, Brazil, in 1974. In 1999 he received the B.S. degree in electrical engineering from FURB – Regional University of Blumenau, Santa Catarina, Brazil. The M. Eng. and Dr. degrees were obtained from the Power Electronics Institute (INEP) of the Federal University of Santa Catarina (UFSC) in 2000 and 2006, respectively. During 2001, he worked as a researcher engineer at the same institute. From 2006 to 2008 was a post-doctoral fellow at the Industrial Electronics Laboratory (LEI) at École Polytechnique Fédérale de Lausanne (EPFL), Lausanne, Switzerland. From 2008 to 2010 worked as scientist with the Power Electronics Systems group at ABB Corporate Research Center, Daetwill, Switzerland. Currently he is an associate professor at Santa Catarina State University (UDESC), Joinville, Brazil, and coordinator of CMEAR (Research Cell on Microgrids of Alternative and Renewable Energies).