Article

Evaluating a Hybrid Circuit Topology for Fault-Ride through in DFIG-Based Wind Turbines

Sarmad Saeed 1, Rafiq Asghar 2, Faizan Mehmood 3,*, Haider Saleem 4, Babar Azeem 5 and Zahid Ullah 6,*

1 Department of Computer Science, University of Alabama at Birmingham, Birmingham, AL 35294, USA
2 Department of Electrical Engineering, University of Engineering and Technology, Peshawar 25000, Pakistan
3 Department of Electrical Engineering, University of Engineering and Technology, Taxila 47050, Pakistan
4 Department of Electrical and Computer Engineering, University of Alabama at Birmingham, Birmingham, AL 35294, USA
5 Department of Electrical Energy and Mobility System, Carinthia University of Applied Sciences, 9524 Villach, Austria
6 Department of Electrical Engineering, UMT Lahore Sialkot Campus, Sialkot 51310, Pakistan
* Correspondence: mehmood.faizan@ucy.ac.cy (F.M.); zahid.ullah@skt.umt.edu.pk (Z.U.)

Abstract: Large-scale wind power integration has raised concerns about the reliability and stability of power systems. The rotor circuit of a doubly fed induction generator (DFIG) is highly vulnerable to unexpected voltage dips, which can cause considerable electromotive force in the circuit. Consequently, the DFIG must fulfill the fault-ride through (FRT) criteria to ensure the system’s performance and contribute to voltage regulation during severe grid outages. This paper provides a hybrid solution for DFIG wind turbines with FRT capabilities, using both a modified switch-type fault current limiter (MSFTCL) and a direct current (DC) chopper. The proposed system has the merit of keeping the rotor current and the DC-link voltage within the permissible limits, enhancing the FRT capability of generators. Moreover, the boundness of supply voltage into its reference value ensures dynamic stability during symmetric and asymmetric grid failures. Further, electromagnetic torque variations are significantly reduced during fault events. Finally, the performance validation of the proposed scheme is performed in a simulation setup, and the results are compared with the existing sliding mode control (SMC) and proportional-integral (PI) controller-based approaches. The comparison results show that a hybrid strategy with advanced controllers provides superior performance for all critical parameters.

Keywords: doubly fed induction generator; fault ride-through; grid faults; renewable energy sources

1. Introduction

Fault-ride-through (FRT) capability is often considered the most demanding requirement for grid-connected wind turbine (WT) systems. In accordance with these requirements, WTs must continue operating during voltage dips for a defined period and provide reactive power support in both balanced and unbalanced fault conditions [1]. Figure 1 depicts the FRT requirements for the grid-connected WT system [2]. As shown in Figure 1, grid codes require WTs to withstand voltage sags to a specific level, and if the fault lasts for an extended time, WT may be separated from the system to avoid equipment damage. Currently, the doubly fed induction generator (DFIG) is commonly employed to generate power due to its low cost, simplicity of design, and better controllability when used under active and reactive power regulations [3]. Moreover, DFIG allows variable speed operation to capture optimum energy from the wind while minimizing power converter losses. However, since the DFIG stator is directly linked to distribution networks through transformers, any change in grid conditions has a substantial impact on its performance [4]. Particularly, in DFIGs, the FRT problem is exacerbated by the magnetic flux’s inability to keep up with changes in stator voltage and by high slip ratios, which cause considerable...
voltage and current transients in the rotor circuit [1]. These transients are further exacerbated during unbalanced grid faults due to a negative component in the voltage waveform. In addition, at the start, and after the fault is cleared, the DFIG responses show significant undamped oscillations [5]. Such oscillation may be detrimental to the drivetrain and power electronic converters.

Figure 1. WT system fault ride-through capabilities.

The existing literature has three practical approaches for increasing the FRT capabilities of DFIG-based WTs, including using protective devices, using reactive power injection equipment, and modifying the converter switching frequency [6]. A crowbar circuit has been developed to safeguard the RSC from excessive current during grid disturbances [7]. However, owing to the significant power loss and the conversion of the DFIG to a squirrel induction generator, its application in FRT has been limited [4]. In [8], an energy storage system is proposed to control the DC-link voltage, but it requires RSC to be enough to fulfill the FRT requirements. This problem is alleviated by an improved dynamic voltage restorer (DVR) for FRT enhancement to compensate for voltage fluctuations and disturbances [9]. However, due to the large load and severe harmonic distortion in the inverter’s output, a filter must be added to the DVR output to obtain a pure sine wave.

Similarly, a grid-connected inverter (GCI)-based distributed energy resource is proposed to compensate for voltage fluctuations during grid faults. This control strategy eliminates both active–reactive power oscillations and DC-link voltage oscillations [10,11]. Flexible alternating current transmission system (FACTS) devices are often employed to enhance the FRT capabilities of DFIG-based WTs. For instance, a modified static synchronous compensator (STATCOM) is developed to provide reactive power support and improve power quality in solar and wind-integrated power systems [12]. However, this approach poses the risk of damaging or disconnecting the inverter in the event of a grid fault, and its adoption increases network complexity and costs. In [13], several types of fault current limiters (FCLs) are described for DFIG-based WTs under various configurations but with the drawbacks of non-uniform material heating, inductor saturation, and design complexity. The hybrid techniques for increasing the reliability of the DFIG WT system are given in [14,15]. Specifically, the reliability of the DFIG WT system is improved using dynamic braking resistors (SDBR) and a DC-Chopper [16]. However, the results show that the current and voltage waveforms exhibit significant fluctuations throughout the fault duration. In [17], the FRT capabilities of the WTs system are improved using vector-control techniques that rely on a PI controller. Similarly, DFIG-based WTs may also benefit from using nonlinear controllers based on model parameters and can maintain FRT capability across various operational situations. The nonlinear backstepping control method to increase the DFIG’s FRT capabilities during a network failure is presented in [15]. However, it
does not guarantee robustness against uncertainties. The FRT support of DFIG-WTs using the SMC is discussed in [18], which has the merit of accurately managing both the active and the reactive power.

This article presents a novel hybrid approach, combining the modified-switch type fault current limiter (MSFTCL) and direct current (DC) chopper to improve the FRT capability of DFIG wind turbines. Asymmetrical and symmetrical grid faults have been considered for the proposed FRT system. The hybrid system is simpler to implement and can regulate reactive power while maintaining a constant DC-link voltage during fault conditions. Moreover, rotor current oscillations are reduced significantly, protecting the power converters and DC link capacitors during severe grid faults. Additionally, the proposed system reduces stress on the DFIG by maintaining electromagnetic torque magnitude and oscillation within acceptable limits. Both SMC and PI controllers were used for validation purposes, and the results were compared with reference values and without the proposed circuitry. Simulation results indicate that the hybrid strategy provides superior performance for all critical parameters with the advanced controller.

The remainder of our paper is arranged as follows: Section 2 examines wind turbine modelling and control mechanisms for the converters. Section 3 explains the detailed architecture of the proposed system and control schemes. Section 4 presents the simulation findings for SMC and PI controllers employed on the proposed hybrid circuit, while Section 5 presents some concluding remarks.

2. Modeling of Wind Energy Conversion System

This section presents detailed modelling of the wind energy conversion system, including the modelling of a doubly fed induction generator, rotor side, and grid side converter.

2.1. Wind Energy Conversion System Description

A WECS is an integrated system that combines aerodynamics, automotive, structural, and computational technologies [19]. The WECS system employs a turbine to convert wind kinetic energy into mechanical energy that may be used to generate electricity. The following equations can determine the mechanical power generated by the WTs:

\[
\frac{dm}{dt} = \rho A \frac{dx}{dt}
\]

\[
\frac{dKE}{dt} = \frac{1}{2} \rho A V^3
\]

\[
P_m = \frac{1}{2} \rho AV^3 C_p(\lambda, \delta)
\]

where \(A\), \(\rho\), and \(\delta\) are the swept area, air density, and pitch angle of WTs, respectively. The essential design parameters include the tip speed \(\lambda\) and power coefficient \(C_p\) for total power extraction of WTs [20]. The \(\lambda\) can be described as the turbine’s tangent speed ratio to the actual wind speed, and \(C_p\) is the ratio of the actual power generated to the maximum wind power accessible at the blades. More detailed insight into the modeling of WECS can be found in [21].

\[
\lambda = \frac{\omega R}{V}
\]

\[
C_p(\lambda, \delta) = B_1 \left( \frac{B_2}{\lambda_j} - B_3 \delta - B_4 \right) e^{\frac{B_5}{\lambda_j}} + B_6 \lambda
\]

where \(B_1 = 571.6 \times 10^{-3}\), \(B_2 = 116\), \(B_3 = 0.4\), \(B_4 = 5\), \(B_5 = 21\), \(B_6 = 6.8 \times 10^{-3}\) and \(\lambda_j = \frac{1}{\lambda + 0.085} - \frac{0.035}{\lambda + 1}\). The estimated power efficiency of WTs, represented by Betz’s or Lanchester’s limit, is 0.593 [21]. Therefore, it is important to calibrate the speed at the turbine’s shaft to achieve the maximum tip speed and power coefficient to maximize active power generation. To capture the maximum wind energy, larger turbine blades are often
used to increase the air’s density and covered area. It is also important to note that air density is influenced by temperature, height, and humidity. Such factor variations may cause the density to vary by as much as 10% in certain situations. Further, since the output power is proportional to the speed cube, even modest changes in wind velocity may result in a considerable shift in total wattage.

2.2. Modelling of Doubly Fed Induction Generator

2.2.1. Modelling of DFIG under Balanced Conditions

DFIG’s control is more sophisticated because it can operate in sub-synchronous and super-synchronous regions compared to other induction generators. Since the rotor winding rotates in a periodic pattern, that can change the stator angle, causing a variation of inductance matrix values [22]. Consequently, the current and voltage will have time-dependent values, making it difficult to solve these equations. Hence, the $dqo$ transformation is applied to obtain the static inductance matrices. Now, assuming $T_{dqo}(\beta)$ is a rotating matrix that transforms $abc$ signals into $dqo$ frames, the voltage equations for the DFIG in the $dqo$ frames are as follows:

$$T_{dqo}(\beta) = \sqrt{\frac{3}{2}} \begin{bmatrix} \cos \beta & \cos(\beta - \frac{2\pi}{3}) & \cos(\beta + \frac{2\pi}{3}) \\ -\sin \beta & -\sin(\beta - \frac{2\pi}{3}) & \sin(\beta + \frac{2\pi}{3}) \\ \sqrt{\frac{1}{2}} & \sqrt{\frac{1}{2}} & \sqrt{\frac{1}{2}} \end{bmatrix}$$ (6)

A generalized transformation can be performed using the following equation.

$$\vec{f}_{dqo} = T_{dqo}(\beta) \vec{f}_{abc}$$ (7)

where $\vec{f}_{abc} = \begin{bmatrix} f_a \\ f_b \\ f_c \end{bmatrix}$ and $\vec{f}_{dqo} = \begin{bmatrix} f_d \\ f_q \\ f_0 \end{bmatrix}$. In the case of $abc$ signal-balanced quantities, it becomes

$$f_0 = \sqrt{3}/2(f_a + f_b + f_c) = 0$$ (8)

The DFIG’s voltage equations in the $dqo$ frames are as follows:

$$\vec{v}_{dqs} = R_s \vec{i}_{dqs} + T_{dqo}(\beta) \frac{d}{dt} \left( T_{dqo}(\beta)^{-1} \vec{\varphi}_{dqs} \right)$$ (9)

$$\vec{v}_{dqr} = R_s \vec{i}_{dqr} + T_{dqo}(\beta) \frac{d}{dt} \left( T_{dqo}(\beta)^{-1} \vec{\varphi}_{dqr} \right)$$ (10)

$$\vec{\varphi}_{dqs} = T_{dqo}(\beta)L_s T_{dqo}(\beta)^{-1} \vec{i}_{dqs} + T_{dqo}(\beta)L_{sr} \left( T_{dqo}(\beta)^{-1} \right) \vec{i}_{dqs}$$ (11)

$$\vec{\varphi}_{dqr} = T_{dqo}(\beta)L_r T_{dqo}(\beta)^{-1} \vec{i}_{dqr} + T_{dqo}(\beta)L_{sr}^T \left( T_{dqo}(\beta)^{-1} \right) \vec{i}_{dqr}$$ (12)

The voltage and flux equations can be simplified by [22,23], which gives

$$v_{ds} = R_s i_{ds} + \frac{d\varphi_{ds}}{dt} - \omega \varphi_{qs}$$ (13)

$$v_{qs} = R_s i_{qs} + \frac{d\varphi_{qs}}{dt} + \omega \varphi_{ds}$$ (14)

$$v_{dr} = R_r i_{dr} + \frac{d\varphi_{dr}}{dt} - (\omega - \omega_r) \varphi_{qr}$$ (15)

$$v_{qr} = R_r i_{qr} + \frac{d\varphi_{qr}}{dt} + (\omega - \omega_r) \varphi_{dr}$$ (16)
The impedance matrices without a zero component can be represented as follows [22]:

\[
\begin{align*}
\varphi_{ds} &= (L_{rs} + \frac{3}{2} L_M) i_{ds} + \frac{3}{2} L_M i_{dr} \\
\varphi_{qs} &= (L_{rs} + \frac{3}{2} L_M) i_{qs} + \frac{3}{2} L_M i_{qr} \\
\varphi_{dr} &= (L_{rs} + \frac{3}{2} L_M) i_{dr} + \frac{3}{2} L_M i_{ds} \\
\varphi_{qr} &= (L_{rs} + \frac{3}{2} L_M) i_{qr} + \frac{3}{2} L_M i_{qs}
\end{align*}
\]

The transformation removes the time dependence for impedance and reduces the model’s complications. Since the flux linkages per second are required to solve the electrical equations, their dependency on the synchronous speed must also be considered [22]. Conversely, the electrical equations are calculated using the flux linkage per second \( \Psi = \varphi \omega \) as follows [21]:

\[
\begin{align*}
\Psi_{ds} &= -X_s i_{ds} + X_M i_{dr} \\
\Psi_{qs} &= -X_s i_{qs} + X_M i_{qr} \\
\Psi_{dr} &= -X_r i_{dr} + X_M i_{ds} \\
\Psi_{qr} &= -X_r i_{qr} + X_M i_{qs}
\end{align*}
\]

We calculated the reactance \((X_s, X_r, X_M)\) as follows:

\[
\begin{align*}
X_s &= \omega (L_{rs} + \frac{3}{2} L_M) \\
X_r &= \omega (L_{qr} + \frac{3}{2} L_M) \\
X_M &= \frac{3}{2} \omega L_M
\end{align*}
\]

Similarly, the stator and rotor’s active and reactive power consumption can be determined as follows [24]:

\[
\begin{align*}
P_s &= \frac{3}{2} (v_{ds} i_{ds} + v_{qs} i_{qs}) \\
P_r &= \frac{3}{2} (v_{dr} i_{dr} + v_{qr} i_{qr}) \\
Q_s &= \frac{3}{2} (v_{qs} i_{ds} - v_{ds} i_{qs}) \\
Q_r &= \frac{3}{2} (v_{qr} i_{dr} - v_{dr} i_{qr})
\end{align*}
\]

Considering that \( Tm = Te \), the electromagnetic torque \( Te \) and the input torque \( Tm \) combine to form a homogeneous steady-state system.

\[
Te = \frac{3}{2} p L_M (i_{qs} i_{dr} - i_{ds} i_{qr})
\]

where \( p \) denotes the poles pair.

2.2.2. Behavior of the DFIG under Unbalanced Conditions

Normally, the stator flux has a constant magnitude \( \varphi_s \) and rotates at synchronous speed \( \omega \), whereas the back emf is relatively small. However, the situation changes adequately during voltage dips due to a change in stator flux magnitude, which directly influences the rotor emf. This causes significant transients in both the voltage and current waveforms [1]. Additionally, the DFIG outputs exhibit considerable undamped oscillations.
at the start and after the fault is cleared, which may be detrimental to the drivetrain and power electronic converters [5]. We considered the synchronous reference frame while estimating the impact of unbalanced grid conditions. The following equations can be used to compute the stator and rotor voltages and fluxes in the d-q reference [25]:

\[ v_{sdq} = R_s i_{sdq} + j \omega_s \Psi_{sdq} + \frac{1}{\omega_s} \frac{d \Psi_{sdq}}{dt} \]  
\[ v_{rdq} = R_r i_{rdq} + j \omega_r \Psi_{rdq} + \frac{1}{\omega_r} \frac{d \Psi_{rdq}}{dt} \]  
\[ \Psi_{sdq} = L_s i_{sdq} + L_M i_{rdq} \]  
\[ \Psi_{rdq} = L_r i_{rdq} + L_M i_{sdq} \]

The rotor voltage is calculated using the rotor current and flux from Equations (34)–(36):

\[ v_{rdq} = R_t i_{rdq} + j \omega_r L_t i_{rdq} + \frac{L_t}{\omega_r} \frac{di_{rdq}}{dt} + E_b \]  

\[ R_t, L_t \] are defined as the transient resistance and inductance, respectively, as follows:

\[ R_t = \left( R_s L_s^2 + R_s L_M^2 \right) / L_s^2 \]  
\[ L_t = \left( L_s L_r - L_M^2 \right) / L_s \]

In Equation (37), \( E_b \) represents the back-emf, which significantly affects both the rotor current and the Dc-link voltage. The magnitude of \( E_b \) is determined by the stator flux and voltage and may be computed as follows:

\[ E_b = \frac{L_M}{L_s} (v_{rdq} - j \omega_r \Psi_{sdq} - \frac{R_s}{L_s} \Psi_{sdq}) \]

When a grid fault occurs, the stator voltage rapidly decreases in magnitude, but stator flux does not follow the voltage change [26]. Consequently, the rotor circuit experiences an extra instantaneous flux to maintain stator flux continuity. Hence, the stator flux during a voltage dip can be calculated as follows:

\[ \Psi_{sdq}(t) = \Psi_s^+ + \Psi_s^- e^{-2 \omega_b t} + (\Psi_{in}^+ - \Psi_s^+ - \Psi_s^-) e^{-\sigma t} e^{-j \omega_b t} \]

The symbols +, −, and \( in \) represent the positive sequence, negative sequence, and initial value, respectively. The first and second parts correspond to the steady-state positive and negative sequence fluxes, respectively, while the third describes the natural flux. Each of the three fluxes affects the magnitude of the induced back-emf in the rotor circuit. The rotor back-emf caused by these fluxes may be expressed as

\[ E_b = j \frac{L_M}{L_s} (s \Psi_s^+ - (2 - s) \Psi_s^- e^{-2 \omega_b t} - \omega_r (\Psi_{in}^+ - \Psi_s^+ - \Psi_s^-) e^{-\sigma t} e^{-j \omega_b t} \]

According to the above equation, the negative sequence component oscillates at double the grid’s frequency. Therefore, catastrophic unbalanced voltage sags with large percentages of negative sequence voltage may result in large back-emfs.

2.3. Rotor Side Converter Control

The RSC must simultaneously regulate the generator in sub-synchronous and super-synchronous states, requiring a more advanced regulating system than a conventional grid-side converter (GSC) control system. Further, such converters must be able to respond to the output power patterns of WTgs, thus managing the system’s power factor. Aside from these considerations, the primary objective of RSC is to maintain a constant rotor speed
regardless of variations in wind speed. It is possible to control the power flows in the DFIG with the rotor current content [22]. The voltage equations $v_{ar}$, $v_{br}$, and $v_{cr}$ are described as follows:

\[ v_{ar} = v_r \sin(\omega t + \phi_{gr}) \]  
\[ v_{br} = v_r \sin(\omega t + \frac{2\pi}{3}\phi_{gr}) \]  
\[ v_{cr} = v_r \sin(\omega t - \frac{2\pi}{3}\phi_{gr}) \]

This study employs voltage-oriented control (VOC) for controlling the RSC. The rotor current’s $dr$ and $dq$ components control the amount of active and reactive power, which can be expressed as $P_s \propto i_{dr}$ and $Q_s \propto i_{qr}$. With VOC, the d-axis of the synchronous reference frame (SRF) aligns with the stator-voltage vector. Therefore, $v_{qs} = 0$, $v_{ds} = v_s$. Equation (21) can be simplified as follows [21]:

\[ 0 = -X_s i_{ds} + X_M i_{dr} \]  
\[ i_{ds} = \frac{X_M}{X_s} i_{dr} \]  
\[ \Psi_{qs} = \frac{v_s}{\omega_S} = |\Psi_s| \]  
\[ |\Psi_s| = X_s i_{qs} + X_M i_{qr} \]  
\[ i_{qs} = \frac{|\Psi_s|}{X_s} - \frac{X_M}{X_s} i_{qr} \]

Using the values of $i_q$ and $i_d$ as inputs, the stator powers can be estimated as follows:

\[ P_s = -\frac{3}{2} \frac{X_M}{X_s} v_s i_{dr} \]  
\[ Q_s = -\frac{3}{2} \left( \frac{|\Psi_s|}{X_s} - \frac{X_M}{X_s} i_{qr} \right) v_s \]

According to the above equations, both powers are handled by varying the rotor $d$ or $q$ currents.

2.4. Grid-Side Converter Control

The DC-link voltage oscillates due to active power fluctuations, forcing capacitors to charge and discharge frequently. These voltage oscillations shorten the life of the capacitor and cause power losses. Furthermore, such oscillations generate harmonic components on the grid, which may cause excessive current strains, power quality issues, and significant power losses [10]. Hence, the primary function of the GSC is to maintain a constant DC link voltage regardless of variations in wind speed or grid disturbance. In addition, it also supports the grid voltage with reactive power. Transient currents may occur due to the phase difference between the GSC and the grid voltage. Such transients are prevented by synchronizing GSCs with the grid. Hence, it will provide a steady-state alternative current to the rotor circuit to ensure flux stability. The study employed VOC to control GSCs and regulate the active/reactive power of GSCs and grids. The voltage equations can be derived as [21],

\[ v_{gd} = R_i g_d + L_i \frac{d i_{gd}}{d t} + \omega L_{igq} - U_{cd} \]  
\[ v_{gq} = R_i g_q + L_i \frac{d i_{gq}}{d t} + \omega L_{igd} + v_{cq} \]
The subscripts $g$ and $c$ represent the voltage and current of the grid/converter, respectively. The power equations of the GSC are determined as follows:

$$P_g = \frac{3}{2} (v_{gd}i_{gd} + v_{gq}i_{gq})$$  \hspace{1cm} (55)$$

$$Q_g = -\frac{3}{2} (v_{gd}i_{gq} + v_{gq}i_{gd})$$  \hspace{1cm} (56)$$

As $v_{gd} = |\vec{v}_s|$ and $v_{gq} = 0$, Equation (45) becomes,

$$P_g = \frac{3}{2} (v_{gd}i_{gd})$$  \hspace{1cm} (57)$$

$$Q_g = -\frac{3}{2} (v_{gd}i_{gq})$$  \hspace{1cm} (58)$$

Conversely, the DC-Link energy is denoted by the following expression:

$$W_{dc} = \int Pdt = \frac{1}{2}C_{dc}U_{dc}^2 = P_g - P_r$$  \hspace{1cm} (59)$$

Figure 2 demonstrates an appropriate design for a DC-Link, which exhibits the multiple variables of the system. Current flowing through converters can be calculated as follows:

$$U_{dc}i_{os} = v_{gd}i_{gd}$$  \hspace{1cm} (60)$$

$$v_{gd} = \frac{1}{2\sqrt{2}}m_1U_{dc}$$  \hspace{1cm} (61)$$

$$i_{os} = \frac{1}{2\sqrt{2}}m_1i_{gd}$$  \hspace{1cm} (62)$$

$$C\frac{dU_{dc}}{dt} = i_{os} - i_{or}$$  \hspace{1cm} (63)$$

In this equation, $m_1$, $i_{or}$, $i_{os}$ represent the modulation index, rotor-dc link current, and grid-dc link current, respectively.

$$i_{gd} = \frac{2\sqrt{2}}{3m_1}C\frac{dU_{dc}}{dt}$$  \hspace{1cm} (64)$$

This leads to the conclusion that the voltage control is susceptible to the converter’s current $i_{gd}$. 

![DC-Link model](image-url)
3. Fault Ride through Control Circuit Design

3.1. Proposed Circuit Topology

The single-line diagram used in this study is shown in Figure 3. An MSTFCL is coupled with the DFIG to prevent excessive currents and back-EMFs during symmetrical and asymmetrical fault conditions. The system also includes a DC-chopper, which protects the DC-Link against voltage surges during grid disturbances. In this configuration, integrated 9 MW DFIG-based WTs are connected to an infinite bus with a rated voltage of 575 V. The grid also contains a 150 MVA synchronous generator with an output voltage of 13.8 kV and a source frequency of 60 Hz. The DFIG output voltage is increased from 575 V to 25 kV using step-up transformers, and the point of common coupling (PCC) voltage is adjusted using another step-up transformer. The detailed specifications of the suggested scheme are shown in Table 1.

![Schematic of proposed system.](image)

**Figure 3.** Schematic of proposed system.

**Table 1.** Parameters of DFIG.

| Parameters                        | Values            |
|-----------------------------------|-------------------|
| DFIGs rated power                 | 9 MW              |
| Inertia constant                  | 0.685 pu          |
| DC-Link voltage                   | 1150 V            |
| Stator voltage                    | 575 V             |
| Mutual inductance                 | 2.5 pu            |
| Syn speed                         | $2 \pi 60$        |
| Pitch controller gain             | 150               |
| Turn ratio                        | 1                 |
| Magnetizing resistance            | 0.264 Ω           |
| Magnetizing inductance            | 0.0004 H          |
| Stator-rotor leakage inductance   | 0.18 pu, 0.16 pu  |
| Stator/rotor leakage resistance   | 0.023 pu, 0.016 pu|

3.1.1. Modified Switch Type Fault Current Limiter Design

The components of the MSTFCL are depicted in Figure 3: A snubber circuit ($C_f$, $R_c$), a limiting inductor ($L_d$), a limiting resistor ($R_l$), a bridge circuit ($D_1$–$D_6$), and a semiconductor switch ($S_d$). The semiconductor switch ($S_d$) is normally open, bypassing the limiting inductor and resistor. When a grid fault is detected, the $S_d$ is turned off, and the snubber capacitor initially performs two functions: First, it stores excess energy, and second, it protects the $S_d$ against voltage spikes. Using Kirchhoff’s voltage law (KVL), the stator voltage $v_s = v_m \sin(\omega t + \theta)$ can be represented as follows:

$$v_m \sin(\omega t + \theta) = R_T I_c(t) + L_T \frac{dI_c(t)}{dt} + \frac{1}{C_f} \int I_c(t) dt$$  \hspace{1cm} (65)
\[ v_c(t) = \frac{1}{C_f} \int I_c(t) dt + v_i \]  

Similarly, the current through the capacitor can be determined as

\[ I_c(t) = \frac{v_m}{Z_T} \sin(\omega t + \vartheta - \varphi) + I_{n,r}(t) \]  

where \( I_{n,r}(t) \) and \( Z_T \) denote the system’s natural response and total impedance, respectively. Depending on the system’s resistance, capacitance, and inductance values, the above-indicated system may have underdamped, overdamped, or critically damped natural responses. In this situation, the snubber resistor is eleven times larger than the rotor resistor, resulting in an overdamped system response; hence, the system’s natural response can be expressed as

\[ I_{n,r}(t) = A_1 e^{s_1(t-t_i)} + A_2 e^{s_2(t-t_i)} \]  

\[ Z_T = \left( R_T^2 + \left( \frac{1}{\omega C_f} \right)^2 \right)^{1/2} \]  

\[ \varphi = \tan(\omega T - \frac{1}{\omega C_f}) / R_T \]  

where \( s_1, s_2 \) denote natural frequencies, and after solving Equation (68), the natural frequencies can be determined as follows:

\[ s_{1,2} = \frac{-R_T \pm \sqrt{R_T^2 - 4L_T/C_f}}{2L_T} \]  

When Equation (71) is compared to the generalized equation, the damping coefficient \( (\beta) \) and natural frequency \( (\omega_0) \) may be represented as [27]

\[ s_{1,2} = -\beta \pm \sqrt{\beta^2 - \omega_0^2}, \beta = R_T/2L_T, \omega_0 = 1/\sqrt{L_T C_f} \]  

Given the initial conditions, we can calculate \( A_1 \) and \( A_2 \) as follows [27]:

\[ A_1 = -\frac{v_m}{2Z_T \sqrt{\beta^2 - \omega_0^2}} \left[ s_2 \sin(\omega t + \vartheta - \varphi) + \omega \cos(\omega t + \vartheta - \varphi) \right] - \frac{v_m \sin(\omega t + \vartheta - \varphi)}{2L_T \sqrt{\beta^2 - \omega_0^2}} - v_i \]  

\[ A_2 = -\frac{v_m}{2Z_T \sqrt{\beta^2 - \omega_0^2}} \left[ s_2 \sin(\omega t + \vartheta - \varphi) + \omega \cos(\omega t + \vartheta - \varphi) \right] - \frac{v_m \sin(\omega t + \vartheta - \varphi)}{2L_T \sqrt{\beta^2 - \omega_0^2}} - v_i \]  

When the \( C_f \) voltage reaches its limit, the limiting inductor and resistor are entirely inserted into the circuit to cut off the current through the diode bridge. The limiting inductor and the resistor, which relieved the strain on the DFIG WTs, limited the current fault level. During both symmetrical and asymmetrical faults, the rotor voltage reaches its maximum amplitude as follows:

\[ v_{r,\text{sym, max}} = L_m / L_{T1} \times v_s(s(1 - d) + (1 - s)d) \]  

\[ v_{r,\text{asy, max}} = L_m / L_{T1} \times v_s(0.5 - (1.5 - 2/s)) \]
where \( s, d, \) and \( L_m \) represent the generator slip, depth of voltage dip, and mutual inductance, respectively. \( L_{T1} \) represents the combined stator \( (L_s) \) and limiting inductance \( (L_i) \). \( L_{T1} \) exceeds \( L_m \), so the maximum voltage can be maintained within acceptable limits.

### 3.1.2. DC Chopper Design

The DC-Chopper is a protective device that uses a resistor to short-circuit the DC link if the grid voltage drops. Figure 2 depicts the parallel connection of the DC copper at the DC link, which comprises a chopper resistor \( (R_d) \), a freewheeling diode \( (D_f) \), and a semiconductor switch \( (S_{d1}) \). When the switch \( S_{d1} \) is closed, the \( D_f \) protects the \( S_{d1} \) from high-voltage surges. During regular operation, the switch is open to bypass the resistor; however, when an excessive voltage is detected, the switch closes, enabling the additional energy to be dissipated across the resistor and thereby limiting the voltage at DC-Link. The DC chopper regulates the fault current through the chopper resistor by controlling the duty cycle \( D \) of the semiconductor switch. When the voltage across the DC link exceeds the threshold voltage \( (U_{DC-T}) \) during a fault condition, the equation for \( D \) can be expressed as [28].

\[
D = \left( k_{pce} + \frac{k_{ice}}{s} \right) \left( V_{DC-I} - U_{DC-T} \right), \quad 0 \leq D \leq 1 \tag{77}
\]

where \( k_{pce} \) and \( k_{ice} \) are the PI controller coefficients. \( V_{DC-I} \) is the instantaneous DC link voltage across the capacitor.

### 3.2. Control Scheme Design

#### 3.2.1. SMC Controller

SMC is a more advanced control technology that provides a robust method for obtaining the desired performance even when the system is subject to disturbances and uncertainty [29]. The high-gain feedback of SMC reduces the complexity of the system when compared to nonlinear techniques. Reduced state space designs may provide resilience, ease of installation, and efficient elimination of perturbations, all desirable characteristics. Two stages are involved in developing SMC, such as (a) designing the sliding surface and formulating it and (b) refining the control law. As part of the design process, a hypersurface is created first to enumerate requirements and then to drive the system state trajectory. Following this, a control law is developed to optimize the system’s trajectory and maintain its position on the hypersurface. The state space equations can be expressed in the following way [30]

\[
\dot{x} = A(x, t) + B(x, t) \cdot u(x, t) \tag{78}
\]

where \( x, A(x, t)B(x, t) \) and \( u \) denote the state vector, two nonlinear random functions, and the control vector, respectively. Equation (68) can be used to define the sliding surface or hypersurface

\[
SS \, (x, t) = \left( \frac{d}{dt} + \tau \right)^{n-1} \cdot e \tag{79}
\]

where \( e = x_a - x, x_a = [x, \dot{x}, \ldots, \dot{x}^{n-1}]^T \) are anticipated vectors, \( x = [x, x, \ldots, x^{n-1}]^T \) is the state vector, and \( e = [e, \dot{e}, \ldots, e^{n-1}]^T \) is the error vector. Lyapunov’s solution satisfies the convergence criterion by maintaining a stable and attractive surface.

\[
V = \frac{1}{2} S(x)^2 \tag{80}
\]

The Lyapunov principle predicts that if \( V \) is less than one, the system’s path will be attracted to the hypersurface, remaining until it asymptotically returns to its initial position.

\[
S(x) \dot{S}(x) \leq 0 \tag{81}
\]
Accordingly, the control law that meets the antecedent requirements is:

$$
\begin{align*}
    u &= u^a + u^c \\
    u^c &= -k_p \text{sgn}(\sigma(x,t))
\end{align*}
$$

(82)

where $u$, $u^a$, $u^c$, and $k_p$ denote the control vector, appropriate control vector, correction factor, and controller gain. When the system parameters have reached the hypersurface, the corresponding control maintains its position.

$$
\text{sgn}(\varnothing) = \begin{cases} 
1 & \varnothing > 0 \\
0 & \varnothing = 0 \\
-1 & \varnothing < 0
\end{cases}
$$

(83)

The controller represented by Equation (72) is robust and waterproof to parameter changes and perturbations, but the $\text{sgn}$ function, which is used, causes chattering along the hypersurface. Such significant input variations may be avoided by setting up a border condition with the width of $\varepsilon$. Hence, Equation (72) may be rewritten by substituting $\text{sat}(\sigma(t)/\varepsilon)$ for $\text{sgn}(\sigma(t)/\varepsilon)$.

$$
u^c = -k_p \text{sat}(\sigma(x,t))
$$

(84)

where $\varepsilon > 0$,

$$
\text{sat}(\varnothing) = \begin{cases} 
\text{sgn}(\varnothing) \varnothing \geq 0 \\
\varnothing \varnothing < 0
\end{cases}
$$

(85)

DFIG is regulated by surfaces that measure the difference between the intended and actual signal transmitted.

### 3.2.2. PI Controller

Even though novel control approaches have been developed from a conventional PI controller, it continues to have a wide variety of applications in power systems, particularly WTs, provided the PI-controlled variables are appropriately tuned [31]. Hence, it is a popular choice for industrial and computational modeling because of its simplicity and long life expectancy [32]. Model estimations, pattern designing, and parameter modification are necessary for a PI controller to analyze the output signals accurately. A model estimate involves altering a system’s output values at a specified sample frequency and then utilizing the results to produce an accurate prototype. Following model identification, the controller design should be optimized to reduce chattering issues while demonstrating resilience to load disturbances and parameter changes.

$$
v(t) = v_p(t) + v_i(t)
$$

(86)

$$
v(t) = k_p e(t) + \frac{k_i}{T_i} \int e(t)dt
$$

(87)

$$
e(t) = SP - PV
$$

(88)

where $e(t)$, $SP$, and $PV$ are the error signal, setpoint, and process variable, respectively. The gain of a PI controller can be determined by applying a Laplace transform on Equation (77).

$$
V(s) = k_p E(s) + \frac{k_i}{s}
$$

(89)

$$
V(s) = E(s) \left( k_p + \frac{k_i}{s} \right)
$$

(90)

Notably, the error signal will act as an input, causing the controller’s output to fluctuate. By simplifying Equation (80), we obtain
\[
\frac{V(s)}{E(s)} = k_p \left( 1 + \frac{k_i}{k_p s} \right)
\]  
(91)

\[
\frac{V(s)}{E(s)} = k_p \left( 1 + \frac{1}{\tau_g s} \right)
\]  
(92)

where \( \tau_g = \frac{k_p}{k_i} \) is the controller’s gain.

4. Results and Discussion

This part discusses the interaction between DFIG-based WTs and the power system in case of a grid fault. Two scenarios are evaluated to demonstrate the effectiveness of the proposed approach. Based on scenario one, the system is subjected to a balanced three-phase symmetric fault. The outcomes are compared for the reference value without a control scheme, the SMC, and PI controllers. The suggested system is assessed for the line-line-to-ground (LL-G) asymmetrical fault type in scenario two. In both scenarios, grid disturbances are expected to occur on the 30-km transmission line that connects the step-up transformers to the distribution system. The faults had a length of 150 ms, and we assumed that the wind speed remained constant during the entire simulation period. In both scenarios, five essential factors are investigated for symmetrical and asymmetrical faults, and the findings have been compared for the SMC and PI controllers.

4.1. Symmetrical Grid Fault

The symmetrical fault is the most catastrophic failure in a power system, in which all phases are shorted to one another or the ground. Even though these disturbances can carry a significant amount of current, they are relatively rare. In this scenario, a three-line-to-ground (LLLG) fault is introduced on the transmission line that runs between the step-up transformers, and the findings are simulated. Figures 4–8 compare the simulation results for the reference value, SMC controller, PI controller, and without control schemes, represented by green, red, blue, and black lines, respectively. According to Figure 4, a grid failure caused the DC-Link voltage to rise to 1.8 kV when the SMC controller was applied. However, this voltage stayed unchanged at 1.14 kV throughout the fault and restoration periods. When using the PI controller, the voltage rises to 1.19 kV, and oscillations can be detected up and down in the frequency spectrum. Similarly, both SMC and PI controllers can maintain voltage drops of 78 percent, as shown in Figure 5, demonstrating the effectiveness of DFIG’s reactive power compensation. Even though the rotor current remains equal throughout the restoration time, the proposed SMC-based system exhibits superior control over the PI controller during the fault phase, as seen in Figure 6. Compared to the PI controller, the SMC controller controlled the rotor current to 0.3 pu rather than 0.35 pu. Since the electromagnetic torque was negative in Figure 7, this demonstrates that the DFIG reduces transmission system stresses and improves grid power stability. The findings reveal that the maximum value for SMC was \(-1.18\) pu, whereas the comparable value for the PI controller was \(-1.4\) pu. As can be seen in Figure 8, the SMC controller dominates performance and keeps the stator flux amplitude at approximately 0.32 pu, decreasing the back-EMF influence.

4.2. Asymmetrical Grid Fault

When such failures occur, which is relatively prevalent in power systems, each phase is affected negatively, resulting in several harmonic components. Consequently, the waveforms are imbalanced, resulting in the formation of large oscillations in the frequency spectrum. In this scenario, an LL-G fault is applied on a 30-km transmission line for 150 ms to evaluate the effects on various parameters. The LL-G is estimated to be responsible for 20 percent of overall power transmission failures, the second-highest cause after a line-ground fault. The effects on the DFIG essential factors and the connection between SMC and PI simulated outcomes are shown in Figures 9–13. In the event of an LL-G failure,
the DC-Link’s voltage approaches 1170 V for SMC and 1200 V for the PI controller, with
the latter attaining slightly elevated values, as shown in Figure 9.

![Figure 4. DC link voltage during symmetrical fault.](image1)

![Figure 5. Terminal voltage during symmetrical fault.](image2)

![Figure 6. Rotor current during symmetrical fault.](image3)

![Figure 7. Electromagnetic torque during symmetrical fault.](image4)
4.2. Asymmetrical Grid Fault

When such failures occur, which is relatively prevalent in power systems, each phase is affected negatively, resulting in several harmonic components. Consequently, the waveforms are imbalanced, resulting in the formation of large oscillations in the frequency spectrum. In this scenario, an LL-G fault is applied on a 30-km transmission line for 150 ms to evaluate the effects on various parameters. The LL-G is estimated to be responsible for 20 percent of overall power transmission failures, the second-highest cause after a line-ground fault. The effects on the DFIG essential factors and the connection between SMC and PI simulated outcomes are shown in Figures 9–13. In the event of an LL-G failure, the DC-Link's voltage approaches 1170 V for SMC and 1200 V for the PI controller, with the latter attaining slightly elevated values, as shown in Figure 9.

Figure 7. Electromagnetic torque during symmetrical fault.

Figure 8. Stator flux during symmetrical fault.

Figure 9. DC link voltage during asymmetrical fault.
Figure 10. Terminal voltage during asymmetrical fault.

Figure 11. Rotor current during asymmetrical fault.

Figure 12. Electromagnetic torque during asymmetrical fault.
Nonetheless, when the system is operated without the control scheme, the voltage reaches 1390 V and fluctuates throughout the fault, as shown in Figure 10. Similarly, the voltage drops are maintained at 47, 49, and 70% for SMC, PI, and without a controller, respectively, indicating that both controllers have contributed to minimizing flux linkage disturbances. While both controllers’ rotor currents fluctuate during the LL-G fault, the SMC’s overall performance improves throughout the fault phase, as shown in Figure 11. SMC’s rotor current peaked at 0.24 pu, whereas the values for the PI controller and without a control scheme were approaching 0.29 pu and 0.39 pu, respectively. Conversely, both controllers maintained an appropriate torque amplitude, as seen in Figure 12, with SMC reaching 0.1 pu and PI attaining 0.2 pu, respectively. In contrast, the value reached positive 1.0 pu and negative 2.3 for a system without a control strategy.

Similarly, a comparison of flux amplitudes showed that the SMC and PI. Similarly, Figure 13 shows that both SMC and PI controllers experience oscillations; however, the magnitude of fluctuations is higher for PI. The proposed FRT technique can significantly control stator flux even with a negative sequence component and reduce overcurrent in the rotor circuit. Hence, the DFIG transitory response’s undamped oscillations are eliminated, and the FRT’s capabilities are enhanced.

5. Conclusions

This paper presents a hybrid approach combining MSFTCL with DC chopper techniques to address the problem of FRT in DFIG-based wind turbines during grid faults. The simulations are performed to assess the viability and effectiveness of the proposed system. SMC and PI controllers were used to achieve the desired performance, and the results were compared with reference values and without a control scheme. Further, two scenarios are discussed, which are (i) when the system is exposed to a balanced 3-phase symmetric fault and (ii) the most catastrophic failure on a transmission line (LL-G asymmetrical fault type). The results conclude that the proposed method supplies voltage to the reference value during symmetric and asymmetric grid failures to maintain the dynamic stability in the DFIG control system. Further, the simulation results show that the back EMF is kept to an absolute minimum, and the variations of electromagnetic torque during faults are significantly reduced. Thus, the proposed hybrid system can also regulate the reactive power efficiently while maintaining a constant DC-link voltage. Simulation results indicate that the hybrid strategy provides superior performance for all critical parameters with the advanced controller. The proposed FRT strategy is anticipated to be a major solution for improving control flexibility and operational stability in future grids with higher wind-power penetration. Moreover, the proposed strategy may also be applied to other forms of electronic power interfaced sources, such as solar energy. Although the performance of

Figure 13. Stator flux during asymmetrical fault.
Author Contributions: Conceptualization, S.S., R.A., F.M. and Z.U.; data curation, H.S., B.A. and Z.U.; formal analysis, R.A., H.S. and Z.U.; investigation, S.S., B.A. and F.M.; methodology, R.A., Z.U. and F.M.; project administration, F.M., B.A. and H.S.; resources, F.M.; software, R.A., Z.U. and B.A.; validation, S.S., R.A. and H.S.; visualization, R.A. and F.M.; writing—original draft, S.S., R.A., H.S. and Z.U.; writing—review and editing, B.A. and F.M. All authors have read and agreed to the published version of the manuscript.

Funding: This research received no external funding.

Institutional Review Board Statement: Not applicable.

Informed Consent Statement: Not applicable.

Conflicts of Interest: The authors declare no conflict of interest.

References

1. Jabbour, N.; Tsionsmas, E.; Mademlis, C.; Solomin, E. A Highly Effective Fault-Ride-Through Strategy for a Wind Energy Conversion System with a Doubly Fed Induction Generator. IEEE Trans. Power Electron. 2020, 35, 8154–8164. [CrossRef]

2. Moheb, A.M.; El-Hay, E.A.; El-Fergany, A.A. Comprehensive Review on Fault Ride-Through Requirements of Renewable Hybrid Microgrids. Energies 2022, 15, 6785. [CrossRef]

3. Asghar, R.; Ullah, Z.; Azeem, B.; Aslam, S.; Hashmi, M.H.; Rasool, E.; Shaker, B.; Anwar, M.J.; Mustafa, K. Wind Energy Potential in Pakistan: A Feasibility Study in Sindh Province. Energies 2022, 15, 8333. [CrossRef]

4. Asghar, R.; Rehman, F.; Ullah, Z.; Aman, A.; Iqbal, K.; Nawaz, A.A. Modified Switch Type Fault Current Limiter for Low-Voltage-Ride-through Enhancement and Reactive Power Support of DFIG-WT under Grid Faults. IET Renew. Power Gener. 2020, 14, 1481–1490. [CrossRef]

5. Kerrouche, K.D.E.; Wang, L.; Bossche, A.V.D.; Draou, A.; Mezouar, A.; Boumediene, L. Dual Robust Control of Grid-Connected DFIGs-Based Wind-Turbine-Systems under Unbalanced Grid Voltage Conditions. In Stability Control and Reliable Performance of Wind Turbines; IntechOpen: London, UK, 2018. [CrossRef]

6. Jerin, A.R.A.; Kaliannan, P.; Subramaniam, U.; El Moursi, M.S. Review on FRT Solutions for Improving Transient Stability in DFIG-WTs. IET Renew. Power Gener. 2018, 12, 1786–1799. [CrossRef]

7. Yifan, X.; Aimin, A.; Yingying, Z.; Wei, C. Design and Implementation of Crowbar Circuits Combined with Chopper Circuits for LVRT in Wind Farms. J. Phys. Conf. Ser. 2020, 1639, 012040. [CrossRef]

8. Shen, Y.-W.; Liang, L.-Q.; Cui, M.; Shen, F.; Zhang, B.; Cui, T. Advanced Control of DFIG to Enhance the Transient Voltage Support Capability. J. Energy Eng. 2018, 144, 04018009. [CrossRef]

9. Inci, M.; Bayindir, K.C.; Tümay, M. The Performance Improvement of Dynamic Voltage Restorer Based on Bidirectional Dc–Dc Converter. Electr. Eng. 2017, 99, 285–300. [CrossRef]

10. Meral, M.E.; Çelik, D. Mitigation of DC-Link Voltage Oscillations to Reduce Size of DC-Side Capacitor and Improve Lifetime of Power Converter. Electr. Power Syst. Res. 2021, 194, 107048. [CrossRef]

11. Çelik, D.; Meral, M.E. Voltage Support Control Strategy of Grid-Connected Inverter System under Unbalanced Grid Faults to Meet Fault Ride through Requirements. IET Gener. Transm. Distrib. 2020, 14, 3198–3210. [CrossRef]

12. Hamdan, I.; Ibrahim, A.M.A.; Nouralleen, O. Modified STATCOM Control Strategy for Fault Ride-through Capability Enhancement of Grid-Connected PV/Wind Hybrid Power System during Voltage Sag. SN Appl. Sci. 2020, 2, 364. [CrossRef]

13. Rehman, F.; Azeem, B.; Mehmoond, C.A.; Ali, S.M.; Khan, B.; Rehman, F. Control of Grid Interfaced Doubly Fed Induction Generator (DFIG) Using Adaptive SMC Improved with LVRT Enhancement. In Proceedings of the 2016 International Conference on Frontiers of Information Technology (FIT), Islamabad, Pakistan, 18–20 December 2017; pp. 336–341. [CrossRef]

14. Mehmoond, F.; Ashraf, N.; Alvarez, L.; Malik, T.N.; Qureshi, H.K.; Kamal, T. Grid Integrated Photovoltaic System with Fuzzy Based Maximum Power Point Tracking Control along with Harmonic Elimination. Trans. Emerg. Telecommun. Technol. 2022, 33, e3856. [CrossRef]

15. Roy, T.K.; Mahmud, M.A.; Islam, S.N.; Amanullah, M.T.O. Direct Power Controller Design for Improving FRT Capabilities of DFIG-Based Wind Farms Using a Nonlinear Backstepping Approach. In Proceedings of the 2018 8th International Conference on Power and Energy Systems (ICPES), Colombo, Sri Lanka, 21–22 December 2018; pp. 240–245. [CrossRef]

16. El Makrini, A.; El Karkri, Y.; Boukriss, Y.; El Markhi, H.; El Moussaoui, H. LVRT Control Strategy of DFIG Based Wind Turbines Combined the Active and Passive Protections. Int. J. Renew. Energy Res. 2017, 7, 1258–1269. [CrossRef]

17. Qais, M.H.; Hasanien, H.M.; Alghuwainem, S. Optimal Transient Search Algorithm-Based PI Controllers for Enhancing Low Voltage Ride-Through Ability of Grid-Linked PMSG-Based Wind Turbine. Electronics 2020, 9, 1807. [CrossRef]

18. Xiong, L.; Wang, J.; Mi, X.; Khan, M.W. Fractional Order Sliding Mode Based Direct Power Control of Grid-Connected DFIG. IEEE Trans. Power Syst. 2018, 33, 3087–3096. [CrossRef]
19. Shoaib, M.; Siddiqui, I.; Rehman, S.; Khan, S.; Alhems, L.M. Assessment of Wind Energy Potential Using Wind Energy Conversion System. *J. Clean. Prod.* **2019**, *216*, 346–360. [CrossRef]

20. Boubzizi, S.; Aber, H.; Hajjaji, A.E.; Chaabane, M. Comparative Study of Three Types of Controllers for DFIG in Wind Energy Conversion System. *Prot. Control Mod. Power Syst.* **2018**, *3*, 1–12. [CrossRef]

21. Ali, M.A.S.; Mehmood, K.K.; Baloch, S.; Kim, C.H. Modified Rotor-Side Converter Control Design for Improving the LVRT Capability of a DFIG-Based WECS. *J. Clean. Prod.* **2019**, *216*, 346–360. [CrossRef]

22. Wu, Y.K.; Shu, W.H.; Cheng, H.Y.; Ye, G.T.; Jiang, D.C. Mathematical Modelling and Simulation of the DFIG-Based Wind Turbine. In Proceedings of the 2014 CACS International Automatic Control Conference, Kaohsiung, Taiwan, 26–28 November 2014; pp. 57–62. [CrossRef]

23. Velpula, S.; Rajaram, T. A Simple Approach to MODELLING and Control of DFIG-Based WECS in Network Reference Frame. *Int. J. Ambient. Energy* **2020**, *43*, 2475–2485. [CrossRef]

24. Jabal Laafou, A.; Ait Madi, A.; Addaim, A.; Intidam, A. Dynamic Modeling and Improved Control of a Grid-Connected DFIG Used in Wind Energy Conversion Systems. *Math. Probl. Eng.* **2020**, *2020*, 1651648. [CrossRef]

25. Rahimi, M.; Parniani, M. Low Voltage Ride-through Capability Improvement of DFIG-Based Wind Turbines under Unbalanced Voltage Dips. *Int. J. Electr. Power Energy Syst.* **2014**, *60*, 82–95. [CrossRef]

26. Nadour, M.; Essadki, A.; Nasser, T. Improving Low-Voltage Ride-through Capability of a Multimegawatt DFIG Based Wind Turbine under Grid Faults. *Prot. Control Mod. Power Syst.* **2020**, *5*, 33. [CrossRef]

27. Ghanbari, T.; Farjah, E.; Naseri, F.; Tashakor, N.; Givi, H.; Khayam, R. Solid-State Capacitor Switching Transient Limiter Based on Kalman Filter Algorithm for Mitigation of Capacitor Bank Switching Transients. *Renew. Sustain. Energy Rev.* **2018**, *90*, 1069–1081. [CrossRef]

28. Zhu, J.; Deng, Z.; Yu, L.; Li, S.; Qu, C.; Qiu, W.; Adam, G.P.; Booth, C. A DC Chopper-Based Fast Active Power Output Reduction Scheme for DFIG Wind Turbine Generators. *IET Renew. Power Gener.* **2021**, *15*, 2480–2490. [CrossRef]

29. Sami, I.; Khan, B.; Asghar, R.; Mehmood, C.A.; Ali, S.M.; Ullah, Z.; Basit, A. Sliding Mode-Based Model Predictive Torque Control of Induction Machine. In Proceedings of the 2019 International Conference on Engineering and Emerging Technologies (ICEET), Lahore, Pakistan, 21–22 February 2019. [CrossRef]

30. Saad, N.H.; Sattar, A.A.; Mansour, A.E.A.M. Low Voltage Ride through of Doubly-Fed Induction Generator Connected to the Grid Using Sliding Mode Control Strategy. *Renew. Energy* **2015**, *80*, 583–594. [CrossRef]

31. El-Naggar, M.F.; Mosaad, M.I.; Hasanien, H.M.; AbdulFattah, T.A.; Bendary, A.F. Elephant Herding Algorithm-Based Optimal PI Controller for LVRT Enhancement of Wind Energy Conversion Systems. *Ain Shams Eng. J.* **2021**, *12*, 599–608. [CrossRef]

32. Zeb, K.; Islam, S.U.; Uddin, W.; Ullah, K.; Asghar, R.; Busarello, T.D.C.; Kim, H.J. DC-Link Voltage Regulation of Single-Phase Grid-Tied PV System Using Fuzzy-PI Controller. In Proceedings of the 2019 15th International Conference on Emerging Technologies (ICET), Peshawar, Pakistan, 2–3 December 2019. [CrossRef]