Finding Deep-Learning Compilation Bugs with NNSmith

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Abstract
Deep-learning (DL) compilers such as TVM and TensorRT are increasingly used to optimize deep neural network (DNN) models to meet performance, resource utilization and other requirements. Bugs in these compilers can produce optimized models whose semantics differ from the original models, and produce incorrect results impacting the correctness of downstream applications. However, finding bugs in these compilers is challenging due to their complexity. In this work, we propose a new fuzz testing approach for finding bugs in deep-learning compilers. Our core approach uses (i) light-weight operator specifications to generate diverse yet valid DNN models allowing us to exercise a large part of the compiler’s transformation logic; (ii) a gradient-based search process for finding model inputs that avoid any floating-point exceptional values during model execution, reducing the chance of missed bugs or false alarms; and (iii) differential testing to identify bugs. We implemented this approach in NNSmith which has found 65 new bugs in the last seven months for TVM, TensorRT, ONNXRuntime, and PyTorch. Of these 52 have been confirmed and 44 have been fixed by project maintainers.

1 Introduction
Deep learning (DL) compilers such as TVM [11], TensorRT [44], and TensorFlow XLA [1] are increasingly being used to deploy deep neural network (DNN) models in many different applications. These compilers optimize DL models to meet desired performance, energy, and resource requirements, allowing their use by interactive or safety-critical applications deployed on a variety of devices. However, as compiler implementations are complex, we must be vigilant about detecting bugs in these systems. Compiler bugs can result in crashes or generating an incorrect executable that produces different results than those intended by the user-specified input model. In this paper, we develop techniques to automatically find bugs in deep-learning compilers. Similar to prior work [29, 33, 57], we adopt a fuzzing and differential testing based approach: we generate random models, compile them using the compiler being tested, and then compare results obtained from the compiled model with those from a reference implementation. This basic approach faces two main challenges, which are not adequately addressed by prior work. First, how to generate structurally diverse and valid models? Deep-learning compilers express a model as a computation graph of tensor operators. For better test coverage, we must ensure model diversity, which requires us to generate graphs by combining operators in different ways. However, it is often invalid to connect two arbitrary operators together; invalid models are rejected and will not be compiled. For example, a compiler will reject any computation graph containing a MatMul (matrix multiplication) operator for which the number of rows in the first input differs from the columns for the second. Therefore, for test efficiency, our graph generation method must also ensure the validity of generated models. Second, given a compiled model, what weights/inputs should we use to run it for differential testing? Naively testing generated models with random or default weights/inputs can easily lead to floating point (FP) exceptional values, i.e., NaNs or infinities (Infs). In such cases, we cannot compare the compiled model with its reference implementation. Therefore, to enable equivalence checking, we must be able to generate computational inputs that can avoid FP exceptional values during model execution.

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We address these two challenges to build NNSmith, a tester for deep-learning compilers including TVM [11], ONNXRuntime [38], and TensorRT [44]. NNSmith adopts a three-step approach for finding bugs: (i) first, it automatically generates an arbitrary but valid computation graph expressing some model $M_I$; (ii) it then uses the compiler being tested to produce a compiled model $M_O$ from $M_I$, and a reference backend to produce an executable model $M_R$; and (iii) finally it generates random inputs which it passes to $M_O$ and $M_R$, and compares their outputs.

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† As deep-learning models use floating-point operations, a correctly compiled executable model can have close but not identical results as those of the input model. We do not regard this case as a bug.
NNSmith addresses the challenges of generating models and their inputs as follows.

**Generating diverse and valid computation graphs:** The computation graph expressing a deep-learning model consists of tensor operators with attributes attached to both the operators and graph edges. Operator attributes specify parameters such as kernel sizes that impact the operator’s semantics, while edge attributes are used to specify input and output tensor types\(^2\). Before proceeding with the actual compilation steps, deep-learning compilers check the validity of the input computation graph, e.g., whether an operator’s output tensor type matches the expected input tensor type of its downstream operators and whether an operator’s attributes are valid. In order to produce valid graphs, NNSmith aims to capture and ensure the type matching constraints of a computation graph during its generation. To do so, NNSmith requires that users provide operator specifications, which specify constraints that must be satisfied by an operator’s input tensors/attributes and guarantee about its output type, which it uses to check the validity of generated graphs. During NNSmith’s incremental graph generation, it inserts one candidate operator at a time by solving for the satisfiability of its type matching constraints given the existing graph. NNSmith uses an existing SMT solver [40] for constraint solving.

**Executing compiled models without FP exceptional values.** In order to meaningfully compare the outputs of a compiled computation graph with those from a reference implementation, NNSmith aims to select computation inputs (aka model weights and inputs) that do not result in NaNs or Infs during execution. Instead of random search, NNSmith uses gradient-guided search to efficiently find viable model inputs/weights for 98% of the generated models with negligible overhead.

In addition to addressing the two main challenges above, we designed NNSmith so it can be easily extended to add support for new operators or to work with other deep-learning compilers. We do so by providing users with a framework for writing operator specifications that are needed to ensure graph validity, and by providing a library of common patterns. In our experience, using this framework and library, users can write new operator specifications in a few lines of code. We evaluated the efficacy of our approach by using NNSmith to identify bugs in TVM, ONNXRuntime, TensorRT, and PyTorch.

Over the last seven months, NNSmith found 65 new bugs in these frameworks. Developers have confirmed 52 and fixed 44 of these bugs. Our coverage evaluation also shows that NNSmith outperforms the state-of-the-art fuzzer by 1.8× for ONNXRuntime and 1.08× for TVM in total branch coverage, as well as 32.7× and 10.8× respectively in unique branches.

## 2 Background

### 2.1 The DNN Computation Graph

DL frameworks represent a model’s underlying computation as a directed graph of tensor operators. In this work, we focus on DNN inference, where the graph captures the forward NN computation that given inputs generates predicted labels or outputs. For example, the model in Figure 1 is invoked by specifying its inputs (i.e., input variables \(\%x0\) and \(\%x1\)) and the model weights (i.e., input variable \(\%w0\)), and the DNN runtime computes the output tensor \(\%v2\) from these inputs.

In what follows, we use the term tensor type to refer to the shape and element type of a tensor. In the DNN computation graph, each edge is marked with the tensor type that corresponds to the output of the edge’s upstream operator, as shown in Figure 1. When instantiating an operator, model developers must specify certain additional attributes that dictate its output tensor type. For example, on line 4 in Figure 1, the Reshape operator takes \(\%v1\) as an input tensor and \([62, 62, 2]\) as an attribute indicating the output shape. Because each operator expects its input tensors to be of certain types, it is often invalid to connect two arbitrary operators together by an edge: e.g., the reshape operator on line 4 is valid if and only if its upstream operator’s output \(\%v1\) has 7688 elements \((62\times62\times2)\). This is akin to a “type checking error” in traditional programs. We say that a DNN computation graph is valid if and only if all operators in the graph are valid.

### 2.2 DL Compilers

State-of-the-art DL compilers turn a user-specified model, expressed as a DNN computation graph, into an executable implementation. As shown in Figure 2, DL compilers process an input DNN model in two stages during its compilation.

First, DL compilers need to convert an input computation graph into their own internal formats. For interoperability, DL training frameworks typically export trained models to a standardized and commonly supported format such as ONNX [2]. DL compilers take ONNX models as input and convert them to a compiler-specific Intermediate Representation (IR) that makes it easier to perform compiler optimization.

Second, DL compilers invoke various transformation passes which rewrite their input IR into a more efficient version. These passes include: graph-optimization passes that simplify the graph (e.g., constant folding) or fuse operators (merge Add and Softmax into BiasSoftmax [45]; low-level passes that optimize computation using arithmetic simplification and loop tiling/fusion, to reduce computational overheads.

---

\(^2\)A tensor’s type defines its shape and its elements’ data type.
Bug not triggered!

Listing 1: DNN patterns that can expose compiler bugs.

```python
def M0(): # M0 triggers a compiler crash bug!
    A = Conv2d(...) # shape: (1,2,1,48)
    B = Ones(1,1,48) # shape: (1,1,48)
    return A + B

def M1(): # bug NOT triggered!
    A = Conv2d(...) # shape: (1,2,1,48)
    B = Ones(1,2,1,49) # different shape: (1,2,1,49)
    return A + B

def M2(): # bug NOT triggered!
    A = Conv2d(...) # shape: (1,2,1,48)
    B = Ones(1,1,1) # trivial shape: (1,1,1)
    return A + B

def M3(): # M3 can trigger a semantic bug
    Y = Conv2d(Conv2d(...), ...)... # Bug lies here
    Y = Pow(Y, BIG_NUM) # bug not exposed due to Infs
    return Y
```

Challenge #3: Running compiled models to produce numerically valid output. Using arbitrary inputs and model weights to test a compiled model can result in FP exceptional values (i.e., NaNs and Infs) during execution. Such cases occur when the given inputs to some operator are outside of its expected domain, e.g., feeding Sqrt negative values results in NaNs, and feeding Pow large base or exponents results in Infs. Larger graphs are particularly prone to encountering FP exceptional values. For example, we have found that NaN/Inf occurs in 56.8% of 20-node models generated by NNSmith when using PyTorch’s default weight initializer. Previous testing frameworks did not consider these issues, and consequently up to 41% of their bug reports can be false-alarms because of the undefined/non-deterministic behaviors arising from the NaN/Inf [16].

Clearly we should not compare the output of a compiled model to those of the reference implementation if the results themselves contain NaN/Inf. What about those scenarios with “normal” final results (aka without any NaN/Inf) where some internal operator has produced FP exceptional values during graph execution? For example, operator ArgMax can output a normal FP value even though one of its upstream operators gives it NaN as input. It is a subtle requirement that we must also exclude these results from differential testing or risk incurring false positives in bug detection. This is because when handling FP exceptional values, otherwise semantically equivalent operators could produce different results. Therefore, to be able to test effectively, we must generate model inputs/weights that avoid FP exceptional values for all operators in the graph. Only then we refer to the model’s output as numerically valid. Otherwise, we might miss detecting bugs. As an example, Listing 1’s model M3 can trigger a semantic bug. However, this bug is not exposed because the execution results in Inf values which are not used for comparison.

### 3 NNSmith’s Design

**Overview of the approach.** Figure 3 shows an overview of NNSmith’s workflow. NNSmith generates valid random models to be compiled and executed. To ensure graph validity, NNSmith captures the “type checking” constraints of a graph as operator specifications (§3.1), and uses an SMT solver to generate valid operator attributes during graph generation (§3.2). To run a compiled model, NNSmith uses a gradient guided search procedure to find benign weights/inputs so that no FP exceptional values are produced at any step of the execution (§3.3). Finally, NNSmith compares the results obtained from multiple deep learning libraries and compilers to those from a reference implementation to identify bugs.

#### 3.1 Modeling DNN Operators

NNSmith generates random DNN models expressed as computational graphs by connecting together different operators. We aim to generate valid graphs that “type check”, i.e., graphs where each operator’s attributes and input tensor type meet requirements imposed by the compiler.

In order to generate valid graphs, we require users to provide operator specifications that explicitly state the compiler’s requirements for each operator and guarantees about its output. An operator’s specification codifies rules for checking validity and depends on its inputs and attributes: For example, the 2-D convolution operator (Conv2d) has several attributes, including a kernel, and takes an
image as input. A model that uses a `Conv2d` operator is valid if the input image is a rank-4 tensor that is larger than the kernel’s size.

While our implementation includes specifications for common operators (detailed in §4), we designed NNSmith to make it easy for users to write specification for additional operators. NNSSmith specifications are written using symbolic integers and abstract tensors. An abstract tensor is specified with its data type, rank and shape. As we will see later in §3.2, NNSmith uses an SMT solver to assign concrete integers to each symbolic integer during graph generation. NNSmith operator specifications provide input and output types (specified using abstract tensors), constraints on inputs and attributes, as well as transfer rules for each operator. Listing 2 shows the operator specification for a 2-D pooling operator (`Pool2d`), and we describe each of part below:

**Inputs and outputs.** An operator’s attributes are inferred from the inputs to its `__init__` function. The class variables `input_type` and `output_type` describe the input and output tensor types respectively (Lines 3 and 5). Programmers specify a list of tuples, each tuple says what data types can be used for an input (or provided as output). In the listing, the `Pool2d` operator accepts a single rank 4 tensor of 32-bit or 64-bit floats.

**Constraints.** The operator’s `requires` function (Line 10) returns constraints that its inputs and attributes must satisfy as a list of logical predicates. For example, among other constraints, the `Pool2d` operator requires that the kernel size should be greater than 0 (Line 12).

**Type transfer function.** The operator uses a `type transfer` function (Line 16) to specify how its output tensor relates to its inputs. For example, on Line 21, `Pool2d`’s `type transfer` function relates the shape of the operator’s output tensor to its kernel size (`self.kw` and `self.kh`) and its input shapes. Observe that the constraints output by the `type transfer` function are the input constraints on a downstream operator. These constraints are used to combine constraints from connected operators in a computation graph, and thus allow NNSmith to generate valid models.

### 3.2 Model Generation

Given a set of operator specifications, NNSmith generates models that are topologically diverse and whose operators use diverse attributes. Below we first detail our approach for generating diverse model topologies and then present our binning based approach to assigning diverse attributes.

**Generating computation graphs.** Our model generation algorithm is designed to ensure that the computation graphs it generates are fully connected, as is the case with most real-world models. Additionally, it is also designed so that it can generate a rich variety of models, including ones similar to existing multi-modal and multi-task models [3, 20, 42, 51] that can accept multiple inputs and/or produce multiple outputs.

NNSmith generates connected computation graph by extending an existing graph while maintaining connectivity. It does so by starting with a graph that contains a single `placeholder` node, and extending it by either (a) adding a new node whose input edges are connected to the output of an existing node (we refer to this as a forward insertion) or (b) replacing an existing placeholder node with an operator node whose input edges are connected to one or more placeholder nodes (we refer to this as backward insertion). In both cases, the node added by NNSmith is picked at random from the set of symbolic operator specification (op) it is provided. Placeholder nodes have one output, and at the end of the graph generation process they are replaced by input nodes or by weights (which are constant inputs). Algorithm 1 shows our graph generation algorithm. We
detail the steps taken when inserting a randomly selected operator (op) into an existing compute graph below:

1. Type matching: To insert op, NNSmith must first find a feasible insertion point in the current graph. When using forward insertion, this means finding an output edge in the graph whose constraints (as provided by the operator that node represents) satisfy op’s input constraints. Similarly, when using backward insertion, this means finding a placeholder node whose output is connected to node(s) whose input constraints are satisfied by op’s output constraints. To do so we need to check constraint satisfaction, and we use a SMT solver for this. Rather than invoking an SMT solver for all possible insertion points, we use a simple type matching heuristic to filter out nodes that are obviously infeasible because of incompatible data types or ranks. For example, when using forward insertion for Where (cond, T, F), type matching (Lines 7) will filter out any output edges which are not boolean.

2. Constraint solving: Next, NNSmith generates constraints for any feasible insertion points that have not been filtered out by its type matching heuristic, and uses an SMT solver to check their satisfiability. NNSmith caches constraints for the current model (in M-solving) to reduce constraint generation overheads, and uses incremental solving [5] to reduce time taken for checking constraints (Line 5).

3. Node insertion: As we stated previously, we use one of two approaches to insert nodes into the graph: forward insertion and backward insertion:
   - Forward insertion (Line 6) selects one group of plausible tensors (v) as the inputs of op (Line 8) and inserts op as their consumer (Line 10) if the insertion constraints are satisfiable (Line 9).
   - Backward insertion (Line 11) replaces an existing placeholder node with op. To do so it first determines a placeholder candidate (v) by matching op’s output type and the candidate’s type (Line 12–13). Next, it infers op’s input type from v. If op’s input type constraints can be satisfied for the candidate, NNSmith replaces the candidate with op and creates new placeholder nodes (of the inferred types) to act as op’s inputs (Lines 18 and 19).

Attribute binning. In addition to graph topological diversity, attribute diversity is also crucial as discussed in §2.3. We use the model generated by an SMT solver (Z3 [40] in our implementation) when checking satisfiability for the graph’s constraints to determine attributes. However, we found that when producing models for integer constraints, most SMT solvers pick boundary values, e.g., when given a constraint that requires tensor dimensions to be at least 1, all returned models have a dimension of 1. This limits attribute diversity and prevents us from finding bugs in practice. For example, we found that naively using the model returned by Z3 led us to usually using a batch size of 1, and that prevented us from finding some bugs (§2.3). We address this problem by adding binning constraints that confine each symbolic integer to a randomly chosen range. Adding binning constraints to the graph’s constraints can produce an unsatisfiable constraint system, leading to a situation where we can find no attributes for a valid graph. We avoid this situation by adding constraints only after a graph has been generated (§3.2) and only when doing so does not impact satisfiability.

Specifically, we group all (positive) integers exponentially into k bins with the i-th bin representing integers \( i \in [2^{k-1}, 2^k) \) for all

\[ i = 1, \ldots, k-1, \text{ and the last bin } [2^k, \infty). \] To sample a range \([l, r]\), we select a bin and sample two integers \((l, r)\) from the selected bin.\(^3\) We use bins with exponential ranges because, in practice, systems are more sensitive to changes in smaller values, e.g., changing a variable from 0 to 1 generally has larger effect on the output than changes from 30 to 31. Other fuzzers, including AFL [66], use a similar binning-based strategy.

### 3.3 Improving Numeric Validity with Gradients

Next, NNSmith generates inputs and weights that can be used to test the generated models. We initially considered using randomly selected numbers for this, however we found that the generated graphs produce FP exceptional values, including NaN (not a number) and Inf (infinite number). For example, when generating 20-operator graphs, FP exceptional values occur in 56.8% of generated graphs if we use random weight and inputs.

This is because some operators, which we refer to as vulnerable operators [63], produce real (e.g., \( \sqrt{x} \) returns NaN if \( x < 0 \)) or stable (e.g., \( x^y \) returns Inf for large \( x \) and \( y \)) results only for a subset of their input domain. If a vulnerable operator’s input lies outside of this domain, the operator outputs an FP exceptional value, which propagates through the model and impacts the model’s output, preventing us from comparing model outputs during differential testing. Table 1 lists examples of vulnerable operators we encountered in our evaluation.

One way to address this problem is to use additional heuristics to extend and fix vulnerable operators. For example, changing Div(x, y) to \( Div(x, \lfloor y \rfloor + e) \) renders the Div operator safe. However, this requires changing inputs to the operator, which limits graph diversity as discussed in §2.3. We thus propose an alternate approach, where we use a gradient-search algorithm to find inputs that ensure that the model’s output is numerically valid. Our approach is inspired

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\(^3\)If the last bin is chosen we use \( l = 2^{k-1} \) and \( r = \infty \). For all other bins \( i \), values are chosen by sampling \( x \sim U(i-1,i) \) and using \( [2^k] \).
We do so by first rewriting each predicate so that it is either of the values. We allow users to specify the loss function for each operator, and here we describe the approach we adopted to produce loss functions, and our input search algorithm (Algorithm 2) uses these inputs so that no operator in the graph produces an FP exceptional value. In each iteration, different in the region $U$, we use the closest left-derivative instead.

Table 1: Representative vulnerable operators.

| Operator          | Domain       | Violation | Loss functions                                                                 |
|-------------------|--------------|-----------|--------------------------------------------------------------------------------|
| $\arctan(X)$      | $|X| \leq 1$ | NaN       | $L(|X| - 1 \leq 0)$                                                             |
| $\text{Div}(X, Y)$ | $|Y| > 0$    | NaN       | $L(|Y| > 0)$                                                                   |
| $\text{Pow}(X, Y)$ | $X > 0$      | NaN/Inf   | $L(X > 0)$                                                                     |
| $\log_2(X)$       | $X > 0$      | NaN       | $L(X > 0)$                                                                     |

Table 2: Tensor inequality to loss function conversions.

| Tensor Ineq. | Loss function $L$ |
|--------------|-------------------|
| $f(X) \leq 0$ | $\sum_{x \in X} \max(f(x), 0)$ |
| $f(X) < 0$   | $\sum_{x \in X} \max(f(x) + \varepsilon, 0)$ |

At a high-level, our approach first associates a loss function with each operator; then, starting with random inputs, it iteratively refines these inputs so that no operator in the graph produces an FP exceptional value. In each iteration, NNSMITH identifies the first operator in the model that produces an FP exceptional value. It then uses the loss function associated with the operators to compute new model inputs and uses these for the next iteration. The algorithm terminates when no FP exceptional values are found. We provide details below:

**Loss functions for avoiding FP exceptional values.** NNSMITH requires that each vulnerable operator is associated with a set of loss functions, and our input search algorithm (Algorithm 2) uses these loss functions to update the model’s inputs to avoid FP exceptional values. We allow users to specify the loss function for each operator, and here we describe the approach we adopted to produce loss functions.

As we noted above, vulnerable operators produce valid outputs (i.e., outputs that are not FP exceptional values) when inputs are drawn from a particular domain, and this domain can be expressed (or approximated) by the conjunction of a few (usually one or two) inequality predicates on the operator’s input. We refer to this conjunction of inequality predicates as the operator’s tensor inequalities. For example, the $\text{sqrt}(X)$ operator takes a tensor $X$ as input, and it is numerically valid if and only if $X \geq 0$ (i.e., all elements of $X$ are positive). Similarly, the $\text{pow}(X, Y)$ operator’s numerically valid domain can be under-approximated as $X \geq 0 \land \text{Ylog}(X) \leq 40$, which requires that all elements of $X$ be positive to avoid NaNs (since $Y$ might contain fractional elements) and bounds $\text{Ylog}(X)$ to avoid outputs that are too large (and would be represented by infinity) $^4$. We associate a loss function with each predicate in an operator’s tensor inequality. We do so by first rewriting each predicate so that it is either of the form $f(X) < 0$ or $f(X) \leq 0$, and then use the formulas in Table 2 to convert this canonical form to a scalar loss. We show examples of the loss functions produced in this manner in Table 1. When an operator produces invalid outputs, the search algorithm picks which loss function to use by finding a predicate that is violated by the operator’s current input and using the loss function associated with it. For simplicity, our design assumes that a loss function is positive if and only if its associated predicate is violated by the operator’s input, allowing us to use any positive loss function associated with the operator (Line 8) without evaluating its associated predicate.

**Proxy derivative.** Given a vulnerable operator’s loss, NNSMITH uses gradient propagation to compute changes to the model inputs and weights. Doing so requires computing gradients (derivatives) for each operator in the graph (Line 9). However, some operators are either undefined for some inputs (e.g., $\text{Floor}$, $\text{Ceil}$, and other operators cannot be differentiated at integers) or have zero gradient in some region (e.g., $\text{ReLU}$ has gradient 0 for all negative inputs), and this prevents backward propagation. For these functions, we use Proxy Derivative Functions $^4$ instead of actual derivatives during gradient propagation.

Given an operator $F$ whose gradient is 0 in region $U$ we use $dF(x)/dx = \alpha$ as the derivative. We set $\alpha$’s sign based on the overall trend of the function, e.g., we use a positive $\alpha$ for $\text{ReLU}$ because it is monotonic. Similar to $\text{LeakyReLU}$, we choose a small magnitude for $\alpha$, thus avoiding large discrepancies between the proxy and the actual derivative. On the other hand, if the operator $F$ cannot be differentiated in the region $U$, we use the closest left-derivative instead.

Algorithm 2: Gradient-guided value search.

| Line | Code                                                                 |
|------|----------------------------------------------------------------------|
| 1    | Function GradSearch($\text{DNN M}$, learning rate $\mu$):            |
| 2    | $\langle X, W \rangle \leftarrow$ randomly initialized inputs and weights |
| 3    | OUTER: while time budget not exhausted do                            |
| 4    | for operator $F_i$ in topologicalSort($M$) do                        |
| 5    | $I_i \leftarrow$ input to $F_i$                                     |
| 6    | $O_i \leftarrow F_i(I_i)$                                           |
| 7    | if exists NaN/Inf $\in O_i$ then                                     |
| 8    | $L \leftarrow$ first positive loss functions of $F_i$               |
| 9    | $\langle X, W \rangle \leftarrow$ $\langle X, W \rangle - \mu L_{\langle X, W \rangle} I_i$ |
| 10   | if $\langle X, W \rangle$ not changed then // Zero gradients        |
| 11   | $\langle X, W \rangle \leftarrow$ randomly initialized values      |
| 12   | else if exists NaN/Inf $\in \langle X, W \rangle$ then              |
| 13   | Replace NaN/Inf with random values                                   |
| 14   | continue OUTER // Go to Line 3                                      |
| 15   | return $\langle X, W \rangle$                                       |
| 16   | raise failed to find viable $\langle X, W \rangle$                  |

Search process. The overall input search algorithm (Algorithm 2) proceeds as follows: Given a model $M$ and time budget $T$, we first randomly initialize inputs and weights ($X, W$) (Line 2) used by the first iteration of the search algorithm (Line 3). In each iteration, we find the first operator (in topological order, Line 4) that produces an FP exceptional value (Line 7). We use its loss function as an optimization objective (Line 8) to tune ($X, W$). If the gradient is neither zero nor a FP exceptional values then we move on to the next iteration (Line 14), otherwise we restart the search with a different initial value (Line 11 and 13). The algorithm throws an exception (Line 16) if it does not terminate within the time budget.

Because loss functions can vary by orders-of-magnitude across operators, we use Adam $^{[23]}$, an adaptive learning rate scheduling algorithm, to set the learning rate. We also reset the learning rate whenever we switch the loss functions used for optimization (as would be the case when an iteration finds a different operator). While this design can lead to a scenario where optimizing for one operator leads to another producing invalid outputs and vice-versa,
we found that this to be rare in practice (it occurred less than 1% of the time). We found that the most common reason for the search algorithm failing was that the model has no valid inputs.

4 Implementation

NNSmith is implemented in 5157 lines of Python code. Consistent with Algorithm 1, NNSmith outputs a symbolic graph and its SMT solution for being valid with the help of the Z3 [40] solver. We then concretize the symbolic graph by invoking the materialized PyTorch functors in the topological order, and export the model to the deployment-friendly ONNX [2] format using PyTorch’s exporter. We also use PyTorch to implement our algorithm for finding model inputs/weights that result in numerically valid output (§3.3).

Since DL compilers vary in operator and data type support, we infer the set of operators supported by the compiler being tested by trying to compile single-operator models with different data types. We use this information when generating graphs, so as to avoid “Not-Implemented” errors.

Our implementation uses PyTorch as a reference backend, and we compare the optimized model’s output to PyTorch’s output. If they disagree, we further compare it with results from the model compiled in “O0” mode to narrow down the cause of the bug (i.e., PyTorch or the compiler).

We wrote operator specifications in NNSmith using information obtained from framework documentation [50] and source code [46]. To simplify this task, we implemented several meta types including, unary/binary, reduce and broadcast that further reduce the amount of code needed to specify an operator. Using these, we found that we could implement 59 (out of 73) operator specifications within 4 lines of code. Furthermore, even for the most complex specification, which was for Conv2d, the require function has 9 inequalities and the type_transfer function is only 7 lines of code (formatted by PEP8 [52]) that can be quickly implemented in a few minutes. Furthermore, these specifications can be written once and then shared by all compilers that can accept ONNX models as input.

5 Evaluation

5.1 Experimental Setup

Metrics. We mainly target the following metrics for evaluation:

- **Code coverage**: Following prior fuzzing work [7, 8, 59], we trace source-level branch coverage for both the entire systems and their pass-only components, measuring 1) total coverage counts all hit branches; and 2) unique coverage counts unique branches (“hard” branches) that other baselines cannot cover.

- **Bug counting**: Following prior work [29, 59, 60], we use the number of independent patches as the number of detected bugs, except that we directly count the number of bug reports for closed-source systems (i.e., TensorRT) and unfixed ones.

Baselines. We compare NNSmith with both the state-of-the-art general DNN model generators (LEMON and GraphFuzzer) and fuzzer specifically designed for TVM (i.e., Tzer).

- **LEMON** [57] is a mutation-based model generator that mutates pre-trained Keras [17] models [58]. We convert Keras models into ONNX, to reuse the same differential testing and evaluation framework of NNSmith for fair comparison;

- **GraphFuzzer** [33] generates models by randomly connecting nodes from a block corpus. While LEMON is limited to shape-preserving unary operators, GraphFuzzer also supports non-unary operators by aligning input tensor shapes with slicing/padding and uses specific attributes to create shape-preserving instances for a few non-shape-preserving operators such as Conv2d. As its implementation is not open-sourced, for a fair comparison, we reimplemented its main design, e.g., stitching operators via padding/slicing, by replacing NNSmith’s specification-based node insertion.

- **Tzer** [29] is a coverage-guided and mutation-based fuzzer targeting TVM’s low-level IR. As DNNs generated by NNSmith can also be lowered to low-level IR, we compare Tzer with NNSmith to see if NNSmith can well cover low-level optimizations as Tzer.

Systems under test. NNSmith finds bugs in the following commonly used compilers:

- **ONNXRuntime** [45] (by Microsoft) is a graph-optimized DNN library for ONNX models, with over 130 source files on various graph optimizations. Like many runtime-based frameworks (e.g., PyTorch), though ONNXRuntime enables optimizations, the optimized graph will still be directly mapped into pre-compiled kernel functions (i.e., no code generation). To evaluate pass-only coverage, we only instrument files under onnxruntime/core/optimizer;

- **TVM** [11] is an end-to-end compiler for deploying DNNs on various platforms. In addition to 61 graph-level passes, TVM also performs up to 58 low-level optimizations to generate highly optimized target code. As a front-end, ONNX models will be converted into TVM’s graph-level IR to perform further optimization. TVM also has a much higher coverage upper limit (i.e., 116k) than ONNXRuntime (i.e., 65k) given its higher capability/complexity. For pass-only instrumentation, we consider files in all transforms folders.

- **TensorRT** [43] is a compiler and runtime highly optimized for NVIDIA GPUs and has been used by more than 350k developers across 27.5k companies. Since TensorRT is closed-sourced, we exclude it for coverage evaluation.

Experimental configurations. The testbed hardware configurations include: 1) Intel 10700k CPU (16 threads); 2) 64 GB memory (3200 Mhz); and 3) 2TB NVMe SSD. The operating system is Ubuntu 20.04 and targeted DL systems are compiled by Clang 14 under release mode. Except that we performed bug findings on various latest compiler versions over the last seven months, the default software versions used in evaluation are: ONNXRuntime v1.12 (c556f5), TVM v0.8 (9ab3a1), TensorRT v8.4 and PyTorch v1.13 (dev28220615).

When evaluating NNSmith, for Algorithm 1 we choose between forward and backward at every insertion randomly with equal probability. For the binning approach we use $k = 6$ bins (§3.2) to ensure a decent amount of attribute diversity while keeping the models small for fuzzing efficiency. For the gradient search, the initial learning rate is set to be 0.5, $\epsilon$ in the tensor inequality loss function is set to $10^{-10}$. While LEMON does not explicitly control the graph sizes (since it mutates existing models), we set the default generated graph size of NNSmith and GraphFuzzer to be 10. For coverage evaluation, we run fuzzers for 4 hours by default (following Tzer [29]) as we observe that code coverage curves generally converge before that point (e.g., as shown in Figure 4).
We first compare NNSMITH with our graph-level baselines (i.e., GraphFuzzer and LEMON) in terms of code coverage on TVM and ONNXRuntime (since TensorRT is closed-sourced). Figure 4 shows the coverage growth (y axis) over four hours (x axis). As shown in the Figure, NNSMITH beats the 2nd-best baseline (i.e., GraphFuzzer) by 1.8× on ONNXRuntime and by 1.08× on TVM. NNSMITH also achieves a decent percentage of total coverage, i.e., 17.9% on ONNXRuntime and 18.6% on TVM. Figure 5 further shows the number of generated test cases (x axis) within 4 hours and their accumulated total coverage (y axis, consistent to Figure 4). We can observe that with fewer test cases generated within the same time limit (mainly due to the overhead incurred by constraint solving), NNSMITH can still achieve higher coverage than the 2nd-best baseline (i.e., GraphFuzzer), indicating that NNSMITH can generate higher-quality test cases. It is also worth noting that LEMON is the slowest technique (e.g., up to 103× slower than NNSMITH). The reason is that LEMON mutates real-world models which can be very costly to run. We also have similar observations on the pass-only coverage. For example, as shown in Figure 6, NNSMITH outperforms GraphFuzzer by 1.85× on ONNXRuntime and 1.09× on TVM, showing its effectiveness for testing compiler transformation passes.

Another interesting observation is that NNSMITH’s coverage improvement on TVM is relatively smaller than that on ONNXRuntime (1.08× vs. 1.8×). This can be inferred by the difference in their fundamental designs. While ONNXRuntime implements over 130 optimization files targeting various specific graph patterns, TVM’s graph-level optimization is more general. For example, TVM’s operator fusion does not check specific operator types, but high-level operator properties such as injective, reduce, etc. Therefore, TVM’s coverage is less sensitive to the diversity of generated graph patterns.

To show the unique coverage for each studied technique, Figure 7 further breaks down the coverage sets of different fuzzers through Venn diagrams [61]. It shows that NNSMITH can achieve much higher unique coverage than the 2nd-best baseline (i.e., LEMON), e.g., 32.7× higher on ONNXRuntime and 10.8× higher on TVM. Despite that GraphFuzzer beats LEMON in total coverage, LEMON contrastingly outperforms GraphFuzzer in unique coverage. This is because LEMON has a different design from NNSMITH and GraphFuzzer: it mutates existing real-world models rather than generating new models from scratch, creating different model patterns. Please note that we omitted the unique coverage distribution analysis for pass-only files as it follows a similar pattern as Figure 7.

Figure 8 also compares NNSMITH against Tzer on TVM (as Tzer is specifically designed for TVM). On all TVM files, NNSMITH as a general graph-level fuzzer, can outperform state-of-the-art IR-level TVM-specific fuzzer by 1.4× in total coverage and 13× in unique coverage. Interestingly, while other graph-level baselines can at most exclusively cover 117 branches (i.e., LEMON in Figure 7b), Tzer has an unique coverage of 461. This is because Tzer directly manipulates low-level IR and some low-level operations are not exposed at the graph level. Moreover, in terms of pass-only coverage, NNSMITH outperforms Tzer even more, e.g., by 123× in unique coverage, demonstrating the superiority of graph-level fuzzing.

5.3 Ablation Study

Attribute binning. Figure 9 evaluates the effectiveness of attribute binning from the perspective of redundancy. Note that for implementation convenience we use the type system from TVM’s Relay IR
(parsed from ONNX models) to distinguish operators. It shows that within 4 hours, our binning approach achieves 2.07x unique operator instances, which are distinguished by input types and operator attributes.

Turning to system coverage, as shown in Figure 10, attribute binning improves the unique branch coverage by 2.2x for ONNXRuntime (Figure 10a) and 1.8x for TVM (Figure 10b). The total coverage improvement is relatively subtle (up to 2.3%) as the binning approach aims at covering the hard-to-hit branches whose proportion is expected to be minor. For example, simply importing TVM’s libraries with “import tvm” can hit 4015 branches but those branches are unlikely to have bugs.

**Gradient guidance.** Figure 11 evaluates the effectiveness of three input/weight searching methods: 1) **Gradient (Proxy Deriv.):** searching values via the full gradient-based approach; and 3) Gradient: method two without proxy derivatives. The experiment is conducted on three model groups, each of which contains 512 models of 10, 20 and 30 nodes respectively. Every model has at least one vulnerable operator. The **Sampling** baseline randomly samples values from the range of [1,9] which is empirically obtained selecting the best one from various tested ranges. For fairness, all methods run on the same groups of models with the same initial weights/inputs generated by the **Sampling** baseline. We assign different per-model searching time to each method and observe the ratio of models with numeric-valid inputs/weights (y-axis) over group-wide average searching time (x-axis). Figure 11 shows that our full gradient search improves the numerical validity of **Sampling** by 1.6-1.34x as the node size/difficulty grows. Also, the proxy derivative mechanism consistently helps our gradient search achieve higher success rate within shorter amount of time.

We also observe that searching time is negligible compared with model generation time, e.g., generating a 10-node model costs 83ms on average while our gradient-based searching only takes 3.5ms (4.2%) to achieve a success rate of 98%.

To date, NNSmith has uncovered 65 new bugs as shown in Table 3, where 52 have been **confirmed** and 44 have been **fixed.** Others are awaiting developer responses. Interestingly, in addition to compiler bugs, since NNSmith generates models through PyTorch ONNX exporter (§4), it also found 10 conversion bugs in PyTorch as a byproduct. Among the bugs we found, 15 are semantic bugs (result inconsistencies with PyTorch) and 50 are crash bugs (segmentation faults or exceptions). In total, there are 36 **transformation** bugs in ONNXRuntime (9), TVM (23) and TensorRT (4), accounting for the majority of the detected bugs. We found that most of these were optimization bugs: of the 20 fixed transformation bugs we found, 19 are **optimization** bugs (and the remaining one is an **unclassified** bug in TensorRT whose code is not available).

Of the 65 bugs we found, 43 bugs cannot be triggered using the algorithms implemented by LEMON or GraphFuzzer. Of these 25 are transformation bugs and 14 are conversion bugs. LEMON’s algorithms can trigger at most 16 of all bugs we found, while GraphFuzzer’s algorithms can trigger at most 22 of these. The core difference is that these prior approaches limit how non-shape preserving operators are connected in the graph, thus limiting graph diversity. In addition to this theoretical analysis, we also evaluated all tools by running them for four hours under the **same** setting (e.g., all on the **default** compiler versions as shown in §5.1). NNSmith triggers 38 unique crashes (by error messages) for ONNXRuntime and 13 for TVM, while LEMON triggers none and GraphFuzzer only triggers 1 crash for each of ONNXRuntime and TVM. For instance, the only ONNXRuntime bug detected by GraphFuzzer is the wrong fusion during the connection (element-wise and thus shape-preserving).

We next describe transformation and conversion bugs we found by illustrating prominent bug patterns with examples. We use ★ to denote bugs exclusively found by NNSmith.
Transformation bugs. Wrong expression simplification: We found 5 such bugs in ONNXRuntime (4) and TVM (1). One bug* happens in FuseMatMulScale when ONNXRuntime optimizes \((s_g \cdot A) \cdot (s_f \cdot B)\) to \((s_g \cdot s_f) \cdot (A @ B)\) for scalars \(s_g, s_f\) and matrices \(A, B\) where \(@\) denotes MatMul. However, when \(B\) is a \(1 \times 1\) matrix, ONNXRuntime can mistake matrix \(B\) as a scalar and rewrite it into \((s_g \cdot B) \cdot (A @ s_f)\), which is illegal as MatMul does not accept scalar inputs, causing a compiler exception. Prior work cannot use the non-shape-preserving MatMul operator, thus missing such bugs. Wrong expression simplification can also lead to semantic bugs, which may lead to wrong decisions in downstream AI applications, introducing security threats in critical scenarios (e.g., self-driving). For example, TVM has a buggy arithmetic optimization pass that switches the order of division and multiplication when rewriting \(\frac{x \mod y}{i} \times \mod z\), simplifying it to \((x \mod y) \mod z\) incorrectly.

Wrong layout analysis: Memory layout optimizations in TVM first rewrite layouts of the most beneficial operators (e.g., Conv2d) to efficient ones and then let remaining operators adapt changed layouts. We found 7 layout transformation bugs* in TVM, related to non-shape-preserving operators including broadcasting, reduce and slicing, which cannot be handled by prior work. For example, TVM can rewrite NCHW Conv2d to the SIMD-friendly NCHW4c layout (NCHW4c for short), by packing every 4 elements on \(C\) to the new sub-dimension (4c). However, using this optimization when the Conv2d is followed by a Slice operator whose stride for \(C\) is greater than one causes TVM to crash. GraphFuzzer cannot find this bug because to ensure shape alignment it always uses a stride of 1.

Integer type mismatch: Like traditional compilers (e.g., LLVM[24]), DL compilers leverage IRs to simplify optimization. IR type mismatch can happen if one pass makes wrong assumption for the IR being transformed. This is especially a pain for TVM: we found 8 bugs* stopping the compilation due to int32-int64 mismatch and one core TVM developer also admits that "TVM has a pretty fragile system of using 32 vs 64, I personally experienced it a few times before...". int64 is often introduced by shape-related operators (e.g., shape attributes of Reshape and BroadcastTo), which are not supported by prior work as they cannot handle those complicated shape constraints. Since our first bug report on such issues, there have been 12 fixes (7 from us and 5 from followers) within 5 months to resolve similar issues, one of which even blocked models in production. Interestingly, a bug we found also helped the developers find another bug that had previously been diagnosed as the outcome of a flaky test [32].

Conversion bugs. Wrong scalar handling: We found 6 crash bugs* triggered when TVM imports reduce-like operators with a scalar input. Since these operators are not shape-preserving, prior work cannot trigger such bugs. Similarly in PyTorch, when exporting Log2 with a scalar input, the exporter mistakenly sets its output to a rank-1 tensor instead of a scalar, causing a semantic issue. A few days after our report, developers identified 37 other similar bugs. Concurrently, NNSmith also identified a subset of these bugs, but in our evaluation we only treat the first bug (Log2) as one found by NNSmith.

Wrong broadcasting: Given a 3-way broadcasting \(\text{Where}(C_{1 \times 1}, T_{3 \times 1}, F_2)\), a TVM bug* causes the lower-ranked tensor \(F_2\) being ignored during shape inference, resulting in the wrongly inferred shape \(3 \times 1\), which should be \(3 \times 2\). This incurs a compiler failure in later phases.

Another TVM bug* causes an import failure to MatMul with single-rank broadcasting (one input is a vector) and notably, one month after our bug report, real-world TVM users also encountered such issues and pushed for its fix, showing that NNSmith can synthesize real-world model patterns. Prior work cannot detect them since their design are incompatible with broadcasting operations.

Data type mismatch: Operators’ data type supports vary by ONNX versions, which are often mishandled. For example, PyTorch can mistakenly (and silently) export Clip whose data type is int32 which is not supported by ONNX version 11. Such ill-formed models will be rejected by most compilers; however, it can also be mistakenly compiled by TensorRT, producing unexpected model outputs (i.e., semantic bugs in TensorRT), due to the wrongly interpreted attributes.

False alarms. As we discussed in the introduction, floating point semantics [47, 49] mean that even correct optimizations can lead to scenarios where an optimized model’s output differs from the reference output. Consequently, we check output equivalence by checking that the distance between model outputs, when scaled by their overall magnitude is small. However, in some cases valid optimizations can lead to a large relative change in outputs and produce false alarms. For example, optimizing a model where a Sigmoid operator produces the input to a Floor operator, can result in a scenario where the optimized output differs from the reference output by 1, causing NNSmith to falsely report a bug.

6 Related Work

Since the first proposal of fuzzing [39], various techniques have been proposed for fuzzing systems of different application domains [6, 14, 28, 30, 34, 35, 41, 55, 56, 70]. In this section, we mainly talk about the most closely related work in DL system fuzzing and compiler fuzzing.

6.1 DL System Fuzzing

In recent years, a number of techniques have been proposed to test DL libraries and compilers. As one of the first techniques in this direction, CRADLE [36] directly runs existing DNN models on different DL libraries to detect potential inconsistencies via differential testing. Later on, AUDEE [19] and LEMON [57] further extend CRADLE by applying search-based mutation strategies on the DNN models and their inputs to cover more library code. While AUDEE mainly focuses on mutating layer parameters and weight/input tensors, LEMON further applies more advanced mutation rules, including layer deletions/additions. Meanwhile, to ensure correctness of generated models, LEMON [57] only mutates type-preserving operators (or blocks of operators) from the real-world models, to avoid handling type constraints. However, there are many non-shape-preserving operator types, e.g., even the commonly used Conv2d cannot be completely handled by LEMON. More recently, GraphFuzzer [33] allows a slightly larger operator search space using padding/slicing to align unmatched tensor shapes and also specifically controls the attributes of shape-changing operator types to create shape-preserving instances (e.g., Conv2d with kernel size/stride of 1). However, this design still substantially limits model diversity (as demonstrated in §2.3). The very recent (and concurrent) Muffin work [18] shares a similar limitation as GraphFuzzer: it uses "reshaping" layers to align tensor shapes during model generation; in addition, Muffin focuses on finding gradient computation bugs in DL libraries rather than
DL compiler bugs. In this work, we aim to support more diverse/valid model generation for DL compiler fuzzing via a fundamentally different design powered by symbolic constraint solving [10] and gradient-driven search.

To complete DL system testing at the model/graph level, researchers have also proposed DL system fuzzing techniques focusing on directly generating or manipulating the low-level model IRs [29, 48]. TVMFuzz [48] aims to automatically generate arbitrary low-level IRs based on a set of predefined grammar rules for fuzzing the popular TVM compiler [11]. The more recent Tzer work [29] leverages coverage feedback to perform joint mutation of both the low-level IR and optimization passes for TVM. While Tzer has shown promising results over TVMFuzz, the low-level IR mutation adopted by Tzer can hardly test the graph-level optimizations widely adopted by various DL compilers (as shown in §5.2).

In recent years, researchers have also investigated techniques to fuzz each DL system API in isolation. Meanwhile, DL APIs are usually exposed in Python, a dynamically typed language, making it hard even to determine their argument types for test generation. Therefore, prior techniques, such as Predoo [68], require users to manually set up the function arguments, and can only be evaluated on a limited number of APIs. More recently, FreeFuzz [59] aims to address this challenge via dynamically tracing API executions from various sources (including library documents, developer tests, and real-world models), and further mutates the traced inputs for each API to test DL libraries. While such API-level testing techniques are adequate for testing first-generation DL libraries (§2.1), they can hardly find bugs in graph-level optimizations (e.g., 86% of the transformation bugs detected by NNSMITH require multiple operators to trigger).

6.2 Compiler Fuzzing

As one of the most widely studied compiler fuzzing approaches in the literature [36], grammar-based techniques (such as Csmith [64], jsfunfuzz [53], and LangFuzz [21]) aim to generate syntactically valid input programs acceptable by the underlying compilers. While effective, it is hard for grammar-based techniques to ensure the semantic correctness of the generated programs to cover deep code paths, and highly specialized analyses have to be employed for specific languages. Therefore, various mutation-based techniques [15, 25, 26, 54, 67] have also been proposed for fuzzing compilers via mutating existing seed input programs. Moreover, given the advances in DL, researchers have also proposed learning-based techniques for compiler fuzzing. DeepSmith [13] and DeepFuzz [31] directly leverage recurrent neural networks (RNNs) to generate test programs from scratch, while Montage [27] performs mutation-based fuzzing, and replaces code snippets of the seed programs with new code fragments generated by RNNs. More recently, researchers have also leveraged the advanced pre-trained language models (e.g., GPT [9]) for more powerful test program generation for compiler fuzzing [65]. Such existing compiler fuzzing techniques can be potentially applied to the low-level IRs (C-like) for fuzzing DL compilers [29]. However, they can be hardly directly applied for graph-level DL compiler fuzzing, and our study has also shown the superiority of NNSMITH over state-of-the-art IR-level DL compiler fuzzers.

7 Conclusion

NNSMITH is a tool for generating diverse and valid test cases for deep learning compilers. It creates abstract operator models to ensure the validity of the generated models, and further utilizes incremental graph generation and attribute binning to ensure its diversity. To avoid false alarms and bug escapes, NNSMITH leverages gradient search to find inputs that do not introduce NaN/Inf in the computation. NNSMITH is easily extensible to support new operators with few lines of code. Lastly, NNSMITH is implemented to generate models in the popular format ONNX and is readily applicable to any systems with ONNX support. To date NNSMITH has found 65 new bugs in TVM, TensorRT, ONNXRuntime, and PyTorch, 52 of which have been confirmed or fixed, demonstrating its effectiveness.

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