Rectification Performance of a Broadband RF-to-DC Rectifier on Wideband Pulses

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Abstract—For wireless power transfer and energy harvesting applications, an advantage of having a broadband rectifier is the coverage of a wide range of narrowband frequencies as well as wideband pulses. Many broadband rectifiers have been recently proposed, but their reported characteristics are mainly evaluated at discrete frequencies. Due to the nonlinear nature of rectifiers, such frequency domain characteristics may not directly correspond to the time-domain, especially when input signals are wideband pulses. In this letter, we report the performance evaluation of a broadband rectifier on wideband pulses with various pulse parameters. Here, we use our recently proposed broadband rectifier as a test device. Using Gaussian-modulated RF pulses with various pulse parameters (i.e., pulsewidth, pulse repetition interval, and power), the rectification performance is verified via simulation and measurement. The results demonstrate sufficient rectification efficiency even for short pulses under appropriate parameters, where a maximum rectification efficiency of 71% is obtained.

Index Terms—Broadband, efficiency, energy harvesting, rectifier, short pulse, time-reversal, wireless power transfer (WPT).

I. INTRODUCTION

FAR-FIELD wireless power transfer (WPT), which enables wireless powering of small, low-power devices in a range of applications [i.e., mobile, Internet of Things (IoT), sensors, and biomedical], has been a topic of considerable interest in recent years [1]–[3]. Although many WPT and energy harvesting scenarios are based on using narrowband signals [4]–[6], WPT based on wideband pulses (i.e., time-reversed pulses and ultrawideband pulses) has also been proposed [7]–[11].

Since successful far-field WPT depends on efficient rectification as well as effective delivery RF power, RF-to-dc rectifier plays an important role in determining the efficiency of WPT [12]–[18]. In particular, a broadband rectifier may not only be utilized in rectifying narrowband signals in a wide frequency range, but also in rectifying wideband short pulses.

While various broadband rectifiers with excellent bandwidth (BW) and rectification efficiency have been proposed [15]–[18], their reported performances were mainly evaluated at stepped discrete frequencies. However, due to the nonlinear nature of the rectifier, the rectification characteristics based on discrete frequencies are not linearly translated to time-domain wideband pulses. In this regard, for WPT based on wideband pulses, an accurate performance evaluation of a broadband rectifier using actual wideband signals is essential.

In this letter, we demonstrate the rectification performance of our recently proposed broadband RF-to-dc rectifier [15] on short pulses with various pulse parameters [i.e., pulsewidth (PW), pulse repetition interval (PRI), and power level] via simulation and measurement. In addition to demonstrating the rectification performance of the rectifier under test, it is hoped that the results from this letter will provide general insight into the behavior of a broadband rectifier with short pulses.

II. BROADBAND RECTIFIER AND SHORT PULSE EXPERIMENT

A recently developed broadband rectifier [15] is used as a test device in this work. This rectifier is based on a voltage doubler configuration, and the inductor connected to each SMS-7630 Schottky diode is the basis for broadband matching, as shown in Fig. 1(a). That is, an inductor in series with the diode cancels out the capacitance of the Schottky diode and generates multiple resonances, thereby enabling broadband matching to be achieved. Here, in the negative cycle of RF, charge is stored in the junction and package capacitance of the diode, so that the voltage doubler behavior is possible without the charging capacitor on the input side [15], [19].

Fig. 1(b) and (c), respectively, shows the reflection coefficient and rectification efficiency of the rectifier measured in a stepped frequency sweep. Here, an impedance BW (IBW) of 76% (0.61–1.36 GHz) is obtained, which leads to a wide efficiency BW (EBW) of 82.6% (where at least 50% of efficiency is maintained) at 5-dBm input power. A more detailed design and frequency-domain performance of the broadband rectifier can be found in [15].

In order to evaluate the rectification performance on wideband pulses, we choose a train of Gaussian-modulated RF pulses as our signal type. Various pulse parameters, including PW, PRI, and RF average input power level ($P_{\text{in,avg}}$), are used. The Fourier transform $G(f)$ of a single Gaussian-modulated RF pulse $g(t)$ is expressed as

$$G(f) = \frac{1}{\sigma \sqrt{2\pi}} e^{-\frac{(\sigma_0 f)^2}{2}}$$

(1)

where $f_0$ is the center frequency and $\sigma$ determines the pulse BW [20]. Since the rectifier under test has an IBW of 750 MHz centered around 1 GHz, $f_0$ is set to 1 GHz and BW is varied from 100 to 700 MHz at a 100-MHz increment to be completely included within the IBW of the rectifier.
Fig. 1. Broadband RF-to-dc rectifier tested in this work [15]. (a) Fabricated rectifier. (b) Reflection coefficient ($|S_{11}|$) of the rectifier through stepped frequency sweep at 5-dBm input power. (c) Measured rectification efficiency through stepped frequency sweep.

Fig. 2. Example Gaussian-modulated RF pulse (2.4-ns PW, 1-GHz center frequency, and 10-ns PRI). (Left) Time-domain. (Right) frequency-domain.

Fig. 2 shows an example of normalized $g(t)$ and $G(f)$. Here, BW is a $-3$ dB BW and PW is defined to be the reciprocal of BW. To prevent the repeated pulses from overlapping, the minimum value of PRI is set to be at least twice the PW (PRI$_{\text{min}}$ $\geq$ 2PW). Table I shows the pulse parameters used in the experiment. The PRI values are set between the minimum value defined above and 50 ns. For each given PW (BW) and PRI pair, the pulse amplitude is varied so that a set of average power ranging from $-20$ to 20 dBm are obtained. Setting the average power this way allows the same input power range as that used for discrete frequency measurements in [15], thereby providing a reasonable platform for comparison.

III. RECTIFICATION RESULTS

Using the abovementioned various input pulse parameters, rectification efficiency is obtained via simulation and measurement. The simulation is carried out with the Advanced Design System. The experiment setup (see Fig. 3) consists of an arbitrary waveform generator (Tektronix AWG7102) followed by an amplifier (Mini-circuits ZFL-2500VH+). Since the amplifier has a wide operating band (10-2500 MHz), which ensures the BW coverage all pulses tested. For each set of pulse parameters, the time-domain signal is monitored with an oscilloscope (Tektronix TDS6154C) to ensure the proper signal amplitude for a given average power level. A voltmeter (Mastech MS8261) is used to measure the output dc voltage of the rectifier. The rectification efficiency $\eta$ can be defined as the ratio of the output dc power and average power of RF input, that is,

$$\eta = \frac{P_{\text{dc}}}{P_{\text{in,avg}}} = \frac{V_{\text{dc}}^2}{R_L/P_{\text{in,avg}}}$$

where $P_{\text{dc}}$ and $V_{\text{dc}}$, respectively, are the dc power and dc voltage delivered to the load and $R_L$ is the load resistance of 1.4 kΩ optimized for narrowband signals while providing sufficient RC time constant even for the longest PRI of 50 ns.

Fig. 3. Short pulse rectification measurement setup.

Fig. 4 shows the simulated and measured $\eta$ versus the input average power and PRI for short pulses with different PWs. In order to make a visually effective comparison, different colors are used to represent the rectification efficiency at an increment of 10%. In the measured plots, there is a small portion of blank spots (white color), which indicate unmeasured pulse parameters due to the output power limitation of the amplifier, which corresponds to a maximum peak voltage of about 5.6 V. Therefore, the amplifier was limited to producing pulses with peak voltage less than this maximum value. In particular, at a given average input power, for shorter PW and longer PRI, the number of unmeasured pulses increases due to its higher peak voltage exceeding the maximum value. Nevertheless, the measured and simulated results in general show a good agreement exhibiting a similar trend. In both simulated and measured results, higher efficiency seems to occur with shorter PRIs. This can be explained by the fact that the shorter the pulse interval, the sparser the power spectrum becomes in the frequency-domain. In other words, as PRI is shortened, the power spectrum has sparser peaks with higher amplitudes and the signal effectively becomes narrowband with less frequency content, which enables more stable rectification.
Fig. 4. Rectification efficiency versus PRI and input power for various PWs: (a) 10, (b) 5, (c) 3.33, (d) 2.5, (e) 2, (f) 1.67, and (g) 1.43 ns.

Fig. 5. Measured maximum rectification efficiency for each PW.

At a given input average power, even though the peak voltage in the time-domain increases with longer PRIs to maintain the average power level, more frequency content (due to denser spectral peaks with lower amplitudes) causes rectification to be less efficient compared to the same pulse at lower PRIs. Moreover, a maximum efficiency seems to occur around an average input power ranging from 0 to 10 dBm depending on PW. The reason for this variance (of the average input power level at which a maximum efficiency occurs) is that depending on PW, the time-domain pulses have different shapes, which causes them to reach the diode breakdown differently from each other.

The maximum measured rectification efficiency for each PW is plotted in Fig. 5. These results clearly show that for each PW, a maximum rectification efficiency occurs with the shortest PRI (except for the 1.43-ns pulse) and at different input average power levels. It can be seen that the values of maximum efficiency are generally maintained greater than 50%, and a peak value of 71% occurs at 5-dBm average power for 5-ns PW. In particular, for shorter PW, maximum efficiency of greater than 50% is achieved even at a relatively low input average power (at 0 dBm). Considering a maximum rectification efficiency of 76% at 5-dBm input power for a narrowband signal [see Fig. 1(c)], the results in this study suggest that a rectification efficiency comparable to narrowband signals can be achieved for short pulses by selecting an appropriate PRI and power level. Furthermore, since the load resistance of the rectifier is originally optimized for narrowband signals, efficiency can be further improved by optimizing the load for short pulses.

IV. CONCLUSION

The rectification performance of a broadband RF-to-dc rectifier on wideband pulses is tested and evaluated. The overall results demonstrate that the rectifier characteristics obtained at discrete frequencies do not linearly translate to short pulses due to the nonlinear nature of the rectifier as expected, highlighting the importance of an accurate investigation of the rectifier using actual short pulses to properly evaluate its rectification performance on short pulses. By using Gaussian-modulated RF pulses as input signals, it was shown that a rectification efficiency greater than 50% can be maintained with a maximum efficiency of 71%. The results imply that sufficiently high rectification efficiency (comparable to that for narrowband input) can be achieved for short pulses under certain pulse conditions, validating the utility of a broadband rectifier for WPT based on wideband pulses.
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