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Miniaturizing transmon qubits using van der Waals materials

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Quantum computers can potentially achieve an exponential speedup versus classical computers on certain computational tasks, recently demonstrated in systems of superconducting qubits. However, the capacitor electrodes that comprise these qubits must be large in order to avoid lossy dielectrics. This tactic hinders scaling by increasing parasitic coupling among circuit components, degrading individual qubit addressability, and limiting the spatial density of qubits. Here, we take advantage of the unique properties of van der Waals (vdW) materials to reduce the qubit area by $>1000$ times while preserving the required capacitance without increasing substantial loss. Our qubits combine conventional aluminum-based Josephson junctions with parallel-plate capacitors composed of crystalline layers of superconducting niobium diselenide and insulating hexagonal-boron nitride. We measure a vdW transmon $T_1$ relaxation time of $1.06\,\mu s$, which demonstrates a path to achieve high-qubit-density quantum processors with long coherence times, and the broad utility of layered heterostructures in low-loss, high-coherence quantum devices.

Superconducting qubits form an attractive platform to build scalable quantum computers, as recently demonstrated in quantum processors consisting of large arrays of qubits (1). Achieving fault-tolerant quantum computation would provide a transformative technology in quantum simulation, chemistry, and optimization (2, 3). However, the industry-standard transmon qubit consists of Josephson junctions (JJ) shunted by capacitors with a large footprint of $\sim 10^5\,\mu m^2$. In order to acquire a sufficient capacitance, these capacitors are made of large planar electrodes without dielectric materials that can host two-level systems (TLSs), which are a major source of decoherence (4). While brute-force scaling using conventional transmons remains possible (5), there is growing interest in alternative approaches that could lay the foundation of next-generation quantum processors. Approaches such as protected qubits can decouple from
environmental noise (6), but improving the constituent materials can dramatically impact qubit technology by reducing the footprint and enhancing qubit coherence. As such, new material platforms for qubits are now actively explored (7–13).

Van der Waals (vdW) heterostructures are a promising material platform for quantum devices. Cleaved from pure bulk crystals, the surfaces of these heterostructures are pristine and atomically flat. They can be assembled into stacks with their layers held together by weak bonds without straining the crystals (14). This architecture allows for diverse properties of various vdW materials, such as gate-tunability (15, 16), high kinetic inductance (17), and giant thermal response to microwave photons (18, 19), which can be exploited for potential quantum applications (20). Here, we take advantage of the high-quality vdW heterostructures to fabricate compact vdW parallel-plate capacitors (PPC) using superconducting niobium diselenide (NbSe₂) and insulating hexagonal boron nitride (hBN). We observe quantum coherence of a transmon qubit that uses a vdW PPC as the shunt capacitor. Our result goes against the current trend of requiring large-footprint shunt capacitors in transmons (5), and demonstrates the potential of using crystalline materials to improve qubit coherence.

Fig. 1a shows schematically the fabrication process of the vdW PPC (13) (details in Supplementary Information). It is different from the conventional process for making high-quality vdW heterostructures (14) because qubits operate in the single-photon regime, where any dissipation or loss channel introduced by the materials or fabrication process can destroy quantum information. Therefore, we assemble each heterostructure within an inert atmosphere to avoid oxidation and contamination of the interfaces. Then, the vdW heterostructure is placed onto the qubit substrate with pre-patterned niobium resonators. To make a vdW transmon, we connect the vdW PPC in parallel with two Al-AlOₓ-Al JJs that form a superconducting quantum interference device (SQUID) loop for tuning of the qubit frequency with a magnetic flux generated by a bias current through an off-chip coil. A conventional qubit formed using a planar Nb ca-
pacitor and a single JJ is created at the same time for comparison. Fig. 1b schematically shows these elements, with the transmons capacitively coupled to the readout resonator (red) and drive line (green). Fig. 1c and 1d show optical micrographs of the assembled vdW PPC (109 µm² in area and 35 nm in thickness) and the vdW transmon, respectively. The optical image in Fig. 1e shows the dramatic size reduction enabled by the vdW PPC, which is about 1400 times smaller than the conventional planar shunt capacitor. For this study, we fabricated and measured two vdW transmons. We fully characterize one of them and present the data in the main text.

We perform spectroscopy experiments to characterize the coupling of the vdW qubit to its readout cavity. With high probing power, the cavity is unperturbed by the qubit and resonates at 6.9073 GHz with a linewidth of 290 kHz (Fig. 2a). Measuring under low power, we find the strong coupling with the qubit dispersively shifts the cavity frequency by 1.00 MHz (21). Fig. 2b shows how the flux-bias current modulates the cavity frequency. We perform qubit spectroscopy by applying a control signal at various frequencies to the qubit while probing the cavity nearly on resonance. Fig. 2c shows the transition frequency, $\omega_{01}/2\pi$, between the ground, $|0\rangle$, and excited, $|1\rangle$, states of the qubit at 5.2815 GHz. Using the two-photon transition frequency between the $|0\rangle$ and $|2\rangle$ states, i.e. $\omega_{02}/2\pi = 5.216$ GHz, we find the qubit anharmonicity, $\alpha/2\pi = -131$ MHz. $\alpha$ agrees with the charging energy of the qubit, $E_C$ ($-\alpha = E_C/h$ for a transmon with $h$ being the Planck constant), calculated using the PPC formula with an hBN dielectric constant of 4.4. Fig. 2d shows $\omega_{01}/2\pi$ versus flux-bias current. It has a maximum at $\approx 0.2$ mA, which is consistent with the flux dependence of the readout resonator in Fig. 2a and represents the sweet spot where the transmon is the least susceptible to flux noise. In 5-µA steps of flux-bias current, we observe no anticrossing in the qubit spectroscopy due to the coupling of the vdW transmon with coherent TLSs. From the dispersive shift and detuning from the readout cavity, $\Delta$, of 1.6258 GHz at the sweet spot, we find the qubit-resonator coupling, $g/2\pi = 40.3$ MHz. The Josephson energy, $E_J/h$, calculated from $\omega_{01}$, is 28.0 GHz. Our qubit
is in the transmon regime with \( E_J/E_C \simeq 214 \).

We demonstrate the quantum coherence of the vdW transmon by performing Rabi experiments (see Fig. 3a). By driving the qubit with a resonant pulse for a time, \( t_{\text{Rabi}} \), the vdW transmon Rabi oscillates coherently on the Bloch sphere between the ground, \( |0\rangle \), and excited, \( |1\rangle \), states before being measured using the dispersive shift of the resonator (21). The oscillation frequency, \( \omega_{\text{Rabi}}/2\pi \) is set by the drive strength, \( \Omega \), and drive detuning from the qubit frequency, \( \delta \omega_d \), as \( \omega_{\text{Rabi}} = \sqrt{\Omega^2 + \delta \omega_d^2} \). When performed at different excitation frequencies and a high pulse power, we obtain the data as shown in Fig. 3b. We quantify the qubit lifetime by measuring the \( T_1 \) energy relaxation time at the sweet spot (Fig. 3c). After applying an 80-ns-long \( \pi \)-pulse to excite the qubit from \( |0\rangle \) to \( |1\rangle \) and waiting for a time delay, \( t_{\text{delay}} \), the qubit state is read out. We repeatedly measure the exponential decay of excited state probability, \( P_{|1\rangle} \), to quantify the mean and fluctuations of \( T_1 \) over a period of 8 hours. The histogram (see inset) shows \( T_1 \) of 1.06 \( \mu s \) \( \pm \) 0.09 \( \mu s \) (standard deviation). Next, we quantify the qubit coherence by measuring the dephasing time \( T_2^* \) during a Ramsey experiment. Fig. 3d shows the qubit readout signal after the Ramsey pulse sequence of \( \pi/2-t_{\text{delay}}-\pi/2 \). The oscillation is due to \( \delta \omega_d \). We fit the data to a decay oscillation and obtain \( T_2^* \) of 1.67 \( \pm \) 0.20 \( \mu s \). Fig. 3e shows Ramsey oscillations at different qubit drive frequencies. Finally, we probe for evidence of excessive low frequency noise that may dephase the vdW transmon using a Hahn echo pulse sequence, which contains an additional \( \pi \)-pulse in the middle of the Ramsey sequence. Fig. 3f shows the qubit readout signal after the Hahn echo sequence. We obtain the echo dephasing time, \( T_{2E} \), of 1.72 \( \pm \) 0.27 \( \mu s \), that is nearly energy-relaxation limited, i.e. \( T_{2E} \approx 2T_1 \).

We can compare the performance of our vdW transmon with the conventional one fabricated on the same substrate. The \( T_1 \) and \( T_2^* \) of the conventional transmon are 11.5 \( \pm \) 0.4 \( \mu s \) and 10.5 \( \pm \) 0.6 \( \mu s \), respectively. Since both transmons are subjected to the same fabrication process and measurement setup environment, this result suggests that the JJs are not the limiting factor.
of the quantum coherence of our vdW transmon. On the other hand, based on finite-element simulations, only 87% of the capacitive energy of our vdW transmon design is confined internally to the PPC. This could result in degraded qubit performance through a finite qubit coupling with residue, external to the PPC, from the vdW stacking process or Al lift-off. If we assume the energy relaxation is entirely caused by the capacitive loss and use the equation, \(1/T_1 = \omega_{01} \tan \delta\), we can estimate the upper bound of the loss tangent, \(\tan \delta\), of the vdW PPC to be \(2.83 \times 10^{-5}\). This value is not the intrinsic \(\tan \delta\) of the hBN, but is approaching the measured values from PPCs using silicon as the dielectric \((\approx 10^{-6})\) \((22, 23)\). To extend qubit \(T_1\) relaxation, we will need to improve the fabrication process and further concentrate the electric field within the pristine interior of the vdW PPC.

Recently, experiments have taken a similar direction using the intrinsic JJ PPC to make the merged-element transmon (MET) \((12, 24)\). While both the MET and vdW transmon accomplish miniaturization, the METs demonstrated so far use amorphous insulators which are known to harbor TLSs. As the vdW heterostructure can also function as a high-quality JJ \((8, 25, 26)\), one can envision a future vdW-MET consisting of a crystalline vdW material as the tunnel barrier. Moreover, the thickness of the vdW insulator is defined by discrete atomic layers. This highly-uniform, crystalline tunnel barrier can potentially allow for a reduced TLS defect density and precise control of qubit frequencies, which can address the frequency-crowding problem \((27)\).

In the future, we plan to extend our result to further miniaturize quantum processors by reducing footprints of circuit components using, e.g. compact lumped-element resonators.

During the preparation of this manuscript, we became aware of a complementary work \((28)\).
Figure 1: Fabrication of vdW transmon and measurement schematics. (a) Fabrication procedures for the vdW parallel-plate capacitor (PPC). In clockwise direction: dry polymer-based stacking of vdW heterostructure by sequential pick-up of NbSe$_2$, hBN, and NbSe$_2$ flakes. Transfer of the heterostructure to a void within the pre-patterned Nb layer on a silicon substrate, deposition of Al contacts, and deposition of JJs and interconnects. (b) Schematic circuit diagram showing our frequency-tunable qubit (orange box) and fixed-frequency conventional qubit (teal box) as a control. Both qubits are coupled to coplanar-waveguide resonators for readout through a common bus line with low noise amplifiers (LNA) after circulators. (c) Optical micrograph of the NbSe$_2$-hBN-NbSe$_2$ heterostructure used to make the PPC (pink dashed line) with an area of 109 $\mu$m$^2$ and hBN thickness of 35 nm. (d) Optical micrograph of the vdW transmon formed by the SQUID loop (yellow) shunted by the vdW capacitor with capacitive coupling to the readout resonator (red) and control line (green). (e) Optical micrograph of both qubits in the same field of view, illustrating the miniaturization of transmon qubit by the vdW capacitor.
Figure 2: Spectroscopy of a vdW transmon (a) Readout-cavity resonance as a function of applied microwave power at the sweet spot of flux bias. (b) Readout-cavity resonance versus flux-bias current. (c) High power spectroscopy shows the $\omega_{01}$ and two-photon $\omega_{02}/2$ qubit transitions at the sweet spot of flux bias. (d) Qubit frequency, $\omega_{01}/2\pi$, as a function of flux tuning.
Figure 3: Quantum coherence of the vdW transmon at the sweet spot of flux bias. (a) Rabi oscillation vs. the pulse width, $t_{Rabi}$ near the qubit frequency. (b) Rabi oscillations at a high power for different excitation frequencies. (c) $T_1$ energy relaxation time measurement with the pulse control and measurement sequence (inset). The exponential fit yields the $T_1$ relaxation time. The inset histogram shows data from 8 hours of repeated measurements. (d) Ramsey experiment shows $T_{2^*}$ dephasing time. (e) Ramsey fringes at different drive tone frequencies. (f) Hahn echo experiment to obtain the echo dephasing time, $T_{2E}$. 
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Supplementary information

List of supplementary materials:

- Methods
- Figures S4-S5
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Methods

Flakes of hexagonal-boron nitride (hBN) and niobium diselenide (NbSe$_2$) of thickness about 35 and 20 nm, respectively, were exfoliated onto silicon substrates with 285 nm SiO$_2$ epilayers. The thickness of each hBN flake was measured using atomic force microscopy prior to stacking. Because the NbSe$_2$ is highly air sensitive, NbSe$_2$ exfoliation and assembly of the NbSe$_2$/hBN/NbSe$_2$ heterostructure (Fig. 1A) was performed inside the inert atmosphere of a nitrogen filled glovebox, using a polypropylene carbonate/Polydimethylsiloxane based dry pick-up and transfer process ($^{14}$). Since the loss in the polypropylene carbonate is one of the major causes of qubit relaxation, special care was taken to keep the NbSe$_2$-hBN-NbSe$_2$ interfaces clean. The heterostructure was then transferred to a 400 $\mu$m $\times$ 400 $\mu$m sized bare silicon landing site, on a high resistivity Si substrate coated with 200 nm thick niobium (Nb). Coplanar transmission lines for control and readout resonators, as well as coplanar capacitor pads for a secondary benchmark/control qubit were patterned into the Nb layer prior to the incorporation of vdW materials. E-beam lithography was used to define Ti/Al contacts to the two NbSe$_2$ flakes. In situ ion milling to remove the oxide that forms on the exposed NbSe$_2$ surfaces prior to deposition was crucial to achieving ultralow-loss contacts ($^{13}$). Al-AlO$_x$-Al JJs were fabricated using standard e-beam lithography and double-angle evaporation. Further steps of e-beam lithography and high quality Al deposition were performed to connect the Nb resonators and
ground plane to the contacts and to create superconducting interconnects.
Figure S4: Quantum coherence of the Conventional transmon (a) $T_1$ energy relaxation time measurement. (b) Ramsey experiment shows $T_2^*$ dephasing time.

Figure S5: Quantum coherence of the second vdW transmon device. (a) Rabi oscillations at a high power at different excitation frequencies. (b) $T_1$ energy relaxation time measurement.
Table S1: **List of vdW parameters.** Values from device S111 that is presented in the main text. $R_n$ is measured at room temperature from a JJ fabricated on the same qubit substrate. $I_c$ is inferred from Ambegaokar-Baratoff relation using $R_n$. $E_J$ is calculated using ω₀₁ and $α$ whereas $E'_J = Φ_0 I_c/2π$ is the Josephson energy calculated using $I_c$ with $Φ_0$ being the magnetic flux quanta. All quantities, if dependent on the flux-bias, are taken at the sweet spot.
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