Accelerating Convolutional Neural Networks via Activation Map Compression

Georgios Georgiadis
Samsung Semiconductor, Inc.
g.geor@samsung.com

Abstract
The deep learning revolution brought us an extensive array of neural network architectures that achieve state-of-the-art performance in a wide variety of Computer Vision tasks including among others, classification, detection and segmentation. In parallel, we have also been observing an unprecedented demand in computational and memory requirements, rendering the efficient use of neural networks in low-powered devices virtually unattainable. Towards this end, we propose a three-stage compression and acceleration pipeline that sparsifies, quantizes and entropy encodes activation maps of Convolutional Neural Networks. Sparsification increases the representational power of activation maps leading to both acceleration of inference and higher model accuracy. Inception-V3 and MobileNet-V1 can be accelerated by as much as $1.6 \times$ with an increase in accuracy of $0.38\%$ and $0.54\%$ on the ImageNet and CIFAR-10 datasets respectively. Quantizing and entropy coding the sparser activation maps lead to higher compression over the baseline, reducing the memory cost of the network execution. Inception-V3 and MobileNet-V1 activation maps, quantized to 16 bits, are compressed by as much as $6 \times$ with an increase in accuracy of $0.36\%$ and $0.55\%$ respectively.

1. Introduction
With the resurgence of Deep Neural Networks occurring in 2012 [33], efforts from the research community have led to a multitude of neural network architectures [20, 55, 59, 60] that have repeatedly demonstrated improvements in model accuracy. The price paid for this improved performance comes in terms of an increase in computational and memory cost as well as in higher power consumption. For example, AlexNet [33] requires 720 million multiply-accumulate (MAC) operations and features 60 million parameters, while VGG-16 [55] requires a staggering 15 billion MAC’s and features 138 million parameters. While the size and number of MAC operations of these networks can be handled with modern desktop computers (mainly due to the advent of Graphical Processing Units (GPU)), low-powered devices such as mobile phones and autonomous driving vehicles do not have the resources to process inputs at an acceptable rate (especially when the application demands real-time processing). Therefore, there is a great need to develop systems and algorithms that would allow us to run these networks under a limited resource budget.

There has been a plethora of works that attempt to meet this need via a variety of different methods. The majority of the approaches [18, 26, 29] attempt to approximate the weights of a neural network in order to reduce the number of parameters (model compression) and/or the number of MAC’s (model acceleration). For example, pruning [18] compresses and accelerates a neural network by reducing
the number of non-zero weights. Sparse tensors can be more effectively compressed leading to model compression, while multiplication with zero weights can be skipped leading to model acceleration.

Accelerating a model can also be achieved not only by zero-skipping of weights but also by zero-skipping input activation maps, a fact widely used in neural network hardware accelerators [2, 17, 31, 47]. Most modern Convolutional Neural Networks (CNN) use the Rectified Linear Unit (ReLU) [11, 16, 42] as an activation function. As a result, a large percentage of activations are zero and can be safely skipped in multiplications without any loss. However, even though the last few activation maps are typically very sparse, the first few often contain a large percentage of non-zero values (see Fig. 1 for an example based on the ResNet-34 architecture [20]). It also so happens that the first few layers process the largest sized inputs/outputs. Hence, a network would greatly benefit in terms of memory usage and model acceleration by effectively sparsifying even further these activation maps.

However, unlike weights that are trainable parameters and can be easily adapted during training, it is not obvious how one can increase the sparsity of the activations. Hence, the first contribution of this paper is to demonstrate how this can be done without any drop in accuracy. In addition, and if desired, it is also possible to use our method to trade-off accuracy for an increase in sparsity to target a variety of different hardware specifications.

Activation map size also plays an important role in power consumption. In low-powered neural network hardware accelerators (e.g. Intel Movidius Neural Compute Stick1, DeepPhi DPU2 and a plethora of others) on-chip memory is extremely limited. The accelerator is bound to access both weights and activations from the off-chip DRAM, which requires approximately 100× more power than on-chip access [21], unless the input/output activation maps and weights of the layer fit in the on-chip memory. In such cases, activation map compression is in fact disproportionately much more important than weight compression. Consider the second convolutional layer of Inception-V3, which takes as input a 149 × 149 × 32 tensor and returns one of size 147 × 147 × 32, totalling 1,401,920 values. The weight tensor is of size 32 × 32 × 3 × 3 totalling just 9,216 values. The total of input/output activations is in fact 150× larger in size than the number of weights. Compressing activations effectively can reduce dramatically the amount of data transferred between on-chip and off-chip memory, decreasing in turn significantly the power consumed. Thus, our second contribution is to demonstrate an effective (lossy) compression pipeline that can lead to great reductions in the size of activation maps, while still maintaining the claim of no drop in accuracy. Following the footsteps of weight compression in [18], where the authors propose a three-stage pipeline consisting of pruning, quantization and Huffman coding, we adapt this pipeline to activation compression. Our pipeline also consists of three steps: sparsification, quantization and entropy coding. Sparsification aims to reduce the number of non-zero activations, quantization limits the bitwidth of values and entropy coding losslessly compresses the activations.

Finally, it is important to note that in several applications including, but not limited to, autonomous driving, medical imaging and other high risk operations, compromises in terms of model accuracy might not be tolerated. As a result, these applications demand networks to be compressed losslessly. In addition, lossless compression of activation maps can also benefit training since it can allow increasing the batch size or the size of the input. Therefore, our final contribution lies in the regime of lossless compression. We present a novel entropy coding algorithm, called sparse-exponential-Golomb, a variant of exponential-Golomb [61], that outperforms all other tested entropy coders in compressing activation maps. Our algorithm leverages on the sparsity and other statistical properties of the maps to effectively losslessly compress them. This algorithm can stand on its own in scenarios where lossy compression is deemed unacceptable or it can be used as the last step of our three-stage lossy compression pipeline.

2. Related Work

There are few works that deal with activation map compression. Gudovskiy et al. [13] compress activation maps by projecting them down to binary vectors and then applying a nonlinear dimensionality reduction (NDR) technique. However, the method modifies the network structure (which in certain use cases might not be acceptable) and it has only been shown to perform slightly better over simply quantizing activation maps. Dong et al. [10] attempt to predict which output activations are zero to avoid computing them, which can reduce the number of MAC’s performed. However, their method also modifies the network structure and in addition, it does not increase the sparsity of the activation maps. Alwani et al. [3] reduce the memory requirement of the network by recomputing activation maps instead of storing them, which obviously comes at a computational cost. In [9], the authors perform stochastic activation pruning for adversarial defense. However, due to their sampling strategy, their method achieves best results when ~100% samples are picked, yielding no change in sparsity.

In terms of lossless activation map compression, Rhu et al. [52] examine three approaches: run-length encoding [53], zero-value compression (ZVC) and zlib compression [1]. The first two are hardware-friendly, but only achieve competitive compression when sparsity is high, while zlib

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1https://ai.intel.com/nervana-mpn/
2http://www.deepphi.com/technology/
cannot be used in practice due to its high computational complexity. Lossless weight compression has appeared in the literature in the form of Huffman coding (HC) [18] and arithmetic coding [51].

Recently, many lightweight architectures have appeared in the literature that attempt to strike a balance between computational complexity and model accuracy [22, 24, 54, 68]. Typical design choices include the introduction of 1 × 1 point-wise convolutions and depthwise-separable convolutions. These networks are trained from scratch and offer an alternative to state-of-the-art solutions. When such lightweight architectures do not achieve high enough accuracy, it is possible to alternatively compress and accelerate state-of-the-art networks. Pruning of weights [14, 18, 19, 34, 36, 40, 48, 63, 69] and quantization of weights and activations [6, 7, 12, 15, 18, 23, 50, 64, 67] are the standard compression techniques that are currently used. Other popular approaches include the modification of pre-trained networks by replacing the convolutional kernels with low-rank factorizations [25, 29, 56] or grouped convolutions [26].

One could falsely consider viewing our algorithm as a method to perform activation pruning, an analog to weight pruning [18] or structured weight pruning [38, 39, 43, 66]. The latter prunes weights at a coarser granularity and in doing so also affects the activation map sparsity. However, our method affects sparsity dynamically, and not statically as in all other methods, since it does not permanently remove any activations. Instead, it encourages a smaller percentage of activations to fire for any given input, while still allowing the network to fully utilize its capacity, if needed.

Finally, activation map regularization has appeared in the literature in various forms such as dropout [57], batch normalization [27], layer normalization [4] and $L_2$ regularization [41]. In addition, increasing the sparsity of activations has been explored in sparse autoencoders [44] using Kullback-Leibler (KL) divergence and in CNN’s using ReLU [11]. In the seminal work of Glorot et al. [11], the authors use ReLU as an activation function to induce sparsity on the activation maps and briefly discuss the use of $L_1$ regularization to enhance it. However, the utility of the regularizer is not fully explored. In this work, we expand on this idea and apply it on CNN’s for model acceleration and activation map compression.

### 3. Learning Sparser Activation Maps

A typical cost function, $E_0(w)$, employed in CNN models is given by:

$$E_0(w) = \frac{1}{N} \sum_{n=1}^{N} c_n(w) + \lambda_w r(w),$$

where $n$ denotes the index of the training example, $N$ is the mini-batch size, $\lambda_w \geq 0$, $w \in \mathbb{R}^d$ denotes the network weights, $c_n(w)$ is the data term (usually the cross-entropy) and $r(w)$ is the regularization term (usually the $L_2$ norm).

Post-activation maps of training example $n$ and layer $l \in \{1, \ldots, L\}$ are denoted by $x_{l,n} \in \mathbb{R}^{H_l \times W_l \times C_l}$, where $H_l, W_l, C_l$ denote the number of rows, columns and channels of $x_{l,n}$. When the context allows, we write $x_l$ rather than $x_{l,n}$ to reduce clutter. $x_0$ corresponds to the input of the neural network. Pre-activation maps are denoted by $y_{l,n}$. Note that since ReLU can be computed in-place, in practical applications, $y_{l,n}$ is often only an intermediate result. Therefore, we target to compress $x_l$ rather than $y_l$. See Fig. 2 for an explanatory illustration of these quantities.

The overwhelming majority of modern CNN architectures achieve sparsity in activation maps through the usage of ReLU as the activation function, which imposes a hard constraint on the intrinsic structure of the maps. We propose to aid training of neural networks by explicitly encoding in the cost function to be minimized, our desire to achieve sparser activation maps. We do so by placing a sparsity-inducing prior on $x_l$ for all layers, by modifying the cost function as follows:

$$E(w) = E_0(w) + \frac{1}{N} \sum_{n=1}^{N} \sum_{l=0}^{L} \alpha_l \|x_{l,n}\|_1$$

$$= \frac{1}{N} \sum_{n=1}^{N} \sum_{l=0}^{L} \left( c_n'(w) + \lambda_w r(w) \right),$$

where $\alpha_l \geq 0$ for $l = 1, \ldots, L-1$, $\alpha_0 = \alpha_L = 0$ and $c_n'$ is given by:

$$c_n'(w) = c_n(w) + \sum_{l=0}^{L} \alpha_l \|x_{l,n}\|_1.$$  

In Eq. 2 we use the $L_1$ norm to induce sparsity on $x_l$ that acts as a proxy to the optimal, but difficult to optimize, $L_0$ norm via a convex relaxation. The technique has been widely used in a variety of different applications including among others sparse coding [45] and LASSO [62].

While it is possible to train a neural network from scratch using the above cost function, our aim is to sparsify activation maps of existing state-of-the-art networks. Therefore, we modify the cost function from Eq. 1 to Eq. 2 during only the fine-tuning process of pre-trained networks. During fine-tuning, we backpropagate the gradients by computing the following quantities:

$$E(w) = E_0(w) + \frac{1}{N} \sum_{n=1}^{N} \sum_{l=0}^{L} \alpha_l \|x_{l,n}\|_1,$$
Recently, connections between CNN’s and Convolutional Sparse Coding (CSC) have been drawn [46, 58]. In a similar manner, it is also possible to interpret our proposed solution in Eq. 2 through the lens of sparse coding. Let us assume that for any given layer \( l \), there exists an optimal underlying mapping \( \hat{y}_l \) that we attempt to learn. Collectively \( \{\hat{y}_l\}_{l=1,\ldots,L} \) define an optimal mapping from the input of the network to its output, while the network computes \( \{y_l = f_l(x_{l-1}; w_l)\}_{l=1,\ldots,L} \). We can then think of training a neural network as an attempt to learn the optimal mappings \( \hat{y}_l \) by minimizing the difference of the layer outputs:

\[
\min_{w_l} \frac{1}{N} \sum_{n=1}^{N} \sum_{l=1}^{L} \|\hat{y}_l - y_l\|_2
\]

subject to \( r(w_l) \leq C, l = 1, \ldots, L \).

In Eq. 6 we have also explicitly added the regularization term \( r(w_l) \). \( C \geq 0 \) is a pre-defined constant that controls the amount of regularization. In the following, we restrict \( r(w_l) \) to the \( L_2 \) norm, \( r(w_l) = \|w_l\|_2 \), as it is by far the most common regularizer used and add the \( L_1 \) prior on \( x_l \):

\[
\min_{w_l} \frac{1}{N} \sum_{n=1}^{N} \sum_{l=1}^{L} \|\hat{y}_l - y_l\|_2 + \sum_{l=0}^{L} \alpha_l \|x_l\|_1
\]

subject to \( \|w_l\|_2^2 \leq C, l = 1, \ldots, L \).

We can re-arrange the terms in Eq. 7 (and make use of the fact that \( \alpha_L = 0 \)) as follows:

\[
\min_{w_l} \frac{1}{N} \sum_{n=1}^{N} \sum_{l=1}^{L} P_{l,n}
\]

subject to \( \|w_l\|_2^2 \leq C, l = 1, \ldots, L \).

where:

\[
P_{l,n} = \|\hat{y}_l - y_l\|_2^2 + \alpha_{l-1} \|x_{l-1,n}\|_1.
\]

For mappings \( f_l(x_{l-1}; w_l) \) that are linear, such as those computed by the convolutional and fully-connected layers, \( f_l(x_{l-1}; w_l) \) can also be written as a matrix-vector multiplication, by some matrix \( W_l, y_l = W_l x_{l-1} \). \( P_l \) can then be re-written as \( P_l = \|\hat{y}_l - W_l x_{l-1}\|_2^2 + \alpha_{l-1} \|x_{l-1}\|_1 \), which
can be interpreted as a sparse coding of the optimal mapping \( \tilde{y} \). Therefore, Eq. 8 amounts to computing the sparse coding representations of the pre-activation feature maps.

5. Quantization

We quantize floating point activation maps, \( x_l \), to \( q \) bits using linear (uniform) quantization:

\[
x_l^{\text{quant}} = \frac{x_l - x_l^{\min}}{x_l^{\max} - x_l^{\min}} \times (2^q - 1),
\]

where \( x_l^{\min} = 0 \) and \( x_l^{\max} \) corresponds to the maximum value of \( x_l \) in layer \( l \) in the training set. Values above \( x_l^{\max} \) in the testing set are clipped. While we do not retrain our models after quantization, it has been shown by the literature to improve model accuracy [5, 37]. We also believe that a joint optimization scheme where we simultaneously perform quantization and sparsification can further improve our results and leave it for future work.

6. Entropy Coding

A number of different schemes have been devised to store sparse matrices effectively. Compressed sparse row (CSR) and compressed sparse column (CCS) are two representations that strike a balance between compression and efficiency of arithmetic operation execution. However, such methods assume that the entire matrix is available prior to storage, which is not necessarily true in all of our use cases. For example, in neural network hardware accelerators data are often streamed as they are computed and there is a great need to compress in an on-line fashion. Hence, we shift our focus to algorithms that can encode one element at a time. We present a new entropy coding algorithm, sparse-exponential-Golomb (SEG) (Alg. 1) that is based on the effective exponential-Golomb (EG) [61, 65]. SEG leverages on two facts: (1) Most activation maps are sparse and, (2) The first-order probability distribution of the activation maps has a long tail.

Exponential-Golomb is most commonly used in H.264 [28] and HEVC [30] video compression standards with \( k = 0 \) as a standard parameter. \( k = 0 \) is a particularly effective parameter value when data is sparse since it assigns a code word of length 1 for the value \( x = 0 \). However, it also requires that the probability distribution of values falls-off rapidly. Activation maps have a non-trivial number of large values (especially for \( q = 8, 12, 16 \)), rendering \( k = 0 \) ineffective. Fig. 3 demonstrates this fact by showing histograms of activation maps for a sample of layers from Inception-V3. While this issue could be solved by using larger values of \( k \), the consequence is that the value \( x = 0 \) is no longer encoded with a 1 bit code word (see Table 1). SEG solves this problem by dedicating the code word ‘1’ for \( x = 0 \) and by pre-appending the code word generated by EG with a ‘0’

### Algorithm 1 Sparse-exponential-Golomb

| Algorithm | \( k = 0 \) | \( k = 4 \) | \( k = 8 \) | \( k = 12 \) |
|-----------|-----------|-----------|-----------|-----------|
| EG        | 1         | 5         | 9         | 13        |
| SEG       | 1         | 1         | 1         | 1         |

Table 1. Code word length comparison of \( x = 0 \) between EG and SEG for different values of \( k \).

for \( x > 0 \). Sparse-exponential golomb can be found in Alg. 1 while exponential-Golomb [61] is provided in Appendix C.

7. Experiments

In the experimental section, we investigate two important applications: (1) acceleration of computation and (2) compression of activation maps. We carry our experiments on three different datasets, MNIST [35], CIFAR-10 [32] and ImageNet ILSVRC2012 [8] and five different networks, a LeNet-5 [35] variant3, MobileNet-V1 [22], Inception-V3 [60], ResNet-18 [20] and ResNet-34 [20]. These networks cover a wide variety of network sizes, complexity in design and efficiency in computation. For example, unlike AlexNet [33] and VGG-16 [55] that are over-parameterized and easy to compress, MobileNet-V1 is an architecture that is already

3 https://github.com/pytorch/examples/
| Dataset | Model | Variant | Top-1 Acc. | Top-5 Acc. | Acts. (%) | Speed-up |
|---------|-------|---------|------------|------------|-----------|----------|
| MNIST   | LeNet-5 | Baseline | 98.45%     | -          | 53.73%    | 1.0 x    |
|         |        | Sparse  | **98.48% (+0.03%)** | -          | **23.16%** | **2.32 x** |
| CIFAR-10| MobileNet-V1 | Baseline | 89.17%     | -          | 47.44%    | 1.0 x    |
|         |        | Sparse  | **89.71% (+0.54%)** | -          | **29.54%** | **1.61 x** |
|         | Inception-V3 | Baseline | 75.76%     | 92.74%     | 53.78%    | 1.0 x    |
|         |        | Sparse  | **76.14% (+0.38%)** | **92.83% (+0.09%)** | 33.66%    | **1.61 x** |
|         |        | Sparse_v2 | 68.94% (-6.82%) | 88.52% (-4.22%) | **33.66%** | 2.32 x    |
| ImageNet| Inception-V3 | Baseline | 69.64%     | 88.99%     | 60.64%    | 1.0 x    |
|         |        | Sparse  | **69.85% (+0.21%)** | **89.27% (+0.08%)** | 49.51%    | **1.22 x** |
|         |        | Sparse_v2 | 68.62% (-1.02%) | 88.41% (-0.58%) | **34.29%** | **1.77 x** |
|         | ResNet-18 | Baseline | 73.26%     | 91.43%     | 57.44%    | 1.0 x    |
|         |        | Sparse  | **73.95% (+0.69%)** | **91.61% (+0.18%)** | 46.85%    | **1.23 x** |
|         |        | Sparse_v2 | 67.73% (-5.53%) | 87.93% (-3.50%) | **25.34%** | **2.12 x** |
|         | ResNet-34 | Baseline | 69.64%     | 88.99%     | 60.64%    | 1.0 x    |
|         |        | Sparse  | **69.85% (+0.21%)** | **89.27% (+0.08%)** | 49.51%    | **1.22 x** |
|         |        | Sparse_v2 | 68.62% (-1.02%) | 88.41% (-0.58%) | **34.29%** | **1.77 x** |

Table 2. Accelerating neural networks via sparsification. Numbers in brackets indicate change in accuracy. Acts. (%) shows the percentage of non-zero activations.

| Network | Algorithm | Top-1 Acc. Change | Top-5 Acc. Change | Speed-up |
|---------|-----------|-------------------|------------------|----------|
| ResNet-18 | Ours (Sparse) | +0.21% | +0.28% | 18.4% |
|         | Ours (Sparse_v2) | -1.02% | -0.58% | 43.5% |
|         | BWN [50] | -3.65% | -2.30% | 34.6% |
|         | XNOR [50] | -8.50% | -6.20% | 50.0% |
| ResNet-34 | Ours (Sparse) | +0.69% | +0.18% | 18.4% |
|         | Ours (Sparse_v2) | -5.53% | -3.05% | 48.4% |
|         | LCCL [10] | -0.43% | -0.17% | 24.8% |
|         | PFEC [36] | -1.06% | -0.24% | 24.2% |
| LeNet-5 | Ours (Sparse) | +0.03% | 56.9% | 18.4% |
| (p = 70%) | [18] | -0.12% | -0.34% | 73.9% |
| (p = 80%) | [18] | -0.57% | -1.47% | 73.9% |

Table 3. Comparison between various state-of-the-art acceleration methods on ResNet-18/34 and LeNet-5. For ResNet-18/34, we emphasized in bold Sparse_v2 as a good compromise between acceleration and accuracy. \( p = 70\% \) indicates that the pruned network has 30\% non-zero weights. Speed-up calculation follows the convention from [10], where it is reported as 1 - (non-zero activations of sparse model) / (non-zero activations of baseline).

Many of our sparse models not only have an increased sparsity in their activation maps, but also demonstrate increased accuracy. This alludes to the well-known fact that sparse activation maps have strong representational power [11] and the addition of the sparsity-inducing prior does not necessarily trade-off accuracy for sparsity. When the regularization parameter is carefully chosen, the data term and the prior can work together to improve both the Top-1 accuracy and the sparsity of the activation maps. Inception-V3 can be accelerated by as much as 1.6\times with an increase in accuracy of 0.38\%, while ResNet-18 achieves a speed-up of 1.8\times with a modest 1\% accuracy drop. LeNet-5 can be accelerated by 2.3\times, MobileNet-V1 by 1.6\times and finally ResNet-34 by 1.2\times, with all networks exhibiting accuracy increase. The regularization parameters, \( \alpha_l \), can be adapted for each layer independently or set to a common constant. We experimented with various configurations and the selected parameters are shared in Appendix B. To determine

![Figure 4. Evolution of Top-1 accuracy and activation map sparsity of MobileNet-V1 and Inception-V3 during training on the validation sets of CIFAR-10 and ILSVRC2012 respectively. The red arrow indicates the checkpoint selected for the sparse model.](https://github.com/kuangliu/pytorch-cifar/)

![Figure 4.](https://pytorch.org/docs/stable/torchvision/)
the selected \( \alpha \), we used grid-based hyper-parameter optimization. The number of epochs required for fine-tuning varied for each network as Fig. 4 illustrates. We chose to typically train up to 90-100 epochs and then selected the most appropriate result. In Fig. 3, we show the histograms of a selected number of Inception-V3 layers before and after sparsification. Histograms of the sparse model have a greater proportion of zero-values, leading to model acceleration as well as lower entropy, leading to higher activation map compression.

In Fig. 4, we show how the accuracy and activation map sparsity of MobileNet-V1 and Inception-V3 changes during training evaluated on the validation sets of CIFAR-10 and ILSVRC2012 respectively. On the same figure, we show the checkpoint selected to report results in Table 2. Selecting a checkpoint trades-off accuracy with sparsity and which point is chosen depends on the application at hand. In the top row of Fig. 5 we show the percentage of non-zero activations per layer for the five networks. While some layers (e.g. last few layers in MobileNet-V1) exhibit tremendous decrease in non-zero activations, most of the acceleration comes by effectively sparsifying the first few layers of a network. Finally, in Table 3 we compare our approach to other state-of-the-art methods as well as to a weight pruning algorithm [18]. In particular, note that [18] is not effective in increasing the sparsity of activations. Overall, our approach can achieve both high acceleration (with a slight accuracy drop) and high accuracy (with lower acceleration).

**Compression.** We carry our compression experiments on a subset of each network’s activation maps, since caching activation maps of the entire training and testing sets is prohibitive due to storage constraints. In Table 4 we show the effect of testing size on the compression performance of two algorithms, SEG and zero-value compression (ZVC) [52] on MNIST and ImageNet. Evaluating compression yields minor differences as a function of size. Similar observations can be made on all investigated networks and datasets. In subsequent experiments, we use the entire testing set of MNIST to evaluate LeNet-5, the entire testing set of CIFAR-10 to evaluate MobileNet-V1 and 5,000 randomly selected images from the validation set of ILSVRC2012 to evaluate Inception-V3 and ResNet-18/34. The Top-1/Top-5 accuracy, however, is measured on the entire validation sets. In Table 5, we summarize the compression results. Compression gain is defined as the ratio of activation size before and after compression. We evaluate our method (SEG) against exponential-Golomb (EG) [61], Huffman Coding (HC) used in [18], zero-value compression (ZVC) [52] and ZLIB [1]. We report the Top-1/Top-5 accuracy of the quantized models at 16 bits. Parameter choice for SEG and EG is provided in Table 7. SEG outperforms all methods in both the baseline and sparse models validating the claim that it is a very effective encoder for distributions with high sparsity and long tail. SEG achieves almost \( 7 \times \) compression gain on LeNet-5, almost \( 6 \times \) on MobileNet-V1 and Inception-V3 and more than \( 4 \times \) compression gain in the ResNet architectures while also featuring an increase in accuracy.

It is also clear that our sparsification step leads to greater compression gains, as can be seen by comparing the base-

| Dataset | Model | Algorithm | \( t = 1,000 \) | \( t = 10,000 \) | \( t = 30,000 \) | \( t = 60,000 \) |
|----------|-------|-----------|----------------|----------------|----------------|----------------|
| MNIST    | LeNet-5 | SEG       | 1.700 \times 100% | 1.700 \times 100% | 1.701 \times 100% | 1.701 \times 100% |
|          |        | ZVC[52]   | 1.665 \times 100% | 1.666 \times 100% | 1.666 \times 100% | 1.667 \times 100% |
| ImageNet | Inception-V3 | SEG       | 1.763 \times 100% | 1.769 \times 100% | 1.774 \times 100% | 1.779 \times 100% |
|          |        | ZVC[52]   | 1.652 \times 100% | 1.655 \times 100% | 1.661 \times 100% | 1.667 \times 100% |

**Table 4.** Effect of evaluation set size on compression performance. Varying the size yields minor compression gain changes, indicating that a smaller dataset can serve as a good benchmark for evaluation purposes.

![Figure 5. Percentage of non-zero activations (above) and compression gain (below) per layer for various network architectures before and after sparsification.](image)

![Figure 5. Percentage of non-zero activations (above) and compression gain (below) per layer for various network architectures before and after sparsification.](image)

| Model    | Variant | Measurement | float32 | uint8 | uint16 | uint32 | uint64 |
|----------|---------|-------------|---------|-------|--------|--------|--------|
| LeNet-5 (MNIST) | Baseline | Top-1 Acc. | Compression | SEG | Baseline | Top-1 Acc. | Compression | SEG | Baseline | Top-1 Acc. | Compression | SEG | Baseline | Top-1 Acc. | Compression | SEG | Baseline | Top-1 Acc. | Compression | SEG |
|          |        |             |         |       |        |        |        |       |        |             |         |       |        |             |         |       |        |             |         |       |        |             |         |
| MobileNet-V1 (CIFAR-10) | Baseline | Top-1 Acc. | Compression | SEG | Baseline | Top-1 Acc. | Compression | SEG | Baseline | Top-1 Acc. | Compression | SEG | Baseline | Top-1 Acc. | Compression | SEG |

**Table 5.** Effect of quantization on compression on SEG. LeNet-5 is compressed by \( 11 \times \) and MobileNet-V1 by \( 10 \times \). In brackets, we report change in accuracy and compression gain over the float32 baseline.
The sparsification step induces higher sparsity and lower entropy in the distribution of values, which both lead to these additional gains (Fig. 3). While all compression algorithms demonstrate that our pipeline can achieve even greater compression gain compared to the floating-point baseline model. LeNet-5 can be compressed by as much as $11 \times$ with a 0.01% accuracy gain, while MobileNet-V1 can be compressed by $10 \times$ with a 0.45% accuracy gain.

## 8. Conclusion

We have presented a three-stage compression and acceleration pipeline that sparsifies, quantizes and encodes activation maps of CNN's. The sparsification step increases the number of zero values leading to model acceleration on specialized hardware, while the quantization and encoding stages lead to compression by effectively utilizing the lower entropy of the sparser activation maps. The pipeline demonstrates an effective strategy in reducing the computational and memory requirements of modern neural network architectures, taking a step closer to realizing the execution of state-of-the-art neural networks on low-powered devices. At the same time, we have demonstrated that adding a sparsity-inducing prior on the activation maps does not necessarily come at odds with the accuracy of the model, alluding to the well-known fact that sparse activation maps have a strong representational power. Furthermore, we motivated our proposed solution by drawing connections between our approach and sparse coding. Finally, we believe that an optimization scheme in which we jointly sparsify and quantize neural networks can lead to further improvements in acceleration, compression and accuracy of the models.

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A. Appendix overview

In the appendices, we discuss (1) the parameter choice of the regularization parameter $\alpha_l$ for sparsification for each layer $l$ of a network (Sec. B) and (2) present the exponential-Golomb algorithm for completeness (Sec. C).

B. Regularization parameter

We present the regularization parameters per layer for each network in Tables 8, 9, 10. While we finely-tuned the regularization parameter of each layer for the smaller networks (LeNet-5, MobileNet-V1), we chose a global regularization parameter for the bigger ones (Inception-V3, ResNet-18, ResNet-34). Finely-tuning the regularization of each layer can yield further sparsity gains, but also adds an intensive search over the parameter space. Instead, for the bigger networks, we showed that even one global regularization parameter per network is sufficient to successfully sparsify a model. For layers not shown in the tables, the regularization parameter was set to 0. Note that we never regularize the final layer $L$ of each network.

| Model         | Variant | $\alpha_l$ | $l = 1, \ldots, L - 1$ |
|---------------|---------|------------|------------------------|
| LeNet-5       | conv1   | $0.25 \times 10^{-5}$ | $2.00 \times 10^{-5}$ | $5.00 \times 10^{-5}$ |
|               | conv2   |            |                        |                      |
|               | fc1     |            |                        |                      |

Table 8. LeNet-5 [35] regularization parameters.

| Model         | Variant | $\alpha_l$ | $l = 1, \ldots, L - 1$ |
|---------------|---------|------------|------------------------|
| MobileNet-V1  | Conv1   | $1 \times 10^{-7}$ |                        |
|               | Conv2   | $5 \times 10^{-7}$ |                        |

Table 9. MobileNet-V1 [22] regularization parameters.

| Model         | Variant | $\alpha_l$ | $l = 1, \ldots, L - 1$ |
|---------------|---------|------------|------------------------|
| Inception-V3  | Sparse  | $1 \times 10^{-8}$ |                        |
|               | Sparse_v2 | $1 \times 10^{-7}$ |                        |
| ResNet-18     | Sparse  | $1 \times 10^{-8}$ |                        |
|               | Sparse_v2 | $1 \times 10^{-7}$ |                        |
| ResNet-34     | Sparse  | $5 \times 10^{-8}$ |                        |
|               | Sparse_v2 | $1 \times 10^{-7}$ |                        |

Table 10. Inception-V3 [60] and the ResNet-18/34 [30] regularization parameters.

C. Exponential-Golomb

We provide pseudo-code for the encoding and decoding algorithms of exponential-Golomb [61] in Alg. 2.

Algorithm 2 Exponential-Golomb

Input: Non-negative integer $x$, Order $k$
Output: Bitstream $y$

function $encode_{\text{exp\_Golomb}}(x, k)$
{
    $y = encode_{\text{exp\_Golomb\_0\_order}}(x)$
    Else:
        $q = \text{floor}(x/2^k)$
        $q_c = encode_{\text{exp\_Golomb\_0\_order}}(q)$
        $r = x \mod 2^k$
        $r_c = \text{to\_binary}(r, k)$ // to\_binary$(r, k)$ converts $r$ into binary using $k$ bits.
        $y = \text{concatenate}(q_c, r_c)$
        Return $y$
}

Input: Bitstream $x$, Order $k$
Output: Non-negative integer $y$

function $decode_{\text{exp\_Golomb}}(x)$
{
    $y, l = decode_{\text{exp\_Golomb\_0\_order}}(x)$
    Else:
        $q, l = decode_{\text{exp\_Golomb\_0\_order}}(x)$
        $r = \text{int}(x[l : l + k])$
        $y = q \times (2^k) + r$
        Return $y$
}

Input: Non-negative integer $x$
Output: Bitstream $y$

function $decode_{\text{exp\_Golomb\_0\_order}}(x)$
{
    $q = \text{to\_binary}(x + 1)$
    $q_{eb} = \text{length}(q)$
    $p = \text{“0”} \ast (q_{eb} - 1)$ // replicates “0“ $q_{eb} - 1$ times.
    $y = \text{concatenate}(p, q)$
    Return $y$
}

//The notation $x[a : b]$, follows the Python rules, i.e. selects characters in the range $[a, b]$