Power Quality Improvement Using Distributed Power Flow Controller with BWO-Based FOPID Controller

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Abstract: The integration of hybrid renewable energy sources (HRESs) into the grid is currently being encouraged to meet the increasing demand for electric power and reduce fossil fuels which are causing environmental-related problems. Integration of HRESs into the grid can create some power quality (PQ) problems. To mitigate PQ problems and improve the performance of grid-connected HRESs some flexible devices should be used. This paper presents a distributed power flow controller (DPFC), as a type of flexible device to mitigate some PQ problems, including voltage sag, swell, disruptions, and eliminating the harmonics in a hybrid power system (HPS). The HPS presented in this work comprises a photo voltaic (PV) system, wind turbine (WT) and battery energy storage system (BESS).

As a result, black widow optimization (BWO) with DPFC with real and reactive power (DPFC-PQ) is built in this paper to solve the PQ issues in HRES systems. The main aim of the work is to mitigate PQ problems and compensate for load demand in the HRES scheme. The controller used to drive this DPFC-PQ is a fractional-order PID (FOPID) controller optimized by the black widow optimization (BWO) technique. To assess the capability of BWO in fine-tuning the FOPID controller parameters, twelve optimization techniques were presented: P&O, PSO, Cuckoo, GA, GSA, BBO, Whale, ESA, RFA, ASO, and EVORFA. Additionally, a comparison between the FOPID controller and the classical PI controller is introduced. The results showed that the proposed BWO-FOPID controller for DPFC had mitigated the PQ problems in grid-connected HRESs. The system’s performance with the presented BWO-FOPID controller is compared with eleven optimization techniques used to optimize the FOPID controller and also compared with the conventional PI controller. The design of the proposed system is implemented in the MATLAB/Simulink platform and performances were analyzed.

Keywords: DPFC; HRES; voltage sag; voltage disturbances; voltage interruptions; voltage swell; harmonics

1. Introduction

HRESs are currently playing a vital role, as traditional energy sources face several challenges related to the usage of fossil fuels that have great negative environmental impacts. HRESs can mitigate emissions and global warming in addition to their significant economic advantages [1]. Due to global economic problems and increasing ecological awareness, HRESs-based distributed generation (DG) is becoming more relevant [2]. Many HRESs have been implemented and integrated into the classical power networks, like wind energy, photovoltaic energy, fuel cells (FCs), and biomass [3]. Each RES has distinct characteristics different from other sources in terms of being AC or DC [4]. The PV and FC generated DC voltages with different levels, while wind energy generates either AC or DC based on the
generator used in the wind energy conversion system (WECS) [5]. These diversities represent a challenge in connecting these HRESs to the electrical network. Another challenge that appears when integrating these HRESs into the electrical grid is the instability of the power generated from HRESs due to their dependence on some changing environmental conditions, such as temperature and irradiance in PV systems and wind speed in WECSs. The critical solution to such challenges is using power electronic devices to adjust the DC levels of all DC sources using some converters. Besides, these converters may be used for maximizing the power extracted from each HRES [6]. Then DC–AC inverters are used to connect the output of these HRESs to the electrical grid. The power electronic devices, used to integrate different types of HRESs to the grid, will increase the PQ problems, such as voltage sags, swell, disruptions, interference harmonics, etc. These problems might cause continuous fluctuation in power generated and tripping of some HRESs if the grid standards or codes were not fulfilled [7]. Some flexible devices should be added to the whole system to support the PQ and improve system performance; these devices are called flexible AC transmission system (FACTS) devices [8]. According to these FACTS devices’ connection to the network, there are three types: series, parallel, and combination of series and parallel. These controllers are identified based on their specific characteristics and controlling mechanism before integrated into the system. To change the line reactance and expand the limit of transmission line, a static synchronous series compensator (SSSC) and the dynamic voltage restorer (DVR) are used [9]. Shunt types are utilized for supporting the voltage by exchanging the reactive power with the system during voltage sag/swell conditions. Shunt compensators like static compensators (STATCOM), distribution STATACOM (DSTATCOM), and thyristor regulated reactor (TCR) were introduced to improve the connection of some RESs to the grid [10]. The series/shunt combination of these FACTS devices combined the characteristics of the series and the shunt devices like the unified power flow controller (UPFC) and distributed power flow controller (DPFC) [11,12]. UPFC is a combination of STATCOM and SSSC linked together through DC link to exchange the active/reactive with the system. Some modifications in the structure of UPFC is performed by eliminating the DC link capacitor and connecting STACOM and SSSC through a transmission line with third-frequency components [13]. This elimination of the DC link gives more prominent adaptability to put the STATCOM and SSSC autonomously. Based on the discussions above, some FACTS devices are needed to support the PQ issues in grid-connected HRESs. The performance of the type of FACTS devices mainly depends on the controller used. The traditional PI controller and the modified fractional-order proportional integral controller (FOPIC) are the most dominant due to their simplicity and good performance with the superiority of the FOPI type [14]. Adjustment or tuning the controller parameters of FOPID employed in FACTS devices to mitigate the PQ problems in grid-connected HRESs is a non-linear complex optimization problem that desires some metaheuristic optimization techniques. Many optimization techniques were presented for optimal tuning of PI and FOPID controller parameters in many HRES applications [15].

This paper presents the utilization of one of the combined FACTS devices, DPFC, to mitigate some PQ problems in a grid-connected HRES system. This system consists of wind, PV, and batteries as storage units. The PQ issues presented in this paper are the irregularity in power generated, voltage sag, swell, disruption conditions, and the total harmonic distortion (THD). The power irregularity comes from PV and wind energy conversion systems due to variation in the irradiance and wind speed, respectively. At the same time, non-linear loads simulate the harmonics in the system. In the proposed system, the instantaneous PQ theory and FOPID controller with BWO are initialized. To provide the optimal pulses to the converter the instantaneous PQ theory is used for the series and shunt controllers. The results of the proposed system are validated by comparing them with the results of the conventional PI controller and the intelligent techniques like P&O, PSO, Cuckoo, GA, GSA, BBO, Whale, ESA, RFA, ASO, and EVORFA. Among all the controllers BWO provides the best control parameters to the FOPID controller which produces better performances in terms of compensation of voltage and current related swell, sag, and harmonic reduction. The proposed framework is developed and examined in MATLAB/Simulink. The rest of the paper is composed as follows;
Section 2 describes a brief survey of recent research works, Section 3 proposes the HRES system, Section 4 proposes control model of DPFC, Section 5 a BW optimization, in Section 6 we present results and discussions, Section 7 includes a comparison analysis, and Section 8 is the conclusion.

2. A Brief Review: Recent Research Works

Researchers at several research institutions use various ways to minimize the PQ issues associated with integrated FACT devices. In this paper, we look at some of the work that has been done on PQ issue mitigation and it is presented in Table 1.

| Contribution                                                                 | Year | Reference |
|------------------------------------------------------------------------------|------|-----------|
| They have presented that the bacterial graphical user interface oriented by particle swarm optimization method for optimization of various type DFACTS for power quality improvement in the distribution system. | 2017 | [16]      |
| EBFO based multiple DFACTS allocation utilizing DSSSC, APC, and DSTATCOM are preferred to decrease power losses, improve load balancing, and increase voltage deviation index to 70%, 38%, and 132%, correspondingly, and also it may improve loading factor without extra power loss. | 2017 | [16]      |
| The multi-objective issue in which four objective functions include the maximization of voltage stability, power loss reduction, and minimizing of voltage deviation and equalizing the feeder load balancing in the distribution system. | 2017 | [16]      |
| The single-phase voltage source inverter that connects the distributed generation plant to the grid must address several issues related to the quality of current injected into the grid, output power factor, and power exchange between the plant and the grid. | 2017 | [17]      |
| For single-phase grid linked VSI, the model predictive current controller (MPCC) is developed, which can perform well in both steady-state and transient state operations. | 2017 | [17]      |
| The current study also shows how to build the digital MPCC in hardware utilizing a low-cost digital signal processor (DSP). A single-phase grid-connected VSI of 1 kW is modeled and simulated. | 2017 | [17]      |
| They proposed the DSTATCOM method for minimizing power quality problems during power delivery. One of the unique power gadgets was this technique. | 2017 | [18]      |
| Some of the features such as reimbursement of imaginary power, reduction of voltage harmonics, reduction of power factor, and reduction of current harmonics were accomplished using the DSTATCOM method. | 2017 | [18]      |
| In terms of utilizing these methods, the primary goal was to improve power quality by minimizing noise and reducing voltage fluctuation. Authentication and demonstration of the suggested method were carried out here. | 2017 | [18]      |
| They proposed the unified power quality conditioner (UPQC) method for improving power quality. The achievement of UPQC’s series and shunt active power filter (APF) virtual power-sharing. | 2017 | [19]      |
| These methods of power-sharing were predicated on the instability of the voltage source. Some of the problems arose as a result of voltage level instabilities. | 2017 | [19]      |
| To overcome these problems, a combination of power angle control (PAC) and synchronous reference frame (SRF) was employed. Parameters like load current were utilized to mitigate power quality problems while efficiently using the UPQC inverters. | 2017 | [19]      |
| They proposed that the two methods, UPQC-O and PV array with battery and UPQC-O and PV array without batteries, be combined. In the method of UPQC-O and PV array with battery, the PV array generates some quantity of energy that is often stored. Those energies were used during peak hours to conduct efficient energy storage operations. | 2018 | [20]      |
| The PV array was injected directly into the network in the UPQC-O and PV array without the battery method. These two methods were coupled with operational parameters like bus voltage. | 2018 | [20]      |
| In this case, the replacement was done in terms of UPQC-O with the PV array in the networking methods. Some of the goals include lowering the operating costs of batteries, PV arrays, and inverters, as well as reducing energy loss. In addition, the particle swarm optimization method was utilized to improve the energy preservation solution. | 2018 | [20]      |
Table 1. Cont.

| Contribution                                                                 | Year | Reference |
|------------------------------------------------------------------------------|------|-----------|
| • They proposed a static synchronous compensator (STATCOM). This method was used to reduce reactive power using the Icos algorithm. In this method, the continuous power supply was maintained while conducting the power quality enhancement. | 2018 | [21]      |
| • The uninterrupted power supply protects both the telecommunications and computer systems. Some supply-side commotion, such as sag, swell, and harmonics, were reduced as compensation for poor power quality. Voltage and current were both unstable; therefore they were balanced. |      |           |
| • They demonstrated the multi-functional grid-connected inverter (MFGCI) for increasing power quality in both micro-grid (MG) and distributed generation (DG) systems. The MFGCI method was used to reduce the voltage-based power quality approach reimbursing. In addition, the shunt series switched multi-functional grid-connected inverter (SSS-MFGCI) method was utilized to address both current and voltage-based power quality problems. | 2018 | [22]      |
| • They proposed using the shunt active power filter (SAPF) method to minimize power supply problems. This kind of technique was also used to reduce harmonic turbulences. | 2019 | [23]      |
| • This filter was specifically designed for regulating DC voltage amid power quality. In this method, the total harmonic distortion (THD) value was evaluated in terms of harmonic mitigation. In this technique, sinusoidal waveforms were generated to mitigate power quality problems. |      |           |
| • They had developed a control method that could give a fast and reliable solution as the operating conditions of the system varied. The UPQC coupled wind energy to increase energy quality is the design of the technique. Voltage sag, source tension swell, and current source power harmonics reduction. The Adaptable PI controller has been used in shunt-based and serial APFs, which enable the adaptive PI controller using the Park control mechanism. This signifies that there are no anomalous effects on any modification to the external state of the system depicted. | 2019 | [24]      |
| • They proposed a shunt active power filter and a series active power filter for improving power quality. The adaptive ANFIS method was utilized to assess power quality performance in UPQC devices. Some drawbacks, including voltage swell and voltage sag, exist with UPQC devices. These types of problems were minimized by using UPQC-based methods. | 2020 | [25]      |
| • They had introduced a hybrid flow management approach (PFM) of the micro-grid (MG) connected hybrid renewable source (HRES). This strategy is proposed by the integration of both the whale optimization algorithm (WOA) and the SSAWVSquirrel search (SSA) algorithm. Here the SSA develops voltage source inverter control signals subject to the power exchange difference between the source and the load side. The multi-objective function is determined by the active and reactive power variants needed for the grid generated using the accessible source power. The technology-based control model created improve the power controller control parameters, given the variety of power flows. They proposed an enhanced fuzzy-based multi converter-unified power quality conditioner for improving power quality (MC-UPQC). Using this method also helped to reduce inefficiencies in voltage and current. In addition, the fuzzy incremental conductance method was utilized to determine the power point that occurs at the maximum level. Using this fuzzy-based method, the wind energy system (WES) was enhanced. | 2020 | [26]      |
| • They proposed an enhanced fuzzy-based multi converter-unified power quality conditioner for improving power quality (MC-UPQC). Using this method also helped to reduce inefficiencies in voltage and current. | 2021 | [27]      |
| • They proposed a new technique for DVR. They optimized FOPID controller with GS algorithm. They have addressed various PQ issues like voltage regulation, fault compensation, sag, swell, and THD reduction. | 2021 | [28]      |

3. Modeling of the Proposed HRES System

In the distribution system, HRESs such as PV and WT are used more extensively. HRES is considered to be the best option in an integrated DG system [29]. In the distribution system, HRES should satisfy the load demand expected by customers, which implies problems of flexibility and reliability of power quality [30]. For the smooth running of the device, these PQ issues must be avoided [31]. FACT devices are utilized with the
development of power electronics devices to mitigate PQ problems. DPFC is designed to mitigate power PQ problems such as voltage sag, current sag, disruptions, and THD [32]. The combination of PV, WT, and the battery as energy storing system (BESS) interfaced with the grid is proposed. As the sources considered are intermittent, BESS is designed to satisfy the load demand under critical environmental conditions. PQ problems are generated in the HRES system because of the faults, non-linear loads, and sudden loads. [33]. Due to these problems, voltage instability and mismatches in reactive power are created. PQ issues such as voltage sag, current sag, real power, reactive power, and THDs are discussed in this paper [34]. To improve the voltage regulations in the HRES interfaced grid-connected system DPFC-FOPID controller is designed to solve these problems. The tuning of the FOPID controller which controls the operation of the series controller and shunt controller is done using the BWO technique. Using perturb and observe (P&O) maximum power point tracking (MPPT), the PV and wind powers are derived [35]. A reference DC bus voltage is produced from the BWO. The DC relation voltage reference is set to its default value at a time when there is no solar energy [36]. The HRES’s PQ problems are considered primarily due to faults, non-linear load, and sudden grid side loads [37].

The proposed system is equipped with DPFC, shown in Figure 1 [38]. It comprises series and shunt controllers which are operated using control techniques to compensate the PQ problems and for stable operation power compensation can be accomplished by providing the best gain parameters for the FOPID controller [39], which filters and injects the necessary power by selecting the best gain parameters for the FOPID controller. The FOPID controller is controlled by a BWO technique, which is programmed to select the values necessary to run the control operation under PQ conditions [40].

![Figure 1. The architecture of DPFC.](image)

### 3.1. Modeling of PV

The design PV panel current and terminal voltage are computed based on the following Equations (1)–(3) [41].

\[
I_P = I_{SC} - I_0 \left\{ \exp \left[ \frac{Q}{akt} (V_P + I_P R_{SE}) - 1 \right] \right\} - \frac{V_P + I_{SC} R_{SE}}{R_{SH}} 
\]

\[
V_P = \frac{akt}{Q} \ln \left\{ \frac{I_{SC}}{I_P} + 1 \right\} 
\]

The PV is the ideal option for producing power from solar energy from various HRESs while avoiding greenhouse gas pollution, with durability, long life, good efficiency, and less maintenance. It consists of cells connected in series in the PV system to achieve the voltage
required. Figure 2 shows the built PV panel model, the power produced from the PV panel is formulated as follows.

\[ P_{PV}(t) = N_{PV}(t) \times I_{PV}(t) \times V_{PV}(t) \]  

(3)

The maximum power extraction from the grid would not always exist as the variations in both the load and the environmental conditions and, consequently, the current drawn from HRESs, especially PV systems.

![Diagram of PV panel](image)

Figure 2. Model of PV [41].

### 3.2. Modeling of Wind Turbine

The output power produced by the WT is dependent on the wind speed under the stated hub height. WT power is expressed as (4). The PV and WT systems on the grid side are used to compensate for load requirements. Often the requirement for load rises, which cannot be covered by HRES, because the battery is often attached to the device. The battery system’s statistical modeling is presented in the section below.

\[ P_{WT}(t) = \begin{cases} 
0, & V < V_{cutin} \text{ or } V > V_{cutout} \\
 p_{MAX} \left( \frac{\frac{V(t)}{V_{cutin}} - \frac{V_{MAX}^{WT}}{V_{cutin}}}{\frac{V_{cutout}}{V_{cutin}} - \frac{V_{MAX}^{WT}}{V_{cutin}}} \right)(V(t) - V_r), & V_r < V \leq V_{cutout} \\
 p_{MAX}^{WT} \left( \frac{V(t) - V_{cutin}}{V_r - V_{cutin}} \right)^3, & V_{cutin} \leq V \leq V_r 
\end{cases} \]

(4)

### 3.3. Modeling of BESS

When the power provided by the HRES is insufficient, the battery meets the needed load requirement. The battery output is calculated using the reference autonomy day (AD) under the condition of the system’s required power consumption. AD is defined as the total number of days that the battery is capable of producing power to counterbalance the demand for loads. This battery output may be described as (5).

\[ P_{capacity} = \frac{Autonomyday \times P_{L}}{\eta^I \times \eta^B \times DOD} \]

(5)

The battery in the HRES system is charged using the excess electricity produced by RES. The battery capacity is calculated using Equations (6) and (7). State of charge (SOC) is a critical parameter in the battery related to further energy production and energy shortfall in HRES as discussed below. It can impact PQ issues such as sag, swell, voltage interference, and so on in the proposed design framework. The problems of power efficiency in the system must be resolved to increase the reliability of the system obtained with the usage of the DPFC unit in the HRES system. In the segment below, the modeling of DPFC is discussed.

\[ B^P = P_{PV}(t) + P_{WT}(t) - \frac{P_L(t)}{\eta^I} \]

(6)
SOC = \begin{cases} 
SOC(t - 1)(1 - \mu) + \left( P_{PV}(t) + P_{wT}(t) - \frac{P_i(t)}{\eta} \right) \times \eta^B, P_{PV}(t) + P_{wT}(t) > P_L(t) \\
SOC(t - 1)(1 - \mu) + \left( \frac{P_i(t)}{\eta} - P_{PV}(t) + P_{wT}(t) \right) \times \eta^B, P_{PV}(t) + P_{wT}(t) < P_L(t) 
\end{cases}
(7)

3.4. Modeling of DPFC

To transfer the actual power, the shunt and series controllers will be linked to the AC terminals of a transmission line [42]. The mean value of the product of non-sinusoidal current and voltage is used to calculate real power (see Figure 1). The cross-product of the integral parameters is 0 at various frequencies. The active power may be expressed as in the following (8)

\[ P = \sum_{i=1}^{n} V_i I_i \cos \phi_i \]
(8)

Since real power has no frequency dependency, it enhances the converter’s mobility and capacity to create active power even in the absence of an electrical source, which is a benefit that at varying frequencies, the same strength may be consumed. Actual power must be received from the grid through a shunt converter operating at the fundamental frequency. At the harmonic standard, current must be injected into the grid through the distribution line. The high-pass filter allows for harmonic modules while disabling the portion of the frequency in DPFC that provides the harmonic components with a return path. The filter will harmonic current converters with a locked loop. In comparison, the DPFC’s monitoring power is high because it comprises several converters of the small-rated set, the process is not impaired by the failure of one converter, showing no effect on the whole system while other converters must continue to contribute to the overall system’s regular function. The appearance of the device is rendered more effective by bypass protection.

3.5. Perturb and Observe MPPT

MPPT approaches are often used to monitor maximum power from intermittent sources such as renewable energy sources. P&O is the most extensively used approach for producing duty pulses that are required to run the converter. It compares and monitors the reference voltages continually until the best value is reached. P&O is set at a relatively tiny size to guarantee little power loss. The sole disadvantage is that optimal power is not obtained under rapidly changing air conditions. The procedure described in Figure 3 is relatively simple and widely used. The suggested HRES system for PQ enhancement seen in Figure 4 is shown in the flow chart below. The next part, Section 3, has a full explanation of all of the modules.
Figure 3. Perturb and observe MPPT [43].

Figure 4. Flow chart of HRES system to mitigate PQ issues [44].
4. Proposed Control Model of DPFC

In this section, a HRES connected system with DPFC model is proposed. The control model of DPFC which includes series and shunt controllers are used to mitigate PQ issues. DPFC is operated with the use of a FOPID controller. The tuning of the FOPID controllers is done with BWO technique which obtains the best gain values. The series controller mitigates the current related problems and shunt controller is used to mitigate voltage-related problems.

4.1. Implementation of the Principle of Coordinated PQ in DPFC

The DPFC consists of three basic controllers, as indicated in Figure 5 (e.g., central, series, and shunt controllers). Because it provides signals to the other controllers in the same device, the central controller is the primary controller in this system. At the same time, current harmonics are corrected for voltage using shunt and series controllers.

4.2. Central Control

On several levels, it interacts with the DPFC characteristics (e.g., balancing of unbalanced components and power flow control). For the shunt and series controllers, a reference signal will be created. This satisfies the method’s criteria by generating a reference to the series and shunt regulators, as well as voltage signals. The fundamental frequency will be used to create all of the reference voltage signals.

4.3. Series Controller

Figure 6 depicts the control process based on the series active power filter. The phase locked loop (PLL) is used to determine the reference voltage at first. The three-phase voltage measurement is converted into the d–q axis using the dq transformation technique (Clarke transformation) [33]. The power filter was also employed in this manner to monitor the
DPFC to tackle the system’s power quality issues [45]. The mechanism of dq transformation from three-phase voltage is formally represented in Equation (9),

\[
\begin{bmatrix}
V^0 \\
V^d \\
V^q
\end{bmatrix}
= \frac{2}{3}\begin{bmatrix}
\frac{1}{2} \sin(at) & \frac{1}{2} \sin(at - \frac{2\pi}{3}) & \frac{1}{2} \sin(at + \frac{2\pi}{3}) \\
\cos(at) & \cos(at - \frac{2\pi}{3}) & \cos(at + \frac{2\pi}{3})
\end{bmatrix}
\begin{bmatrix}
V^a \\
V^b \\
V^c
\end{bmatrix}
\]

(9)

The d axis voltage is represented as a direct voltage as well as the alternating component voltage. The d axis voltage can be smoothing by the low-pass filter (LPF) which is mathematically formulated as (10),

\[
V^{d(\text{dc})} = V^d - V^{d(\text{ac})}
\]

(10)

After that, the voltage is changed into three phases (11), the voltage’s hysteresis band is controlled using the control pulses which are calculated and tuned by FOPID with BWO. Similarly, the shunt active power filter with a control algorithm is presented in the following section.

\[
\begin{bmatrix}
V^{R_a} \\
V^{R_b} \\
V^{R_c}
\end{bmatrix}
= \frac{2}{3}\begin{bmatrix}
\sin(at) & \frac{1}{2} \sin(at - \frac{2\pi}{3}) & 1 \\
\sin(at) & \frac{1}{2} \sin(at - \frac{2\pi}{3}) & 1 \\
\cos(at) & \cos(at - \frac{2\pi}{3}) & 1
\end{bmatrix}
\begin{bmatrix}
V^d \\
V^q \\
V^o
\end{bmatrix}
\]

(11)

![Figure 6](https://example.com/figure6.png)

**Figure 6.** Series active filter controller.

### 4.4. Shunt Active Power Filter Control Strategy

The shunt active power filter control strategy with the proposed controller is illustrated in Figure 7. The three-phase currents and voltages are changed into α and β, which are described in the following Equations (12) and (13) [46,47]. Based on phase neutral voltages and load currents, the actual and unconsidered powers of instantaneous values are computed. In the shunt active filter, the real and reactive power is computed based on Equation (14)

\[
\begin{bmatrix}
V^{s0} \\
V^{sa} \\
V^{s\beta}
\end{bmatrix}
= \sqrt{\frac{2}{3}}\begin{bmatrix}
\frac{1}{\sqrt{3}} & \frac{1}{\sqrt{3}} & \frac{1}{\sqrt{3}} \\
1 & -\frac{1}{2} & -\frac{1}{2} \\
0 & \frac{1}{2} & -\frac{1}{2}
\end{bmatrix}
\begin{bmatrix}
V^a \\
V^b \\
V^c
\end{bmatrix}
\]

(12)
\[
\begin{bmatrix}
I_{L0} \\
I_{La} \\
I_{Lb} \\
I_{Lc}
\end{bmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix}
\frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} & \frac{1}{\sqrt{2}} \\
1 & -1 & 0 \\
-i & 0 & i
\end{bmatrix}
\begin{bmatrix}
I_{L0} \\
I_{La} \\
I_{Lb} \\
I_{Lc}
\end{bmatrix}
\] (13)

After that, the reference currents are computed based on Equation (15)

\[
\begin{bmatrix}
I_{Ra} \\
I_{Rb} \\
I_{Re}
\end{bmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix}
1 & 0 & 1 \\
1 & \frac{\sqrt{3}}{2} & \frac{\sqrt{3}}{2} \\
1 & -\frac{\sqrt{3}}{2} & -\frac{\sqrt{3}}{2}
\end{bmatrix}
\begin{bmatrix}
I_{Ra} \\
I_{Rb} \\
I_{Re}
\end{bmatrix}
\] (15)

Based on the \(I_{off}\), the error current is computed which must be compensated with the FOPID controller with the BWO algorithm [48]. In the shunt active power filter, the optical pulses are selected and created on the error values of the system. The optimal pulses are generated with the help of the BWO algorithm. A detailed description of the FOPID controller with BWO optimization is presented in the sections below.

![Shunt active filter controller](image)

**Figure 7.** Shunt active filter controller.

### 4.5. Strategies of DPFC Regulation

The control structure plays a crucial length in identifying and strengthening DPFC’s efficiency in minimizing the quality issues like sag, swell, and harmonics. Usage of this controller for the voltage and harmonics allows the problems arising in the power system to be investigated, recognized, and analyzed. The controller’s management technique solves the PQ challenges on the distribution side by taking three steps: (1) The system voltage under fault conditions has to be recognized. (2) It is important to produce switching pulses for the operation of converters. (3) It should then produce the necessary reference voltage, which is required for modification purposes. DPFC incorporating PI, and FOPID controllers are used to alleviate PQ problems (e.g., sag, swell, and harmonics). A study of harmonics is also done for each controller [49].

### 4.6. Regulation of DPFC

This section will explain the PI controller for DPFC. By comparing the reference and actual voltage levels, the PI controller is used for producing the proper gate signals for the inverter’s operation. Swell, sag, and other circumstances were created to test the PI
controller’s behavior. The PI controller’s settings are tuned using BWO-based optimization. The DPFC controller is then activated to adjust for voltage swells and sags.

4.7. DPFC with BWO

A reference value is used to compute DC voltage in DPFC. As a consequence, the error is conveyed to the FOPID controller based on BWO, which creates the required power to activate the series and shunt controllers [50]. The difference between the reference voltage \(V_{dc}^{(ref)}\) and the actual DC voltage is received by the FOPID controller \(V_{dc}\). The error equation is as follows:

\[
E = V_{dc}^{(ref)} - V_{dc}
\]  

(16)

4.8. FOPID Controller

Figure 8 depicts the basic FOPID diagram [51]. The \(e(s)\) error signal helps in the generation of the \(u(s)\) control output. FOPID is designed to reduce PQ issues caused by voltage and current changes in the HRES device with BWO tuning. The FOPID controller has five parameters which receives the best gain values which are listed in Table 2, are obtained when tuned with BWO techniques with set of instructions as listed in Table 3. The FOPID controller control signal is formally expressed as (17),

\[
u(s) = K_p E(s) + K_i \int E(S) + K_d \frac{d}{dt} E(s)
\]

(17)

The following steps are required during the design of the controller:

- \(K_p\) is optimized for minimizing steady-state error and rise time
- \(K_d\) is optimized for minimizing the settling time and overshoot
- \(K_i\) is optimized for eliminating the steady-state error
- \(s^\lambda\) and \(s^\mu\) are fractional order parameters

Table 2. Gain values of FOPID.

| \(K_p\) | \(K_i\) | \(K_d\) | \(\lambda\) | \(\mu\) |
|---|---|---|---|---|
| 49.426 | 29.750 | 0.3953 | 1 | 0.82 |

Table 3. BWO parameters appendix.

| Objective Function | Parameter Value |
|--------------------|-----------------|
| Population size    | 50              |
| No of Iterations   | 50              |
| No of variables    | 5               |
| \(K_p\)            | 49.426          |
| \(K_i\)            | 29.7500         |
| \(K_d\)            | 0.3953          |
| \(\lambda\)        | 1               |
| \(\mu\)            | 0.82            |
Figure 8. FOPID controller.

4.9. DC Voltage Regulation Using BWO Optimization

The controller’s voltage source inverter (VSI) does not supply reactive power, has no reactive power, and should be utilized at steady state [52]. The primary aim here is to get FOPID optimal values, which result in extremely low DC voltage \( V_{dc} \) dynamics. The ability to fine-tune FOPID to improve settling time and overshoot. This is accomplished using BWO optimization, which reduces the \( V_{dc} \) variation from the reference \( V_{dc}^* \). The major factors that determine the optimality of FOPIDC are the rising time \( t_r \), maximum overshoot \( V_{c \ max} \), and steady-state error \( E_{ss} \) [53]. The major goal here is to reduce the dc voltage variation \( V_{dc} \), which is expressed as

\[
\Delta V_{dc} = V_{dc}^* - V_{dc}
\]

Equation (19) represents the objective function \( J \) which is generally considered to be integral time absolute error (ITAE) which minimizes the error between \( V_{dc} \) and \( V_{dc}^* \). With the use of the BWO technique, the best gain values of FOPID controller are obtained which results in low DC voltage \( V_{dc} \) [54].

\[
J = \int_0^t |e(t)| \, dt = \int_0^t |V_{dc} - V_{dc(ref)}| \, dt
\]

5. Black Widow Optimization

The BWO technique is illustrated in Figure 9. The proposed BWO technique generated the best gain parameters for the FOPID controller. BWO is an evolutionary algorithm that begins with the initialization of the population of spiders in which each spider is a possible solution. The initial population of spiders tries to reproduce the new generation in pairs. During or after mating, the female black widow consumes the male [55]. In her semen, they hold accumulated sperms and releases them into egg sacs. Spiderlings come out of the sacs as early as 11 days after being laid. For many days to a week, they cohabit on the maternal network, during this time sibling cannibalism is detected. Later they leave by being carried on the wind.
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**Figure 9.** Flow chart of BWO optimization.

### 5.1. Step 1: Initialize Population

To solve an optimization problem, first, create an appropriate framework for resolving the current problem using the values of the problem variables. This structure is referred to as a “Chromosome” and a “Particle position” in GA and PSO methods, respectively. In BWO, it is referred to as “Widow.” Each possible option is represented by a black widow spider that shows the problem variables’ values. The structure should be seen as an array to solve benchmark functions in this work.

In a problem of $N_{\text{var}}$-dimensional optimization, a widow is an array of $1 \times N_{\text{var}}$, describing the problem’s solution. Equation (20) defines the array as follows:

$$\text{Widow} = [X_1, X_2, \ldots, X_{N_{\text{var}}}] \quad (20)$$

All the variable values $(X_1, X_2, \ldots, X_{N_{\text{var}}})$ are the floating-point number. The fitness of widow is obtained by evaluation of fitness function $f$ at a widow of $(X_1, X_2, \ldots, X_{N_{\text{var}}})$. So (21),

$$\text{Fitness} = f(\text{Widow}) = f(X_1, X_2, \ldots, X_{N_{\text{var}}}) \quad (21)$$

To begin with the optimization technique, a candidate matrix of widows with an initial spider population, the size $N_{\text{pop}} \times N_{\text{var}}$ is created. Then we randomly choose pairs of parents to execute the procreation stage by mating, in which the female consumes the male black widow during mating.

### 5.2. Step 2: Procreate

The couples begin to mate and reproduce in the present generation since they are self-contained. Each pair mates autonomously from the others in nature. During each mating, around 1000 eggs are laid, with any larger spider babies surviving. This method might alternatively make use of an array named alpha. The offspring are produced using...
alpha, where $x_1$ and $x_2$ are parents and $y_1$ and $y_2$ are ancestors, as illustrated below, as long as the widow array is generated with random values (22).

$$Y_1 = \alpha x_1 + (1 - \alpha)x_2$$

$$Y_2 = \alpha x_2 + (1 - \alpha)x_1$$

(22)

This procedure is continuously repeated for $N_{var}/2$ times, with randomly chosen variables which should not be repeated. Finally, the infants and mothers are added to an array and categorized according to their health importance, now focused on a few of the best citizens which are applied to the newly produced population according to the cannibalism ranking. This procedure is applied to all the pairs.

5.3. Step 3: Cannibalism

In our world, there are three sorts of cannibalism. The first is sexual cannibalism, in which the black widow eats her partner while they are mating or immediately thereafter. Based on their fitness scores, we should be able to distinguish the female and male in this manner. The second kind of sibling cannibalism, in which the stronger spiderlings consume the weaker siblings. In this method, we create a cannibalism rating (CR) ranking, from which the number of survivors is calculated. In circumstances when the newborn consumes its mother, the third kind of cannibalism occurs. We utilize the fitness value of little spiderlings to estimate high fitness value.

5.4. Step 4: Mutations

At this point, we randomly pick the number of people from the population. As Figure 10 reveals, each of the choices selected two items in the sequence is shared at random. The evaluation of mutation decides Mutepop.

![Figure 10. Mutation.](image)

5.5. Step 5: Convergences

Compared to the other most evolutionary techniques, here three-stop requirements can also be considered: (a) the number of predefined iterations; (b) observance in conformity with several iterations, no improvement in the fitness value of the best widow; (c) achieving the degree of precision mentioned. BWO would be extended to some benchmarks in the next segment. Since optimal results are known in advance for benchmark functions, thereby we can achieve a defined degree of accuracy standard. The maximum sum of iteration is often set as a stop as well.

5.6. Step 6: Setting Parameters

To produce vest results with the proposed BWO technique requires certain parameters like rate of procreation (PP), cannibalism rate (CR), and the rate of mutation (PM). The chosen values in this paper for these parameters are seen in Table 3. The parameters should be changed accordingly to boost the algorithm’s efficiency in obtaining the best results. The more the number of parameters is tuned, the greater the probability of jumping out of every optimal and greater local capability to find them in the search space globally. Consequently, the correct set of parameters that regulate the equilibrium between exploitation and exploration should be assured. The proposed algorithm has three controlling
parameters, PP, CR, and PM. The number is PP in procreation, which defines how many persons are interested in procreation. By monitoring the output, further diversification is given by separate offspring which gives more opportunities to more specifically analyze the quest space. The cannibalism operator’s control parameter, the CR, excludes the community from the wrong persons. By adjusting the needed value for this parameter, great efficiency for the exploitation step may be ensured by changing the quest agents from the local to the global stage and vice versa. PM denotes the percentage of people who are involved. In mutation, the appropriate value for this parameter should be employed to ensure that the duration between exploitation and exploration is balanced. This parameter may monitor the transition of the quest agents from the global level to the state and local level and drive them to the correct decision.

5.7. Step 7: Stop

The following step-by-step procedure repeats until the final optimal pulses are generated by the proposed FOPID controller

6. Results and Discussion

In this section, the performance of the proposed method is validated and analyzed. The proposed method for mitigating PQ issues in grid-connected HRES has been developed. The main aim is to increase the system’s reliability by minimizing problems with the quality of electricity. There are drawbacks to the current methods that were listed in Section 4. So, with the aid of the proposed controller and DPFC, the efficiency of the system is enhanced. Here, the proposed method will include the current and voltage regulations in integrated HRES using DPFC with the proposed controller. Based on voltage, current, and fault conditions such as sag, swell voltage distortion, and harmonics with THD, the HRES grid-connected device with the suggested controller is evaluated. The FOPID parameters are evaluated with and without the system’s PQ problems. In MATLAB/Simulink with the processor, the predicted method is executed on a computer with the following specifications: Intel(R) Core(TM) i5-3570S CPU @ 3.10 GHz, and the presentation is analyzed. The projected technique is compared with twelve techniques P&O, PSO, Cuckoo, GA, GSA, BBO, Whale, ESA, RFA, ASO, EVORFA, and BWO with existing PI controller techniques. The criteria for implementation are presented in Table 4.

Table 4. Implementation parameters.

| Parameters                           | Ratings       |
|--------------------------------------|---------------|
| **PV**                               |               |
| Irradiance                           | 1000 W/m²     |
| Diode resistance                     | 595.5 Ω       |
| Forward voltage                      | 0.8 V         |
| **Wind**                             |               |
| Base torque                          | 8500 N/m      |
| Nominal mechanical output power      | 80 kW         |
| The base power of the electrical generator | 80 kW/0.9 |
| Base wind speed                      | 12 m/s        |
| Base rotational speed                | 0.4 m/s       |
| Stator phase resistance              | 1.5 Ω         |
| Armature inductance                  | 8.5e-3 H      |
| **Grid**                             |               |
| \(V_{Ph}\) (Phase Voltage)           | 550 V         |
| Frequency                            | 50 Hz         |
| **Load**                             |               |
| Nominal voltage                      | 550 V         |
| Real power                           | 1000e-3 W     |
The proposed technique is verified in the system using PQ concerns like sag, swell, disturbance, and harmonics. The PQ difficulties are compensated by delivering ideal shunt and series active power filter pulses with the help of the BWO-FOPID controller, which ideally regulates the PQ issues using the DPFC and the needed suggested shunt and series active power filter controller [56]. The DPFC will supply the required power to mitigate the PQ issues and adjust for load demand in the system. In the proposed HRES system, the DPFC configuration is employed to deliver critical power and mitigate PQ concerns. PV and WT systems can generate enough power to fulfill the load demand, which is considered the system’s major source of electricity [57]. The WT and PV may be impacted by external influences that may be mitigated by factors including the MPPT approach in the device [58]. The DPFC system uses the generated electricity to counter load demand and mitigate PQ issues in the system. The DPFC system is compatible with a grid-connected load scheme. Evaluation of the predicted design structure is done under the three failure circumstances of sag, swell, voltage, disturbance, as well as the device’s signal harmonic levels. Three different examples are utilized to assess the device output; these scenarios are explained below.

Case 1: Analysis of HRES during constant irradiance and wind speed
Case 2: Analysis of HRES during variable irradiance and wind speed
Case 3: Condition for voltage sag and current sag
Case 4: Condition for voltage and current swell
Case 5: Condition for voltage and current disturbances

The five cases are individually analyzed with the design parameters of voltage, current, power, and injected power from the DPFC.

Case 1: Analysis of HRES during constant and variable inputs

Here in this case HRES performance is simulated under constant irradiance and wind speed of PV and wind. The input to the PV is set to 1000 W/m² and WT velocity to 12 m/s to compensate for the load demand in the system. From Figure 11 we can observe the power produced with the PV and WT sources is 31 kW and 80 W. Thus, the generated power is transferred to meet the load demand. When surplus energy is generated it can be stored in the battery and can be used under critical conditions. Figure 12 shows the battery requirements for SOC and battery capacity

Case 2: Analysis of HRES during variable irradiance and wind speed

From Figure 13 we can observe the analysis of HRES simulated under variable irradiance and wind speed. The produced power of PV is modified depending on the variance of irradiance since irradiance is directly proportional to the generated power. The power produced by PV is increased if irradiance is increased. Similarly, the pace of WT is increased, as well as the power produced by WT. The analysis of HRES is shown in Table 3.

Case 3: Condition for voltage and current sag

For the stable and linear operation of the device, the voltage sag has to be eliminated. The DPFC is used to provide the necessary power to fulfill the demand for loads and eliminate PQ problems. DPFC designed is used to mitigate the PQ issues related to both voltage and current sag under fault conditions with the proposed controller. The series and shunt active power filters are playing an important role to balance the current and voltage in the HRES system.

In the proposed HRES interface grid-connected system, a three-phase fault has been created. Due to this fault, there is a sudden reduction of voltage and current is observed which is generally termed to be sag. To overcome this condition the proposed DPFC and the controller is designed to operate with the use of the FOPID controller. The optimal pulse is generated using the BWO technique which is used to eliminate the PQ problem and compensate load demand. During a three-phase fault at \( t = 0.75 \) s to \( t = 0.85 \) s, voltage sag of 200 V is observed as shown in Figure 14a. The sag is then eliminated with the injected voltage of 300 V which can be seen in Figure 14b. From Figure 14c the load voltage after elimination of voltage sag can be observed. Similarly, during \( t = 0.75 \) s to \( 0.85 \) s, current...
sag is observed under fault conditions. Figure 15a shows the current decrease during sag of about 0.2A. Figure 15b shows the injected current of 1.8A and Figure 15c shows the compensated load current of 2A.

Figure 11. Performance under constant HRES conditions: (a) PV irradiance, (b) PV power, (c) wind speed, and (d) wind power.
Figure 12. Analysis of battery power.

Figure 13. Cont.
Figure 13. Performance under variable HRES: (a) PV irradiance, (b) PV power, (c) wind speed, and (d) wind power.

Figure 14. Performance under voltage sag: (a) source voltage (b), injected voltage, and (c) load voltage.
Case 4: Condition for voltage and current swell

In the proposed HRES interface grid-connected system, a three-phase fault has been created. Due to this fault, there is a sudden increase of voltage and current is observed which is generally termed swell. The swell is characterized as an increased voltage and current concerning reference voltage and current. For the constant operation in the network, the swell produced under fault conditions must be eliminated using the suggested controller. To overcome this condition, the proposed DPFC and the controller is designed to operate with the use of the FOPID controller. The optimal pulse is generated using the BWO technique which is used to eliminate the PQ problem and compensate load demand. During a three-phase fault at $t = 0.75$ s to $t = 0.85$ s, a voltage swell of 100 V is observed as shown in Figure 16a. The swell is then eliminated with the absorption voltage of 100 V which can be seen in Figure 16b. From Figure 16c the load voltage after elimination of voltage swell can be observed. Similarly, during $t = 0.75$ s to 0.85 s, current swell is observed under fault conditions. Figure 17a shows the current increase during sag of about 1.8 A. Figure 17b shows the absorbed current of 1.8 A and Figure 17c shows the compensated load current of 2 A with the assistance of the suggested controller. The PQ problems, and load demand targets are accomplished. To calculate the right error values by choosing optimum device values, the series, and shunt active power filters with FOPID controller-based BWO are primarily involved. In the segment below, the voltage disruption situations are evaluated.
Case 5: Condition for voltage and current disruptions

Normally, signal distortion happens as a result of the non-linear load relationship. When a non-linear load is introduced to the load side, the impact of voltage changes in the network is instantly established. The suggested controller is meant to adjust for load demand and PQ difficulties in the device, allowing the HRES device to have a continuous impact. The proposed effect is designed to provide for safe working and PQ problems reduction in the HRES framework under non-linear load, essential load, and unbalanced load situations. The BWO is utilized to offer the optimal gain settings of the FOPID controller to reduce the error value of the $V_{ref}$ and $I_{ref}$ in the signals. The suggested controller would correct for load demand and PQ problems in the HRES framework resulting from a variety of research scenarios. It is compared to previously devised methodologies to validate the proposed system evaluation. The anticipated system’s comparative analysis is detailed in the related unit.

From Figure 18a we can observe the variation in three-phase voltages during $t = 0.75$ s to $t = 0.85$ s. Figure 18b shows the voltage difference is then corrected with the use of FOPID controller operating series controller of the DPFC which is mainly used to correct the voltage-related issues. In Figure 18c we can observe the load voltage after mitigating the disturbances. Similarly, in Figure 19a we can observe the variation in current from $t = 0.75$ s to $t = 0.85$ s due to the sudden changes in load. Figure 19b shows the current has been corrected using the FOPID controller using shunt controller which is used to mitigate the problems related to current. Figure 19c shows the load current after mitigation.

![Performance under voltage swell](image-url)
Figure 17. Performance under current swell: (a) source current, (b) injected current, and (c) load current.

Figure 18. Performance under voltage disruptions: (a) voltage disturbance, (b) injected voltage, and (c) load voltage.

Figure 18. Cont.
mitigating the disturbances. Similarly, in Figure 19a we can observe the variation in current from \( t = 0.75 \) s to \( t = 0.85 \) s due to the sudden changes in load. Figure 19b shows the current has been corrected using the FOPID controller using shunt controller which is used to mitigate the problems related to current. Figure 19c shows the load current after mitigation.

Figure 18. Performance under voltage disruptions: (a) voltage disturbance, (b) injected voltage, and (c) load voltage.

Figure 19. Performance under current disruptions: (a) source current, (b) injected current, and (c) load current.

7. Comparison Analysis

PQ issues in the HRES integrated system are analyzed with the use of the FOPID-based BWO technique with DPFC address issues like sag, swell, disruptions, and THD reduction. The FOPID controller is optimized with the proposed technique and compared with various existing methods, and we have proven that the proposed technique gives the best results. In this section, the results from Figure 20 represent the comparative analysis of HRES powers. In Figure 21a a real power comparison and Figure 21b reactive power comparison can be seen which is compared with PI controller, PSO, Cuckoo, GA, GSA,
BBO, Whale, ESA, RFA, ASO, EVORFA, and BWO. Figure 22a depicts THD analysis before compensation and after compensation. Figure 22b shows the THD analysis of various existing methods. The proposed HRES integrated system with BWO controller reduced the THDs, which falls within the limits of IEEE 519 standards. Table 5 provides the THD analysis before DPFC and Table 6 after DPFC.

![Figure 20. Comparative analysis of HRES power.](image)

**Table 5. THD analysis before DPFC.**

| S. No. | Techniques | 5    | 7    | 11   | 13   | 17   | 19   | 23   | 25   | 29   |
|-------|------------|------|------|------|------|------|------|------|------|------|
| 1     | Proposed   | 26.5 | 31.10| 25.11| 50.46| 20.02| 10.91| 20.34| 10.19| 6.34 |
| 2     | EVORFA     | 48.58| 32.03| 35.87| 50.85| 25.04| 12.66| 20.63| 10.36| 6.47 |
| 3     | ASO        | 52.06| 40.18| 40.15| 50.91| 30.04| 13.78| 20.67| 10.39| 6.22 |
| 4     | RFA        | 53.98| 42.26| 41.31| 50.95| 39.05| 14.84| 20.69| 10.40| 7.19 |
| 5     | ESA        | 56.06| 43.35| 42.47| 50.98| 40.04| 16.92| 20.73| 10.42| 8.23 |
| 6     | Whale      | 57.04| 48.40| 43.52| 50.99| 40.04| 17.95| 20.74| 10.42| 8.99 |
| 7     | BBO        | 58.30| 51.44| 45.65| 51.02| 41.05| 18.99| 20.75| 10.43| 8.36 |
| 8     | GSA        | 60.73| 52.54| 48.85| 51.06| 42.05| 24.07| 20.78| 10.45| 8.59 |
| 9     | GA         | 63.37| 57.65| 52.05| 51.11| 43.05| 26.16| 20.82| 10.47| 8.92 |
| 10    | Cuckoo     | 66.25| 59.77| 56.28| 51.16| 44.05| 26.26| 20.85| 10.49| 8.37 |
| 11    | PSO        | 69.41| 62.90| 58.54| 51.22| 46.06| 27.37| 20.91| 10.52| 8.96 |
| 12    | PI         | 76.72| 63.21| 60.12| 51.35| 48.06| 28.62| 20.99| 10.57| 8.64 |
Table 6. THD analysis after DPFC.

| S. No. | Techniques | 5   | 7   | 11  | 13  | 17  | 19  | 23  | 25  | 29  |
|--------|------------|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| 1      | Proposed   | 2.92| 0.20| 0.38| 0.08| 0.004|0.16|0.06|0.03|2.44|
| 2      | EVORFA     | 4.58| 0.12| 0.23| 0.05| 0.003|0.09|0.04|0.02|1.47|
| 3      | ASO        | 5.03| 0.21| 0.40| 0.08| 0.004|0.17|0.07|0.04|2.53|
| 4      | RFA        | 5.21| 0.22| 0.42| 0.09| 0.004|0.17|0.067|0.04|2.62|
| 5      | ESA        | 5.40| 0.23| 0.43| 0.09| 0.004|0.19|0.07|0.04|2.72|
| 6      | Whale      | 5.61| 0.23| 0.45| 0.098|0.005|0.19|0.07|0.04|2.82|
| 7      | BBO        | 5.83| 0.24| 0.46| 0.10| 0.005|0.20|0.08|0.043|2.94|
| 8      | GSA        | 6.07| 0.25| 0.48| 0.11| 0.01|0.21|0.08|0.05|3.06|
| 9      | GA         | 6.37| 0.27| 0.51| 0.11| 0.005|0.22|0.08|0.05|3.20|
| 10     | Cuckoo     | 6.63| 0.28| 0.53| 0.12| 0.006|0.23|0.09|0.05|3.34|
| 11     | PSO        | 6.94| 0.30| 0.55| 0.123|0.006|0.24|0.09|0.05|3.50|
| 13     | PI         | 7.67| 0.32| 0.61| 0.13| 0.006|0.26|0.099|0.05|3.86|

Figure 21. Cont.
Figure 21. Comparison analysis of (a) real power and (b) reactive power.

(a)

Figure 22. Cont.
Figure 22. Comparison analysis of THD: (a) proposed method and (b) existing methods.
8. Conclusions

Power quality improvement in HRES grid-connected systems has become a more advanced research area in DG integrated with HRES systems to eliminate PQ problems. With the use of non-linear loads, instability load, and high-frequency switching characteristics on the load side, PQ issues in the device are increased. FACT devices are playing an important role in solving the PQ issues in the HRES integrated system. DPFC is proposed to solve the PQ problems and compensate for the load demand. The HRES system is modeled with a DPFC device with a BWO-FOPID controller. DPFC is equipped with two proposed controllers, series active power filters and shunt active power filters. With the utilization of the DPFC system, the voltage and existing PQ problems are mitigated. In MATLAB/Simulink framework, the proposed approach was designed and validated. To verify the proposed procedure, three separate cases are evaluated by connecting non-linear loads such as sag, swell, disruption, and harmonics on the grid side. The proposed BWO-based FOPID controller is compared with P&O, PSO, Cuckoo, GA, GSA, BBO, Whale, ESA, RFA, ASO, EVORFA, and PI. The efficiency of the proposed approach has obtained the strongest results in terms of THD.

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Abbreviations

ASO Atom Search Optimization
BBO Biogeography Based Optimization
ESA Extended Search Algorithm
EVORFA Egyptian Vulture Optimization Random Forest Algorithm
GA Genetic Algorithm
GSA Genetic Search Algorithm
PSO Particle Swarm Optimization
RFA Random Forest Algorithm

LIST OF SYMBOLS

\( a \) Diode ideality factor
\( B \) Battery power
\( CR \) Cannibalism
\( DOD \) Depth of discharge rate of battery
\( e_i \) Source voltage
\( e(s) \) Error signal of HRES system
\( G(s) \) FOPID controller Transfer function
\( I_{Sc} \) Current
\( I_{Li} \) Line current
\( I_{ih} \) The output current of shunt active filter
\( I_{iL} \) Load current
\[ I^{\alpha}, I^{\beta} \]  Phase neutral currents
\[ I^{La}, I^{Lb}, I^{Lc} \]  Three-phase load currents
\[ I^{Ra}, I^{Rb}, I^{Rc} \]  Reference current of shunt active power filter
\[ k \]  Boltzmann’s constant
\[ K_p \]  Proportional parameters
\[ K_i \]  Integral parameters
\[ K_d \]  Derivative parameters
\[ N_{PV}(t) \]  Number of cells in the PV array
\[ P_{PV}(t) \]  Power of PV
\[ P_{MAX,W} \]  Maximum power of WT
\[ P(c) \]  Series filter Active power
\[ P_L \]  Demand power
\[ P_L(t) \]  Load demand of the system
\[ PP \]  Procreation
\[ PM \]  Rate of Mutation
\[ Q \]  Electron charge
\[ Q(c) \]  Reactive power of the series filter
\[ R_{SE} \]  Series resistance
\[ R_{SH} \]  Shunt resistance
\[ R^d \]  Transmission line resistance
\[ s^{-\lambda}, s^{\mu} \]  Fractional order parameters
\[ t \]  Temperature in Kelvin
\[ \mu \]  Battery self-discharge rate
\[ U(s) \]  Control output
\[ u(s) \]  Controller output
\[ V_p \]  The voltage of the cell
\[ V_{PV}(t) \]  Voltage of PV
\[ V^{ib} \]  The output voltage of the series active filter
\[ V(t) \]  Wind speed at time \( t \)
\[ V_r \]  Nominal wind speed
\[ V_{cutout} \]  Cut out speed in the WT
\[ V_{cutin} \]  Cut in the speed of WT
\[ V^a, V^b, V^c \]  Three-phase voltages
\[ V^{d(ac)} \]  Ac component voltage
\[ V^{d(dC)} \]  Dc component voltage
\[ V^{Ra}, V^{Rb}, V^{Rc} \]  Three-phase reference voltages
\[ V^{sa}, V^{sb}, V^{sc} \]  Three-phase supply voltages
\[ X_1 \text{ and } X_2 \]  Parents
\[ Y_1 \text{ and } Y_2 \]  Ancestors
\[ V^{d(ac)} \]  Ac component voltage
\[ V^{d(dC)} \]  Dc component voltage
\[ V^{Ra}, V^{Rb}, V^{Rc} \]  Three-phase reference voltages
\[ V^{sa}, V^{sb}, V^{sc} \]  Three-phase supply voltages
\[ X_1 \text{ and } X_2 \]  Parents
\[ Y_1 \text{ and } Y_2 \]  Ancestors

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