Linear Active Disturbance Rejection Control for DC Bus Voltage Under Low-Voltage Ride-Through at the Grid-Side of Energy Storage System

Youjie Ma 1, Luyong Yang 2*, Xuesong Zhou 1,*, Xia Yang 2, Yongliang Zhou 2 and Bo Zhang 2

1 Tianjin Key Laboratory for Control Theory and Application in Complicated Systems, Tianjin University of Technology, Tianjin 300384, China; 183125326@student.tjut.edu.cn;
2 Tianjin University of Technology, School of Electrical and Electronic Engineering, No.391 Binshui West Road, Xiqing District, Tianjin 300384, China; 183125325@student.tjut.edu.cn (X.Y.); 183125325@student.tjut.edu.cn (B.Z.)
* Correspondence: 183125317@student.tjut.edu.cn (L.Y.); 183125322@student.tjut.edu.cn (X.Z.)

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Abstract: The energy storage system has the characteristics of nonlinearity, strong coupling, variable parameters, and flexible mode switching between parallel and off grid. In order to improve the control performance of the grid-side inverter of the energy storage system, an improved Linear Active Disturbance Rejection Control (LADRC) based on proportional differentiation is proposed to replace the traditional LADRC in the voltage outer loop control. In this paper, the observation gain coefficient of the sum of the disturbances of the traditional Linear Extended State Observer (LESO) is improved to a proportional differentiation link, which effectively reduces the degree of the disturbance observation amplitude drop and the phase lag, and increases the observation bandwidth of LESO. Compared with traditional LADRC, it not only improves the observation accuracy of LESO for disturbance, but also improves the anti-interference performance of LADRC. Finally, the control effects of improved LADRC and traditional LADRC on low-voltage ride-through at different degrees are analyzed and compared through simulation, which proves the rationality of the controller designed in this paper.

Keywords: energy storage grid-connected inverter; linear active disturbance rejection control; linear extended state observer; proportional differential; observation accuracy

1. Introduction

In recent years, due to the needs of renewable energy and smart grid construction, power storage technology, as a new regulatory means, has been highly valued because of its flexible control and convenient use [1,2]. At this stage, with the continuous installation of large-capacity equipment into the network, the stable grid connection of energy storage systems is also critical [3,4]. In energy storage systems, energy transmission, power balance, and stability of the DC bus voltage are the key links of the system grid connection [5]. As the DC link voltage is an intermediate link for energy transmission and conversion of the energy storage grid-connected system, its stability directly affects whether the system-side converter and the grid-side converter can achieve independent control, but also directly affects whether the system can operate safely and stably. Therefore, the design of grid-connected inverter control system is the core of the whole grid-connected system. Grid-connected inverter is a power electronic converter to realize DC-AC conversion, and it is the core device to connect the system and power grid [6]. In many energy storage equipment, the super-capacitor has
become the leader of energy storage system, whether in the field of power suppression or low-voltage ride-through, it has demonstrated its unique advantages [7].

For the grid-connected energy storage system, the traditional control method is usually the voltage and current double loop control of proportional integral (PI) regulator, which has the advantages of a simple control structure and being easy to realize. However, in the case of strong coupling, strong nonlinearity, severe unknown disturbance and system parameters changing, the traditional PI control also makes it difficult to achieve the ideal control effect. For this reason, Reference [8] proposed an active disturbance control technology, only the rough model of the controlled object and the input and output signals of the system, which can design Extended State Observer (ESO) to observe the total disturbance and unmodeled error of the system [9], and make feed-forward compensation in the control quantity, so it has good engineering adaptability and robustness. There are successful application examples in many fields, such as wind power generation system, permanent magnet synchronous motor torque control system [10].

ESO is the key link that affects the performance of Active Disturbance Rejection Control (ADRC). In Reference [11], the convergence and boundedness of the observation error of LESO are systematically analyzed when the dynamic model of the system is known or unknown. In Reference [12], the tracking and estimation ability of high-order ESO for sinusoidal signals is analyzed. Reference [13] simplified the structure of nonlinear ADRC and proposed a LADRC method. The parameters of ADRC are simplified into controller bandwidth and observer bandwidth by pole assignment method. The anti-interference and stability of ADRC are studied in frequency domain. Reference [14] analyzes the dynamic tracking estimation capability and filtering characteristics of LESO from the frequency domain characteristics. Based on this, the closed-loop transfer function and frequency band characteristic curve of LADRC are obtained, and then the stability of the controller is analyzed systematically, the ability to suppressed external disturbances and the uncertainty of control input gain are discussed, and the relationship between the above dynamic characteristics and parameter configuration is discussed. However, with the development of industry, traditional LADRC also shows some shortcomings. In Reference [15], the observation of disturbance by LESO is greatly affected by noise with the increase of frequency, which affects the performance of the controller. Therefore, it is necessary to improve the traditional control method to ensure the stable control of the DC-side bus voltage of the energy storage system.

In this paper, the control of DC-side bus voltage of grid-connected inverter with super-capacitor energy storage is studied. Firstly, the modeling process of grid-connected inverter with energy storage is introduced. Based on the analysis of traditional LADRC control method, an improved second-order LADRC controller based on proportional differential is designed. Then, the dynamic process and anti-interference performance of grid-connected inverter with energy storage under the control of improved LADRC and traditional LADRC are compared and analyzed by theoretical proof. Finally, the controller designed in this paper is verified by Matlab / Simulink simulation.

2. Mathematical Modeling of Grid-side Inverter of Energy Storage System

The overall control structure of the grid-side inverter of the energy storage system [16] is shown in Figure 1, where \( u_{dc} \) is representative of the voltage of the DC side bus and \( i_{dc} \) is the current through the DC bus capacitor; \( R \) is the internal resistance of the filter inductance, \( L \) is the filter inductance and \( C \) is the filter capacitance; \( u_{gs} \), \( u_{gb} \) and \( u_{gc} \) are representative of the three-phase grid voltage; \( i_{gs} \), \( i_{gb} \) and \( i_{gc} \) are the output current of the inverter, respectively; \( V_i \), \((i=1,2,3,4,5,6)\) are wholly controlled switching devices Insulated Gate Bipolar Transistors (IGBTs) on the six bridge arms of the inverter. \( e_a \), \( e_b \), and \( e_c \) are AC output voltages of the converter; \( \overline{U_{abc}} \) depict three-phase voltage at the inverter side and output side, and \( \overline{I_{abc}} \) is the three-phase current output. The control structure of grid-connected inverter has two parts: the Space Vector Pulse
Width Modulation (SVPWM) hardware layer, and the control layer, which control methods are traditional Linear Active Disturbance Rejection Control (LADRC) and improved LADRC.

![Diagram of grid-side inverter control structure](image)

**Figure 1.** Overall control structure of grid-side inverter of energy storage system.

The reference direction is set as the incoming current direction. According to the main circuit equivalent model, the voltage equation of grid-connected inverter is as follows [17,18]:

\[
\begin{bmatrix}
    e_a \\
    e_b \\
    e_c 
\end{bmatrix} = \begin{bmatrix}
    u_{ga} \\
    u_{gb} \\
    u_{gc}
\end{bmatrix} + L \begin{bmatrix}
    \frac{di_{ga}}{dt} \\
    \frac{di_{gb}}{dt} \\
    \frac{di_{gc}}{dt}
\end{bmatrix} + R \begin{bmatrix}
    i_{ga} \\
    i_{gb} \\
    i_{gc}
\end{bmatrix}
\] (1)

It can be seen from Equation (1) that the design of the control system is complicated because the three-phase current is coupled with each other and is time variable in the time domain. Therefore, the coordinate transformation method is used to transform the mathematical model of converter in three-phase static coordinate system into two-phase synchronous rotation coordinate system. The transformation matrix is as follows:

\[
T_{abc/dq0} = \frac{2}{3} \begin{bmatrix}
    \cos \theta & \cos(\theta - 2\pi/3) & \cos(\theta + 2\pi/3) \\
    -\sin \theta & -\sin(\theta - 2\pi/3) & -\sin(\theta + 2\pi/3) \\
    \sqrt{2}/2 & \sqrt{2}/2 & \sqrt{2}/2
\end{bmatrix}
\] (2)

In Equation (2), \( \theta = \omega t + \theta_0 \) is the phase angle of Park transformation, that is, the angle between the \( a \) axis in the static three-phase \( abc \) coordinate system and the \( d \) axis in the rotating \( dq0 \) coordinate system, where \( \theta_0 \) is the initial phase angle.

After Park transforming Equation (1), the grid-side grid-connected inverter voltage equation in the synchronous rotating coordinate system is obtained:
In Equation (3), where \( u_{gd} \) and \( u_{gq} \) represent the components of grid voltage on the \( dq \) -axis respectively; \( i_d \) and \( i_q \) represent the components of the grid current on the \( dq \) -axis; \( u_d \) and \( u_q \) represent the components of the inverter output voltage on the \( dq \) -axis. If the phasor is three-phase symmetrical, the projection on axis \( d \) is \( E_m \), and the projection on axis \( q \) is 0, then the output voltage \( u_{gd} = E_m \), \( u_{gq} = 0 \), \( E_m \) of grid-side grid-connected inverter is the amplitude of phase voltage, and Equation (3) can be simplified as follows:

\[
\begin{align*}
    u_d &= -\left( R_i + L \frac{di_d}{dt} \right) + \omega L i_q + E \\
    u_q &= -\left( R_i + L \frac{di_q}{dt} \right) - \omega L i_d
\end{align*}
\]  

(4)

If the reference voltage and power are selected, and the system parameter adopts the unit value, the instantaneous output power of grid-connected inverter [19] can be obtained:

\[
\begin{align*}
    P &= u_d i_d + u_q i_q \\
    Q &= u_q i_d - u_d i_q
\end{align*}
\]  

(5)

![Figure 2. Control structure of energy storage grid-connected inverter.](image)

If the grid voltage is stable, the decoupling control of active power and reactive power can be realized by changing the \( d \) and \( q \) axis components of grid current. In the \( dq \) coordinate system, the calculated reference current is compared with the actual current output from the grid, and the deviation value of the current is obtained. After PI regulator, the command voltage is obtained, and then it is transformed from the rotating coordinate system to the static coordinate system. Finally, the
on–off of the switching tube is controlled by SVPWM to control the DC-side bus voltage. The control structure of grid-side grid-connected inverter is shown in Figure 2.

In the actual project, the grid connection of the system will be realized through a grid connection switch to realize the grid connection and off grid operation of the energy storage system [20]. When the grid voltage drops, the voltage of the grid-connected inverter also decreases. If the original input power is maintained, it will exceed the upper limit of the current regulation, which will inevitably lead to the increase of the grid-connected output current, and components will be destroyed by overcurrent at this time. If the control current is below the upper limit, it will lead to the imbalance of the input and output power, resulting in the sharp rise of the DC-side voltage. Therefore, the impact of grid voltage sags on the transient characteristics of grid-connected energy storage systems will be analyzed from the perspective of energy. The schematic is shown in Figure 3.

![Figure 3](image)

The active power $P_s$ of the super-capacitor energy storage system is input into the intermediate DC bus after passing through the DC/DC converter, ignoring the loss of the DC/DC converter, and the output power $P_s$ of the energy storage system is equal to the grid-connected input power, which can be expressed by the following equation:

$$P_s = u_{dc}i_s$$  \hspace{1cm} (6)

In Equation (6), where $i_s$ is the DC current output by DC/DC converter.

The current $i_c$ flowing through the DC-side capacitor is:

$$i_c = c\frac{du_{dc}}{dt} = i_s - i_g$$  \hspace{1cm} (7)

If the losses of the grid-connected inverter and the AC-side reactor are not considered, then the power input by the inverter from the DC-side to the grid can be expressed by Equation (8):

$$P_g = u_{gq}i_d + u_{gd}i_q = u_{dc}i_g$$  \hspace{1cm} (8)

In Equation (8), $i_d$ and $i_q$ represent the $d$ and $q$ axis components of grid-side current, $i_g$ stands for grid-side input current.

When the grid voltage remains stable, the power of both sides of the grid-connected inverter keeps balance. From Equations (6), (7) and Equation (8) will be obtained:

$$cu_{dc}\frac{du_{dc}}{dt} = u_{dc}i_s - u_{dc}i_g = P_s - P_g = \Delta P$$  \hspace{1cm} (9)

The power stored in the DC-side bus capacitance $C$ is:
\[ cu \frac{du}{dt} \Delta T = (P_s - P_s') \Delta T = \Delta P \cdot \Delta T \quad (10) \]

From the analysis of Equation (9), it can be seen that under the normal condition of power grid, if the voltage \( u_b \) at the DC-side remains unchanged, then the value \( \Delta P \) is zero.

When the grid voltage drops suddenly, the output current of grid-connected inverter is generally required to be no more than 1.1 times of the rated value. In order to limit the damage of components of grid-connected inverter due to over-current, it is necessary to keep the output current of grid-connected inverter within the upper limit of control. Its \( i_d \) and \( i_q \) remain unchanged, the output power \( P_s \) of grid connection will inevitably decrease, while the input power \( P_s \) of energy storage system will not change in a short time, which will lead to power imbalance at both ends of grid-connected inverter, and it is likely that the DC-side capacitance will be continuously increased due to the injected power. If the capacitance voltage rises too high, the DC-side capacitance will be broken down, which will eventually lead to the collapse of the whole system. Therefore, it is very important to design a controller with excellent performance to ensure the stability of DC-side bus voltage for the safety of the whole system.

3. Principle Analysis of Traditional LADRC

Traditional LADRC takes LESO as the control core, and the advantage of LESO is low model degree [13]. In addition to providing the observation values of all order state variables of the system, it can also estimate and compensate the total disturbance of the system in real time. When the estimation ability of LESO disturbance is enough, the system can be compensated into integral series type. Then, the expected performance index can be obtained through simple control rate.

3.1. Design of Traditional Third-order LESO

Generally, the differential equation of the second-order system is taken as an example for analysis:

\[ \ddot{y} = -a_1 \dot{y} - a_0 y + w + bu \quad (11) \]

In Equation (11), where \( u \) is the input and \( y \) is output of the system, \( w \) represent external disturbance, \( a_0 \) and \( a_1 \) are the parameters of the system, \( b \) is the input control gain, and \( b_0 \) is the gain estimate. Let \( x_1 = y \), \( x_2 = \dot{y} \), define \( f(y, \dot{y}, w) = -a_1 \dot{y} - a_0 y + w + (b-b_0)u \) as the system disturbance, and let \( x_3 = f(y, \dot{y}, w) \), \( h = f(y, \dot{y}, w) \), the equation of the system can be obtained:

\[
\begin{bmatrix}
\dot{x}_1 \\
\dot{x}_2 \\
\dot{x}_3
\end{bmatrix} =
\begin{bmatrix}
0 & 1 & 0 \\
0 & 0 & 1 \\
0 & 0 & 0
\end{bmatrix}
\begin{bmatrix}
x_1 \\
x_2 \\
x_3
\end{bmatrix} +
\begin{bmatrix}
0 \\
b_0 \\
0
\end{bmatrix} u +
\begin{bmatrix}
0 \\
h
\end{bmatrix}
\quad (12)
\]

From Equation (12) we can get the following equation:

\[
\begin{bmatrix}
\dot{z}_1 \\
\dot{z}_2 \\
\dot{z}_3
\end{bmatrix} =
\begin{bmatrix}
-\beta_1 & 1 & 0 \\
-\beta_2 & 0 & 1 \\
-\beta_3 & 0 & 0
\end{bmatrix}
\begin{bmatrix}
z_1 \\
z_2 \\
z_3
\end{bmatrix} +
\begin{bmatrix}
0 \\
b_0 \\
0
\end{bmatrix} u +
\begin{bmatrix}
\beta_1 \\
\beta_2 \\
\beta_3
\end{bmatrix} y
\quad (13)
\]

In Equation (13), where \( \beta_1 \), \( \beta_2 \) and \( \beta_3 \) are the coefficients of the observer, respectively. Take the control law of the system as:
The Linear State Error Feedback (LSEF) can be designed as [13]:

\[ u_0 = k_p (v - z_1) - k_d z_2 \]  

(15)

In Equation (15), where \( k_p \) and \( k_d \) represent the proportional and differential control gains, respectively, and the system can be stabilized by selecting the appropriate proportional differential gain coefficient.

Equations (13), (14) and (15) constitute the structural block diagram of LADRC of system Equation (11), as shown in Figure 4:

Figure 4. Structure of a second-order LADRC controller.

3.2. Parameter Tuning of Traditional Second-order LADRC

In traditional LADRC, the parameters of LESO and Linear State Error Feedback (LSEF) rate are mainly adjusted [13]. The parameters that need to be designed for the two are: observer gain coefficients \( \beta_1, \beta_2 \) and \( \beta_3 \) of LESO, and controller parameters \( k_p \) and \( k_d \) of LSEF. According to the separation principle [21], parameter design can be performed for each part separately.

3.2.1. Parameter Design of Third-order LESO

According to the pole configuration, the pole of Equation (13) is arranged on the bandwidth of the observer:

\[ \dot{\lambda}(s) = s^3 + \beta_1 s^2 + \beta_2 s + \beta_3 = (s + \omega_b)^3 \]  

(16)

Then

\[ \beta_1 = 3 \omega_b, \quad \beta_2 = 3 \omega_b^2, \quad \beta_3 = \omega_b^3 \]  

(17)

In Equation (17), \( \omega_b \) belongs to the observer bandwidth. Therefore, it is obvious from observation formula (17) that for LESO, only the parameter \( \omega_b \) needs to be designed. The larger \( \omega_b \), the larger the observation bandwidth of LESO, and the better the tracking effect [22]. However,
during the parameter adjustment process, it will be found that too large $\omega_0$ will also reduce the system’s immunity. Therefore, in actual engineering, the design of the observer bandwidth $\omega_0$ should not be too large.

3.2.2. Parameter Design of LSEF

According to Reference [14], a controller with linear Proportional Differential (PD) control is adopted, and the controller corresponding to the $n+1$ order LESO can take the characteristic polynomial of the closed-loop system through parameterization:

$$s^n + k_{dn-1}s^{n-1} + \cdots + k_d + k_p = (s + \omega_c)^n$$

(18)

In Equation (18), $k_p$, $k_d$, $\omega_c$ represent the gain of the controller to be designed, respectively, $\omega_c$ is the bandwidth of the controller. Through the above relationship, the proportional and differential gain of the controller can be obtained by determining the size of $\omega_c$. According to Equation (18), the gain of PD controller corresponding to third-order LESO can be obtained:

$$k_p = \omega_c^2, \quad k_d = 2\omega_c$$

(19)

Therefore, after parameterization, the controller bandwidth $\omega_c$ is the only parameter that needs to be designed in the PD controller. A larger value of $\omega_c$ can make the output response of the system faster and the dynamic process time becomes shorter. However, in the process of adjusting the parameters, it will be found that too large a value of $\omega_c$ will increase the load of the PD controller, which will increase the sensitivity of the system to noise, and in severe cases will cause system instability. Therefore, in the application of actual engineering, it is necessary to design the system parameters reasonably in combination with the fastness and stability of the system.

For the controller used in this paper, in the process of adjusting parameters, the general principle is to keep $\omega_c$ unchanged, and then gradually increase $\omega_0$, until the value of $\omega_0$ makes the effect of noise meet the system requirements. Then gradually increase $\omega_c$, reduce $\omega_0$ when the influence of noise is unbearable, and then increase $\omega_c$ to achieve the desired control effect. Finally, through the experience of adjusting the aforementioned parameters, we can determine that the LADRC parameters of the voltage outer loop are $\omega_c = 3500$ and $\omega_0 = 500$.

3.3. Performance Analysis of Traditional Third-order LESO

According to Equations (13) and (17), the transfer functions of $Z_1$, $Z_2$, and $Z_3$ can be obtained:

$$Z_1 = \frac{3\omega_0^5s^2 + 3\omega_0^4s + \omega_0^3}{(s + \omega_0)^3}y + \frac{b_0s}{(s + \omega_0)^3}u$$

$$Z_2 = \frac{3\omega_0^5s^2 + \omega_0^3}{(s + \omega_0)^3}y + \frac{b_0(s + 3\omega_0)s}{(s + \omega_0)^3}u$$

$$Z_3 = \frac{\omega_0^3s^2}{(s + \omega_0)^3}y + \frac{b_0\omega_0^3}{(s + \omega_0)^3}u$$

(20)

Make the tracking errors $e_1 = Z_1 - y$, $e_2 = Z_2 - \dot{y}$ will be obtained:
\begin{align*}
&\begin{aligned}
e_1 &= -\frac{s^3}{(s + \omega_0)} y + \frac{b_0 s}{(s + \omega_0)} u \\
e_2 &= -\frac{(s + 3\omega_0)s^3}{(s + \omega_0)^3} y + \frac{b_0 s}{(s + \omega_0)^3} u
\end{aligned}
\end{align*}
(21)

Make \( e_3 = z_3 - f(y, \dot{y}, w) \), according to Equation (12) will be configured as follows:

\[ f(y, \dot{y}, w) = x_3 - b_0 u = \dot{y} - b_0 u \]
(22)

Then

\[ e_3 = z_3 - \dot{y} + b_0 u = b_0 \left( 1 - \frac{\omega_0^3}{(s + \omega_0)^3} \right) u - \left( 1 - \frac{\omega_0^3}{(s + \omega_0)^3} \right) s^2 y \]
(23)

Considering the analysis typicality, \( y \) and \( u \) take the step signal \( y(s) = K / s \), \( u(s) = K / s \) whose amplitude is \( K \), then the steady-state error can be obtained as:

\[ \begin{aligned}
e_{1s} &= \lim_{s \to 0} se_1 = 0 \\
e_{2s} &= \lim_{s \to 0} se_2 = 0 \\
e_{3s} &= \lim_{s \to 0} se_3 = 0
\end{aligned} \]
(24)

The above equation shows that LESO has good convergence and estimation ability.

The following further analyzes the dynamic tracking process. When \( b_0 = 0 \), the response of \( Z_1 \) in Equation (20) to step signal \( y(s) = K / s \) will be:

\[ Z_1 = \frac{3\omega_0 s^2 + 3\omega_0^3 s + \omega_0^3}{(s + \omega_0)^3} = \frac{K}{s} \left( \frac{1}{s} - \frac{1}{s + \omega_0} + \frac{2\omega_0}{(s + \omega_0)^2} - \frac{\omega_0^2}{(s + \omega_0)^3} \right) \]
(25)

By inverse Laplace transformation:

\[ Z_1(t) = K - K \left( \frac{1}{2}\omega_0^2 t^2 - 2\omega_0 t + 1 \right) e^{-\omega_0 t} \]
(26)

Make \( \dot{Z}_1(t) = 0 \) get the extreme point as:

\[ t_1 = \left( 3 - \sqrt{3} \right) / \omega_0, \quad t_2 = \left( 3 + \sqrt{3} \right) / \omega_0 \]
(27)

According to Equation (27), although \( \omega_0 \) does not affect the overshoot, it will affect the tracking speed of LESO. The larger \( \omega_0 \) is, the faster the system response is. Therefore, in order to improve the tracking speed, it is more likely to improve the \( \omega_0 \). However, in the actual system, the size of \( \omega_0 \) is limited by the observation noise, and the increase of \( \omega_0 \) will also make the observation noise amplified, which will affect the control performance of the whole controller. Therefore, it is necessary
to improve the traditional LADRC so that when $\omega_0$ increases, the effect of the observation noise on the system is relatively small.

4. Structural Design and Performance Analysis of Improved LADRC

4.1. Design Principle of Improved LEO

According to Equations (20) and (22), the third-order LEO disturbance observation transfer function can be obtained:

$$\phi(s) = \frac{\beta_3}{s^3 + \beta_3 s^2 + \beta_2 s + \beta_1}$$

(28)

$\phi(s)$ is a third-order system. Because the theoretical analysis of the third-order system is too complex, on the other hand, because its frequency characteristics are similar to the standard second-order system in the middle and low frequency range, the third-order system can be approximately equivalent to the second-order system for analysis:

$$\phi(s) \approx \frac{\beta_3}{\beta_3 s^2 + \beta_2 s + \beta_1} = \frac{\beta_3 / \beta_1}{s^2 + \beta_2 s + \beta_3 / \beta_1}$$

(29)

$\phi(s)$ is a second-order system, it can be proved that there is a contradiction between response speed and overshoot in time domain. In frequency domain, $\phi(s)$ has the characteristics of phase lag and amplitude attenuation with the increase of frequency, which shows that the disturbance observation performance of traditional LEO is defective.

According to the comparison between Equation (29) and the standard second-order system, it can be seen that:

$$\omega_n = \sqrt{\frac{\beta_3}{\beta_1}}, \quad \zeta = \frac{\beta_2}{\sqrt{\beta_1 \beta_3}}$$

(30)

In Equation (30), where $\omega_n$ is the undamped natural oscillation angular frequency of the system while $\zeta$ is the damping ratio of the system.

In the standard second-order system, the time response and frequency response mainly depend on $\omega_n$ and $\zeta$. From Equation (30), it can be seen that the changes of $\omega_n$ and $\zeta$ affect the changes of $\beta_1$, $\beta_2$, and $\beta_3$ gains, and the changes of $\beta_1$ gain can affect $\omega_n$ and $\zeta$ at the same time. In short, among the three parameters, $\beta_3$ has the greatest impact on the system performance.

Through the above derivation and simple analysis, it can be seen that the traditional observation structure of LEO is similar to the standard second-order system, and there are some deficiencies in the structure, resulting in the increase of disturbance frequency, the observation ability of disturbance will be lower and lower. Therefore, an improved linear extended state observer is proposed by improving the observation gain coefficient $\beta_3$ of traditional LEO in this paper, which can effectively reduce the decrease of disturbance observation amplitude and phase lag, and then improve the disturbance rejection ability of LADRC. The improved equation is as follows:

$$\beta_3(s) = \beta_0 \left(1 + \beta_0 s\right)$$

(31)
In Equation (31), where $\beta_a$ and $\beta_b$ are proportional differential coefficients, respectively, and the improved $\beta_3$ includes the proportional differential link. At this time, the transfer function of disturbance observation can be expressed as:

$$\phi(s) = \frac{\beta_a (1+\beta_b s)}{s^3 + \beta_3 s^2 + (\beta_2 + \beta_1 \beta_3) s + \beta_2}$$

(32)

Compared with Equation (32) and Equation (28), the most obvious change is the addition of a zero point. In the control theory, it can be understood as follows: in the time domain, the function of zero point is to reduce the peak time, speed up the response speed of the system, and the closer the zero point is to the virtual axis, the more obvious the effect is; in the frequency domain, a leading network is connected in series, which reduces the slope of amplitude decline and the degree of phase lag, and improves the stability of the system.

![Graph showing the comparison of disturbance estimation capabilities of traditional LESO and improved LESO.](image)

**Figure 5.** Comparison of the disturbance estimation capabilities of traditional LESO and improved LESO.

Figure 5 shows the amplitude frequency and phase frequency characteristic curve of the disturbance transfer function of traditional LESO and improved LESO. The bandwidth and phase characteristics in the middle and low frequency range do not change significantly with the increase of frequency, and the index attenuation capacity at the high frequency is obviously slowed down. Compared with traditional LESO, the observation ability of improved LESO to disturbance is improved, the slope of amplitude decrease and the degree of phase lag are reduced, and the stability of the system is improved. The feasibility and effectiveness of improved LESO are proved.

4.2. **Structural Design of Improved LADRC**

According to Equation (20) and Equation (31), the transfer functions of improved $Z_1$, $Z_2$ and $Z_3$ can be obtained as follows:
\[
Z_1 = \frac{3\omega_0 s^2 + 3\omega_0^2 s + \omega_0^3 y + b_0}{(s + \omega_0)^3} + u
\]
\[
Z_2 = \frac{3\omega_0 s^2 + \omega_0^3 s}{(s + \omega_0)^3} y + b_0 \frac{(s + 3\omega_0) s}{(s + \omega_0)^3} u
\]
\[
Z_3 = \frac{\omega_0^3 s^2 + \omega_0^3 \beta_\ell s^3}{(s + \omega_0)^3} y - b_0 \omega_0^3 + b_0 \omega_0 \beta_\ell s u
\]

(33)

In conclusion, according to Equation (14) and Equation (32), the structure of improved LADRC control system is as follows Figure 6:

![Improved LADRC control system structure.](image)

Combining Equations (14), (15), and (19) can be expressed as the following equation:

\[
u = \frac{1}{b_0} \left( \omega_\ell^2 (v - z_1) - 2\omega_\ell z_2 - z_3 \right)
\]

(34)

Substituting Equation (33) into Equation (34) can be obtained as follows:

\[
u = \frac{1}{b_0} G_i(s) \left[ \omega_\ell^2 v - H(s) y \right]
\]

(35)

In Equation (35):

\[
G_i(s) = \frac{(s + \omega_0)^3}{s^3 + 3\omega_0 s^2 + 3\omega_0^2 s + \omega_0^3 s + 2\omega_\ell s^2 + 6\omega_\ell \omega_0 s - \omega_0^3 \beta_\ell s}
\]

\[
H(s) = \frac{\omega_0^3 \beta_\ell s^3 + (3\omega_0 \omega_\ell^2 + \omega_0^3) s^2 + (3\omega_\ell \omega_0^2 + 2\omega_\ell \omega_0^3) s + \omega_\ell \omega_0^3 + 6\omega_\ell \omega_0}{(s + \omega_0)^3}
\]

According to Equation (12), the controlled object can be recorded as:

\[
y = \frac{1}{s^2} (f + b_0 u)
\]

(36)
Simultaneous Equation (35) and Equation (36) can simplify the improved LADRC control system to be:

![Figure 7. Improved LADRC simplified system structure.](image)

According to Figure 7, the closed-loop transfer function of the system can be obtained by using the superposition theorem as follows:

$$y = \frac{G_1(s)}{s^2 + G_1(s)H(s)} \omega^2_v + \frac{1}{s^2 + G_1(s)H(s)} f$$

(37)

It can be known from Equation (37) that the output of the system is composed of a tracking term and a disturbance term. When the disturbance term is ignored, the system can achieve fast and no overshoot tracking of the reference input by adjusting the controller bandwidth \(\omega_c\) and the observer bandwidth \(\omega_0\).

5. Performance Analysis of Improved LADRC in Energy Storage Grid-connected System

5.1. Structure Design of Improved LADRC in Energy Storage Grid-connected System

According to the above analysis, an improved second-order LADRC voltage external loop controller based on the proportional differential is established. Through the above modeling of grid-side inverter, we can write the mathematical model expression of DC-side of grid-side inverter based on switch function in the \(dq\) coordinate system of two-phase synchronous rotation, as shown in Equation (38):

$$\frac{du_{ik}}{dt} = i_{dc} - \frac{3}{2C} \sum S_{gk}^d i_{gk}$$

(38)

In Equation (38), \(S_{gk}^d\) is the component of the switch function on the \(d\) axis. From the simplification of Equation (38) can be obtained as follows:

$$\frac{d^2u_{dc}}{dt^2} = \frac{3}{2LC} \sum S_{gk}^d \left(u_{gk} + i_{gk} R - e_{gk}\right) - \frac{\omega}{2C} \left(S_{gd}^g i_{gd} - S_{gd}^g i_{gd}\right)$$

(39)

It can be seen from Equation (39) that the mathematical model of the energy storage grid-connected inverter is a second-order system, so the improved second-order LADRC based on the proportional differential can be used in the control of the energy storage inverter. Now, Equation (39) is transformed into state space form can be expressed as the following equation:
\[
\begin{pmatrix}
\dot{x}_1 \\
\dot{x}_2 \\
\dot{x}_3 \\
y
\end{pmatrix} = \begin{pmatrix}
0 & 1 & 0 \\
0 & 0 & 1 \\
0 & 0 & 0
\end{pmatrix}
\begin{pmatrix}
x_1 \\
x_2 \\
x_3
\end{pmatrix} + \begin{pmatrix}
0 & 0 \\
b_0 & 0 \\
0 & 1
\end{pmatrix}
\begin{pmatrix}
u \\
h
\end{pmatrix}
\] (40)

In Equation (39), \( b_0 = \frac{3}{(2LC)} \), \( x_1 \) is bus voltage and \( x_2 \) is its differential, \( x_3 \) is the expanded state variable in LESO, the physical meaning is the total disturbance of the system, that is, \( f, h \) is the differential of \( f \), and the model of the total disturbance is as follows:

\[
f = \frac{3}{2LC} \sum_{k=d,q} S_{f_k} (i_{g_k} R - e_{g_k}) - \frac{\omega}{2C} (S_{p_d} i_{g_q} - S_{g_p} i_{p_d}) + \frac{3}{2LC} S_{g_p} u_{g_p}
\] (41)

The LSEF combined with the actual system can be designed as:

\[
\begin{align*}
    u_0 &= \omega_c^2 (u_{dc-ref} - z_1) - 2w_c z_2 \\
    u &= (-z_3 + u_0) / b_0 = i_{dc-ref}
\end{align*}
\] (42)

In Equation (42), \( u_{dc-ref} \) is the voltage value given by the DC bus, and \( i_{d-ref} \) is the reference value given by the inner ring d-axis. Therefore, the improved second-order LADRC outer loop control block diagram based on the actual energy storage grid-connected inverter can be designed.

---

5.2. Analysis of Interference Immunity Tracking of Improved LADRC in Energy Storage System

From Equation (37), it can be seen that the effect of the disturbance term is related to the bandwidth \( \omega_c \) of the controller and the bandwidth \( \omega_0 \) of the observer.
Select $\omega_c = 100$, $\omega_c = 0.1, 1, 10, 20$ to get the frequency characteristic curve as shown in Figure 9; Take $\omega_c = 100$, $\omega_b = 0.1, 1, 10, 20$ to get the frequency characteristic curve as shown in Figure 10.

It can be known from the above two figures that the increase can make the disturbance gain decrease and the system's anti-interference ability enhanced.

Generally, in the energy storage grid-connected system, for the grid-connected inverter, the disturbance mainly comes from the grid-side (load side) current, based on the grid current feed-forward control strategy can effectively suppress the impact of disturbance on the grid-connected voltage control [23]. In the following, we mainly analyze the influence of controller bandwidth $\omega_c$ and observer bandwidth $\omega_b$ of improved LADRC on the tracking characteristics and anti-grid voltage disturbance characteristics of the actual grid-connected energy storage system.
The control diagram of the actual energy storage grid-connected system can be obtained from Figure 8:

![Control Diagram](image)

Figure 11. Improved LADRC energy storage grid-connected inverter system structure.

According to Figure 11, the voltage transfer function of DC bus output from the controlled system can be obtained:

\[
U_{dc} = \frac{G_1(s) \omega_r^2}{b_0 \left[ L C s^2 + R C s \right] + G_1(s) H(s)} U_{ref} + \frac{b_0 \left( L s + R \right)}{b_0 \left[ L C s^2 + R C s \right] + G_1(s) H(s)} f
\]

\[
= C_1(s) \omega_r^2 U_{ref} + C_2(s) f
\]  

(43)

In Equation (43), where \( U_{dc} \), \( U_{ref} \) and \( f \) represent the DC bus voltage of the energy storage grid-connected inverter system, respectively, the reference value of the system output DC bus voltage and the grid voltage disturbance suffered by the system. \( C_1(s) \) represents the transfer function from the reference value \( U_{ref} \) of the output DC bus voltage of the energy storage grid-connected inverter system to the actual output voltage value \( U_{dc} \) of the system, that is, to realize the fast tracking performance (trace-ability) of the grid-connected inverter to the output voltage of the system; \( C_2(s) \) represents the transfer function from the grid voltage disturbance \( f \) to the actual output voltage \( U_{dc} \) of the system, that is, to achieve the grid-connected inverter to maintain the stability (disturbance rejection) of the DC bus voltage of the system.

It can be seen from the Figure 12 that compared with the traditional LADRC, the amplitude change of the improved LADRC control under the disturbance of the actual energy storage grid-connected system is significantly lower than that of the traditional LADRC, and the anti-interference ability is stronger. Therefore, in the energy storage grid-connected system, the improved active disturbance rejection control technology based on proportional differentiation designed in this paper has more advantages in practical systems, and also validates the effectiveness of the controller designed in this paper.
Figure 12. Phase curves of noise immunity of improved LADRC and traditional LADRC in energy storage grid-connected inverter system.

5.3. Stability Analysis of Improved LADRC in Energy Storage Systems

The influence of external disturbance and model parameter uncertainty is not considered temporarily, namely

\[ f(t, y, \dot{y}, w) = (b - b_0)u(t) \]

Laplace transform is

\[ F(s) = (b - b_0)U(s). \]

Simultaneous Equation (36) can be obtained:

\[ y = \frac{1}{s^2} \left[(b - b_0)U(s) + b_0U(s)\right] \]  

(44)

According to Equation (44), the closed-loop transfer function of the system will be:

\[ y = \frac{(s + \omega_0^2 \omega_c^2)}{a_5 s^5 + a_4 s^4 + a_3 s^3 + a_2 s^2 + a_1 s + a_0} \]  

(45)

In Equation (45):

\[ a_5 = b_0LC ; \quad a_4 = b_0RC + 3\omega_0 b_0 LC + 2\omega_1 b_0 LC \]

\[ a_3 = 3\omega_0^2 b_0 LC + \omega_c^2 b_0 LC + 6\omega_0 \omega_c b_0 LC + 3\omega_1 b_0 RC + 2\omega_1 b_0 RC + \omega_0^3 \beta_b - b_0 LC \omega_0^3 \beta_b \]

\[ a_2 = 3\omega_0^2 b_0 RC + \omega_c^2 b_0 RC + 6\omega_0 \omega_c b_0 RC + 3\omega_0^2 \omega_0^2 - b_0 RC \omega_0^3 \beta_b \]

\[ a_1 = 3\omega_0^2 \omega_c^2 + 2\omega_0^3 \omega_c ; \quad a_0 = \omega_0^3 \omega_c^2 + 6\omega_0^2 \omega_c \]

Since the controller bandwidth \( \omega_c \) and observer bandwidth \( \omega_0 \) are both positive, it can be seen from the parameters in Table A1 and Table A2 that \( a_i > 0 \) for \( i = 0, 1, 2, 3, 4, 5 \).
According to Liénard–Chipart stability criterion, the necessary and sufficient condition for the stability of the grid-connected inverter system is that its odd-order Hurwitz determinant is positive, namely \( \Delta_5 > 0, \Delta_3 > 0 \). According to the coefficients of Equation (45) and the parameters in Table A1 and Table A2 can be expressed as the following equation:

\[
\Delta_5 = \begin{vmatrix}
    a_1 & a_0 & 0 & 0 & 0 \\
    a_3 & a_2 & a_1 & a_0 & 0 \\
    0 & 0 & a_5 & a_4 & a_3 \\
    0 & 0 & 0 & 0 & a_1 \\
\end{vmatrix} > 0, \quad \Delta_3 = \begin{vmatrix}
    a_1 & a_0 & 0 \\
    a_3 & a_2 & a_1 \\
    a_5 & a_4 & a_3 \\
\end{vmatrix} > 0
\]  

(46)

Therefore, the energy storage grid-connected inverter system remains stable.

6. Simulation Analysis

In order to verify the effectiveness of the control strategy designed in this paper, the simulation model of grid connection of 0.3 MW super-capacitor energy storage system was established in MATLAB/Simulink. The simulation content was divided into two parts, one being the comparative analysis of the control effect of improved LADRC and traditional LADRC in the grid-connected system under steady state, the other being the comparative analysis of the control effect of the grid-side of the system under low-voltage ride-through. The main parameters of the grid-side of the super-capacitor energy storage system are shown in Table A1, and the parameters of the controller are shown in Table A2.

6.1. Comparative Analysis of Simulation in Steady State

The energy storage system operated in a stable state (without internal and external interference), and the system simulation time was 2 s. Other conditions were the same, comparing the DC-side bus voltage waveform under the two control modes of improved LADRC and traditional LADRC. At this time, the grid voltage waveform was normal and unaffected, as shown in Figure 13a. The voltage waveform of the DC-side bus under the two control modes is shown in Figure 13b.

![Waveform comparison of DC-side bus voltage in stable state under two control modes. (a) Grid connection voltage waveform; (b) DC-side bus voltage.](image)

From Figure 13b, it can be seen that the DC bus voltage fluctuation range under traditional LADRC control is significantly higher than that under improved LADRC control when the system is in stable state. Therefore, through the simulation comparison under the stable condition, the control effectiveness of improved LADRC is effectively proved.
It can be seen from Figure 14 that in improved LADRC control mode, the harmonic distortion rate of grid-connected current is significantly reduced, and the harmonic content of grid-connected current is reduced from 3.33% to 1.62%. It shows that improved LADRC has a significant role in restraining the harmonic distortion rate of grid current and improving the power quality of grid connection, and also shows the effectiveness of improved LADRC in system control.

![Figure 14](image-url)

**Figure 14.** Comparison of harmonic distortion rate of grid-connected current under two control modes (a) Analysis of harmonic distortion rate under traditional LADRC control; (b) Analysis of harmonic distortion rate under improved LADRC control.

6.2. Comparative Analysis of Simulation Under Disturbance

In fact, most of the systems work under disturbance. In order to further verify the effectiveness of the improved LADRC control in order to meet the actual needs of the system under the disturbance condition, aiming at the two different control modes of the improved LADRC and the traditional LADRC, this paper designs the grid-side low-voltage ride-through fault of the super-capacitor energy storage system, and carries out the simulation analysis of the symmetrical and asymmetric grid-side faults according to the magnitude of the low-voltage ride-through. The simulated fault conditions are as follows:

1. the voltage at the grid-side drops 30% symmetrically, falls at 0.3 s and recovers at 0.7 s;
2. the voltage at the grid-side drops by 30% asymmetrically, falls at 0.3 s and recovers at 0.7 s;
3. the voltage at the grid-side drops 60% symmetrically, falls at 0.3 s and recovers at 0.7 s;
4. the voltage at the grid-side drops by 60% asymmetrically, falls at 0.3 s and recovers at 0.7 s.

**Scenario 1**

Set the grid-side voltage to drop 30% symmetrically due to the low-voltage ride-through fault, when $t = 0.3$ s, the fault starts, when $t = 0.7$ s, the fault ends, and the system simulation time is 2 s, as shown in Figure 15a.

Under the same other conditions, compare the dynamic response waveform of DC-side bus voltage under 30% symmetrical voltage drop of grid-side under the two control modes, as shown in Figure 15b. Voltage fluctuation range and voltage stability are selected as important reference indices of control performance.

It can be seen from Figure 15b that the fluctuation range of the DC bus voltage controlled by traditional LADRC is 0.996p.u.–1.022p.u. during the low-voltage ride-through of 0.3p.u., while the fluctuation range of the bus voltage controlled by improved LADRC is 0.996p.u.–1.008p.u., and it can quickly reach the stable state of 1.0p.u. By comparison, improved LADRC control can promote the
stability of DC bus voltage under disturbance, which shows that improved LADRC has better anti-interference performance.

**Figure 15.** Dynamic response of bus voltage at DC-side under 30% symmetrical voltage drop at grid-side under two control modes (a) Grid connection voltage waveform; (b) DC-side bus voltage.

**Scenario 2**

The model is set so that the voltage at the grid connection side drops by 30% due to the low-voltage ride-through fault asymmetry. The fault starts at t = 0.3 s, and ends at t = 0.7 s. The system simulation time is 2 s, as shown in Figure 16a.

From Figure 16b, it can be seen that the fluctuation range of traditional LADRC controlled DC bus voltage is 0.994p.u.–1.013p.u. during the low-voltage ride-through of 0.3p.u., while the fluctuation range of improved LADRC controlled bus voltage is 0.996p.u.–1.008p.u., and it can quickly reach the stable state of 1.0p.u. Compared with symmetrical faults with the same degree of sag, the fluctuation range of DC-side bus voltage is significantly reduced, but the fluctuation duration is relatively long during the fault period, which is not easy to stabilize the system. Therefore, no matter what kind of fault, improved LADRC control can promote the stability of DC bus voltage better under the disturbance condition, which shows that improved LADRC has better anti-interference performance.

**Figure 16.** Dynamic response of bus voltage at DC-side under 30% asymmetrical voltage drop at grid-side under two control modes (a) Grid connection voltage waveform; (b) DC-side bus voltage.

**Scenario 3**

Set the grid-side voltage to drop 60% symmetrically due to the low-voltage ride-through fault, when t = 0.3 s, the fault starts, when t = 0.7 s, the fault ends, and the system simulation time is 2 s, as shown in Figure 17a.
From Figure 17b, it can be seen that the fluctuation range of traditional LADRC controlled DC bus voltage is 0.931p.u.–1.061p.u. during the low-voltage ride-through of 0.6p.u., while improved LADRC controlled bus voltage fluctuation range is 0.970p.u.–1.061p.u., and it can quickly reach stable state of 1.0p.u. By comparison, improved LADRC control can promote the stability of DC bus voltage under disturbance, which shows that improved LADRC has better anti-interference performance. Compared with the symmetrical voltage drop of 0.3p.u. at grid-side under Scenario 1, the fluctuation range of bus voltage at DC-side is significantly larger. Therefore, the symmetrical voltage drop at grid-side affects the fluctuation range of bus voltage at DC-side. The larger the voltage drop, the larger the fluctuation range of bus voltage at DC-side; the smaller the voltage drop, the smaller the fluctuation range of bus voltage at DC-side. Therefore, the improvement of voltage outer loop controller to ensure the stability of DC bus voltage is very important for the stable operation of the whole system.

![Figure 17](image1.png)

**Figure 17.** Dynamic response of bus voltage at DC-side under 60% symmetrical voltage drop at grid-side under two control modes (a) Grid connection voltage waveform; (b) DC-side bus voltage.

**Scenario 4**

Set the voltage at the grid connection side to drop 60% due to the low-voltage ride-through fault asymmetry. The fault starts at \( t = 0.3 \) s, and ends at \( t = 0.7 \) s. The system simulation time is 2 s, as shown in Figure 18a.

![Figure 18](image2.png)

**Figure 18.** Dynamic response of bus voltage at DC-side under 60% asymmetric voltage drop at grid-side under two control modes (a) Grid connection voltage waveform; (b) DC-side bus voltage.

It can be seen from Figure 18b that the fluctuation range of the DC bus voltage controlled by traditional LADRC is 0.990p.u.–1.029p.u. during the low-voltage ride-through of 0.6p.u., while the fluctuation range of the bus voltage controlled by improved LADRC is 0.990p.u.–1.020p.u., and it can quickly reach the stable state of 1.0p.u. In contrast, improved LADRC control can promote the
stability of DC bus voltage under disturbance, which shows that improved LADRC has better anti-interference performance. It also shows that improved LADRC has good applicability under asymmetric fault.

7. Conclusion

The DC-side bus voltage control of grid-connected inverter with energy storage is an important problem in the realization of stable grid-connected energy storage system. The quality of control performance directly determines the quality of grid-connected energy storage and the stability of DC-side bus voltage. In this paper, the energy storage grid-connected inverter is taken as the control object, and the DC-side bus voltage is taken as the research purpose. Aiming at the influence of low-voltage ride-through on the DC-side bus voltage, a method of improving the LADRC DC-side bus voltage based on proportional differential is proposed. The innovation of this paper is that on the basis of traditional LESO structure, the observation gain coefficient of total disturbance is improved to a proportional differential link. With the increase of frequency, the decrease of disturbance observation amplitude and phase lag are reduced effectively, the observation band width of LESO is increased, the effect of fast tracking compensation for total disturbance is achieved, at the same time, the disturbance rejection ability of the controller is improved. In this paper, the simulation analysis of symmetrical and asymmetric faults is carried out by setting 30% and 60% low-voltage ride-through at the grid-side, it is proved that the controller designed in this paper has better control performance of stabilizing DC-side bus voltage than the traditional controller.

In this paper, by improving the traditional LADRC, the control performance of the controller is obviously improved, but at the same time, the noise is amplified in the high frequency band, so that the system cannot be controlled. Therefore, the next research goal of this paper is to correct the improved LADRC link, so that the LESO is not affected by noise even in the high frequency range, so as to achieve better control effect of the controller. At the same time, we clearly realize that the biggest deficiency of this paper is the lack of physical experiments, which makes the theoretical results unable to be further verified. Therefore, in the next research, on the basis of the inadequate design of the controller in this paper, we should improve the theoretical results and carry out physical experiments, so that the theoretical results are more practical.

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Acronyms

| Acronym | Description |
|---------|-------------|
| LADRC   | Linear Active Disturbance Rejection Control |
| LESO    | Linear Extended State Observer |
| ADRC    | Active Disturbance Rejection Control |
| ESO     | Extended State Observer |
| PI      | Proportional Integral |
| IGBTs   | Insulated Gate Bipolar Transistors |
| DC      | Direct Current |
| AC      | Alternating Current |
| SVPWM   | Space Vector Pulse Width Modulation |
Appendix A

Table A1. Parameters of grid-connected side of 0.3MW super-capacitor energy storage system.

| Parameter                              | Parameter Value |
|----------------------------------------|-----------------|
| Rated power/ MW                        | 0.3             |
| Rated voltage/ V                       | 590             |
| Rated Frequency/ Hz                    | 50              |
| DC-side bus voltage/ V                 | 1070            |
| DC-side bus capacitor/ μF              | 240             |
| Grid-side filter resistance/ Ω         | 0.942           |
| Grid-side filter inductance/ μH        | 120             |
| Grid-side filter capacitor/ μF         | 147             |

Appendix B

Table A2. Controller parameters.

| Parameter                              | Parameter Value |
|----------------------------------------|-----------------|
| Controller bandwidth/ Wc               | 3500            |
| Observer bandwidth/ Wo                 | 500             |
| Control gain/ b0                       | 10000           |
| Proportional link coefficient/ βa      | 1000            |
| Differential link coefficient/ βb      | 0.4             |

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