A New Class of Multiple-rate Codes Based on Block Markov Superposition Transmission

Chulong Liang, Jingnan Hu, and Xiao Ma, Member, IEEE,
and Baoming Bai, Member, IEEE

Abstract

Hadamard transform (HT) as over the binary field provides a natural way to implement multiple-rate codes (referred to as HT-coset codes), where the code length $N = 2^p$ is fixed but the code dimension $K$ can be varied from 1 to $N - 1$ by adjusting the set of frozen bits. The HT-coset codes, including Reed-Muller (RM) codes and polar codes as typical examples, can share a pair of encoder and decoder with implementation complexity of order $O(N \log N)$. However, to guarantee that all codes with designated rates perform well, HT-coset coding usually requires a sufficiently large code length, which in turn causes difficulties in the determination of which bits are better for being frozen. In this paper, we propose to transmit short HT-coset codes in the so-called block Markov superposition transmission (BMST) manner. At the transmitter, signals are spatially coupled via superposition, resulting in long codes. At the receiver, these coupled signals are recovered by a sliding-window iterative soft successive cancellation decoding algorithm. Most importantly, the performance around or below the bit-error-rate (BER) of $10^{-5}$ can be predicted by a simple genie-aided lower bound. Both these bounds and simulation results show that the BMST of short HT-coset codes performs well (within one dB away from the corresponding Shannon limits) in a wide range of code rates.

Index Terms

Fast Hadamard transform (HT), iterative soft successive cancellation, multiple-rate codes, short polar codes, spatial coupling.

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Chulong Liang, Jingnan Hu and Xiao Ma are with the Department of Electronics and Communication Engineering, Sun Yat-sen University, Guangzhou 510006, China (e-mail: lchul@mail2.sysu.edu.cn, hujingn@mail2.sysu.edu.cn, maxiao@mail.sysu.edu.cn).

Baoming Bai is with State Lab. of ISN, Xidian University, Xi’an 710071, Shaanxi, China (e-mail: bmbai@mail.xidian.edu.cn).
I. INTRODUCTION

In practical communication systems, it is frequently required to implement several codes with different code rates. This is critical for wireless communication systems to implement the adaptive coded modulation [11], which can lower down the outage probability of a single code and hence allows more efficient use of the scarce bandwidth resources [2]. It is then desirable to implement codes with all different rates of interest with a pair of encoder and decoder [3]. Rate-compatible (RC) codes are a class of multiple-rate codes, which are usually constructed from a mother code by the use of code modifying techniques such as shortening information bits and extending or puncturing parity-check bits [4]–[8], as summarized in [9]. The rate-compatible codes usually have different code lengths for different code rates. For some applications, the code lengths can be varied in a wide range. For example, Raptor codes (with length as long as required) were optimized by designing the degree profiles to maximize the average throughput [10]. In 2009, Casado et al proposed multiple-rate codes with fixed code length by combining rows of the parity-check matrix of a mother code. In 2012, Liu et al constructed multiple-rate nonbinary low-density parity-check (LDPC) codes with fixed block length by using higher order Galois fields for codes of lower rates [11]. Although fixed-length multiple-rate codes are not applicable to hybrid automatic repeat request (HARQ) with incremental redundancies, they may find applications in some other scenarios.

- Fixed-length multiple-rate codes can be applied to the scenarios where a framing constraint is imposed on the physical layer. This occurs, for example, when orthogonal frequency division modulation (OFDM) with a fixed number of subcarriers is used [12]. In this scenario, fewer information bits should be encoded to maintain the reliability requirement when the channel is in a low signal-to-noise ratio (SNR) level.

- Fixed-length multiple-rate codes may also find applications in flash memory systems, where the channel capacity decreases with ages but the number of memory cells keeps unchanged [13]. The older flash memory, which has undergone a large number of erase cycles, has a degraded capacity and requires a code of lower rate; while the “younger” flash memory has a larger capacity and supports a code of higher rate. In this scenario, fixed-length multiple-rate codes with the same pair of encoder and decoder are preferred.
Block Markov superposition transmission (BMST) is a construction of big convolutional codes from short codes (referred to as basic codes) [14], [15], which has a good performance over the binary-input additive white Gaussian noise channel (BI-AWGN). In [15], it has been pointed out that any short code with fast encoding algorithm and soft-in soft-out (SISO) decoding algorithm can be chosen as the basic code. A sliding-window decoding (SWD) algorithm with a tunable decoding delay, as similar to the pipeline message-passing decoding algorithm of the LDPC convolutional codes [16], [17], can be implemented to decode the BMST system. Most importantly, the performance of the SWD algorithm in the low error rate region can be predicted by a simple genie-aided lower bound.

In this paper, the BMST is used to construct a new class of multiple-rate codes with fixed code length. First, we propose to construct short multiple-rate codes by adjusting the set of frozen bits in Hadamard transform (HT), resulting in HT-coset codes. This family of codes, which have length \( N = 2^p \) for some \( p > 0 \) and dimension \( K \) ranging from 1 to \( N - 1 \), can be implemented by the use of a pair of encoder and decoder with complexity of order \( O(N \log N) \). Such a pair of encoder and decoder were first demonstrated using normal graph for Reed-Muller (RM) codes by Forney in [18]. The HT-coset codes considered in this paper are also closely related to the polar codes [19], however, channel polarization plays a less important role here because the considered codes will be very short (typically \( N \leq 16 \)). Then, to improve the performance of the short HT-coset codes, we propose to transmit short HT-coset codes in the BMST manner [14], [15]. The resulting system, referred to as a BMST-HT system for convenience, takes as the basic code a Cartesian product of short HT-coset codes and hence has encoding/decoding complexity linearly growing with the transmission block length. In addition, the unified structure of the HT-coset codes can significantly simplify the hardware implementation. Simulation results show that the BMST-HT systems perform well (within one dB away from the corresponding Shannon limits) in a wide range of code rates.

The rest of this paper is organized as follows. In Section II, we present the encoding and decoding algorithm for the multiple-rate HT-coset codes. In Section III, we construct BMST-HT systems with a general design procedure and present the encoding and decoding algorithm for the multiple-rate BMST-HT systems. Section IV concludes this paper.
II. SHORT MULTIPLE-RATE CODES

A. Fast Hadamard Transform (FHT)

We consider the Hadamard transform (HT) defined over the binary field. Let $N = 2^p$ for some positive integer $p$. Any non-negative integer $j < N$ has a binary expansion $j \triangleq (j_{p-1}j_{p-2} \cdots j_1j_0)_2$ in the sense that $j = \sum_{0 \leq s \leq p-1} j_s 2^s$ and $j_s \in \{0, 1\}$. We call $(j, j') (j < j')$ an $s$-complementary pair if and only if their binary expansions differ only in the $s$-th bit. For example, $(0, 1)$ is a 0-complementary pair, $(4, 6)$ is a 1-complementary pair and $(3, 7)$ is a 2-complementary pair. A Hadamard matrix of order $N$ over the binary field $\mathbb{F}_2 = \{0, 1\}$ can be defined recursively as

$$H_N = \begin{bmatrix} H_{N/2} & H_{N/2} \\ 0 & H_{N/2} \end{bmatrix} \quad (1)$$

for $p > 1$ with $H_2 = \begin{bmatrix} 1 & 1 \\ 0 & 1 \end{bmatrix}$. Notice that $H_2$ is the transpose of $F_2$ in [19]. Let $u_0 = (u_{0,0}, u_{0,1}, \cdots, u_{0,N-1}) \in \mathbb{F}_2^N$ be a binary vector. The Hadamard transform of $u_0$ is defined as $u_p = u_0 H_N$, which can be computed recursively in $p$ stages, see Fig. 1 for reference.

Algorithm 1: The Fast Hadamard Transform

- At stage $s = 0, 1, \cdots, p-1$, compute $u_{s+1} \triangleq u_s T_s$, where $T_s$ is a linear transform, whose basic operation is the transform defined by $H_2$ as

$$u_{s+1,j} = u_{s,j},$$

$$u_{s+1,j'} = u_{s,j} + u_{s,j'},$$

for each $s$-complementary pair $(j, j')$, see Fig. 1(b) for reference.

We use $U_0$ and $U_p$ to denote a random vector and its Hadamard transform, respectively. That is, $U_p = U_0 H_N$, or equivalently, $U_0 = U_p H_N$. In this paper, a message associated with a discrete random variable is represented by its probability mass function. Suppose that, for each $j$, $0 \leq j \leq N - 1$, the a priori messages of $U_{0,j}$ and $U_{p,j}$ are available, which are denoted as

\footnote{This can be verified by checking that $H_N^2 = I_N$, the identity matrix of order $N$.}
Fig. 1. Fast Hadamard transform. (a) A general framework with $N = 2^p$. (b) The basic building block of the Hadamard transform at the $s$-stage, where $(j, j')$ is an $s$-complementary pair, meaning that their binary expansions differ only in the $s$-th bit.

For any given component (say $U_{0,j}$) of interest in $U$ and $V$, the extrinsic message (denoted by, say, $P^e_{U_{0,j}}(u), u \in \mathbb{F}_2$) is defined as a probability mass function that is proportional to the likelihood function, i.e.,

$$P^e_{U_{0,j}}(u) \propto \sum_{u_0, j} P_{U_p}(u_0 H_N) \prod_{0 \leq k \leq N-1, k \neq j} P^a_{U_{0,k}}(u_{0,k}), u \in \mathbb{F}_2$$

and

$$P^e_{U_{p,j}}(u) \propto \sum_{u_p, j} P_{U_0}(u_p H_N) \prod_{0 \leq k \leq N-1, k \neq j} P^a_{U_{p,k}}(u_{p,k}), u \in \mathbb{F}_2.$$

Note that the extrinsic message associated with a random variable is irrelevant to the associated a priori message. The complexity to compute all exact extrinsic messages using the above equations
is of order $O(N2^N)$, which, however, can be reduced if approximate extrinsic messages are tolerable. This can be attained by performing an iterative message passing/processing algorithm over the normal graph as shown in Fig. [1]a. The normal graph has $p$ super nodes, corresponding to $p$ stages, respectively, where the node $T_i$ imposes a constraint that $U_{s+1} = U_sT_s$. Recalling that the basic building block to implement the transformation $T_s$ is the transform defined by $H_2$, we have the following algorithm, which falls into the general framework given by Forney [18].

**Algorithm 2**: The Forward-backward SISO Algorithm

- **Initialization**: Assume that $P_{U_{s,j}}^a(u), u \in \mathbb{F}_2$ and $P_{U_{p,j}}^a(u), u \in \mathbb{F}_2(0 \leq j \leq N - 1)$ are available. All intermediate variables are initialized to have a uniform distribution over $\mathbb{F}_2$.
- **Iteration**: Perform iteratively the following steps $J$ times for a preset integer $J > 0$.

  **Backward recursion**: For $s = p-1, p-2, \cdots, 0$, for each $s$-complementary pair $(j, j')$, compute

  $$P_{U_{s,j}}^e(u) \propto \sum_{u' \in \mathbb{F}_2} P_{U_{s,j'}}^a(u') P_{U_{s+1,j}}^a(u) P_{U_{s+1,j'}}^a(u + u'),$$

  $$P_{U_{s,j'}}^e(u') \propto \sum_{u \in \mathbb{F}_2} P_{U_{s,j}}^a(u) P_{U_{s+1,j}}^a(u) P_{U_{s+1,j'}}^a(u + u')$$

  and update $P_{U_{s,j}}^a(u) = P_{U_{s,j}}^e(u)$ and $P_{U_{s,j'}}^a(u') = P_{U_{s,j'}}^e(u')$ for $u, u' \in \mathbb{F}_2$.

  **Forward recursion**: For $s = 0, 1, \cdots, p-1$, for each $s$-complementary pair $(j, j')$, compute

  $$P_{U_{s+1,j}}^e(u) \propto \sum_{u' \in \mathbb{F}_2} P_{U_{s+1,j'}}^a(u') P_{U_{s,j}}^a(u) P_{U_{s,j'}}^a(u + u'),$$

  $$P_{U_{s+1,j'}}^e(u') \propto \sum_{u \in \mathbb{F}_2} P_{U_{s+1,j}}^a(u) P_{U_{s,j}}^a(u) P_{U_{s,j'}}^a(u + u')$$

  and update $P_{U_{s+1,j}}^a(u) = P_{U_{s+1,j}}^e(u)$ and $P_{U_{s+1,j'}}^a(u') = P_{U_{s+1,j'}}^e(u')$ for $u, u' \in \mathbb{F}_2$.

**Remark.** It can be seen that both Algorithm 1 and Algorithm 2 have complexity of order $O(N \log N)$ since the fast Hadamard transform has $p = \log N$ stages and each stage can be decomposed into $N/2$ independent building blocks as specified by $H_2$, each of which is for one complementary pair.

**B. Multiple-rate Codes Based on Hadamard Transform**

Given $N$, we can construct a family of codes with dimension $K$ ranging from 1 to $N-1$. Each code in this family is encoded in the same manner $v = uH_N$. The code rate $K/N$ is
attained by fixing some $N - K$ bits in $u$ and leaving the other bits free to carry the information. Following [19], this family of codes are called $H_N$-coset (or HT-coset) codes. The issue is how to determine a good active set for a given $K$. For this end, we define a permutation matrix $\Pi_N$ of order $N$ such that the rows of the matrix $G_N = \Pi_N H_N$ has a desired order. We may define $\Pi_N$ following the polar coding approach [19], which requires sorting the Bhattacharyya parameters or the mutual information rates for some artificial channels and usually depends on the channel condition, say, the SNR. Here we define $\Pi_N$ according to a channel-independent rule, which was referred to as RM-rule in [19], such that the rows of $G_N = \Pi_N H_N$ are ordered with non-decreasing Hamming weight. Given $\Pi_N$, the encoding algorithm and decoding algorithm for the HT-coset codes are essentially the same as Algorithm 1 and Algorithm 2 respectively. We only need to freeze some $N - K$ bits when encoding and to initialize these frozen bits with deterministic messages when decoding. These two basic adaptations are summarized in the following algorithms for completeness, where $K$ is specified. See Fig. 2 for reference.

**Algorithm 3:** The Encoding Algorithm for HT-coset Codes

- **Input:** Take as input the information sequence $u = (u_0, u_1, \ldots, u_{K-1})$.
- **Zero padding and permutation:** The information sequence $u$ is expanded by padding $N - K$ zeros and then permuted by $\Pi_N$. The resulting vector is denoted by $\tilde{u} = (u_0, u_1, u_{K-1}, 0, \ldots, 0)\Pi_N$.
- **Output:** Deliver as output the codeword $v = \tilde{u} H_N$, which can be computed by Algorithm 1.

**Algorithm 4:** The SISO Decoding Algorithm for HT-coset Codes

- **Input:** Take as input $P_{V_j}^a(v), v \in \mathbb{F}_2$ for $0 \leq j \leq N - 1$ and $P_{U_j}^a(u), u \in \mathbb{F}_2$ for $0 \leq j \leq K - 1$, where the former are usually computable from the channel observations and the
latter are available from the source distribution, which are usually assumed to be uniform distribution.

- **A priori message freezing:** Set $P_{u_j}^a(0) = 1$ and $P_{u_j}^a(1) = 0$ for $K \leq j \leq N - 1$.
- **Output:** Deliver as output $P_{u_j}^e(u)$, $u \in \mathbb{F}_2(0 \leq j \leq K - 1)$ for hard decisions and $P_{v_j}^e(v)$, $v \in \mathbb{F}_2(0 \leq j \leq N - 1)$ for possible use in concatenated systems. These extrinsic messages can be computed by Algorithm 2 with the exception that the permutation $\Pi_N$ need to be taken into account.

C. A Construction Example

We take $N = 8$ as an example to illustrate the construction procedure. The Hadamard matrix of order 8 is given by

$$H_8 = \begin{bmatrix}
1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 \\
0 & 1 & 0 & 1 & 0 & 1 & 0 & 1 \\
0 & 0 & 1 & 1 & 0 & 0 & 1 & 1 \\
0 & 0 & 0 & 1 & 0 & 0 & 0 & 1 \\
0 & 0 & 0 & 0 & 1 & 1 & 1 & 1 \\
0 & 0 & 0 & 0 & 0 & 1 & 0 & 1 \\
0 & 0 & 0 & 0 & 0 & 0 & 1 & 1 \\
0 & 0 & 0 & 0 & 0 & 0 & 0 & 1
\end{bmatrix},$$

whose rows have Hamming weights $(8, 4, 4, 2, 4, 2, 2, 1)$, respectively. Hence a permutation matrix $\Pi_N$ is required to swap the 3rd row and the 4th row (counting from zero). The encoding process is depicted in Fig. 5 where the dimension $K$ can be varied from 1 to 7.

Assume that the codeword is transmitted over an additive white Gaussian noise (AWGN) channel with binary phase-shift keying (BPSK) signalling. The performances of the HT-coset codes at the BER around $10^{-5}$ can be evaluated by simulation, while the performances at the extremely low BER (say $10^{-10}$) can be predicted using the union bound with the help of the input-output weight enumerating function (IOWEF) [20], [21]. Since the number of codewords of the considered HT-coset codes with $N = 8$ is small, we compute the IOWEFs by an exhaustive search. Table II presents the IOWEFs of all HT-coset codes with $N = 8$. We have simulated all these HT-coset codes under the SISO decoding algorithm (Algorithms 2 and 4) with an iteration number $J = 3$. We find that all simulation curves (not shown here) except that of rate $4/8$
TABLE I
THE IOWEFs FOR HT-coset CODES WITH \(N = 8\)

| Codes \([8, k]\) | IOWEFs |
|-----------------|--------|
| \([8, 1]\)      | \(1 + XY^8\) |
| \([8, 2]\)      | \(1 + XY^4 + XY^8 + X^2Y^4\) |
| \([8, 3]\)      | \(1 + 2XY^4 + XY^8 + 3X^2Y^4 + X^3Y^4\) |
| \([8, 4]\)      | \(1 + 3XY^4 + XY^8 + 6X^2Y^4 + 4X^3Y^4 + X^4Y^4\) |
| \([8, 5]\)      | \(1 + XY^2 + 3XY^4 + XY^8 + 2X^2Y^2 + 7X^2Y^4 + X^2Y^6 + 7X^3Y^4 + 3X^3Y^6 + X^4Y^2 + 4X^4Y^4 + X^5Y^4\) |
| \([8, 6]\)      | \(1 + 2XY^2 + 3XY^4 + XY^8 + 5X^2Y^2 + 8X^2Y^4 + 2X^2X^6 + X^3Y^2 + 12X^3Y^4 + 7X^3Y^6 + 3X^4Y^2 + 11X^4Y^4 + X^4Y^6 + 4X^5Y^4 + 2X^5Y^6 + X^6Y^2\) |
| \([8, 7]\)      | \(1 + 3XY^2 + 3XY^4 + XY^8 + 9X^2Y^2 + 9X^2Y^4 + 3X^2Y^6 + 3X^3Y^2 + 20X^3Y^4 + 12X^3Y^6 + 9X^4Y^2 + 23X^4Y^4 + 3X^4Y^6 + 12X^5Y^4 + 9X^5Y^6 + 3X^6Y^2 + 3X^6Y^4 + X^6Y^6 + X^7Y^2\) |

match well with the corresponding union bounds at the BER lower than \(10^{-3}\). Fig. 4 shows the performance curves of the code with rate \(4/8\) (the \([8, 4]\) Reed-Muller code). We can see that the performance curve of the rate \(4/8\) code under the maximum a posterior probability (MAP) decoding matches well with the union bound at the BER lower than \(10^{-3}\). However, compared with the union bound, the SISO decoding algorithm (Algorithms 2 and 4) with \(J = 3\) causes a performance loss of about 0.5 dB. This performance loss can be narrowed to 0.2 dB by increasing \(J\), as already pointed out in [18].

III. BMST OF SHORT HT-COSET CODES

A. Choice of Encoding Memory

We have demonstrated that Hadamard transform can be used to construct a family of codes with constant length \(N\) and flexible dimension \(K\). This family of codes can be implemented by the same pair of encoder and decoder but with adjustable inputs. However, the performances are far away from the Shannon limits, as evidenced by the construction example with \(N = 8\). One
possible way to improve the performance is to enlarge the order of the Hadamard transform. As $N$ becomes sufficiently large, for any given dimension $K$, we have chance to select active set to approach the channel capacity. The difficulty lies in the choice of the active set when $N$ becomes large. Here we propose to combine the BMST with the HT-coset codes. As pointed out in [15], any short code can be embedded into the BMST system to obtain extra coding gain in the low BER region. The critical parameter for BMST is the encoding memory $m$, which predicts the extra coding gain of $10 \log_{10}(m + 1)$ dB over AWGN channels. Given this predictable extra coding gain, we can use the following deterministic procedure to find the encoding memory $m_K$ required by the BMST-HT system ($N = 2^p, p > 1$) to approach the Shannon limit at a target BER $p_{\text{target}}$.

**A General Procedure of Determining the Encoding Memories for the BMST-HT Systems**
The Memory Required for Each Code Rate Using the BMST of HT-coset Codes with $N = 8$ to Approach the Shannon Limit at the BER of $10^{-5}$

| Rate $R = K/8$ | 1/8 | 2/8 | 3/8 | 4/8 | 5/8 | 6/8 | 7/8 |
|----------------|-----|-----|-----|-----|-----|-----|-----|
| $\gamma^*_K$ (dB) | -1.2 | -0.8 | -0.3 | 0.2 | 0.8 | 1.6 | 2.9 |
| $\gamma_K$ (dB) | 9.6 | 9.8 | 8.4 | 7.7 | 8.9 | 8.6 | 8.2 |
| Gap $\gamma_K - \gamma^*_K$ (dB) | 10.8 | 10.6 | 8.7 | 7.5 | 8.1 | 7.0 | 5.3 |
| Memory $m_K$ | 11 | 10 | 6 | 5 | 5 | 4 | 2 |

Table II shows the memory required for each code rate using the BMST of HT-coset codes with $N = 8$ to approach the Shannon limit at a target BER of $10^{-5}$.

B. Encoding of BMST-HT System

A BMST-HT system is constructed by taking the encoder for the HT-coset codes as the basic encoder, which accepts as input a binary information sequence of length $BK$ and delivers as output a binary coded sequence of length $BN$. More precisely, the basic code $\mathcal{C}[n, k]$ is a $B$-fold Cartesian product of $\mathcal{C}[N, K]$, where $n = BN$ and $k = BK$. To approach the capacity,
we usually choose $B$ such that $BN > 10000$. Since $K$ can be varied from 1 to $N - 1$, the basic encoder actually works as a multiple-rate encoder. To guarantee that all members from the family of the considered HT-coset codes have capacity-approaching performance, the encoder must be furnished with $m \left(= \max_{1 \leq K \leq N-1} m_K \right)$ interleavers. The basic structure of the encoder is shown in Fig. 5 which consists of one basic encoder, represented by ENC, and $m$ interleavers, represented by $\Pi_1, \cdots, \Pi_m$, respectively. The switches between the buffers $D$ are used to adjust the encoding memory. In our simulations, all interleavers are randomly generated but fixed. The encoding algorithm is described as follows, where $K$ is specified. See Fig. 5 for reference.

**Algorithm 5:** The Encoding Algorithm for BMST-HT System

- **Input:** Take as input $L$ blocks of data $u^{(0)}, u^{(1)}, \cdots, u^{(L-1)}$, where $u^{(t)} \in \mathbb{F}_2^{BK}$.
- **Memory choosing:** Turn on the $m_K$ left-most switches and turn off the other $m - m_K$ switches see Fig. 5 for reference.
- **Output:** Deliver as output $L + m_K$ coded sub-blocks $c^{(0)}, c^{(1)}, \cdots, c^{(L+m_K-1)}$ for transmission, where $c^{(t)} \in \mathbb{F}_2^{BN}$. This can be done by the encoding algorithm of BMST with memory $m_K$ [22].

**Remark.** The code rate decreases to $\frac{KL}{N(L+m_K)}$ due to the termination. However, the rate loss is negligible for large $L$.

C. Decoding of BMST-HT System

Assume that $c^{(t)}$ is transmitted with BPSK signalling over AWGN channels, resulting in a received vector $y^{(t)}$. The decoding algorithm can be implemented as an iterative message processing/passing algorithm over a unified (high-level) normal graph, see Fig. 6 for an example.
of a BMST system with $L = 4$ and $m = 2$. The normal graph can be divided into $L + m$ layers, each of which typically consists of one node of type $\Box$ for the basic code, one node of type $\equiv$ that connects to $m$ future layers via interleavers, and one node of type $\oplus$ that connects to $m$ past layers. The basic structure is applicable to all members from the family of HT-coset codes. Once $K$ (hence the memory $m_K$) is specified, we only need to disconnect the edges (if they exist) between the $t$-th layer and the $(t + \ell)$-th layer for $t \geq 0$ and $\ell > m_K$. With this adaptation, the sliding-window decoding algorithm is the same as Algorithm 3 in [15] with $m$ replaced by $m_K$.

**Remark.** We need to point out that both the encoding and the decoding have linear complexity in the code length $n$. Actually, since the basic code is a $B$-fold Cartesian product of a short HT-coset code, the computational complexity at the node $\Box$ is linear in $B$ (equivalently, the basic code length $n$). In addition, the computational complexity at the node $\equiv$ as well as the node $\oplus$ is also linear with the basic code length $n$.

**D. A Construction Example (Continued)**

We continue the construction example given in Section II-C. We take the $B$-fold Cartesian product $C[8, K]^B(1 \leq K \leq 7)$ as the basic code, where $B = 1250$. The memory required for each $K$ to approach the corresponding Shannon limit at the BER of $10^{-5}$ is specified in Table II. Hence we need an encoder with a maximum memory $m = 11$. The SISO decoding algorithm (Algorithms 2 and 4) with an iteration number $J = 3$ is used to implement the SISO decoding algorithm for the basic code. The iterative sliding-window decoding algorithm for the BMST-HT system is performed with a maximum iteration number of 18, where the entropy-based early stopping criterion [15], [23] is used with a threshold of $10^{-5}$. Simulation results with $L = 1000$ for encoding are shown in Fig. 7, where the decoding delay is specified as $d_K = 2m_K$ for code rate $K/N$. Also shown in Fig. 7 are the genie-aided lower bounds, which are obtained by shifting the corresponding performance curves of the basic codes to left by $10 \log_{10}(1 + m_K)$ dB. We can see that the performances of the BMST-HT system match well with the respective genie-aided lower bounds in the low BER region for all considered code rates. To evaluate the bandwidth efficiency of the BMST-HT system, we plot the required SNR to achieve the BER of $10^{-5}$ against the code rate in Fig. 8. We can see that the BMST-HT system achieves the BER of $10^{-5}$ within one dB from the Shannon limit for all considered code
Fig. 7. Performance of the BMST-HT system using the Cartesian products of the HT-coset codes $[8, K]^{1250} (1 \leq K \leq 7)$ with $L = 1000$. The sliding-window decoding algorithm is performed with a maximum iteration number $I_{\text{max}} = 18$ and a decoding delay $d_K = 2m_K$ for the code of rate $K/8$.

rates. However, the performance gap of the $[8, 4]$ code is slightly larger. This is due to the sub-optimality of Algorithm 2 as mentioned previously. If an optimal (locally) SISO algorithm (the MAP decoding) is used for the $[8, 4]$ code, the performance can be improved, as marked in Fig. 8 by the cross $\times$.

E. Further Discussions

We have also constructed a BMST-HT system using the $B$-fold Cartesian product $C[16, K]^B (1 \leq K \leq 15)$ as the basic code, where $B = 625$. The required encoding memories can be determined following the procedure described in Section III-A, which are shown in Table III. The required SNR to achieve the BER of $10^{-5}$ is plotted against the code rate in Fig. 9. We can see that the BMST-HT system also has a BER of $10^{-5}$ within one dB away from the Shannon limits for all considered code rates. Using HT-coset codes with larger $N$, we can implement codes with finer code rates. However, this simple approach can only construct codes with rates of form $K/2^p$. In practice, other code rates (say $1/3$ and $4/5$ in the standard of Long Term Evolution (LTE)) are required [24]. In this case, we can construct the basic code by combining several HT-coset codes with different rates. Taking rate $1/3$ as an example, we can use the Cartesian product of the HT-coset codes $[[4, 1] \times [4, 1] \times [4, 2]]^B$ as the basic code.
In this paper, we have proposed a new class of multiple-rate codes by embedding the Hadamard transform (HT) coset codes into the block Markov superposition transmission (BMST) system, resulting in the BMST-HT system. The implementation complexity of the system is linear in the code length, while the performance in the low error rate region can be predicted. The simulation results show that the BMST-HT system can approach the Shannon limit within one dB at the BER of $10^{-5}$ for a wide range of code rates.
TABLE III
The memory required for each code rate using the BMST of HT-coset codes with $N = 16$ to approach the Shannon limit at the BER of $10^{-5}$

| $R = K/16$ | $\gamma^*_K$ (dB) | $\gamma_K$ (dB) | $\gamma_K - \gamma^*_K$ (dB) | $m_K$ |
|------------|-------------------|----------------|-----------------------------|------|
| 1/16       | -1.4              | 9.6            | 11.0                        | 12   |
| 2/16       | -1.2              | 9.8            | 11.0                        | 12   |
| 3/16       | -1.0              | 8.4            | 9.4                         | 8    |
| 4/16       | -0.8              | 7.7            | 8.5                         | 6    |
| 5/16       | -0.6              | 7.4            | 8.0                         | 5    |
| 6/16       | -0.3              | 8.1            | 8.4                         | 6    |
| 7/16       | -0.1              | 7.9            | 8.0                         | 5    |
| 8/16       | 0.2               | 7.6            | 7.4                         | 4    |
| 9/16       | 0.5               | 7.4            | 6.9                         | 4    |
| 10/16      | 0.8               | 7.2            | 6.4                         | 3    |
| 11/16      | 1.2               | 7.5            | 6.3                         | 3    |
| 12/16      | 1.6               | 8.2            | 6.6                         | 4    |
| 13/16      | 2.2               | 8.4            | 6.2                         | 3    |
| 14/16      | 2.8               | 8.4            | 5.6                         | 3    |
| 15/16      | 3.9               | 8.3            | 4.4                         | 2    |

REFERENCES
[1] D. Tse and P. Viswanath, Fundamentals of wireless communication. Cambridge, U.K.: Cambridge University Press, 2005.
[2] S. M. Alamouti and S. Kallel, “Adaptive trellis-coded multiple-phase-shift keying for Rayleigh fading channels,” IEEE Trans. Commun., vol. 42, pp. 2305–2314, June 1994.
[3] Y. Sun, M. Karkooti, and J. R. Cavallaro, “VLSI decoder architecture for high throughput, variable block-size and multi-rate LDPC codes,” in Proc. Int. Symp. Circuits. Syst., New Orleans, LA, May 2007, pp. 2104–2107.
[4] J. Hagenauer, “Rate-compatible punctured convolutional codes (RCPC codes) and their application,” IEEE Trans. Commun., vol. 36, pp. 389–400, Apr. 1988.
[5] O. Acikel and W. Ryan, “Punctured turbo-codes for BPSK/QPSK channels,” IEEE Trans. Commun., vol. 47, pp. 1315–1323, Sept. 1999.
[6] M. Yazdani and A. Banihashemi, “On construction of rate-compatible low-density parity-check codes,” IEEE Commun. Lett., vol. 8, pp. 159–161, Mar. 2004.
[7] J. Ha, J. Kim, and S. McLaughlin, “Rate-compatible puncturing of low-density parity-check codes,” IEEE Trans. Inform. Theory, vol. 50, pp. 2824–2836, Nov. 2004.
[8] K. Zhang, X. Ma, S. Zhao, B. Bai, and X. Zhang, “A new ensemble of rate-compatible LDPC codes,” in Proc. IEEE Int. Symp. Inf. Theory, Cambridge, MA, July 2012, pp. 2536–2540.
[9] A. I. V. Casado, W.-Y. Weng, S. Valle, and R. Wesel, “Multiple-rate low-density parity-check codes with constant blocklength,” *IEEE Trans. Commun.*, vol. 57, pp. 75–83, Jan. 2009.

[10] M. Uppal, G. Yue, X. Wang, and Z. Xiong, “A rateless coded protocol for half-duplex wireless relay channels,” *IEEE Trans. Signal Process.*, vol. 59, pp. 209–222, Jan. 2011.

[11] L. Liu, W. Zhou, and S. Zhou, “Nonbinary multiple rate QC-LDPC codes with fixed information or block bit length,” *J. Commun. Netw.*, vol. 14, pp. 429–433, Aug. 2012.

[12] J. Huang, S. Zhou, and P. Willett, “Nonbinary LDPC coding for multicarrier underwater acoustic communication,” *IEEE J. Sel. Areas Commun.*, vol. 26, pp. 1684–1696, Dec. 2008.

[13] X. Huang, A. Kavčič, X. Ma, G. Dong, and T. Zhang, “Multilevel flash memories: Channel modeling, capacities and optimal coding rates,” *International Journal on Advances in Systems and Measurements*, vol. 6, pp. 364–373, Dec. 2013.

[14] X. Ma, C. Liang, K. Huang, and Q. Zhuang, “Obtaining extra coding gain for short codes by block Markov superposition transmission,” in *Proc. IEEE Int. Symp. Inf. Theory*, Istanbul, Turkey, July 2013, pp. 2054–2058.

[15] ——, “Block Markov superposition transmission: Construction of big convolutional codes from short codes,” submitted to *IEEE Trans. Inform. Theory*. [Online]. Available: http://arxiv.org/abs/1308.4809

[16] A. J. Felstrom and K. S. Zigangirov, “Time-varying periodic convolutional codes with low-density parity-check matrix,” *IEEE Trans. Inform. Theory*, vol. 45, pp. 2181–2191, Sept. 1999.

[17] Y.-L. Ueng, Y.-L. Wang, L.-S. Kan, C.-J. Yang, and Y.-H. Su, “Jointly designed architecture-aware LDPC convolutional codes and memory-based shuffled decoder architecture,” *IEEE Trans. Signal Process.*, vol. 60, pp. 4387–4402, Aug. 2012.

[18] G. D. Forney, Jr., “Codes on graphs: Normal realizations,” *IEEE Trans. Inform. Theory*, vol. 47, pp. 520–548, Feb. 2001.

[19] E. Arikan, “Channel polarization: A method for constructing capacity-achieving codes for symmetric binary-input memoryless channels,” *IEEE Trans. Inform. Theory*, vol. 55, pp. 3051–3073, July 2009.

[20] R. J. McEliece, “How to compute weight enumerators for convolutional codes,” *Communications and Coding*, pp. 121–141, Jan. 1998.

[21] S. Lin and D. J. Costello, Jr., *Error Control Coding: Fundamentals and Applications (2nd Ed.)*. Englewood Cliffs: Prentice-Hall, 2004.

[22] X. Ma, C. Liang, K. Huang, and Q. Zhuang, “Obtaining extra coding gain for short codes by block Markov superposition transmission,” in *Proc. IEEE Int. Symp. Inf. Theory*, Istanbul, Turkey, July 2013, pp. 2054–2058.

[23] X. Ma and L. Ping, “Coded modulation using superimposed binary codes,” *IEEE Trans. Inform. Theory*, vol. 50, pp. 3331–3343, Dec. 2004.

[24] *LTE: Evolved Universal Terrestrial Radio Access (E-UTRA); Multiplexing and channel coding (Release 11)*. 3GPP, 3rd generation partnership project Std., Feb. 2013.