On Storage Allocation in Cache-Enabled Interference Channels with Mixed CSIT

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Abstract—Recently, it has been shown that in a cache-enabled interference channel, the storage at the transmit and receive sides are of equal value in terms of Degrees of Freedom (DoF). This is derived by assuming full Channel State Information at the Transmitter (CSIT). In this paper, we consider a more practical scenario, where a training/feedback phase should exist for obtaining CSIT, during which instantaneous channel state is not known to the transmitters. This results in a combination of delayed and current CSIT availability, called mixed CSIT. In this setup, we derive DoF of a cache-enabled interference channel with mixed CSIT, which depends on the memory available at transmit and receive sides as well as the training/feedback phase duration. In contrast to the case of having full CSIT, we prove that, in our setup, the storage at the receive side is more valuable than the one at the transmit side. This is due to the fact that cooperation opportunities granted by transmitters’ caches are strongly based on instantaneous CSIT availability. However, multi-casting opportunities provided by receivers’ caches are robust to such imperfection.

I. INTRODUCTION

Nowadays, the available bandwidth to transfer data has become one of the most important and costly communication resources due to increasing use of communication networks and significant exponential growth of the Internet traffic load. Considering the fact that Internet video traffic has become the main part of all consumer Internet traffic [1], using novel approaches such as caching seems to be of high significance in reducing bandwidth consumption. Content Delivery Networks (CDNs) are typical examples of enterprises using the caching techniques to deliver requested content to the consumers [2]. Simply put, in such methods the geographically distributed storage units cache content during network low-peak hours based on the statistics of demands, given their cache sizes. Then, upon consumer requests arrival, this cached data will be used during network high-peak hours to relieve congestion. Both cache content placement and content delivery phases need careful design considerations to manage network congestion.

Employing the caching idea in communication networks has been the subject of many recent works. Shanmugam et al. propose using cache-enabled helpers in cellular networks to offload traffic from congested base stations [3]. In [4], the idea of Device-to-Device (D2D) communication with distributed caching has been investigated. Also, in [5] and [6], the authors analyze the role of proactive edge caching in 5G wireless networks. Finally, the throughput-memory trade-off for cache-enabled ad-hoc networks has been investigated in [7].

The concept of Coded Caching introduced in [8] considers the caching problem from an information theoretic perspective, resulting in substantial performance gains. Subsequent works extended the results to the D2D setting [9], networks with hierarchical caches architecture [10], multi-server setup [11], cache-enabled interference channels [12], and MISO broadcast channels with delayed CSIT [13], [14].

In the present study, we consider an interference channel where both transmitters and receivers are equipped with cache memories of limited size. In the cache content placement phase, the caches of both sides are filled with content from a library, without knowing the actual requests of the receivers. In the content delivery phase, by knowing the receivers’ requests, the transmitters send proper signals on the wireless medium to fulfill these demands. In contrast to [12], which considers the same setup with full CSIT, here we assume a mixed CSIT setting. In other words, in order to provide CSI at the transmit side, the transmitters send training symbols at the beginning of each time coherence block. Based on this training process, the CSI is fed back to the transmitters, leading to delay in obtaining CSIT. This model is closer to practical scenarios where CSIT should be provided by some sort of feedback [15].

As shown in [12], with full CSIT assumption, increasing cache size at the transmitters or receivers results in higher DoF. That is due to the fact that, by providing memory at the transmitters, they can cooperate in sending data, and the more the storage size is, the higher the cooperation chances will be. On the other hand, receive-side memories provide multi-casting opportunities as discussed in [8]. However, the interesting result of [12] emphasizes that the transmitter and receiver memories are of equal importance in terms of DoF, when there is full CSIT. On the contrary, in this paper with the mixed CSIT model, we prove that the receive-side caches are much more valuable than the ones at transmit side. In fact, the cooperation of transmitters heavily relies on perfect instantaneous CSIT, which diminishes in practical mixed CSIT scenarios. The multi-casting opportunities provided by receive-side caches, however, are not sensitive to such information. This result has an important practical design guideline on storage allocation in interference channels: push the storage towards end-users.
Other closely related works to our study are [16], [17], [13], and [18]. The papers [16] and [17] also consider cache-enabled interference channels, with the difference that they assume memory only at the transmit side. In [13], Zhang et al. consider the effect of delayed CSIT in a MISO broadcast channel, which is equivalent to having unlimited memory at the transmit side, similar to [11]. Here, in contrast, we consider limited transmit-side memory. Finally, [18] considers a Wyner model, and that the transmitters are connected via a Cloud RAN, which makes their model different from ours.

The rest of the paper is organized as follows. In Section II, we present the system model. In Section III, DoF of a cache-enabled interference channel with full CSIT is derived. In Section IV, we introduce DoF of a cache-enabled interference channel with mixed CSIT. Finally, Section V presents numerical illustrations, and concludes the paper.

**Notation:** \( (\cdot)^T \) represents transpose operation, \(|\cdot|\) denotes the cardinality of a set, and \([K] \triangleq \{1, 2, \ldots, K\}\), for any \(K \in \mathbb{N}^+\).

**II. System Model**

We consider a block time-varying memoryless wireless interference channel through which \(K_t\) transmitters are connected to \(K_r\) receivers. Such channel, at each time instant \(\tau\), can be modeled as a \(K_r\)-by-\(K_t\) matrix which is denoted by \(H(\tau) = [h_{i,j}(\tau)] \in \mathbb{C}^{K_r \times K_t}\), whose entry in the \(i\)th row and the \(j\)th column represents the channel gain from transmitter \(j\) to the \(i\)th receiver, and is an independent complex Gaussian random variable \(\mathcal{CN}(0, 1)\). This matrix relates the channel’s inputs to the outputs by following equation:

\[
y(\tau) = H(\tau)x(\tau) + z(\tau),
\]

in which, \(x(\tau) = (x_1(\tau), \ldots, x_{K_t}(\tau))^T\) is the vector consisting of all the transmitted messages, \(y(\tau) = (y_1(\tau), \ldots, y_{K_r}(\tau))^T\) represents the output vector which includes the received signals, and \(z(\tau) = (z_1(\tau), \ldots, z_{K_r}(\tau))^T \in \mathbb{C}^{K_r \times 1}\) denotes the additive white Gaussian noise (AWGN) with the covariance matrix \(\sigma^2 I_{K_r}\),

Then, we define the channel gain vector to the \(k\)th receiver as follows:

\[
h_k(\tau) \triangleq (h_{k,1}(\tau), \ldots, h_{k,K_t}(\tau))^T, \quad k = 1, 2, \ldots, K_r,
\]

which is the transpose of the \(k\)th row of matrix \(H(\tau)\), so the received signal of receiver \(k\) based on the channel’s input vector is as follows:

\[
y_k(\tau) = h_k(\tau)^T x(\tau) + z_k(\tau).
\]

We divide the time domain into time slots of equal length, \(T_c\), such that the channel state during each time slot is fixed, while it is completely independent for different time slots. In other words, the channel transfer matrix elements do not alter in the middle of any time slot, so a discrete-time process, i.e. \(H[m] = H(\tau)(m-1)T_c \leq \tau < mT_c\), process represents channel transfer matrix for all time slots \(m = 1, 2, \ldots\).

In this paper, we assume that the CSI is measured at the receivers from training symbols sent by the transmitters. Then, via a feedback channel, this information is fed back to the transmitters. We assume this training/feedback procedure takes the time of length \(T_f\). Therefore, in the first \(T_f\) seconds of each time coherence block of \(T_c\) seconds, the transmitters do not have any instantaneous CSI, while in the rest of the coherence time they possess full CSI, as shown in Fig. 1. We define \(\alpha \triangleq T_f/T_c\), as a measure for channel training efficiency, with \(\alpha = 0\) denoting full CSIT, and \(\alpha = 1\) representing delayed CSIT. This model is called mixed CSIT in the literature [19].

We consider a library of \(N\) files, \(\mathcal{W} = \{W_1, \ldots, W_N\}\), of size \(F\) bits each. We also assume that each transmitter has a cache of size \(M_t F\) bits, and each receiver is equipped with a cache of size \(M_r F\) bits. For later use, we define parameters \(t_t \triangleq \frac{K_t M_t}{N}\) and \(t_r \triangleq \frac{K_r M_r}{N}\). We, further, suppose that aggregate cache size of the transmitters is greater than the total size of all the \(N\) files in the library, so the condition \(t_t \geq 1\) should be satisfied.

The network operates in two phases, namely cache content placement and content delivery. In the former, the cache of transmitters and receivers are filled with data from the library, without prior knowledge of receivers’ actual requests. In the latter, each receiver requests a single file from the library, where file \(W_{d_k}\) is demanded by \(k\)th receiver. Then, accordingly, the transmitters cooperate to send proper data such that each receiver is able to retrieve its demanded file using the received data and the content stored in its cache.

In this paper, as the performance metric, we use Degrees of Freedom (DoF) as a measure of the number of non-interfering parallel paths which can be used to deliver different data streams simultaneously (or equivalently in our case the number of users being served at the same time), defined as follows.

**Definition 1.** For an interference channel consisting of \(K_t\) transmitters, each with a cache of size \(M_t F\) bits, and \(K_r\) receivers, each with a cache of size \(M_r F\) bits, the optimum DoF is defined as follows:

\[
d^*(t_t, t_r) = \lim_{P \to \infty} \inf \frac{C_S(t_t, t_r, P)}{\log P},
\]

where \(C_S(t_t, t_r, P)\) is the supremum of all achievable sum-rates received by all the receivers for normalized cache sizes of \(t_t\) and \(t_r\) on transmit and receive sides respectively, as well as a power constraint of \(P\) held for transmitter antennas.

According to this definition, the value of DoF cannot be greater than the number of all receivers in the system (\(K_r\)).
Suppose an interference channel with a transfer matrix of i.i.d. elements, and a library of $N$ files each of size $F$ bits. If there are $K_t$ transmitters each with a cache of size $M_t F$ bits, and $K_r$ receivers each with a cache of size $M_r F$ bits, the following DoFs (and their lower convex envelope) are achievable:

$$d(K_t, t_r) = \min(K_t + t_r, K_r),$$

where $t_r = \frac{K_r M_r}{N} \in \{0, 1, ..., K_r\}$ should be held true.

**Proof:** The proof is given in [11].

The main point of Lemma 1 is that each transmitter has access to the full library as depicted in Fig. 2a. Next, in Theorem 1, we generalize Lemma 1 to the case where transmitters have limited memories, such that no transmitter is able to cache the entire library.

**Theorem 1.** Suppose an interference channel with a transfer matrix of i.i.d. elements, and a library of $N$ files each of size $F$ bits. If there are $K_t$ transmitters each with a cache of size $M_t F$ bits, and $K_r$ receivers each with a cache of size $M_r F$ bits, the following DoFs (and their lower convex envelope) are achievable:

$$d_C(t_t, t_r) = \min(t_t + t_r, K_t),$$

where $t_r = \frac{K_r M_r}{N} \in \{0, 1, ..., K_t\}$ and $t_r = \frac{K_r M_r}{N} \in \{0, 1, ..., K_r\}$ should be held true.

**Proof:** First, we divide each file into $\binom{K_t}{t_t}$ non-overlapping sub-files:

$$W_n = \{W_{n,T,R} : T \subseteq \{l_1, ..., l_i, ..., l_t\}, |T| = t_t, R \subseteq \{l_1, ..., l_i, ..., l_r\}, |R| = t_r\}.$$  

Then, throughout the cache content placement phase, receiver $k$ caches sub-file $W_{n,T,R}$ if $k \in R$, and transmitter $l$ caches sub-file $W_{n,T,R}$ if $l \in T$, as follows:

$$Z_{t_t} = \{W_{n,T,R} : l \in T\},$$

$$Z_{r_r} = \{W_{n,T,R} : k \in R\},$$

where $Z_{t_t}$ and $Z_{r_r}$ are referred to as the content of transmitter $l$’s cache and the receiver $k$’s cache, respectively.

It can be seen that $W_{n,T,R} = \{W_{n,T,R} : R \subseteq \{l_1, ..., l_t\}, |R| = t_r\}$ is present in the caches of exactly $t_t$ transmitters, i.e. the ones which belong to $T$. So, we can divide the primary library $W$ into $\binom{K_t}{t_t}$ sub-libraries as follows:

$$W_T = \{W_{1,T}, ..., W_{N,T}\}, \ T \subseteq \{l_1, ..., l_t\},$$

each of which is completely cached in precisely $t_t$ transmitters, and also is comprised of $N$ smaller files each of size $F' = F \binom{K_t}{t_t}$ bits (see Fig. 2b).

Let us consider one of these sub-libraries, say $W_{T_i}$. We define sub-problem $P(T_i)$ to be the problem of delivering $(W_{d_k,T_i})_{k=1}^{K_r}$ by $t_t$ transmitters (members of $T_i$) all having access to the whole sub-library $W_{T_i}$, as depicted in Fig. 2c. Consequently, if the necessary transmissions of all sub-problems $(P(T_i))_{i=1}^{K_t}$ are fulfilled in sequence, each receiver $k$ will be able to decode $W_{d_k}$. This reduces the original problem to $\binom{K_t}{t_t}$ multi-server problems with smaller sub-libraries.

Then, the result of Lemma 1 can be applied with $t_t$ transmitters to each sub-problem, which states that, for each $T_i$, the delivery of $(W_{d_k,T_i})_{k=1}^{K_r}$ can be accomplished with a DoF of

$$d(t_t, t_r) = \min(t_t + t_r, K_r).$$

Since sub-problems are handled in sequence, the same DoF is achievable for the whole library, and this concludes the proof.

It should be noted that a similar result, for the full CSIT case, has also been obtained independently in [12], via a different proof approach without relying on Lemma 1. Since, in the next section, we need more details of the delivery scheme, we have provided the delivery details in the Appendix.
IV. Cache-enabled interference channel with mixed CSIT

The interesting result of [20] shows that delayed CSIT can still be of much benefit in order to achieve greater DoF than the one without CSIT. Considering a MISO broadcast channel in which transmitters access channel coefficients of all preceding time slots excluding the present time slot, the authors in [20] propose a novel method which employs such past knowledge to achieve an optimum DoF of the channel. We will state the main theorem of the aforementioned work in the following lemma.

**Lemma 2 ([20]).** For a MISO broadcast channel with $L$ transmit antennas, $K$ receivers, and one-time-slot-delayed CSIT, for $L \geq K + j + 1$ we have:

$$d^*_j(L, K) \geq \frac{K - j + 1}{\frac{j}{1} + \frac{j+1}{1} + \ldots + \frac{j+1}{1}}.$$  

(11)

where $d^*_j(L, K)$ denotes the maximum DoF to transfer messages of order $j$ (messages which are intended for exactly $j$ receivers).

Based on Lemma 2, we can state the following result for the delayed CSIT cache-enabled interference channel.

**Corollary 1.** In a cache-enabled $K_t \times K_r$ interference channel with delayed CSIT the following DoFs (and their lower convex envelope) are achievable:

$$d_D(t_t, t_r) = t_t \left( \frac{1}{\frac{1}{t_{t+1}} + \frac{1}{t_{t+2}} + \ldots + \frac{1}{t_{t+1}}} \right).$$  

(12)

where $t_t = \frac{K_t M_t}{N} \in \{1, 2, \ldots, K_t\}$ and $t_r = \frac{K_r M_r}{N} \in \{0, 1, \ldots, K_r\}$, and $t_t + t_r \leq K_r$ should be held true.

**Proof:** Choosing a $t_t$-subset of transmitters, $T$, and a subset of receivers of cardinality $t_t + t_r$, $S$, according to the procedure introduced in Appendix, results in a pair $(T, S)$ such that messages of order $t_t + 1$ (each beneficial to a $(t_t+1)$-subset of $S$) can be generated using the content which is present in the caches of transmitters in $T$. Drawing the analogy between the aforementioned set-up and the one in Lemma 2, we observe that DoF order $t_r + 1$ can be calculated from above lemma substituting $t_r + 1$, $t_t$, and $t_t + t_r$ for $j, L$, and $K$ respectively:

$$d^*_{t_t + 1}(t_t, t_t + t_r) \geq \frac{t_t}{\frac{1}{t_{t+1}} + \frac{1}{t_{t+2}} + \ldots + \frac{1}{t_{t+1}}}.  

(13)

Thus, the procedure described in Algorithm 1 results in the desired DoF.

Now, we proceed to state the main theorem that introduces the achievable DoF which can be attained in case that transmitters experience a feedback channel resulting in mixed CSIT.

**Theorem 2.** For an interference channel comprising $K_t$ transmitters and $K_r$ receivers, each transmitter with a cache of size $M_t F$ bits and each receiver equipped with a $M_r F$-bit memory, and a library including $N$ files each of size $F$ bits, the achievable DoF order $t_t$:

$$d_M(t_t, t_r) = t_t + t_r + \alpha \left( \frac{t_t}{\frac{t_{t+1}}{1} + \frac{t_{t+2}}{1} + \ldots + \frac{t_{t+1}}{1}} - t_t - t_r \right).$$  

(14)

if the state of CSIT availability is characterized by a factor $0 \leq \alpha \leq 1$, the following DoF is achievable:

$$d_M(t_t, t_r) = t_t + t_r + \alpha \left( \frac{t_t}{\frac{t_{t+1}}{1} + \frac{t_{t+2}}{1} + \ldots + \frac{t_{t+1}}{1}} - t_t - t_r \right).$$  

(14)

where $t_t = \frac{K_t M_t}{N} \in \{1, 2, \ldots, K_t\}$, $t_r = \frac{K_r M_r}{N} \in \{0, 1, \ldots, K_r\}$, and $t_t + t_r \leq K_r$ should be held true.

**Proof:** Availability of mixed CSIT characterized by the parameter $\alpha$ shows that during $\alpha$ portion of time, transmitters access CSIT with a delay of one time slot, and have current CSIT for the rest of time.

Thus, in each time coherence block $T_r$, during the training phase Algorithm 1 is used for delivery, and, in the remaining period Algorithm 2 is used. Therefore, it is clear that the total DoF equals the linear combination of the ones in Theorem 1 and Corollary 1 with coefficients $(1 - \alpha)$ and $\alpha$ respectively, which leads to (14).

V. Numerical Illustrations and Conclusions

In this section, we examine the result of Theorem 2 in order to gain an insight into how each of parameters which describe the system model contributes to the achievable DoF in (14). To this end, we consider two main parameters of the system and investigate the interaction of achievable DoF and these parameters. The first one is the portion of each time block during which perfect CSIT is not available denoted by $T_r$. As shown in the diagram, we can see that the DoF is inversely related to $\alpha$ in a linear manner, for fixed cache sizes at both transmit and receive sides. For a fixed $\beta$, it also can be figured out that increasing total cache size of system not only leads to a greater DoF, but also steepens the slope of

### Algorithm 1: DELAYED CSIT DELIVERY

1. $t_t \leftarrow \frac{K_t M_t}{N}$
2. $t_r \leftarrow \frac{K_r M_r}{N}$
3. $i \leftarrow 1$
4. foreach $T \subseteq [K_t], |T| = t_t$ do
5.    foreach $S \subseteq [K_r], |S| = t_r + t_t$ do
6.       foreach $R \subseteq S, |R| = t_r + 1$ do
7.         $U_i \leftarrow \oplus_{r \in R} W_{d_r, T, R \setminus \{r\}}$
8.         $i \leftarrow i + 1$
9.     end
10.    $U \leftarrow (U_1, ..., U_{(t_r+1)})$
11.   MAT$(T, S, t_r + 1, U)^*$
12. end
13. end

*MISO BC delivery procedure in [20] with transmitters $T$ and receivers $S$ to transfer messages in $U$ of order $t_r + 1$.
the diagram of DoF versus α, which means that the more the system aggregate cache size is, the more the influence of full CSIT on enhancing DoF will be.

Also, the effect of β on DoF is shown in Fig. 4. This figure indicates how the caches at receive side can be more beneficial in practical conditions than the caches at transmit side. In this figure, we assume a fixed normalized total cache size $t_r + t_t = 100$. The case $α = 0$ refers to the setup in [12] which indicates equal value for allocating memory at either transmit or receive side for the current CSIT case. For $α > 0$, i.e. mixed CSIT case, by increasing $β$ (allocating more storage to the receive side), DoF will increase.

Therefore, considering an interference channel with caches at both transmit and receive sides, the present study demonstrated that equipping receivers with caches benefits content delivery phase in terms of DoF more than doing so for transmitters when a more practical scenario is encountered. In particular, we assumed a pragmatic approach of providing CSIT including a training/feedback process, leading to a significant increase in DoF more than doing so for transmitters. For $α = 0$, the case $β = 0$ refers to the setup in [12] which indicates equal value for allocating memory at either transmit or receive side, the present study demonstrated that equipping receivers with caches benefits content delivery phase in terms of DoF more than doing so for transmitters when a more practical scenario is encountered.

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### APPENDIX

Proof of Theorem 1 with Detailed Delivery Procedure

**Proof:** Suppose receiver $k$, which has demanded $W_{d_k}$ in the second phase, and all the subfiles of form $W_{d_k,T,R}$, where $k \notin R$ should be delivered to this receiver. Note that $W_{d_k,T,R}$ is available in:

1. all the $t_r$ receivers $k \in R$, and
2. all the $t_t$ transmitters $l \in T$.

If $W_{d_k,T,R}$ is broadcasted to all the receivers cooperatively by $t_t$ transmitters $l \in T$, then

![Fig. 3: DoF for mixed CSIT, $d_M$, versus CSIT availability amount, $\alpha$, for fixed $\beta = 1$ and different aggregate cache sizes $t_r + t_t$.](image3)

![Fig. 4: DoF for mixed CSIT, $d_M$, versus the ratio of cache size on receive side to the one on transmit side, $\beta$, for fixed $t_r + t_t = 100$, and different CSIT availability values $\alpha$.](image4)
1. receiver $k$ will benefit from this subfile to decode $W_{d_k}$.
2. $t_r$ receivers in $R$ can eliminate this interference with the help of their cache contents.
3. this interference can be zero-forced at an arbitrary subset of receivers of size $t_r - 1$, and
4. this will inevitably cause interference for the other $K_r - (t_r + t_t)$ receivers.

From the above observations, one can see that it is possible to design coded messages which are useful for $t_t + t_r$ receivers leading to DoF of $t_t + t_r$.

In order to design such common message, we further divide each sub-file into $\binom{K_r - t_t - t_r - 1}{t_r - 1}$ mini-files, such that during the transmission of each mini-file, interference cancellation will be performed for a definite subset of receivers that neither have the mini-file in their caches, nor need to receive the mini-file to restore their demands:

$$ W_{n,T,R} = (W_{n,T,R}^r : R' \subseteq [K_r - t_t - t_r - 1], |R'| = t_r - 1). $$

Next, we define an arbitrary bijective function $f_{K_r}^{R' \cup \{k\}} : [K_r - |U|] \rightarrow [K_r - |U| \cup \{k\}]$ for each $U \subseteq [K_r]$, thus to send mini-file $W_{d_k}^{R' \cup \{k\}}$ to the receiver $k$, we perform a pre-coding through the transmitters in $T$ to zero-force the interference at the receivers in $f_{K_r}^{R' \cup \{k\}} (R')$, which denotes a $(t_t - 1)$-subset of receivers and is defined as follows:

$$ f_{K_r}^{R' \cup \{k\}} (R') = \{ f_{K_r}^{R' \cup \{k\}} (k') : k' \in R' \}. $$

In the content delivery phase, we claim that it is possible to benefit a set of $t_t + t_r$ receivers simultaneously. To explain this, let’s consider a subset $S \subseteq [K_r]$ of cardinality $t_t + t_r$ and a $t_t$-subset $T \subseteq [K_r]$. For this specific $S$, a $(t_t + t_r)$-subset of receivers, we denote all possible subsets of $S$ of cardinality $t_r + 1$ by $S_i$, $i = 1, ..., \binom{t_t + t_r - 1}{t_r}$, which means $S_i \subseteq S$ and $|S_i| = t_r + 1$. We then determine a $t_t$-by-1 vector $u_{T,S}^{S_i}$ for each $S_i$ such that makes the below statements hold true:

$$ u_{T,S}^{S_i} \perp h_T^T, \forall j \in S_i, $n
$$ u_{T,S}^{S_i} \not\perp h_T^T, \forall j \in S_i,$n

where $h_T^T = [h_{j_1}, ..., h_{j_{|T|}}]^T$ is defined for any $T = \{l_1, ..., l_{|T|}\} \subseteq [K_r]$.

It can be shown that the above conditions are true with high probability if the size of the field from which transmitted symbols are chosen is much greater than $(t_r + 1)\binom{K_r}{t_r - 1}$.\footnote{11.}

Next, for each $S_i$, define:

$$ G_T(S_i) = L_{r \in S_i} \left( W_{d_r, T, S_i \setminus \{r\}}^{R_i} \right), $$

with $R_i$ is required to be such that $f_{K_r}^{R_i} (R_i) = S \setminus S_i$, and $L_{r \in S_i}$ represents a random linear combination of the corresponding mini-files for all $r \in S_i$. In addition, $W_{d_r, T, S_i \setminus \{r\}}^{R_i}$ is a mini-file of the file demanded by receiver $r$, which exists in the caches of transmitters in $T$ and receivers in $S_i \setminus \{r\}$, and we need to perform zero-forcing to cancel its interference to the receivers in $S \setminus S_i$.

Next, for such $t_t$-subset $T$ and $(t_t + t_r)$-subset $S$ we define:

$$ X_T(S) = \sum_{S_i \subseteq S, |S_i| = t_t + 1} u_{T,S}^{S_i} G_T(S_i). $$

We repeat the aforementioned procedure $\binom{t_t + t_r - 1}{t_r}$ times with different random coefficients to obtain different $t_t$-by-$1$ vectors $X_T^w(S)$, $\omega = 1, ..., \binom{t_t + t_r - 1}{t_r}$ as follows:

$$ G_T^w(S_i) = L_{r \in S_i} \left( W_{d_r, T, S_i \setminus \{r\}}^{R_i} \right), $$

$$ X_T^w(S) = \sum_{S_i \subseteq S, |S_i| = t_t + 1} u_{T,S}^{S_i} G_T^w(S_i), $$

where the random coefficients for calculating linear combinations in $L_{r \in S_i}$’s are different for different $\omega$’s in order to achieve independent linear combinations of the corresponding mini-files, with high probability.

Then, for these $T$ and $S$ sets, the transmitters in $T$ transmit the following block:

$$ [X_T^1(S), ..., X_T^{t_t + t_r - 1}(S)]. $$

After accomplishing the transmission of the above block, a different pair $(T, S)$ should be chosen (such that $T \subseteq [K_r], |T| = t_t$, and $S \subseteq [K_r], |S| = t_t + t_r$), and the same procedure is performed until all such pairs have been taken into account.

It can be shown that, after the above procedure is concluded, all the receivers will be able to decode their requested files. Also, since each message in (19) benefits $t_t + t_r$ receivers simultaneously, achievable DoF of this delivery scheme is $\min(t_t + t_r, K_r)$. The procedure to transmit the desired files is shown in Algorithm 2.

**Algorithm 2: CURRENT CSIT DELIVERY**

1. $t_t \leftarrow K_t M_t / N$
2. $t_r \leftarrow K_r M_r / N$
3. foreach $T \subseteq [K_r], |T| = t_t$
   4. foreach $S \subseteq [K_r], |S| = t_t + t_r$
      5. foreach $R \subseteq S, |R| = t_r + 1$
         6. Design $u_{T,S}^{R} \not\perp h_T^T$ such that: for all $j \in S$
            $h_T^T \perp u_{T,S}^{R}$ if $j \notin R$, and $h_T^T \not\perp u_{T,S}^{R}$ if $j \in R$
      7. foreach $\omega = 1, ..., \binom{t_t + t_r - 1}{t_r}$
         8. foreach $R \subseteq S, |R| = t_r + 1$
            9. $R' \leftarrow f_{K_r}^{-1}(S \setminus R)$
            10. $G_T^w(R) = L_{r \in R} \left( W_{d_r, T, R \setminus \{r\}}^{R_i} \right)$
            11. $X_T^w(S) \leftarrow \sum_{R \subseteq S, |R| = t_t + 1} u_{T,S}^{R} G_T^w(R)$
      12. transmit
      13. $X_T(S) = [X_T^1(S), ..., X_T^{t_t + t_r - 1}(S)]$
   14. end
4. end
3. end
2. end
1. end