Design and Simulation of a Soft Switched Dc Boost Converter for Switched Reluctance Motor

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Abstract: Problem statement: This study presents the design and simulation of a soft switched boost converter for a switched reluctance motor with a closed loop controller. A soft switching scheme is proposed in the converter with minimum components, which reduces the switching loss and stress across the switch so that the harmonic generation is reduced in the output. The switch used in this converter switched ON at zero current and switched OFF at zero voltage. Approach: Most of the research was done on the power converter circuit of the SRM to control the speed. But in the proposed system a soft switched boost converter was designed to regulate the input voltage to SRM for any line variations and a power converter is used to control the speed of SRM. The PI controller was used as closed loop controllers, which improves the speed control of the switched reluctance motor for any load and regulates the input voltage to SRM for any line variations. The duty cycle of the switch is controlled by PI controller using PWM technique. A 500W/50KHz PWM based soft switched boost converter was designed and simulated for 6/4 pole 3 phase SRM with variable loads. The results were simulated using MATLAB SIMULINK. Results: The output of the soft switched dc converter gives a constant output voltage for any line variations, so that the input voltage to SRM is constant. The output speed of the SRM is controlled using PI controller and rotor current. Conclusion: The soft switched dc converter regulates the input supply and a power converter control the speed of Switched reluctance motor simultaneously.

Key words: Soft switched boost converter, zero voltage switching, zero current switching, switched reluctance motor, power converter, pi controller, non-linearity, input voltage, power density, stator poles, zero current, switched converter

INTRODUCTION

Switched Reluctance Motor Drives (SRD) are now become popular compared to conventional ac or dc motor drives due to their simple construction, robustness, high efficiency and high power density. However, in speed control this motor facing some problems due to its nonlinear characteristics. The function of flux linkage depending on phase current and rotor position represents the key characteristic of the Switched Reluctance Motors (SRM) and it is difficult to describe such a relationship due to the effects of magnetic saturation and double saliency of the construction. In order to improve its performance some advanced control strategies have been proposed, such as current or flux linkage profile control. To achieve these strategies the motor phase current or phase flux linkage should be controlled precisely. All these require increasing the switching frequency of converter to upgrade stability and dynamic response speed. On the other hand, the higher switching frequency may cause the higher switching losses, higher Electro-Magnetic Interference (EMI) and the lower overall efficiency.

The use of soft-switching techniques in converter can contribute to reduce them. A great deal researches and developments on SRM soft-switching converter have been reported. There are several converter methods available to regulate the power supply of SRM. The converters have different topologies (Lenin and Arumugam, 2011). These topologies are having number of switches according to the design (Maruthachalam and Palaniswamy, 2011). The SRM drives always have a phase winding in series with a switch. In case of a shoot through fault, the inductance of the winding limits the rate of rise in current and provides time to initiate protective relaying to isolate
the faults. The phases of the SRM are independent and, in case of one winding failure, uninterrupted operation of the motor drive operation is possible. Many researchers presented studies on speed control of SRM using converters. In some studies soft switched based converters used to control the speed for SRM (Song et al., 2010). Some of the researchers presented neuro and fuzzy controller based converters (Kumar et al., 2006; Li and Yue, 2009; Mianhua. 2009). Some different converters were designed exclusively for SRM drives. A new converter for Switched Reluctance Motor (SRM) drive is discussed in a study (Reis et al., 2009), which uses one switch in each phase.

The proposed converter is suitable for high speed drive application which is mainly due to the fast phase current commutation capability of this converter and due to generation of negative torque consequently generation of large ripple torque in SRM drives are resolved. A study described a soft switching converter (Guanxu, et al., 2009) for switched reluctance motor to improve the performance of the drive. All these methods have some disadvantages in the switching operation without soft switching or speed control are not precise and they do not consider the load and line variations.

In this study we designed a soft switched boost converter in the dc link of the 6/4 structure SRM to regulate the input supply and a power converter is designed to control the speed, for load changes and supply disturbances using two closed loop PI controllers. In these converters with closed loop control we achieve a constant speed and voltage for any load and line variations.

**Switched reluctance motor:** The Switched Reluctance Motor (SRM) drives for industrial applications are of recent origin. Key to an understanding of any machine is its torque expression, which is derived from first principles. The implications of machine operation and its salient features are inferred from the torque expression. The torque expression requires a relationship between machine flux linkages or inductance and the rotor position. The reluctance motor is a type of synchronous machine. It has wound field coils of a DC motor for its stator windings and has no coils or magnets on its rotor. Figure 1 shows its typical structure of 6/4. It can be seen that the stator and rotor have salient poles; hence, the machine is a doubly salient machine. The rotor is aligned whenever the diametrically opposite stator poles are excited. In a magnetic circuit, the rotating part prefers to come to the minimum reluctance position at the instance of excitation. While two rotor poles are aligned to the two stator poles, another set of rotor poles is out of alignment with respect to a different set of stator poles.

Then, this set of stator poles is excited to bring the rotor poles into alignment.

**Development of torque:** The most general expression for the torque produced by one phase at any rotor position is Eq. 1:

\[
T = \left[ \frac{\partial W}{\partial \Theta} \right] i = \text{Constant (1)}
\]

Since \( W = \text{Co-energy} = \frac{1}{2} F \Phi = \frac{1}{2} N I \Phi \) (2)

This Eq. 2 shows that input electrical power goes partly to increase the stored magnetic Energy \( \frac{1}{2}L*I^2 \) and partly to provide mechanical output power \( \frac{1}{2} N I \Phi \times \omega \), the latter being associated with the rotational e.m.f. in the stator circuit.

Neglecting saturation non-linearity Eq. 3:

\[
L = \text{Inductance} = \frac{N\Phi}{I} \quad (3)
\]

\[
T = \frac{1}{2} I^2 \frac{dL}{d\Theta} \quad (4)
\]

This Eq. 4 shows that the developed torque independent of direction of current but only depends on magnitude of current and direction of \( \frac{dL}{d\Theta} \).

**Generalized Equation of motor:**

\[
V = ri + d\Psi / dt \quad (5)
\]

\[
\Psi = Li = Nq, \quad \text{For} \ r = 0
\]

\[
V = L \frac{di}{dt} + i \left( \frac{dL}{d\Theta} \right) \left( \frac{d\Theta}{dt} \right) \quad (6)
\]

Fig. 1: SRM with 6/4 poles

Fig. 2: Block diagram of the proposed converters and controllers for 6/4 poles 3 phase SRM
Fig. 3: Design and analysis of the proposed converter

\[ V = L \frac{di}{dt} + i \omega \left(\frac{dL}{d\theta}\right) \]  

(7)

The Eq. 5-7 shows the input voltage to SRM mainly depends on voltage drop in the inductance and resistance.

The block diagram in Fig. 2 shows the proposed controller with two converters. The rectifier converts ac to dc and dc fed to the soft switched DC-DC converter, which regulates the voltage to the power converter with the help of the closed loop PI controller. The switch used in this converter switched ON at zero current and switched OFF at zero voltage. The regulated output for any line variations from the converter is given to the power converter. The power converter has four switches and all are used to control the speed of srm. The closed loop PI controller triggered the gate signals of these switches by the variable speed signals taken from the motor. The rotor position is sensed by sensor less control method. According to the rotor position and variable speed, PI controller sends triggering signal to the gate. This closed loop controller maintains the speed constant, which is set as reference. Thus the controllers used in these converters regulate the input supply and control the speed for any load and line variations.

Design and analysis of soft switched dc-dc boost converter: The configuration diagram of the proposed converter with soft switching scheme is shown in Fig. 3, the switch S1, L1, D3 and C2 are the main boost converter components, while R represents the resistive load on the converter. Inductor L2, L3, D1, D2 and C1 form the auxiliary circuit for accomplishing the soft switching of S1. Inductors L2 and L3 are much smaller than L1 and C1 is much smaller than C2. There are seven modes of operation.

Operational analysis of the proposed converter: Mode 1: This mode begins with the turn on of S1, at zero current at \( t_0 \). The expressions are Eq. 8-10:

\[ i_{L2}(t) = \frac{V}{L_2} \]  

(8)

\[ v_{C1}(t) = [V_i - V_{C1}(t_0)][1 - \cos \omega t] + V_{C1}(t_0) \]  

(9)

\[ i_{L3}(t) = [V_{C1}(t_0) - V_i] \frac{\sin \omega t}{\omega L_3} \]  

(10)

Where:

\[ \omega = \frac{1}{\sqrt{L_2C_1}} \]

When D3 stops conducting and this mode comes to an end.

Mode 2: The initial conditions on \( L_3, L_2 \) and \( C_1 \) are, \( i_{L3}(t_1), i_{L2}(t_1) + I \) and \( V_c \) respectively, attained at the end of Mode 1. The expressions are Eq. 11-13:

\[ v_{C1}(t) = -V_{C1}(t_1)[1 - \cos \omega t] \]

\[ + \frac{i_{L3}(t_1)}{\omega C_1} \sin \omega t - V_{C1}(t_0) \]  

(11)

\[ i_{L3}(t) = \frac{V_{C1}(t_1)}{\omega(L_2 + L_1)} \sin \omega t + i_{L3}(t_1) \cos \omega t \]  

(12)

\[ i_{L2}(t) = \frac{V_{C1}(t_1)}{\omega(L_2 + L_1)} \sin \omega t + i_{L2}(t_1) \cos \omega t + I \]  

(13)

Where:

\[ \omega = \frac{1}{(L_2 + L_1)C_1} \]

This mode comes to an end when \( V_{C1} \) reaches zero at \( t_2 \).

Mode 3: The initial conditions on \( i_{L3}, i_{L2} \) and \( V_{C1} \) for this mode \( i_{L3}(t_2), i_{L2}(t_2) \) are zero. The expression For \( i_{L3} \) is Eq. 14-16:

\[ i_{L3}(t) = -\frac{V_{L2}}{(L_2L_1 + L_1L_3 + L_3L_2)} t + i_{L3}(t_2) \]  

(14)

This mode comes to an end at \( t_3 \) when \( i_{L3} \) reaches zero at \( t_3 \).

Mode 4: In this mode current buildup in L1 and L2 and \( V_{out}(t) \) are governed by the Eq. as follows.
\[ i_{L_1}(t) = i_{L_2}(t) = \frac{V_S}{L_1 + L_2} t + I_i \]  \hspace{1cm} (15)

\[ V_{out}(t) = V_i e^{-\frac{t}{RC}} \]  \hspace{1cm} (16)

This mode comes to an end when \( S_1 \) is turned off at zero voltage at \( t_4 \).

**Mode 5:** This mode begins with the turn off of \( S_1 \) at zero voltage at \( t_4 \). The expressions are Eq. 17-20:

\[ V_{C_1}(t) = V_i (1 - \cos \omega_0 t) + \frac{I_2}{\omega_0 C_1} \sin \omega_0 t \]  \hspace{1cm} (17)

\[ I_{L_1}(t) = \frac{L_2}{(L_2 + L_1)} [V_C C_1 \sin \omega_0 t I_{L_2} (1 - \cos \omega_0 t)] + I_2 \]  \hspace{1cm} (18)

\[ I_{L_2}(t) = \frac{L_2}{(L_2 + L_1)} [-V_C C_1 \sin \omega_0 t I_{L_2} (1 - \cos \omega_0 t)] \]

**Mode 6:** In this mode \( i_{L_1} \) reduces to zero. This mode comes to an end at \( t_6 \) when \( i_{L_1} \) becomes zero. The expression for \( i_{L_1} \) and \( V_{C_1} \) for these mode is:

\[ i_{L_1} = \frac{V_{C_1}(t_s)}{L \omega_0} \sin \omega_0 t + i_{L_1}(t_s) \cos \omega_0 t \]  \hspace{1cm} (19)

\[ V_{C_1}(t) = [V_{C_1}(t_s) - V_2 [[\cos \omega_0 t - 1] \frac{i_{L_1}(t_s)}{\omega_0 C_1}] \sin \omega_0 t \]  \hspace{1cm} (20)

**Mode 7:** In this mode \( i_{L_1} \), \( i_{L_2} \) are zero. This mode comes to an end at \( t_7 \) when \( S_1 \) is turned on at zero current. This is the normal mode of the boost converter. The expressions are Eq. 21-22:

\[ V_{out}(t) = e^{-\alpha t} [A \sin \omega_0 t + B \sin \omega_0 t] + V_S \]  \hspace{1cm} (21)

\[ i_{L_1}(t) = \frac{V_{out}(t)}{R} + e^{-\alpha t} [(-BC_2 + AC_2 \omega_0 t) \cos \omega_0 t] \]  \hspace{1cm} (22)

Where:

\[ \alpha = \frac{1}{2RC_2}, \omega_0 = \frac{1}{\sqrt{L_2 C_2}} \]

\[ \omega = \frac{I_2}{\omega_0 C_2} - \frac{V_2}{R \omega_0 C_2} + \frac{\alpha (V_2 - V_S)}{\omega_0} \]

**MATERIALS AND METHODS**

**Design of PI controller:** A PI Controller (proportional-integral controller) is a feedback controller which drives the plant to be controlled with a weighted sum of the error (difference between the output and desired set-point) and the integral of that value. PI controller is mainly used to improve the performance of the system under disturbances. The dynamic performance of the PI controller can be improved by giving feedback to the converter to overcome the disturbances. A proportional-integral controller employed with a feedback loop can take the place of manual adjustment in DC-DC converter and act much more quickly than is possible.

Consider the DC-DC converter as “a process.” The DC-DC converter includes the converter itself, plus the DC power supply. To automate the control process, the “feedback loop” is closed, producing an error signal (+ or –). The PI controller acts upon the error with parallel proportional and integral responses in an attempt to drive the error to zero. When \( \alpha V \) out equals V set, then the error is zero. It can be used with the op-amp implementation of the controller. A proportional-integral controller (i.e., PI) employed with a feedback loop can take the place of manual adjustment in DC-DC converter and act much more quickly than is possible “by hand.” Consider the DC-DC converter as “a process.”

The controller output is given by Eq. 22.1-22.3:

\[ K_p \Delta + K_i \int \Delta dt \]  \hspace{1cm} (22.1)

where, \( \Delta \) is the error or deviation of actual measured value (PV) from the Set-Point (SP).

\[ \Delta = SP - PV \]  \hspace{1cm} (22.2)

A PI controller can be modeled easily in software such as Simulink using a “flow chart” box involving Laplace operators:

\[ C = \frac{G(1 + T_\text{s})}{T_\text{s}} \]  \hspace{1cm} (22.3)

Where:

\[ G = K_p = \text{proportional gain} \]

\[ G / \tau = K_i = \text{integral gain} \]
Setting a value for $G$ is often a tradeoff between decreasing overshoot and increasing settling time.

### Design of PI controller for a boost converter:

PI control is a traditional linear control method used in industrial applications. The linear PI controller controllers are usually designed for dc-dc converters using standard frequency response techniques and based on the small signal model of the converter. A Bode plot is used in the design to obtain the desired loop gain, crossover frequency and phase margin. The stability of the system is guaranteed by an adequate phase margin. However, linear PID and PI controllers can only be designed for one nominal operating point. A boost converter’s small signal model changes when the operating point varies. The poles and a right-half plane zero, as well as the magnitude of the frequency response, are all dependent on the duty cycle. Therefore, it is difficult for the PID controller to respond well to changes in operating point. The PI controller is designed for the boost converter for operation during a start up transient and steady state respectively. The load current of the proposed converter is given to the PI controller. The time constant of the controller is designed according to the small signal transfer function of the boost converter which is given below. Then the output of the PI controller changes the pulse width of the square wave which changes the firing angle of the MOSFET switch, so the output of the converter is controlled for different load disturbances.

The small signal model of the boost converter is designed based on the average state space averaging techniques, the small signal transfer function of a boost converter is Eq. 22.4:

$$\frac{V_o(s)}{D(s)} = \frac{V_s}{(1-D)^2} \left( 1 - s \frac{L}{R(1-D)^2} \right) \frac{L}{1+s^2} \frac{1}{R(1-D)^2} \frac{L}{s^2 + \frac{1}{LC}}$$  \hspace{1cm} (22.4)

### Design of power Converter:

Figure 4 shows the unipolar converter with two switches per phase which is a power converter for srm. The regulated output from the soft switched converter is fed to the power converter. The gate signals to the switches controls the speed of srm, getting the PWM signal from the PI controller.

### RESULTS AND DISCUSSION

The proposed soft switched converter is designed and simulated using MATLAB/SIMULINK. Two PI controllers are designed and simulated to regulate the input voltage fed to srm and control the speed of srm. The outputs are clearly shown that the soft switched converter with PI controller regulates the input supply to 300V as constant and the power converter with PI controller maintains a constant speed 3000 rpm in a 3phase 6/4 poles SRM. The outputs are constant for any load and line variations.

Figure 5 shows the input three phase ac supply fed to the rectifier.

Figure 6 shows the regulated output voltage from the softswitched dc dc converter. The converter is controlled by a PI controller. It is clearly seen that the output voltage is constant for any line variations. In the speed control method of srm most of the control is done by current, which is called as current controller. The Fig. 7 shows the rotor current, which is feedback to the converter by comparing with the reference current and variable speed from the SRM.

Figure 8 shows the constant speed 3000rpm is maintained in the srm for any load variations.

![Fig. 5: Input voltage](image1)

![Fig. 6: Regulated output voltage from the proposed Converter](image2)
CONCLUSION

Thus a soft switching scheme is designed and simulated for SRM to regulate the input voltage to SRM and control the speed of SRM with a feedback controller. Two PI controllers are used as feedback control. We have simulated the soft switching technique and achieved the low stress and less switching loss in the converter. The linear PI controllers were designed based on frequency response of the boost converter using frequency response technique. The simulated results show that the only switch used in this converter is switched ON at zero current and switched OFF at zero voltage. The output voltage is boosted and it is constant at steady state. It reaches the steady state value within several micro seconds for any line variations. It also maintains a constant speed for any load variation.

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