High coherence and low cross-talk in a tileable 3D integrated superconducting circuit architecture

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We report high qubit coherence as well as low cross-talk and single-qubit gate errors in a superconducting circuit architecture that promises to be tileable to two-dimensional (2D) lattices of qubits. The architecture integrates an inductively shunted cavity enclosure into a design featuring nongalvanic out-of-plane control wiring and qubits and resonators fabricated on opposing sides of a substrate. The proof-of-principle device features four uncoupled transmon qubits and exhibits average energy relaxation times $T_1 = 149(38) \mu$s, pure echoed dephasing times $T_{\text{me}} = 189(34) \mu$s, and single-qubit gate fidelities $F = 99.982(4)\%$ as measured by simultaneous randomized benchmarking. The 3D integrated nature of the control wiring means that qubits will remain addressable as the architecture is tiled to form larger qubit lattices. Band structure simulations are used to predict that the tiled enclosure will still provide a clean electromagnetic environment to enclosed qubits at arbitrary scale.

INTRODUCTION

Building two-dimensional (2D) lattices of hundreds or thousands of individually addressable, highly coherent qubits is an outstanding hardware challenge. Anticipated applications include demonstrations of logical gates using the surface code (1–4) and quantum simulations of 2D lattice Hamiltonians (5, 6). Superconducting circuits are a promising platform for realizing such lattices (6–8); qubits are lithographically defined on 2D substrates, and tailored coupling circuitry can be included in the regions between qubits to realize a universal gate set. Two requirements for scaling such superconducting qubit lattices are (i) a method to route control wiring to the circuit such that all qubits remain addressable and measurable at progressively larger scales and (ii) a means of preventing low frequency spurious modes from emerging in the circuit as the dimensions increase (9). These modes can arise from sections of spurious planar transmission lines such as slotlines (10, 11), or from 3D cavity enclosures that house the circuit (11). Solutions to these scaling challenges must not introduce substantial decoherence channels to qubits and, if fault tolerance is desired, must be compatible with gate fidelities beyond the thresholds of quantum error correction codes.

To overcome the wiring limitations of edge-connected circuits, 3D integrated control wiring is a practical solution. Various approaches to this have been demonstrated, for example, with spring-loaded pogo pins (12, 13) and with galvanic bonding of the qubit substrate to a wiring/interposer substrate (14–16). To avoid spurious modes due to slotlines, divided ground planes can be inductively shunted with airbridges (10) or with superconducting through substrate vias (15, 17, 18). To avoid low-frequency cavity modes, one solution is to divide the quantum processor into subsystems, with each subsystem enclosed in a cavity with dimensions $\leq 1$ cm (19–22). Alternatively, circuits can be enclosed in inductively shunted cavities that can scale arbitrarily in two dimensions with a cutoff frequency to cavity modes (23, 24).

In this work, we present experimental results on a four-qubit proof-of-principle circuit incorporating this latter concept. The circuit architecture, based on that introduced in (25, 26), features 3D integrated out-of-plane control wiring and qubits and readout resonators that are fabricated on opposing sides of a substrate. We incorporate a key new feature: inductively shunting the circuit enclosure with a pillar that passes through the substrate. This design is established to be compatible with transmon coherence times exceeding 100 $\mu$s, as well as low cross-talk and single-qubit gate errors. Simulations of band structure are used to predict that 2D qubit lattices can be formed by tiling a unit cell within the architecture, without the emergence of low-frequency cavity modes and with exponentially decaying cavity-mediated cross-talk between qubits.

RESULTS

Device architecture

Figure 1 shows optical images of the cavity enclosure and circuit. The enclosure base (Fig. 1A) features a single central “pillar”, and the lid (Fig. 1B) contains a matching cylindrical recess that is filled with a ball of indium (Fig. 1C). The base and lid both contain four tapered through-holes that act as waveguides for qubit and resonator control signals. In Fig. 1D, the circuit substrate is shown placed inside the enclosure base. The $5000 \pm 10$ $\mu$m side lengths of the base recess and the $4975 \pm 15$ $\mu$m side lengths of the substrate set a maximum misalignment of the circuit to the enclosure of 25 $\mu$m. An aperture has been machined in the center of the substrate allowing the pillar to pass through. The four coaxial transmon (27) qubits are visible, arranged in a $2 \times 2$ lattice with 2-mm spacing.

Figure 2 shows a schematic of the out-of-plane wiring design, the inductive shunt design, and the circuit layout. Control signals are...
routed to qubits and resonators by UT47-type coaxial cables with characteristic impedance $Z_{0,1} = 50 \pm 2.5$ ohms. As shown in Fig. 2A, the inner conductor of each cable, radius $r_0$, extends a distance $d_2$ into a through-hole in the circuit base/lid part, radius $r_2$, forming a coaxial waveguide with characteristic impedance $Z_{0,2}$. In this device, $r_0 = 145 \pm 2.5$ μm and $r_2 = 350 \pm 10$ μm (at room temperature), such that when ideally aligned $Z_{0,2} = 52.8 \pm 2.5$ ohms, a close match to the coaxial cable impedance $Z_{0,1}$. The polytetrafluoroethylene (PTFE) in the coaxial cable is separated from the circuit substrate by approximately 5 mm, reducing qubit/resonator electric field participation (28) in this lossy dielectric. The inner conductor of the coaxial cable terminates a distance $d_3$ from the qubit/resonator that it addresses. The coupling of control signals to qubits/resonators is dominantly mediated by an evanescent $\text{TM}_{01}$ circular waveguide mode (29), with coupling strength $\varepsilon \approx \exp(-d_3/\delta_c)$, $\delta_c \approx r_3/2.4$. For $r_2 = 350 \mu$m, $\delta_c \approx 150 \mu$m. In this device, $d_3 \approx 0.9$ mm (0.4 mm) for each qubit (resonator) control line. Figure 2B shows a schematic cross section of the pillar in the enclosure base, passing through the aperture in the circuit substrate and into the recess in the enclosure lid. The galvanic connection is formed by the pointed tip of the pillar pressing into an indium ball in the lid recess, with the contact force being provided by titanium fasteners in the four corners of the enclosure part. The pillar acts as a “bulk via” that inductively shunts the two halves of the enclosure, without requiring side wall metallization of the substrate aperture or a galvanic connection between the substrate and enclosure. Figure 2C shows the circuit layout. The reverse side

Fig. 1. Optical images of cavity enclosure and circuit. (A) Enclosure base with cavity, central pillar, and four tapered through-holes for out-of-plane wiring access. (B) Enclosure lid with a central cylindrical recess and identical through-holes for out-of-plane wiring. (C) Cylindrical recess in the lid filled with a ball of indium. (D) (Grayscale) Four-qubit circuit mounted inside the enclosure base. The four qubits are visible, arranged in a square lattice with 2-mm spacing. (E) A spiral resonator and (F) a transmon qubit with identical electrode dimensions to those in the device.

Fig. 2. Device schematics. (A) Cross section of the out-of-plane wiring design (not to scale), here shown addressing a qubit. PTFE, polytetrafluoroethylene. (B) Cross section of the bulk via inductive shunt design (to scale). The designed dimensions are shown in micrometers. (C) Circuit layout illustration (not to scale). The substrate and enclosure are partially shown, and the out-of-plane wiring is shown for $Q_2$. Examples of the coupling terms and drive voltages in the Hamiltonian in Eqs. 1 and 2 are shown.
of the substrate rests directly on the enclosure base and contains four lumped LC "spiral" resonators. Each resonator is coaxially aligned with and capacitively coupled to a qubit. The cavity enclosure provides the ground, and there are no ground planes on the substrate. The qubit (resonator) electrodes are electrically floating.

**Basic characterization**

An effective dispersive Hamiltonian for the low energy spectrum of the device is given by

$$\frac{\hat{H}}{\hbar} = \sum_{i=1}^{4} \omega_{q,i} \hat{a}_{i} \hat{a}_{i}^\dagger + \frac{\alpha_{i}}{2} \hat{a}_{i}^\dagger \hat{a}_{i} (\hat{a}_{i}^\dagger \hat{a}_{i} - 1) + \omega_{r,i} \hat{b}_{i} \hat{b}_{i}^\dagger + 2 \chi_{ii} \hat{a}_{i}^\dagger \hat{b}_{i} \hat{b}_{i}^\dagger \hat{a}_{i}^\dagger + e_{q}^i (\hat{a}_{i}^\dagger - \hat{a}_{i}) \hat{V}_{q}^i + e_{r}^i (\hat{b}_{i}^\dagger - \hat{b}_{i}) \hat{V}_{r}^i + \hat{H}_{ext}/\hbar$$  \hspace{1cm} (1)

Here, $\hat{a}_{i}^\dagger$ ($\hat{a}_{i}$) and $\hat{b}_{i}^\dagger$ ($\hat{b}_{i}$) are the creation (annihilation) operators for qubit $i$ and resonator $i$, respectively; $\omega_{q,i}$ is the transition frequency of qubit $i$ given zero photons in resonator $i$; $\alpha_{i}$ is the anharmonicity of qubit $i$; $\omega_{r,i}$ is the frequency of resonator $i$ given qubit $i$ in its ground state; $\chi_{ii}$ is the dispersive shift between qubit $i$ and resonator $i$; and $e_{q}^i$ ($e_{r}^i$) describes the coupling of qubit (resonator) $i$ to qubit (resonator) control line $i$, which is driven with a voltage $V_{q}^i$ ($V_{r}^i$). These voltages are applied close to the cylindrical waveguide transition and at a fixed distance from the circuit (see Fig. 2C). $\hat{H}_{s}$ contains undesired cross-talk terms that are discussed in the cross-talk characterization section.

The quantities $\omega_{q,i}$, $\alpha_{i}$, $\omega_{r,i}$, and $\chi_{ii}$ were determined using standard spectroscopic measurements and Ramsey measurements and are shown in Table 1. The relaxation times $T_{1}$ of the four qubits were simultaneously measured repeatedly over a period of 12 hours. The characteristic dephasing times $T_{2}$ and $T_{2e}$ were measured using standard Ramsey and Hahn echo pulse sequences (see the Supplementary Materials), performed on each qubit separately. Both characteristic dephasing times were measured repeatedly for approximately 2 hours per qubit. The coherence times are summarized in Table 2.

**Cross-talk characterization**

The device is a proof-of-principle demonstration of the circuit architecture with no intentional couplings except between qubit-resonator pairs; as such, we identify all other couplings as undesired cross-talk. The cross-talk terms that were considered are defined in the following effective Hamiltonian

$$\hat{H}_{s}/\hbar = \sum_{i=1}^{4} \sum_{j \neq i} \chi_{ij} \hat{a}_{i}^\dagger \hat{a}_{i} + 2 \chi_{r} \hat{a}_{i}^\dagger \hat{b}_{j} \hat{b}_{j}^\dagger + e_{q}^{j} (\hat{a}_{i}^\dagger - \hat{a}_{i}) \hat{V}_{q}^{j} + e_{r}^{j} (\hat{b}_{j}^\dagger - \hat{b}_{j}) \hat{V}_{r}^{j}$$  \hspace{1cm} (2)

Here, $\chi_{ij}$ is a parasitic transverse coupling between qubits $i$ and $j$, satisfying $\chi_{ij} = \chi_{ji}$; and $e_{r}^{j}$ describes a parasitic coupling between qubit (resonator) $i$ and qubit (resonator) control line $j$. Some examples of these different types of cross-talk are shown pictorially in Fig. 2C. The following expression was used to relate the dispersive shift $\chi_{ij}$ to a transverse coupling $g_{ij}$ between transmon qubit $i$ and resonator $j$.

$$\chi_{ij} \approx \frac{(q_{ij})^{2} E_{C,j}/\hbar}{\Delta_{ij}(\Delta_{ij} - E_{C,j}/\hbar)}$$  \hspace{1cm} (3)

where $\Delta_{ij} = \omega_{q,i} - \omega_{r,j}$, and $E_{C,j}$ is the charging energy of qubit $i$.

The experimentally bounded maximum parasitic transverse couplings are summarized in Table 3, along with the predicted maximum values found by applying a simple impedance formula to high-frequency structure simulator (HFSS)-driven terminal simulations (see the Supplementary Materials).

**Qubit control line selectivity**

The qubit control line selectivity $q_{ij}$ is here defined

$$q_{ij}^{\phi} \equiv \left( \frac{e_{q}^{j}}{e_{r}^{j}} \right)^{2}$$  \hspace{1cm} (4)

The selectivity was measured by driving qubit $i$ at frequency $\omega_{q,i}$ from qubit control line $j$ over a range of generator drive voltages $V_{q}^{j,\text{gen}}$ and fitting the induced Rabi oscillation rate $\Omega_{r}$ to the linear function $\Omega_{r} = k_{q} V_{q}^{j,\text{gen}}$. From the measured linear response into the strong drive regime, it is inferred that the effective drive mediated by the $j$ coupling between the qubits was negligible (see the Supplementary Materials). In this case, the selectivity takes the simple form $q_{ij}^{\phi} = (k_{q}/k_{j})^{2}$. The measured qubit control line selectivities are shown in Fig. 4A, and the plots of $\Omega_{r}$ versus $V_{q}^{j,\text{gen}}$ that were used to determine $q_{ij}^{\phi}$ are shown in fig. S4. The linear response in the strong drive regime also leads to a conservative experimental bound on the transverse coupling $J_{ij}$: $|J_{ij}| < \min(q_{ij}^{\phi})^{-0.5} |\Delta_{ij}^{\phi}|$ (see the Supplementary Materials).

**Resonator control line selectivity**

The resonator control line selectivity $q_{ij}$ is here defined

### Table 1. Basic device characterization

Summary of basic circuit parameters. The quantities $\omega_{q}$, $\omega_{r}$, $\alpha$, $\chi$, and $g$ are as defined in the main text. The quantity $E_{C,j}$ is the ratio of the Josephson energy to the charging energy in qubit $i$; $\kappa_{ext}$ is the external decay rate of resonator $i$; $Q_{int}$ is the internal quality factor of resonator $i$; and $n_{x,i}$ is the residual excited state population in qubit $i$.

|   | $\omega_{q}/2\pi$ (GHz) | $\omega_{r}/2\pi$ (GHz) | $\alpha/2\pi$ (MHz) | $E_{C}/E_{C,j}$ (kHz) | $\chi/2\pi$ (kHz) | $g/2\pi$ (kHz) | $\kappa_{ext}/2\pi$ (kHz) | $Q_{int}$ (10^3) | $\rho_{x}$ (%) |
|---|-----------------|-----------------|-----------------|-----------------|-----------------|-----------------|-----------------|-----------------|-----------------|
| Q1/R1 | 3.981 | 7.968 | -199 | 69 | -165 | 124 | 118 | 110 | 13 |
| Q2/R2 | 4.045 | 8.083 | -199 | 71 | -167 | 126 | 73 | 75 | 18 |
| Q3/R3 | 4.130 | 8.183 | -198 | 74 | -169 | 128 | 749 | 515 | 13 |
| Q4/R4 | 4.192 | 8.289 | -197 | 76 | -164 | 128 | 241 | 160 | 10 |
To measure the parasitic dispersive shift parasites qubit-resonator coupling over a range of generator drive powers AC Stark-shift graphs for legibility.

The selectivity was measured by continuously driving resonator $i$ at a detuned frequency $\omega_{AC,i} = \omega_{ij} + \Delta_d$ from resonator control line $j$ over a range of generator drive powers $P_{gen,j}$. The induced AC Stark-shift $\chi_{AC,i}$ in qubit $i$ was then fit to the linear function $\chi_{AC,i} = k_{ij} P_{gen,j}$. The selectivity is then given by $\varphi_{ij}^s = (\chi_{ij}/\chi_{ii}) (k_{ij}/k_{ii})$ (see the Supplementary Materials). The measured resonator control line selectivities are shown in Fig. 4B, and the plots of $\omega_{AC,i}$ versus $P_{gen,j}$ that were used to determine $\varphi_{ij}^s$ are shown in Fig. S5.

**Parasitic qubit-resonator coupling**

To measure the parasitic dispersive shift $\chi_{ij}$ between qubit $i$ and resonator $j$, resonator $j$ was continuously driven at frequency $\omega_{ij}$ from its own control line to populate it with a steady-state photon number $n_{crit,j}/10$, where $n_{crit,j} = (\Delta_d/2g_{ij})^2$ is the critical photon number (31) of resonator $j$ (see the Supplementary Materials). Ramsey experiments were then performed on qubit $i$ ($i \neq j$) to measure the parasitic AC Stark-shift $\omega_{AC,i}$ as shown in Fig. 4C. No AC Stark-shift $\omega_{AC,i}$ was detected for any combination of qubit $i$ and resonator $j$, with a frequency resolution of approximately 1 kHz, resulting in the approximate bound $\chi_{ij}/2\pi < 20$ Hz using the dispersive relation $\omega_{AC,i} = 2\chi_{ij} \bar{n}_j$ (32).

**Single-qubit gate errors**

Single-qubit randomized benchmarking (RB) (33, 34) was performed on all four qubits both separately and simultaneously, using a combination of $24$-ns duration (20-$ns$ Blackman envelope with 4-$ns$ buffer) physical $I, X_{\pi/2,x}$ gates with derivative removal gate (DRAG) pulse shaping (35), and virtual $Z$ gates (36). Single-shot readout was performed for all the RB experiments (see Materials and Methods). Figure 5A shows the fitted RB curves for the simultaneous RB experiment. The RB protocol was run at 31 Clifford sequence lengths and for $k = 80$ different sequences of Clifford gates. Each of the $31 \times 80$ experiments was repeated 5000 times to build statistics. The resulting error-per-physical gates (EPGs) are presented in Table 2.

**Table 2. Qubit coherence and gate errors.** Summary of coherence results and error-per-physical gate (EPG) as found by separate (sep) and simultaneous (sim) randomized benchmarking. The pure echoed dephasing times are given by $1/T_{\chi,e} = 1/T_{\chi,e} - 1/(2T_1)$. The coherence limited EPG (EPG coh lim) was calculated as $(3 - \exp(-T_{\chi,e}/2\chi_{eff}))/6$ (30), where $T_1$ is the total period of each physical gate, here 24 ns.

|     | $T_1$ (μs) | $T'_2$ (μs) | $T_{\chi,e}$ (μs) | $T_{\chi,e}$ (μs) | EPG sep (10⁻⁸) | EPG sim (10⁻⁸) | EPG coh lim (10⁻⁸) |
|-----|------------|-------------|------------------|------------------|---------------|---------------|------------------|
| $Q_1$ | 106(24)    | 95(5)       | 101(9)           | 193(52)          | 2.29(4)       | 1.64(4)       | 1.1(1)           |
| $Q_2$ | 159(30)    | 104(9)      | 116(6)           | 183(25)          | 1.46(6)       | 2.15(8)       | 0.94(5)          |
| $Q_3$ | 179(21)    | 89(12)      | 128(9)           | 199(25)          | 1.16(5)       | 1.31(5)       | 0.85(5)          |
| $Q_4$ | 151(30)    | 99(8)       | 113(4)           | 181(24)          | 2.23(4)       | 2.16(4)       | 0.97(5)          |
| Avg. | 149(38)    | 97(10)      | 115(12)          | 189(34)          | 1.8(5)        | 1.8(4)        | 1.0(1)           |
Correlated RB (37) was performed using the simultaneous RB experiment data. Figure 5B shows a selection of the $2^4 - 1$ Pauli-Z correlators versus Clifford sequence length $m$. These were fit to standard RB curves $A \sigma^n_c + B$, where $S \subseteq B_0$, $S \neq \emptyset$, $B_j = \{0\}, \{1\}, \{2\}, \{3\}$ as defined in (37). From the fitted depolarizing parameters $\alpha_{ij}$, the depolarizing fixed-weight parameters $\epsilon_{ij}$ and the cross-talk metric $\eta$ were calculated (37). The $\epsilon_{ij}$ parameters can be interpreted as the probability of a depolarizing error occurring in subspace $S$ per Clifford gate, and $\eta$ is a scalar quantity that expresses the distance of the measured four-qubit error channel from the nearest product of single-qubit error channels (37). The calculated $\epsilon_{ij}$ values are shown in Fig. 6, and we find $\eta \approx 1 \times 10^{-4}$ (see the Supplementary Materials).

Leakage RB (LRB) (38) was performed separately on $Q_0$, which had the highest readout fidelity (see Materials and Methods). The resultant LRB curve is shown in Fig. 5C, with a leakage-per-physical gate (LPG) of $3.49(7) \times 10^{-3}$, and an EPG of $2(1) \times 10^{-4}$ found using a four fit parameter model, and $2.33(7) \times 10^{-4}$ found using a more robust three fit parameter model that assumes EPG$\gg$LPG (38).

**Table 3. Bounds on parasitic transverse couplings.** Experimentally determined bounds on the magnitude of parasitic transverse couplings in the device, and the maximum predicted values between any qubit-qubit/ qubit-resonator pair found using HFSS-driven terminal simulations and an impedance formula (30).

| Cross-talk quantity | Experiment (kHz) | Simulation (kHz) |
|---------------------|------------------|------------------|
| Qubit-qubit coupling | $|J|/2\pi$ | $< 250$ | 10 |
| Qubit-resonator coupling | $g/2\pi$ | $< 1500$ | 50 |

**Fig. 4. Cross-talk characterization.** (A) Experimentally measured qubit control line selectivity $\phi_q^i = (\epsilon_q^i/\epsilon_q^0)^{1/2}$ from qubit i to qubit control line j, expressed in units of dB as $10 \log_{10}(\phi_q^{ij})$. (B) Experimentally measured resonator control line selectivity $\phi_r^i = (\epsilon_r^i/\epsilon_r^0)^{1/2}$ from resonator i to resonator control line j, expressed in units of dB as $10 \log_{10}(\phi_r^{ij})$. (C) Frequency variation in $Q_i$ found from 20 repeated Ramsey experiments, with either no drive on any resonator or a continuous drive applied to $R_0$, $R_1$, or $R_0$ at frequency $\omega_{ij}$, that populates it with a photon number $n_i$ of at least $n_{\text{low},i} \equiv n_{\text{low},/10}$.

**DISCUSSION**

The architecture presented in this paper uses an inductively shunted cavity enclosure that tightly surrounds the circuit, combined with 3D integrated out-of-plane control wiring and “reverse-side” readout resonators. The results demonstrate that this design is compatible with transmon relaxation times $T_1$ at least in the range of 150 to 200 $\mu$s. The observed variation in $T_1$ is consistent with measured variation in transmon qubits on hour-long time scales and is suggestive of coupling to two-level system defects (47) as the dominant relaxation mechanism (42–44). The marked residual excited state population of the qubits suggests that quasiparticle-induced relaxation may also be significant (45, 46), indicating a potential need for improved infrared filtering of signals to the device (47, 48). We attribute the increase in coherence times compared to previous implementations of the architecture (25, 26) mainly to the change in fabrication process (see Materials and Methods) and to the positioning of the out-of-plane control wiring, which ensured that qubits were not radiatively limited. The $T_1$ time purely due to radiation through all control and readout lines of the device was predicted to be $\sim 5$ ms using HFSS simulations (49, 50) (see Supplementary Materials). Bringing in all the control lines by 100 $\mu$m in simulations, to model a significant misalignment of the out-of-plane wiring, reduces this radiation limited $T_1$ time to $\sim 550$ $\mu$s, implying that the measured qubit coherence times were robust to such a misalignment. The
The average error per gate was approximately 50% coherence limited, and the leakage per gate as characterized on $Q_3$ was found to be less than 20% of the error per gate. These values might be improved in the future by more detailed pulse shaping and phase error correction (37).

The enclosure package could be reused by reshaping the indium ball in the lid recess to ensure a galvanic connection to the pillar on subsequent closure. The circuit was not bonded to the enclosure and could be removed and remounted. A limitation of the current enclosure design is that there was no contact force pressing the substrate to the enclosure. This could be improved by inserting spring-loaded pogo pins into the enclosure lid, which may enhance the circuit thermalization. In the current device, the single pillar in the enclosure is not expected to have contributed to the observed high coherence and low cross-talk results due to the small cavity size. However, these results are expected to be maintained on tiling to larger qubit arrays because of the inductive shunt array formed by the pillars, which causes the qubits to be exponentially insensitive to their nonlocal environment.

Experimentally realizing this tileability will rely on the ability to fabricate larger substrate aperture arrays and to manufacture larger arrays of 3D integrated out-of-plane wiring. Using the same methods as in the present work (see Materials and methods), we have successfully machined square aperture arrays with 2-mm pitch and featuring 725 apertures in 3-inch silicon substrates. Femtosecond laser machining and etching processes could also be explored to form these aperture arrays. Scaling the out-of-plane control wiring will require forming large arrays of 2-mm pitch coaxial waveguides. To minimize reflections and ensure uniform coupling of the control wiring to qubits and resonators, it would be desirable to have approximately 10-μm tolerances on the dimensions shown in Fig. 2A. We anticipate that this is a solvable engineering challenge.

Another important question is whether the introduction of qubit coupling circuitry between neighboring qubits will negate the
We do not anticipate this to be the case. Idealized circuit models for 2D arrays of nearest neighbor coupling circuitry can be used to predict that, for qubit pitches that are significantly smaller than the excitation wavelength at qubit frequencies, this cross-talk channel will also be characterized by a cutoff frequency to spurious modes and exponentially decaying cross-talk with spatial separation.

A shortcoming of the presented device is that it exhibited small, variable external resonator decay rates $\kappa_{\text{ext}}$ and dispersive shifts $\chi$ that were nonoptimal for qubit readout. The small and variable $\kappa_{\text{ext}}$ values may be attributed to slight movement of the control line inner conductors due to material contraction during cooling to cryogenic temperatures. The small $\chi$ values were due to the large qubit-resonator detunings and the choice of qubit and resonator electrode dimensions. We anticipate that future devices can achieve improved readout parameters.

**Takeaways**

In this work, average transmon qubit coherence times of $T_1 = 149(38) \, \mu$s, $T_2^* = 189(34) \, \mu$s, and simultaneous single-qubit gate fidelities of $F = 99.982(4)\%$ have been measured in a four-qubit demonstration of a 3D integrated superconducting circuit architecture. It has been shown that, before the inclusion of qubit coupling circuitry, residual cross-talk is highly suppressed. It is anticipated that a unit cell inside the device can be tiled to form larger devices that feature lattices of qubits. Band structure simulations predict that such devices will have a cutoff frequency to cavity modes that is well above qubit frequencies, in agreement with a metamaterial model that further predicts that cavity-mediated cross-talk between qubits in these lattices will decay exponentially with spatial separation. A potential near-term application for this architecture is the study of correlated errors generated by high energy radiation, where correlations could be probed in lattices of qubits with high coherence and exponentially suppressed cross-talk.

**MATERIALS AND METHODS**

The device enclosure was made from 6061 aluminum using computer numerical control (CNC) machining with $\pm 10 \, \mu$m tolerance on features. The out-of-plane wiring was made from silver-plated copper UT47 coaxial cable. The outer jackets and dielectrics were stripped back to expose the inner conductors. The circuit was fabricated on a double-side polished high-resistivity intrinsic silicon wafer using a double-sided waferscale process. Following a hydrofluoric acid dip, aluminum was deposited onto both sides of the wafer by vaporization. The qubit and resonator electrodes were defined by a wet etching process, and the qubit Josephson junctions were formed using the Dolan Bridge double-angle shadow evaporation technique. After circuit fabrication, the wafer was diced into square dies with side lengths of 4975$\pm$15 $\mu$m using a Disco DAD3430 dicing saw, and a 650-$\mu$m-diameter aperture was then CNC drilled in the center of selected dies using a Loxham Precision $\mu$m micro-machining system. An approximately 0.5-$\mu$m-thick layer of Si805 photoresist was used as a protective layer during these CNC processes.

Qubit readout was performed using a standard heterodyne detection technique. It was possible to perform single-shot readout on all qubits by using 5-$\mu$m measurement pulses, with assignment fidelities: $F = [97.8, 97.7, 98.5, 98.4]\%$, where $F = 1 - p(e | g) - p(g | e)$.

To extract the frequency of Ramsey fringes in Ramsey experiments, an interpolation method was applied to improve the frequency resolution found from the Fourier transform of the time traces. Details are provided in the Supplementary Materials.

Experiments were all carried out during a single cooldown inside an Oxford Instruments Triton 500 dilution refrigerator, with a base stage temperature of $\sim 20 \, \text{mK}$. A diagram of the dilution refrigerator setup is included in the Supplementary Materials.

**SUPPLEMENTARY MATERIALS**

Supplementary material for this article is available at https://science.org/doi/10.1126/sciadv.abl6698

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