Quantum computation with un-tunable couplings

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Most quantum computer realizations require the ability to apply local fields and tune the couplings between qubits, in order to realize single bit and two bit gates which are necessary for universal quantum computation. We present a scheme to remove the necessity of switching the couplings between qubits for two bit gates, which are more costly in many cases. Our strategy is to compute in and out of carefully designed interaction free subspaces analogous to decoherence free subspaces, which allows us to effectively turn off and turn on the interactions between the encoded qubits. We give two examples to show how universal quantum computation is realized in our scheme with local manipulations to physical qubits only, for both diagonal and off diagonal interactions.

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Quantum computation is generally formulated in terms of a collection of qubits subject to a sequence of single and two bit operations [1]. This implies that the effective local fields applied to individual qubits, and the couplings between the qubits, are variable functions subject to external control. In many cases, two bit operations, whose implementation depends on certain interactions between qubits, are more difficult than single bit gates. They can require more sophisticated manipulations, therefore may take a longer time and cause stronger decoherence. This usually results from the requirement to vary (in the simplest case just switch on and off) the couplings between qubits, which is not always possible, or easy to realize. One such example is quantum computing with Josephson junction devices, both charge and flux type [2, 3, 4, 5, 6]. In this case, the coupling between qubits is most naturally realized with a hard wired capacitor or inductor, whose value is fixed by the fabrication and cannot be tuned during the computation. The superconducting quantum computing community has been working hard to devise variable coupling schemes [7, 8, 9], but it is generally agreed that none of these proposed switches is completely satisfactory. Most of them [7, 8] require external controls, thus are likely to be major decoherence sources. Others were designed to avoid such external controls, but may suffer other problems, for instance the number of qubits that can be incorporated into the system can be limited [10, 11], which is at odds with the supposed scalability of a solid state quantum computer.

An always on and un-tunable coupling causes certain problems for quantum computation, depending on the particular form of the interaction. If the interaction Hamiltonian is diagonal in the computational basis, each qubit state will gain additional phases depending on the states of the qubits to which it is coupled, even in the idle mode. It is then necessary to keep track of these phases, or suppress them by repeated refocusing pulses like those used in NMR, which requires high precisions and complicates the operation [12, 13]. The situation is more serious in the case of off diagonal interactions, because these interactions will cause the states of the qubits to propagate, which results in errors. It is then necessary to devise methods to avoid these problems. Even if the couplings can be tuned, a scheme which allows to compute without switching the couplings is very useful, because it simplifies the operation drastically and is likely to help reduce decoherence. This simplified computing scheme, in which the necessity of switching the couplings between qubits is removed, is the goal of this work, and we attack this problem by computing in carefully designed subspaces analogous to decoherence free subspaces.

Let us first explain our approach intuitively. One of the strategies that people came up with in the effort to fight against decoherence is to compute in the so called “decoherence free subspace” (DFS), in which the state of the system (a logical bit consisting of several physical qubits) is unaffected by the environment even though they are always coupled [14]. DFS exists in the case of “collective decoherence”, i.e., when the involved qubits couple to the same mode of the environment. Now imagine that we replace the environment with another collection of qubits. Obviously, we expect analogous subspaces of the two coupled collections of qubits exist, as long as certain conditions similar to those for DFS are satisfied. To be more specific, states that are annihilated by the interaction Hamiltonian will not evolve because of the coupling. If we stay in these subspaces, we can then prevent the (encoded) qubits from affecting each other and operate on the individual logical bits as if they were not coupled to other bits. On the other hand, when we do want the (encoded) bits to interact for two bit operations, we simply drive them out of these subspaces. Therefore, we can effectively turn off and turn on the interaction between the encoded bits by staying in and getting out of these subspaces. We see that even though the subspaces we discussed rely on the same algebraic properties with DFS, we are using them for a different purpose, and we do not intend to stay in them throughout the computa-
tions. A general model for a quantum computer is de-
cided to be used for both diagonal and off diagonal interac-
ditions, we call this new concept the “Interaction 
free subspace” (IFS).

In the following we discuss in detail how our scheme 
can be used for both diagonal and off diagonal interac-
tions. A general model for a quantum computer is de-
scribed by the following Hamiltonian:

\[ H = - \sum_i \vec{f}_i \cdot \vec{d}_i + \sum_{i<j} \sum_{\alpha, \beta} J_{ij}^{\alpha \beta} \sigma_i^\alpha \sigma_j^\beta, \]  

where \( \vec{f}_i \) is the effective local field applied to individual 
qubits, \( \sigma^\alpha \), \( \sigma^\beta \) are the pauli matrices, and \( J \) is the 
coupling strength. As we discussed before, we are interested 
in situations in which local operations are easy and fast to 
implement, while two bit operations are hard and slow. 
We assume that local resources are “free”, i.e., strong 
local pulse fields \( \vec{f}_i \) can be applied and single bit gates 
are instantaneous. We then only count the time when 
the interaction is on (i.e., when the encoded bits sit out 
of the IFS). we assume that the values of \( J_{ij} \), i.e., the 
coupling strength between qubits can be chosen in the 
fabrication or initialization of the system, but cannot be 
tuned during the computation. (The dependence on \( \alpha, \beta \) 
is determined by the nature of the physical interaction.) 
When the interaction is on, we occasionally need to apply 
local gates to the individual qubits involved, and we as-
sume that the field used for these local gates is so strong 
that the local operation is not distorted by the interac-
tions.

We first consider the case of diagonal interactions, 
i.e., when the interaction between two (physical) qubits 
takes the form \( J_{ij}^{\alpha \beta} \cdot \sigma_i^\alpha \sigma_j^\beta \) (the Ising interaction). In this 
case, two physical qubits per logical bit can fulfill our 
needs. As shown in Fig. 1 a possible architecture of the 
quantum computer in this case is a one dimensional 
array consisting of encoded qubits, which are two physi-
cal qubits (a, b in the figure) coupled with strength \( J_0 \).
All physical qubits in neighboring logical bits are cou-
pled with the same strength \( J_1 \), which is not necessar-
ily different from \( J_0 \). Our codes for the IFS are sim-
ply \( |0\rangle = |\uparrow_a \downarrow_b\rangle \) and \( |1\rangle = |\downarrow_a \uparrow_b\rangle \). Indeed, for two 
neighboring encoded bits, the interaction Hamiltonian is 
\( H_{\text{int}} = J_1 (\sigma_1^a + \sigma_1^b) \cdot (\sigma_2^a + \sigma_2^b) \), which annihilates these 
two states. In addition, these two states are degenerate 
under the self Hamiltonian \( J_0 \sigma_1^a \cdot \sigma_1^a \). Therefore, if we 
store information in these states, no evolution whatso-
ever is present. There is thus no need to keep track of 
any phases. Actually, in order to avoid interactions be-
tween the encoded qubits, it suffices to keep half of the 
qubits (all odd or even numbered ones) in the IFS. 
However, as will be seen it is inevitable to get out of the IFS 
even when single bit operations are performed on the en-
coded qubits, since the only resources we have are single 
(physical) bit rotations. We thus keep all the logical bits 
in the IFS during the idle mode.

Now we discuss how universal computation can be re-
alized on these encoded qubits, using local operations to 
the physical qubits only. Suppose we are operating on a 
particular encoded bit (meaning applying single bit gates 
on its qubit a and b). Since its neighboring bits are in 
IFS, we can work on the particular bit as if it were de-
coupled from the rest of the system. First, we notice that 
single bit gates can be decomposed into arbitrary 
rotations around the \( z \) axis and the Hadamard gate 
\[ H = \frac{1}{\sqrt{2}} \begin{pmatrix} 1 & 1 \\ 1 & -1 \end{pmatrix}. \] 
To induce a rotation around the \( z \) axis, all we have to do is to break the degeneracy between \( |0\rangle \) and \( |1\rangle \), which 
can be done by applying a local field in the \( z \) direction to 
bit a (or b) for a certain amount of time. A Hadamard 
gate on the bases \( |0\rangle \) and \( |1\rangle \) is more complex, but as can be 
easily verified it can be realized with the gate sequence 
\( CNOT(a, b) \cdot H_a \cdot CNOT(a, b) \), where \( CNOT(a, b) \) is a 
CNOT gate between a, b with a as the control bit, and 
\( H_a \) is a Hadamard on bit a. \( H_a \) is readily realizable, while 
\( CNOT(a, b) \) can be done by sandwiching a \( CPHASE \) 
gate between a and b with \( H_b \). The \( CPHASE \) gate is 
easy to implement with the Ising interaction and local 
gates and 2 interaction periods, which takes a time 
\( \pi/2J_0 \) (local gates are assumed instantaneous). We note that it 
is necessary to get out of the code space \( |\uparrow_a \downarrow_b\rangle, |\downarrow_a \uparrow_b\rangle \) in 
order to realize the Hadamard. This is unavoidable in the 
current model, since the only allowed resources are local 
unitaries. As a result, single bit gates on the encoded 
qubits cannot be performed simultaneously to neighbor-
ring bits. But a “half parallel” operation mode is still

![Diagram of the quantum computer for diagonal interactions.](image)
allowed, in which all odd or even numbered logical bits are operated on at the same time. This restriction can be removed by exploring more complex encoding schemes (see the example for off diagonal interactions below), but more resources (more physical qubits per logical bit) are required.

We still need to show how two bit gates can be realized. For this purpose, we need drive the involved (neighboring) bits out of the IFS, let them interact for some time, then drive them back. The first step is to apply a local gate to flip the state of b, which changes the two code states to |\uparrow_1\downarrow_2⟩ and |\downarrow_1\downarrow_2⟩. Note these two states are eigenstates of Σ

\[ \sigma_z \] 

with eigenvalues ±1. It is then straightforward that a CPHASE gate

\[ e^{-i\frac{\pi}{4}(\sigma_z \downarrow_1 + \sigma_z \downarrow_2)} \] 

or

\[ e^{-i\frac{\pi}{4}(\sigma_z \downarrow_1 + \sigma_z \downarrow_2)} \] 

Once this is done, we simply flip the state of b again to drive the logical bits back into the IFS. This procedure realizes a CPHASE gate between 2 encoded bits and puts them back in the IFS at the end. Single bit operations (on the logical bits) can then be performed to get other two bit gates like CNOT. A total number of 8 local operations and 1 interaction are necessary, and the time required is π/16J₁. This time is actually shorter than that needed in an ordinary Ising model with switchable couplings, π/4J₁.

For completeness let us discuss briefly how the system can be initialized in the IFS and how the states of the encoded bits can be measured. If we apply a strong global field in the z direction to all the physical qubits, at low temperatures all bits will line up with the field. Then starting with the left most qubit, we can drive all the bits into IFS by simply flipping the state of one of a, b. To read out the state of the encoded bits, a measurement on its a or b suffices.

With the procedures discussed above for diagonal interactions, we can turn on and off the interactions between the logical qubits without a physical switch. This is readily applicable in superconducting quantum computation. Here, local fields can be easily applied simply by changing the biases of the superconducting qubits or applying ac fields. Typical times for single bit gates range from hundreds of ps to tens of ns, depending on the type of the qubits and the choice of the parameters. Coherent control of single superconducting qubits has been experimentally realized [14]. With the fast operation speed [1] and long decoherence time [2] experimentally demonstrated, large scale superconducting quantum computers can be constructed with the aid of IFS. The uniform couplings required (in Fig. 2) between physical qubits in neighboring logical bits should all be J₁) are relatively easy to realize in superconducting designs, as mutual inductances can be calculated and fabricated very precisely. Were it necessary to compensate for fabrication imperfections, simple schemes for minor adjustment of the coupling are readily accomplished [3]. This calibration step can be very slow, so the leads used for calibration can be heavily filtered to keep out the noise. For more detailed discussion on the application of our scheme in superconducting quantum computing, see Ref. [15].

We now turn to the case of off diagonal interactions. We will focus on the isotropic and anisotropic exchange interactions, \( H_{int} = J_{xy} \sigma_x^1 \sigma_x^2 + \sigma_y^1 \sigma_y^2 + J_z \sigma_z^1 \sigma_z^2 \), since these are most frequently encountered in quantum computing proposals. If both dissipation and decoherence are present, DFS requires 4 bits per encoded bit [10]. However as we discussed earlier we have more control in constructing IFS, and it is possible to use less resources. In our case 3 bits per logical bit is enough. A few designs are possible, but let us consider the architecture shown in Fig. 2. Here, each logical bit contains 3 physical qubits. We have represented them with stars and dots, not because they are physically distinct, but because they play different roles. The stars are the information carrying qubits, while the dots are “isolators” in the singlet state (|↑₁, ↓₂⟩ = |↓₁, ↑₂⟩)/√2. To see the origin of the name, we note that all the stars are coupled to its neighboring dots with the same strength \( J_1 \) (meaning same \( J_{xy} \), \( J_{z} \)). Hence the interaction Hamiltonian between the information carrier and its “isolator” is

\[ 2J_{xy}(\sigma_i^x \Sigma_i^z + \sigma_i^y \Sigma_i^z) + J_z \sigma_i^z \Sigma_i^z, \] 

where \( \sigma^z = (\sigma_x \pm i \sigma_y)/2 \) and \( \Sigma_i^z = \sigma_i^z \). Since the singlet state is annihilated by the operators \( \Sigma_i^z \), we see that if all the dots are in the singlet state, the stars will be isolated from each other and no phase exchange and state propagation will happen, hence the name “isolators”. The IFS is spanned by |↑₁, ↓₂⟩ − |↓₁, ↑₂⟩)/√2 and |↓₁⟩(|↑₁, ↓₂⟩ − |↓₁, ↑₂⟩)/√2. To prepare the isolators in the singlet state, we turn on all couplings between the dots (the vertical dashed lines) while keep all couplings between the stars and dots (the solid lines) off during the initialization process. This is necessary to stop the propagation of the states in the qubit array. Here we are assuming that switching of the coupling is possible but hard and slow. Since initialization and computation are subject to different restrictions (initialization needs only be done once, and it does not need to be done quickly), the global switches are used to initialize the system. At low temperatures, the isolators will then relax to the singlet which is the lowest energy eigenstate. Once the initialization is done, we can then start the computation by turning off all couplings represented by dashed lines and turning on those represented by solid lines. According to our assumption, these couplings will remain un-tuned throughout the computation.

We see that single bit gates are trivial, we simply operate on the stars directly as if they were not coupled to anything else. Fully parallel operations are possible, thanks to the use of isolators. Two bit gates are more complicated. For the convenience of discussion, let us assume that we want to do a two bit operation between the stars \( q₁ \) and \( q₂ \) in Fig. 2 who are separated by the isolator \( i₁, i₂ \). In Fig. 2, we notice there is no coupling between \( q₁ \) and \( q₂ \). In order to implement a controlled gate be-
between them, it is necessary to somehow transfer the state of the control bit, say \(q_1\), to the isolator which is coupled to the target bit (or transfer the state of the target bit). One idea is to swap the states of \(q_1\) and \(i_1\), perform a control gate between \(i_1\) and \(q_2\) and swap back the states of \(q_1\) and \(i_1\) back. These steps can be done, if we can simulate dynamics generated by \(\sigma_1^x\sigma_1^x + \sigma_1^y\sigma_1^y\) (and similar for \(i_1\) and \(q_2\)), which can be used to generate swap and CNOT gates [3]. This is possible with our available resources, namely local units and the interaction Hamiltonian \(H_{ij} = J_{xy}(\Sigma_q^x\Sigma_i^x + \Sigma_q^y\Sigma_i^y) + J_{xy}\Sigma_q^x\Sigma_i^y\). Here we discuss a method based on selective coupling. Note that for a small time \(t = \pi/(32NJ_{xy})\), where \(N\) is some large integer, we have \(e^{-iH_{ij}t}\sigma_q^x\sigma_i^x = e^{-iH_{ij}t}\sigma_q^y\sigma_i^y = e^{-iH_{ij}t}\sigma_q^z\sigma_i^z + o(t)\). This can be further used to generate operators we want: \(e^{-i2J_{xy}\Sigma_q^x\Sigma_i^x}\sigma_2^z\sigma_2^z e^{-i2J_{xy}\Sigma_q^y\Sigma_i^y}\sigma_2^z\sigma_2^z = e^{-i4J_{xy}\sigma_q^z\Sigma_i^z}\), \(e^{-iJ_{xy}\sigma_q^z\Sigma_i^z}\sigma_2^z\sigma_2^z = e^{-i8J_{xy}\sigma_q^z\Sigma_i^z}\). Similarly we can synthesize \(e^{-i8J_{xy}\sigma_q^z\Sigma_i^z}\). Repeating this procedure \(N\) times, we then get the transformation 
\(e^{-i(\sigma_q^x\sigma_i^x + \sigma_q^y\sigma_i^y)}\), which is a swap gate between \(q_1\) and \(i_1\) (and multiplication by \(-i\) when their states are different) [3]. In order to reduce the error, the number of repetition \(N\) can be quite large [15], therefore many local gates (44 in total) are needed. The interaction time needed is \(\pi/2J_{xy}\). The CNOT (or CPHASE) gate between \(i_1\) and \(q_2\) after this swap operation, and the operation to swap back the states of \(i_1\) and \(q_1\) can be done by following the same procedure. The interaction times required are \(\pi/2J_{xy}\) each. Therefore the total interaction time is \(3\pi/2J_{xy}\), in comparison with \(\pi/4J_{xy}\) which is needed in a switchable XY model [10]. This prescription verifies the possibility of universal quantum computation in our current example. Finding physical systems to which our scheme discussed above can apply, and a set of manipulations that allow to minimize the complexity of the operation, is of further interest to us.

Before we conclude, we should mention that the “complimentary” problem to ours, in which single bit operations are hard and desired to be avoided, has been discussed [17]. Our scheme is much in the same spirit in the sense of using encoded qubits for computation, but it is for a different purpose and it has a closer relation to decoherence free subspaces (DFS). Other schemes to reduce needed resources exist too. For instance, in [18] a method of quantum computing without local control of qubit-qubit interactions was studied. In [13], the authors discussed how to do quantum computing with only single bit measurements on a class of entangled states, which are prepared by unitary evolution under controllable Ising-type interactions.

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