Observation of non-Abelian exchange statistics on a superconducting processor

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Indistinguishability of particles is a fundamental principle of quantum mechanics\(^1\). For all elementary and quasiparticles observed to date - including fermions, bosons, and Abelian anyons - this principle guarantees that the braiding of identical particles leaves the system unchanged\(^2,3\). However, in two spatial dimensions, an intriguing possibility exists: braiding of non-Abelian anyons causes rotations in a space of topologically degenerate wavefunctions\(^4,5\). Hence, it can change the observables of the system without violating the principle of indistinguishability. Despite the well-developed mathematical description of non-Abelian anyons and numerous theoretical proposals\(^9–22\), their experimental observation has remained elusive for decades. Using a superconducting quantum processor, we prepare the ground state of the surface code and manipulate it via unitary operations to form wavefunctions that are described by non-Abelian anyons. By implementing a unitary protocol\(^23\) to move the anyons, we experimentally verify the fusion rules of non-Abelian Ising anyons and braid them to realize their statistics. Building on our technique, we study the prospect of employing the anyons for quantum computation and utilize braiding to create an entangled state of anyons encoding three logical qubits. Our work represents a key step towards topological quantum computing.

Elementary particles in three dimensions (3D) are either bosons or fermions. The existence of only two types is rooted in the fact that the worldlines of two particles in 3+1 dimensions can always be untied in a trivial manner. Hence, exchanging a pair of indistinguishable particles twice is topologically equivalent to not exchanging them at all, and the wavefunction must remain the same. Representing the exchange as a matrix \( R \) acting on the space of wavefunctions with a constant number of particles, it is thus required that \( R^2 = 1 \), leaving two possibilities: \( R = 1 \) (bosons) and \( R = -1 \) (fermions). Such continuous deformation is not possible in two dimensions (2D), thus allowing collective excitations (quasiparticles) to exhibit richer braiding behavior. In particular, this permits the existence of Abelian anyons\(^2,3,6,8,24,25\), where the global phase change due to braiding can take any value. It has been proposed that there exists another class of quasiparticles known as non-Abelian anyons, where braiding instead results in a change of the observables of the wavefunction\(^4,5,24\). In other words, \( R^2 \) does not simplify to a scalar, but remains a unitary matrix. Therefore, \( R^2 \) is a fundamental characteristic of anyon braiding. The topological approach to quantum computation\(^26\) aims to leverage these non-Abelian anyons and their topological nature to enable gate operations that are protected against local perturbations and decoherence errors\(^5,27–30\).

In solid-state systems, primary candidates of non-Abelian quasiparticles are low-energy excitations in the \( 5/2 \) fractional quantum Hall states\(^31,32\), vortices in topological superconductors\(^33,34\), and Majorana zero modes in semiconductors proximitized by superconductors\(^35–38\). However, direct verification of non-Abelian exchange statistics has remained elusive\(^39–41\).

We formulate the necessary requirements for experimentally certifying a physical system as a platform for topological quantum computation\(^5,26\): (1) create an anyon pair; (2) verify the rules that govern the “collision” of two anyons, known as the fusion rules; (3) verify the non-Abelian braiding statistics reflected in the matrix structure \( R^2 \); (4) realize controlled entanglement of anyonic degrees of freedom. Notably, the observation of steps (2-4) requires measurements of multi-anyon states, via fusion or non-local measurements.

The advent of quantum processors allows for controlled unitary evolution and direct access to the wavefunction rather than the parameters of the Hamiltonian. These features enable the use of local operations for efficient preparation of topological states that can host non-Abelian anyons, and - as we will demonstrate - their subsequent braiding and fusion. Moreover, these platforms allow for multi-qubit (i.e. non-local) measurements.

In order to realize a many-body quantum state that can host anyons, it is essential to control the topological degeneracy. A suitable platform for achieving this requirement is a stabilizer code\(^42\), where the wavefunctions are characterized by a set of commuting operators \( \{ S_p \} \) called stabilizers, with \( \hat{S}_p \psi \rangle = s_p \hat{\psi} \rangle \) and \( s_p = \pm 1 \). The code space is the set of degenerate wavefunctions for which \( s_p = 1 \) for all \( p \). Hence, every independent stabilizer divides the degeneracy of the code space by two.

While the physical layout of qubits is typically used to determine the structure of the stabilizers, the qubits can be considered to be degree-\( j \) vertices (\( D_j V; j \in \{ 2, 3, 4 \} \)) on more general planar graphs (see Fig. 1a)\(^23\). Using this picture, each stabilizer can be associated with a plaquette \( p \), whose vertices are the qubits on which \( \hat{S}_p \) acts:

\[
\hat{S}_p = \prod_{v \in \text{vertices}} \hat{\tau}_{p,v}.
\]

\( \hat{\tau}_{p,v} \) is here a single-qubit Pauli operator acting on vertex \( v \), chosen to satisfy a constraint around that vertex (Fig. 1b). An instance where \( s_p = -1 \) on a plaquette is called a plaquette violation. These can be thought of as quasi-particles, which are created and moved through single-qubit Pauli operators (Fig. 1a). A pair of plaquette violations sharing an edge constitute a fermion, \( \tau \).
FIG. 1. Deformations of the surface code. a, Stabilizer codes are conveniently described in a graph framework. Through deformations of the surface code graph, a square grid of qubits (crosses) can be used to realize more generalized graphs. Plaquette violations (red) correspond to stabilizers with \( s_p = -1 \) and are created by local Pauli operations. In the absence of deformations, plaquette violations are constrained to move on one of the two sub-lattices of the dual graph in the surface code, hence the two shades of blue. b, A pair of D3Vs (yellow triangles) appears by removing an edge between two neighboring stabilizers, \( S_1 \) and \( S_2 \), and introducing the new stabilizer, \( S = S_1 S_2 \). A D3V is moved by applying a 2-qubit entangling gate, \( \exp \left( \frac{\pi}{4} [S', S] \right) \). In the presence of bulk D3Vs, there is no consistent way of checkerboard coloring, hence the (arbitrarily chosen) gray regions. Top right: in a general stabilizer graph, \( \hat{S}_p \) can be found from a constraint at each vertex, where \( \{\tau_1, \tau_2\} = 0 \).

By introducing the graph framework, it was predicted by Lensky et al. that the vertices of degree 2 and 3 host non-Abelian anyons. Consider the stabilizer graph of the surface code, specifically with boundary conditions such that the degeneracy is two. While all the vertices in the bulk are D4Vs, one can create two D3Vs by removing an edge between two neighboring plaquettes \( p \) and \( q \), and introducing the new stabilizer \( S = S_p S_q \) (Fig. 1b). Evidently, the introduction of two D3Vs reduces the number of independent stabilizers by one and thus doubles the degeneracy. This doubling is exactly what is expected when a pair of Ising anyons is introduced; hence, D3Vs appear as a candidate of non-Abelian anyons, and we will denote them as \( \sigma \).

In order to be braided and fused by unitary operations, the D3Vs must be moved. While the structure of the stabilizer graph is usually considered to be static, it was shown in Ref. that the D3Vs can in fact be moved - thus deforming the stabilizer graph - using local two-qubit Clifford gates. To shift a D3V from vertex \( u \) to \( v \), an edge must be disconnected from \( v \) and reconnected to \( u \).

This can be achieved via the gate unitary \( \exp \left( \frac{\pi}{4} \hat{S}_p \right) \), where \( \hat{S}_p \) is the original stabilizer containing the edge and \( u \), and \( \hat{S}'_p \) is the new stabilizer that emerges after moving the edge. In cases where the D3V is shifted between two connected vertices, the unitary simplifies to the form \( U_{\pm} (\hat{\tau}_u \hat{\tau}_v) \equiv \exp (\pm i \frac{\pi}{4} \hat{\tau}_u \hat{\tau}_v) \), where \( \hat{\tau}_u \) and \( \hat{\tau}_v \) are Pauli operators acting on vertices \( u \) and \( v \). We experimentally realize this unitary through a CZ-gate and single-qubit rotations (median errors of \( 7.3 \times 10^{-3} \) and \( 1.3 \times 10^{-3} \), respectively; see Supplementary materials).

In the first experiment we demonstrate the creation of anyons and the fundamental fusion rules of \( \sigma \) and \( \epsilon \) (Fig. 2a). In a 5\( \times \)5 grid of superconducting qubits, we first use a protocol consisting of four layers of CZ-gates to prepare the surface code ground state (panel I in Fig. 2b, see also43). The average stabilizer value after the ground state preparation is \( 0.94 \pm 0.04 \) (individual stabilizer values shown in Fig. S3c). We then remove a stabilizer edge to create a pair of D3Vs (\( \sigma \)) and separate them through the application of two-qubit gates. Panels I-IV in Fig. 2b show the measured stabilizer values in the resultant graph at each step of this procedure (note that the measurements are destructive and the protocol is restarted after each measurement). In panel V, single-qubit Z-gates are applied to two qubits near the lower left corner of the grid to create adjacent plaquette violations, which together form a fermion. Through the sequential application of X- and Z-gates (VI to VIII), one of the plaquette violations is then made to encircle the right \( \sigma \) vertex. Crucially, after moving around \( \sigma \), the plaquette violation does not return to where it started, but rather to the location of the other plaquette violation. This enables them to annihilate (VIII), causing the fermion to seemingly disappear. However, by bringing the two \( \sigma \) back together and annihilating them (IX through XI), we arrive at a striking observation: an \( \epsilon \)-particle re-emerges on two of the square plaquettes where the \( \sigma \)-vertices previously resided.

Our results demonstrate the fusion of \( \epsilon \) and \( \sigma \). The disappearance of the fermion from step V to VIII establishes the fundamental fusion rule of \( \epsilon \) and \( \sigma \):

\[
\sigma \times \epsilon = \sigma .
\]

We emphasize that none of the single-qubit gates along the path of the plaquette violation are applied to the qubits hosting the mobile \( \sigma \); our observations are therefore solely due to the non-local effects of non-Abelian D3Vs, and exemplify the unconventional behavior of the latter. Moreover, another fusion rule is seen by considering the reverse path IV→I, and comparing it to the path VIII→XI. These two paths demonstrate that a pair of \( \sigma \) can fuse to form either vacuum (I) or one fermion (step I and XI, respectively):

\[
\sigma \times \sigma = 1 + \epsilon .
\]

Importantly, the starting points of these two paths (IV and VIII) cannot be distinguished by any local measurement. We therefore introduce a non-local measurement technique that allows for detecting an \( \epsilon \) without fusing the \( \sigma \). The key idea underlying this method is that
A D3V (σ) pair is then created (II) and separated (III-IV), before creating a fermion, ε (V). One of the plaquette violations is brought around the right σ (VI-VIII), allowing it to annihilate with the other plaquette violation (VIII). The fermion has seemingly disappeared, but re-emerges when the σ are annihilated (XI; stabilizer values: -0.86 and -0.87). The path V→VIII demonstrates the fusion rule, \( \sigma \times \varepsilon = \sigma \). The different fermion parities at the end of the paths VIII→XI and IV→I show the other fusion rule, \( \sigma \times \varepsilon = 1 + \varepsilon \). Yellow triangles represent the positions of the \( \sigma \). The brown and red lines denote the paths of the \( \sigma \) and the plaquette violation, respectively. Red squares (diamonds) represent X- (Z-) gates. Upper left: table of two-qubit unitaries used in the protocol. c, Non-local technique for hidden fermion detection: the presence of a fermion in a \( \sigma \)-pair can be deduced by measuring the sign of the Pauli string \( \mathcal{P} \) corresponding to bringing a plaquette violation around the \( \sigma \)-pair (gray path). \( \mathcal{P} \) is equivalent to the shorter string \( \mathcal{P}' \) (black path). Measurements of \( \mathcal{P}' \) in steps VIII (top) and IV (bottom) give values \(-0.85 \pm 0.01\) and \(+0.84 \pm 0.01\), respectively. This indicates that there is a hidden fermion pair in the former case, but not in the latter, despite the stabilizers being the same.

Having demonstrated the above fusion rules involving \( \sigma \), we next braid them with each other to directly show their non-Abelian statistics. We consider two spatially separated \( \sigma \)-pairs, A and B, by removing two stabilizer edges (Fig. 3a and panel II in Fig. 3b). Next, we apply two-qubit gates along a horizontal path to separate the \( \sigma \) in pair A (panel III), followed by a similar procedure in the vertical direction on pair B (IV), so that one of its \( \sigma \) crosses the path of pair A. We then subsequently bring the \( \sigma \) from pairs A and B back to their original positions (V-VIII and IX-XI, respectively). Strikingly, when the two \( \sigma \)-pairs are annihilated in the final step (XII), we observe that a fermion is revealed in each of the positions where the \( \sigma \)-pairs resided (average stabilizer value: \( -0.45 \pm 0.06 \)). This shows a clear change in local observables from the initial state where no fermions were present. As a control experiment, we repeat the experiment with distinguishable \( \sigma \)-pairs, achieved by attaching a plaquette violation to each of the \( \sigma \) in pair B (Fig. 3c,d; see also Fig. S6 for stabilizer measurements through the full protocol). Moving the plaquette violation along with the \( \sigma \) requires a string of single-qubit gates, which switches the direction of the rotation in the multi-qubit unitaries, \( U_\pm \to U_\mp \). In this case, no fermions are observed at the end of the protocol (average stabilizer value: \( +0.46 \pm 0.04 \)), thus providing a successful control.

Importantly, fermions can only be created in pairs in the bulk. Moreover, the fusion of two \( \sigma \) can only create
Braiding indistinguishable, non-Abelian D3Vs

FIG. 3. Braiding of non-Abelian anyons. a, Wordline schematic of the braiding process. b, Experimental demonstration of braiding, displaying the values of the stabilizers throughout the process. Two σ-pairs, A and B, are created from the vacuum |1⟩, and one of the σ in pair A is brought to the right side of the grid. Next, a σ from pair B is moved to the top, thus crossing the path of pair A, before bringing σ-pairs A and B back together to complete the braid. In the final step, two fermions appear in the locations where the σ-pairs resided, constituting a change in the local observables. The diagonal σ move in step IV requires two SWAP-gates (3 CZ-gates each) and a total of 10 CZ-gates. The three-qubit unitary in step VIII requires 4 SWAP-gates and a total of 15 CZ-gates. In the full circuit, a total of 40 layers of CZ-gates are applied (see Supplementary materials). The yellow triangles represent the locations of the σ; the brown and green lines represent the paths of σ from pair A and B, respectively.

c, As a control experiment, we perform the same braid as in a, but with distinguishable σ by attaching a plaquette violation to the σ in pair B (represented as purple triangles). d, Same as b, but using distinguishable σ (only showing steps I, IV and XII).

In contrast to b, no fermions are observed in step XII.

Zero or one fermion (Eqn. 3). Hence, our experiment involves the minimal number of bulk σ (four) needed to encode two fermions and demonstrate non-Abelian braiding. Since the fermion parity is conserved, effects of gate imperfections and decoherence can be partially mitigated by post-selecting for an even number of fermions. This results in fermion detection values of $-0.76 \pm 0.03$ and $+0.79 \pm 0.04$ in Fig. 3b and d, respectively.

Together, our observations show the change in local observables by braiding of indistinguishable σ and constitute a direct demonstration of their non-Abelian statistics. In other words, the double-braiding operation $R^2$ is a matrix that cannot be reduced to a scalar. Specifically, it corresponds to an X-gate acting on the space spanned by zero- and two-fermion wavefunctions.

The full braiding circuit consists of 40 layers of CZ-gates and 41 layers of single-qubit gates (36 of each after ground state preparation). The effects of imperfections in this hardware implementation can be assessed through comparison with the control experiment. The absolute values of the stabilizers where the fermions are detected in the two experiments (dashed boxes in step XII of Fig. 3b,d) are very similar (average values of $-0.45$ and $+0.46$). This is consistent with the depolarization channel model, where the measured stabilizer values are proportional to the ideal values ($\pm 1$).

We next study the prospects of utilizing D3Vs to encode logical qubits and prepare an entangled state of anyon pairs. By doubling the degeneracy, each additional σ-pair introduces one logical qubit, where the $|0\rangle_L$ ($|1\rangle_L$) state corresponds to an even (odd) number of hidden fermions. Importantly, the measurements of the fermion numbers in several σ-pairs are not fully independent: bringing a plaquette violation around one σ-pair is equivalent to bringing it around all the other pairs (due to the conservation of fermionic parity). Hence, $N \geq 2$ anyons encode $N/2 - 1$ logical qubits. Interestingly, the D3Vs we have created and manipulated so far are not the only ones present in the stabilizer graph; with the boundary conditions used here, each of the four corners are D3Vs, no different from those in the bulk23. Indeed, the existence of D3Vs in the corners is the reason why a single fermion could be created in the corner in step V of Fig. 2b. This is also consistent with the fact that the surface code itself encodes one logical qubit in the absence of additional D3Vs. Here we create two pairs of D3Vs, in addition to the four that are already present in the corners, to encode a total of three logical qubits.

Through the use of braiding, we aim to prepare an entangled state of these logical qubits, specifically a GHZ-state on the form, $(|000\rangle + |111\rangle)/\sqrt{2}$. The definition of a GHZ-state and the specifics of how it is prepared is basis-dependent. In most systems the degrees of freedom are local and there is a natural choice of basis. For spa-
tially separated anyons, the measurement operators are necessarily non-local. Here we choose the basis defined as follows: for the first two logical qubits, we choose the logical $Z_{L,i}$ operators to be Pauli strings encircling each of the bulk $\sigma$-pairs, as was used in Fig. 2c (green and turquoise paths in the left column of Fig. 4a). For the logical surface code qubit, we define $\hat{Z}_{L,3}$ as the Pauli string that crosses the grid horizontally through the gap between the bulk D3V pairs, effectively enclosing four $\sigma$ (red path in Fig. 4a). In this basis, the initial state is a product state.

While a double braid was used to implement the operator $X$ in Fig. 3, we now perform a single braid (Fig. 4b) to realize $\sqrt{X}$ and create a GHZ-state. We implement this protocol by bringing one $\sigma$ from each bulk pair across the grid to the other side (Fig. 4c). For every anyon double exchange across a Pauli string, the value of the Pauli string changes sign. Hence, a double exchange would change $|000\rangle$ to $|111\rangle$, while a single exchange is expected to realize the superposition, $|\langle 111 \rangle + |000\rangle\rangle/\sqrt{2}$.

In order to study the effect of this operation, we perform quantum state tomography on the final state, which requires measurements of not only $\hat{Z}_{L,1}$, but also $\hat{X}_{L,3}$ and $\hat{Y}_{L,i}$ on the three logical qubits. For the first two logical qubits, $\hat{X}_{L,1}$ is the Pauli string that corresponds to bringing a plaquette violation around only one of the $\sigma$ in the pair (as demonstrated in Fig. 2b). Both the logical $\hat{X}_{L,3}$ and $\hat{Z}_{L,1}$ operators can be simplified by reducing the original Pauli strings (green and turquoise paths in the left column of Fig. 4c) to equivalent, shorter ones (right column). $\hat{Z}_{L,1}$ can in fact be reduced to a single $\hat{Y}$-operator.

For the logical surface code qubit, we define $\hat{X}_{L,3}$ as the Pauli string that crosses the grid vertically between the bulk D3V pairs (red path in Fig. 4a). Finally, the logical $\hat{Y}_{L,i}$-operators are simply found from $\hat{Y}_{L,i} = i\hat{X}_{L,i}\hat{Z}_{L,i}$. Measuring these operators, we reconstruct the density matrix of the final state (Fig. 4d,e), which has a purity of $\sqrt{\text{Tr} \{ \rho^2 \} } = 0.646 \pm 0.003$ and an overlap with the ideal GHZ-state of $\text{Tr} \{ \rho_{GHZ} \rho \} = 0.623 \pm 0.004$ (uncertainties estimated from bootstrapping). The fact that the state fidelity is similar to the purity suggests that the infidelity is well described by a depolarizing error channel.

In conclusion, we have realized highly controllable braiding of degree-3 vertices, enabling the demonstration of the fusion and braiding rules of non-Abelian Ising anyons. We have also shown that braiding can be used to create an entangled state of three logical qubits encoded in these anyons. With the potential inclusion of error correction, which involves overheads including readout of 5-qubit stabilizers, our observations highlight a new path for fault-tolerant quantum computing.
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Supplemental materials for: Observation of non-Abelian exchange statistics on a superconducting processor

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I. QUBIT DECOHERENCE AND GATE CHARACTERIZATION

The experiments are performed on a quantum processor with frequency-tunable transmon qubits and a similar design to that in Ref.45. Figure S1a shows the measured relaxation times of the 25 qubits that were used in the experiment, with a median value of $T_1 = 21.7 \mu s$. We also measure the dephasing time $T_2$ in a Hahn echo experiment, shown in Fig. S1b, with the same median value of 21.7 $\mu s$.

Next, we benchmark the gates used in the experiment. Fig. S2a and b show the cumulative distribution of the Pauli errors for single- and two-qubit (CZ) gates, respectively. The median Pauli errors are $1.3 \times 10^{-3}$ for the single-qubit gates and $7.3 \times 10^{-3}$ for the two-qubit gates.

![Cumulative distribution of $T_1$ and $T_2$ times](image)

FIG. S1. **Qubit relaxation** ($T_1$) and **coherence** ($T_2$) times. **a, b** Cumulative distributions of $T_1$ (a) and $T_2$ (b), where the latter is measured using a Hahn echo sequence. Dashed lines indicate the median values of 21.7 $\mu s$ for both measures. Insets: $T_1$ and $T_2$ plotted against qubit number.
FIG. S2. **Gate errors.** a,b, Cumulative distributions of the Pauli error for single-qubit (a) and two-qubit CZ (b) gates. We find median error values of $1.3 \times 10^{-3}$ and $7.3 \times 10^{-3}$ for the single-qubit and CZ-gates, respectively.

II. READOUT DETAILS

We mitigate asymmetric readout effects by using a symmetrized readout scheme, in which an $X$-gate is applied to every qubit before the readout step in half of the measurements. Moreover, readout errors are corrected for by dividing each stabilizer value by (symmetrized) measurements of $\langle H_i Z_i \rangle$ on the $|00..00\rangle$-state, where the product runs over all qubits in the stabilizer. Fig. S3 displays the measured readout errors, as well as a comparison of the stabilizer values in the surface code ground state (same data as in step I in main text Fig. 2) before and after readout correction.

FIG. S3. **Readout benchmarking and correction:** a, Histogram of readout error, with a median value of 2.0% (dashed vertical line). Inset: Readout error plotted against qubit number. b,c, Stabilizer values of the surface code ground state before (a) and after (b) readout correction.
III. CIRCUIT DETAILS

In our experiment, the two-qubit unitaries $U_{\pm}(\hat{\tau}_1\hat{\tau}_2)$ are converted to single-qubit rotations and CZ-gates, as shown in Fig. S4a. In the particular case where a D3V is moved diagonally (see step IV in Fig. 3 in the main text), we realize the unitary by including two SWAP-gates (also converted to CZ-gates) since the qubits are connected in a square grid (see Fig. S4b). Moreover, the three-qubit unitary in step VIII in Fig. 3 is equivalent to a combination of single-qubit gates, 4 SWAP-gates and 4 CZ-gates (Fig. S4c), which can be further converted to single-qubit gates and 15 CZ-gates. We also include five single-qubit rotations to permute $\hat{X}$, $\hat{Y}$ and $\hat{Z}$ of the three stabilizers touching the moving D3V in steps V-VIII and IX-XI, as well as three Hadamard gates to return all stabilizers to the original $\hat{Z}\hat{X}\hat{Z}$-form in XII. In the experimental implementation of the circuit, adjacent single-qubit gates on the same qubit are merged together and performed in the layer after the most recent CZ-gate (Fig. S4d).

**FIG. S4. Circuit details.** a, The unitary needed to move a D3V between two neighboring vertices is realized in the experiment through the use of one CZ-gate and single-qubit rotations. b, When D3Vs are moved diagonally, we include two SWAP-gates, requiring three CZ-gates each. c, The three-qubit unitary used in step VIII in Fig. 3 is equivalent to a combination of single-qubit gates, 4 SWAP-gates and 4 CZ-gates. d, Adjacent single-qubit gates are merged and shifted left to the nearest CZ-gate.
**IV. DYNAMICAL DECOUPLING**

In order to mitigate the effects of qubit decoherence during the circuits, we perform dynamical decoupling on qubits that are idle for more than three layers of gates. In particular, we utilize the Carr-Purcell-Meiboom-Gill (CPMG) scheme, consisting of $X$-pulses interspaced by a wait time of $\tau = 25$ ns. Fig. S5 shows an example comparison of the stabilizers in cases with and without dynamical decoupling, after braiding of anyons (41 layers of SQ gates and 40 layers of CZ-gates). A clear improvement is observed, increasing the average absolute stabilizer value from 0.50 to 0.58.

**FIG. S5.** Dynamical decoupling. a,b, Stabilizer values without (a) and with (b) dynamical decoupling, after D3V braiding. Dynamical decoupling improves the average absolute stabilizer value from 0.50 to 0.58.

**V. ADDITIONAL BRAIDING DATA**

In Figure 3 in the main text, we demonstrate that no fermion appears when distinguishable $\sigma$ are braided with each other. In Fig. S6, we show the data for each step in that protocol, analogous to those shown for indistinguishable $\sigma$ in the main text. Moreover, we also present an alternative braiding scheme in Fig. S7, which requires fewer (18) CZ-gates. In this case, however, pair B is not brought back together, and neither of the $\sigma$-pairs are annihilated. Therefore, similar to in Fig. 2c, we measure the Pauli string corresponding to bringing a plaquette violation around pair A (gray path in Fig. S7c), which in this case can be reduced to $\hat{Y}$ on the qubit where the two $\sigma$ overlap. We find $\langle \hat{P} \rangle = \langle \hat{Y} \rangle = -0.71 \pm 0.01$, indicating that braiding the $\sigma$ led to the creation of a fermion (Fig. S7c). Note that we here only search for fermions in one of the $\sigma$-pairs. As a control experiment, we repeat the experiment with distinguishable $\sigma$-pairs, as in the main text (Fig. S7d). In this case, we find $\langle \hat{P} \rangle = +0.71 \pm 0.01$, thus demonstrating that no fermion was produced. Together, these observations constitute another demonstration of non-Abelian exchange statistics of the D3Vs.
FIG. S6. **Braiding distinguishable D3Vs** a, Braiding schematic of worldlines. b, Step-by-step depiction of stabilizers as the two $\sigma$ are braided, analogous to that in Fig. 3 in the main text, but with distinguishable $\sigma$.

FIG. S7. **Alternative protocol for braiding** $\sigma$. a, Schematic displaying the braiding process of the two $\sigma$-pairs. b, Experimental demonstration of braiding, displaying the values of the stabilizers throughout the process. Two $\sigma$-pairs, A and B, are created from the vacuum $\mathbf{1}$, and one of the D3Vs in pair A is brought to the right side of the grid. Next, a $\sigma$ from pair B is moved to the top, thus crossing the path of the first $\sigma$, before bringing the $\sigma$ from pair A back again to complete the braid. The diagonal $\sigma$ move performed in step VI is achieved by including two SWAP-gates, corresponding to 6 additional CZ-gates. The yellow triangles represent the locations of the $\sigma$, while the brown and green lines represent the paths of $\sigma$ from pair A and B, respectively. The average absolute stabilizer value is $0.93 \pm 0.06$ and $0.77 \pm 0.09$ in the first and last step, respectively. c, After braiding the $\sigma$, we search for hidden fermions by measuring the Pauli string $\hat{P}$ (left panels), which here is equivalent to $\hat{Y}$ on the qubit where the two $\sigma$ overlap. The measurement yields $\langle \hat{P} \rangle = \langle \hat{Y} \rangle = -0.71 \pm 0.01$, indicating creation of a fermion. Right: world-lines of braiding process, including non-local measurement based on plaquette violation loop. d, Same as c, but after braiding two distinguishable $\sigma$, achieved by applying the inverse two-qubit gates when moving the $\sigma$ in pair B. The measurement yields $\langle \hat{Y} \rangle = +0.71 \pm 0.01$, indicating no fermion creation.