Research Article

Investigation of Antireflective Porous Silicon Coating for Solar Cells

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1. Introduction

It is one of the most important issues to reduce optical losses in crystalline silicon solar cells by surface modification. Porous silicon as a new material was first reported over 40 years ago by Uhlir. It is well known that a porous silicon (PS) layer formed in the n⁺, emitter of a Si solar cell can be used as an effective antireflection coating. The visible light emission from PS has generated a great deal of interest due to its potential technological applications. It is widely believed that PS can be adapted to mass production of solar cells because of the simple and cheap technology.

The PS is produced by anodisation of Si wafers in a solution of hydrofluoric acid (HF) with current densities below those used for electropolishing. This imposes that the PS formation does not cause any damage to the electrical contacts. The PS formation mechanism in chemical etching depending on solution concentration, temperature, and etching time is a more simple process than the electrochemical one. The porosity was controlled by gravimetric methods and calculated by equations

\[ P = \left( \frac{m_1 - m_3}{m_1 - m_2} \right) \times 100\%, \]  

where \( m_1 \) is initial substrate weight, \( m_2 \) weight after anodisation, and \( m_3 \) weight after removing the PS layer in the NaOH mixture.

In this paper, porous Si layers on the front surface of textured Si substrates have been investigated with the aim of improving the optical losses of the solar cells because an ARC and a surface passivation can be obtained simultaneously in one process.

2. Experiment

The silicon samples used in this study were a resistivity in the range of 0.5–3.0 Ω cm <100> oriented CZ p-Si wafers. All samples had identical thickness of 200 μm and size of 4 cm². Prior to the phosphorous emitter diffusion, the wafers received a standard cleaning and additionally the samples were textured at 80–85°C for 30 min in a KOH (20%) texturing solution [1].

The textured wafers were diffused under optimized conditions using diffusion source of POCl₃. The predeposition was carried out under the condition at 830°C for 20 min, and the drive-in was carried out under the condition at 830°C for 15 min. As a result, the sheet resistance of the n⁺
region was found to be less than 25 Ω/□. The phosphosilicate glass layer was removed from the silicon surface with diluted hydrofluoric acid (HF). The electrical contacts were made by a screen printing process, with FERRO silver paste for front and aluminum paste for the back contacts. Metallization was done at 860°C for 2 min in the conventional annealing furnace. Lastly, the porous silicon layers were grown on the n⁺ region by electrochemical etching process.

In this study, we did not make antireflection coatings. To measure I-V curve characteristics of solar cells, the PS layers were performed in a special Teflon bath using a two-electrode arrangement. Figure 1 is schematic of a conventional single-tank cell.

The anodisation was carried out with a current density of 20 mA/cm² applied during 10 sec. As a result, we obtained a blue-coloured silicon layer between the grid fingers on the surface 4 cm² of the n⁺ emitter silicon solar cells. The sheet resistivity of the porous layer measured by a 4-point method was close to about 100 Ω/□.

3. Result and Discussion

The alkaline textured Si of the A sample in Figure 2, the PS layer formation of the B sample in Figure 3 and the PS layer formation after textured Si of the C sample in Figure 4 are plotted on reflectance characteristics in Figure 5.

It can be seen that the PS layer caused a decrease in reflectivity as did antireflection coating. The minimum reflectivity for a blue-colored PS layer is 600 nm which is optimum for a solar cell. In contrast with antireflection coating, the PS layer caused noticeable decrease in reflectivity for shot wavelengths below 400 nm in Figure 5.

In the case of textured surfaces, the PS layer gives additional decrease of reflectivity (400–1000 nm) for a silicon solar cell. This demonstrates that PS could be used as antireflection coating in solar cells, bearing in mind that its preparation is much simpler to implement and it is less
Table 1: The results from the three kinds of solar cells measured under an AM 1.5 global spectrum.

| Solar cell parameters | A sample | B sample | C sample |
|-----------------------|----------|----------|----------|
| Isc (A)               | 0.13     | 0.15     | 0.16     |
| Voc (V)               | 0.56     | 0.56     | 0.57     |
| FF (%)                | 55.27    | 64.33    | 65.77    |
| Eff. (%)              | 9.55     | 10.52    | 11.28    |

Figure 4: Formation of textured Si with porous layer.

Figure 5: Surface reflectance of textured Si and reflectivity of forming porous layer and reflectivity of Textured Si with porous layer.

The silicon solar cells performed as PS/n+/p-Si structure, shown in Figure 5, have been measured under AM 1.5 global spectrum, and the electrical parameters of A, B, and C samples are reported in Table 1.

The following effects can be observed on the basis of experimental results and the data listed in Table 1. Short circuit current of Cz-Si PS layer formation after texturing gains more than 0.02 A for solar cell on the textured Si-Cz and more than 0.01 A for a solar cell on the PS layer formation.

Voc remains the same in solar cells with a porous layer as compared to the solar cells with textured Si, demonstrating that PS does not introduce noticeable degradation in this respect and does not attack the metallic contacts and their adherence to the cell. The cell efficiency increases by about 2% for Cz-Si PS layer formation after texturing more than textured Cz-Si for a solar cell (Figure 6).

The main conclusions can be extracted from these results: a porous surface layer might act as an antireflection coating that gives as a result an increase in short circuit current and efficiency of a solar cell. Additionally, the formation of the PS layer removes the highly doped dead layer between the fingers from the top surface of the emitter.

Due to this process, we can make the selective emitter and antireflection coating in one technological step. The very important fact is that the porous layers reported here have been homogeneous on a large surface of the silicon solar cells.

We think that it is a very simple and low-cost porous formation process that can be easily applied, in the future, in photovoltaic cell technology as a standard procedure.
Figure 6: PS ARC formation by electrochemical etching on a Si solar cell with the electrical front and back contacts.

Acknowledgment

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References

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